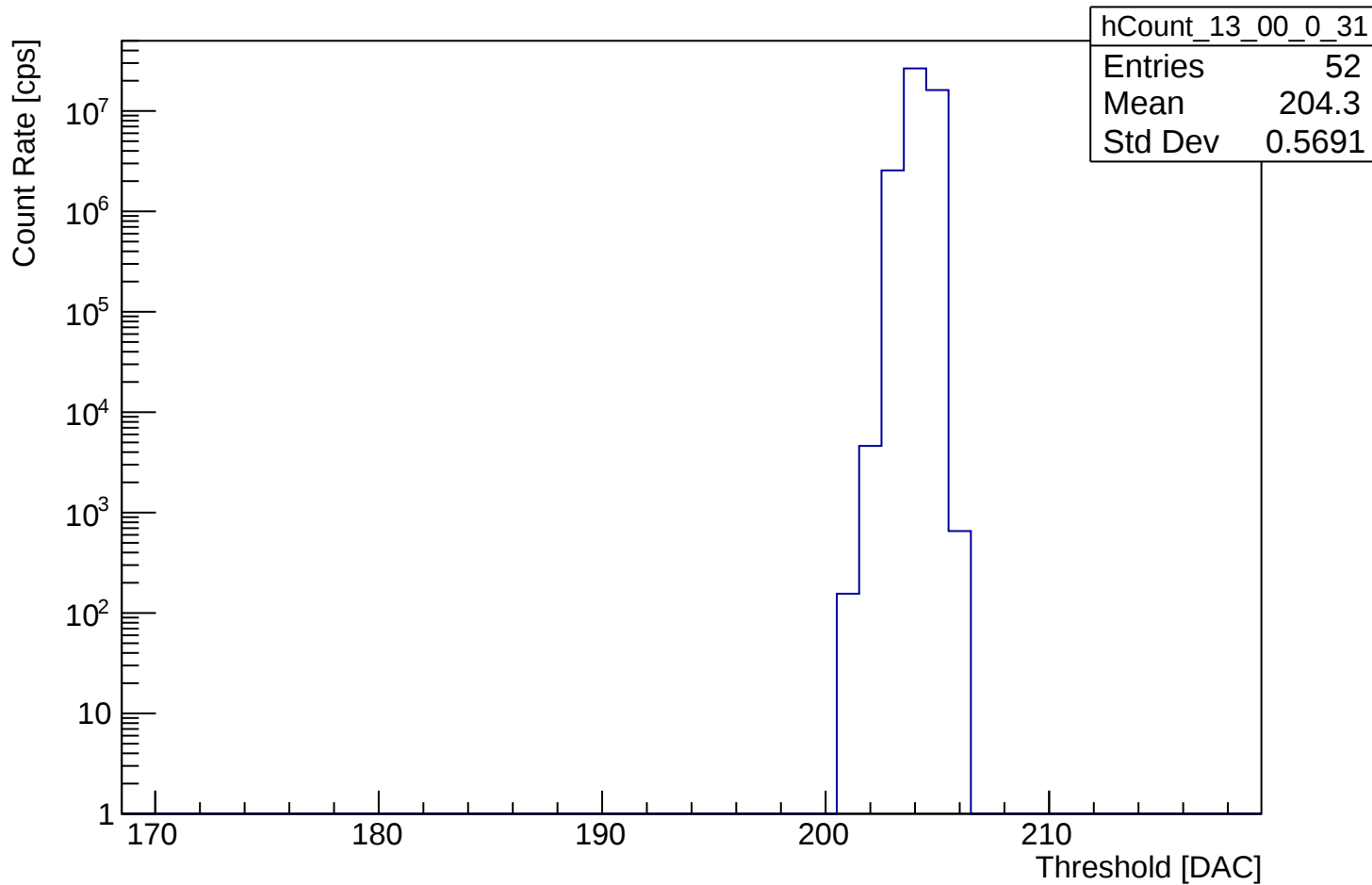
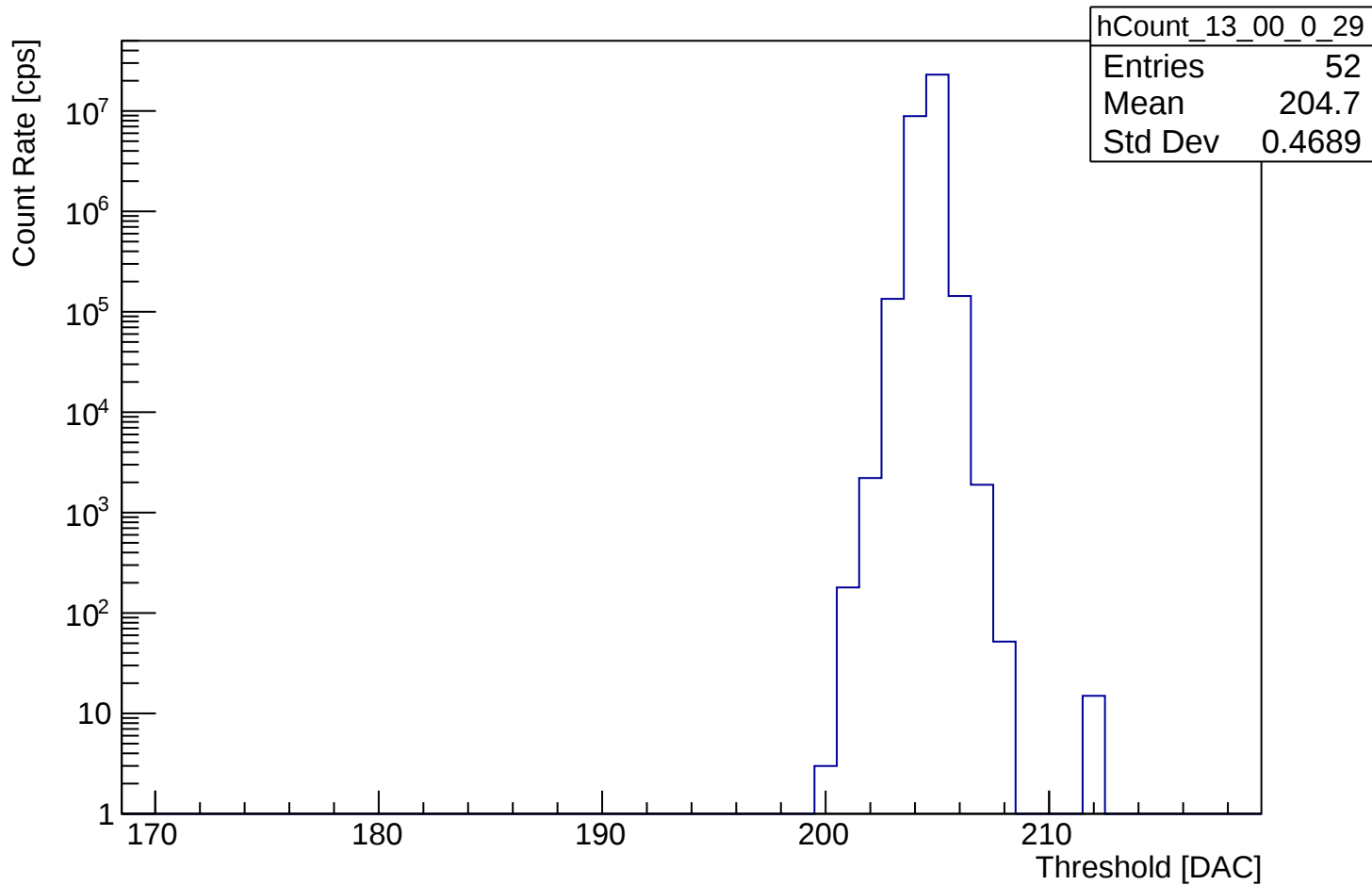


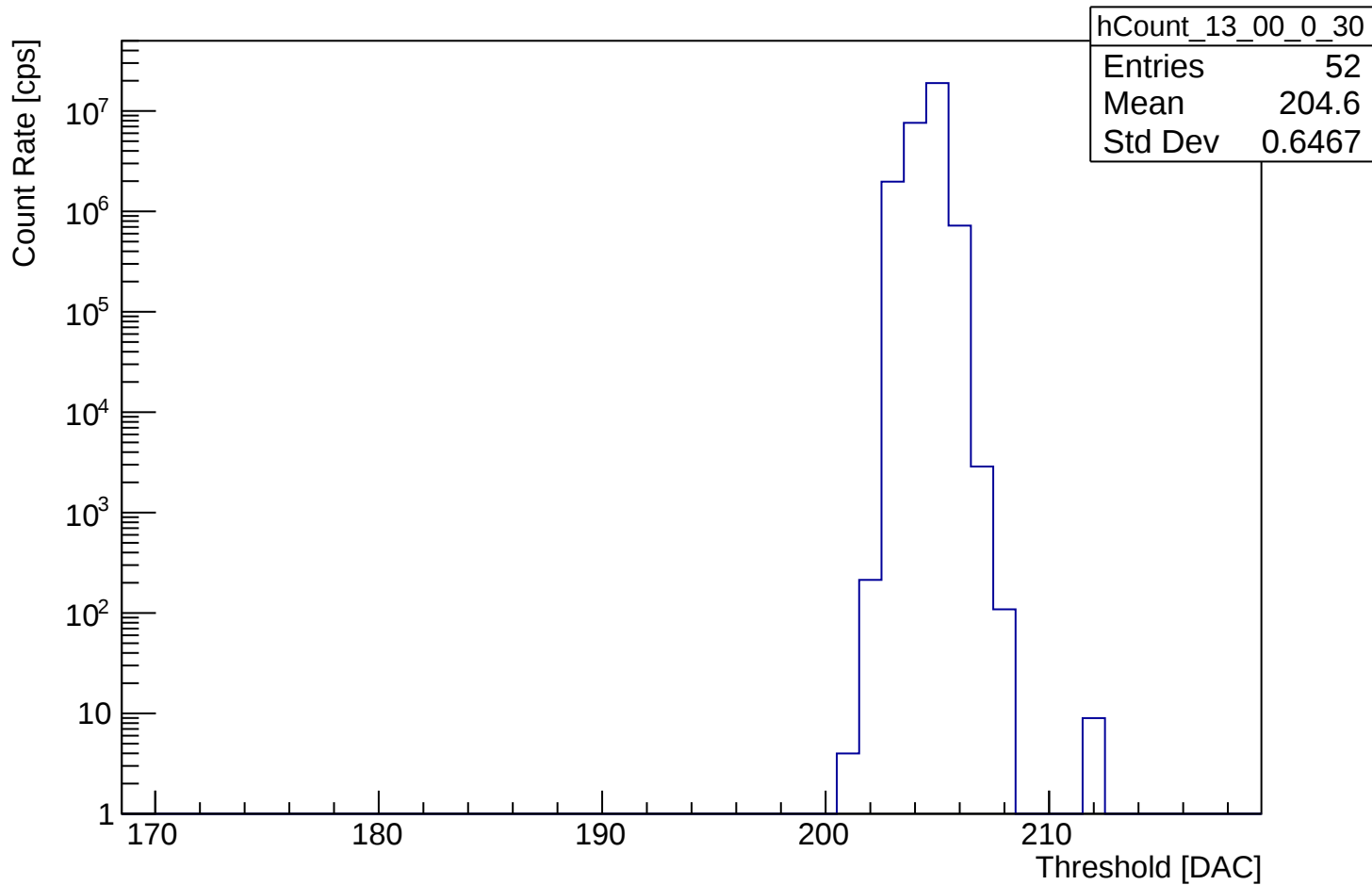
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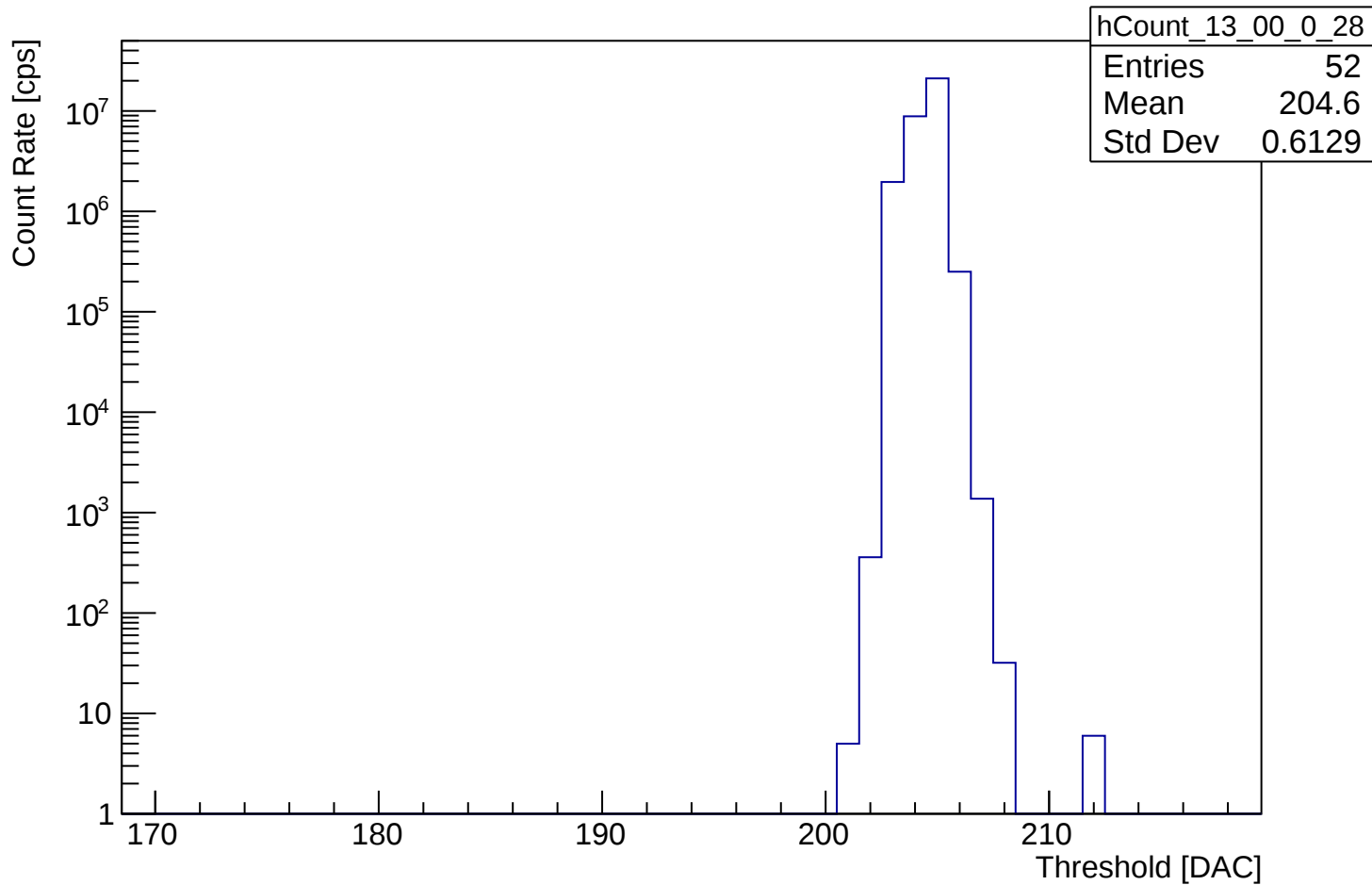
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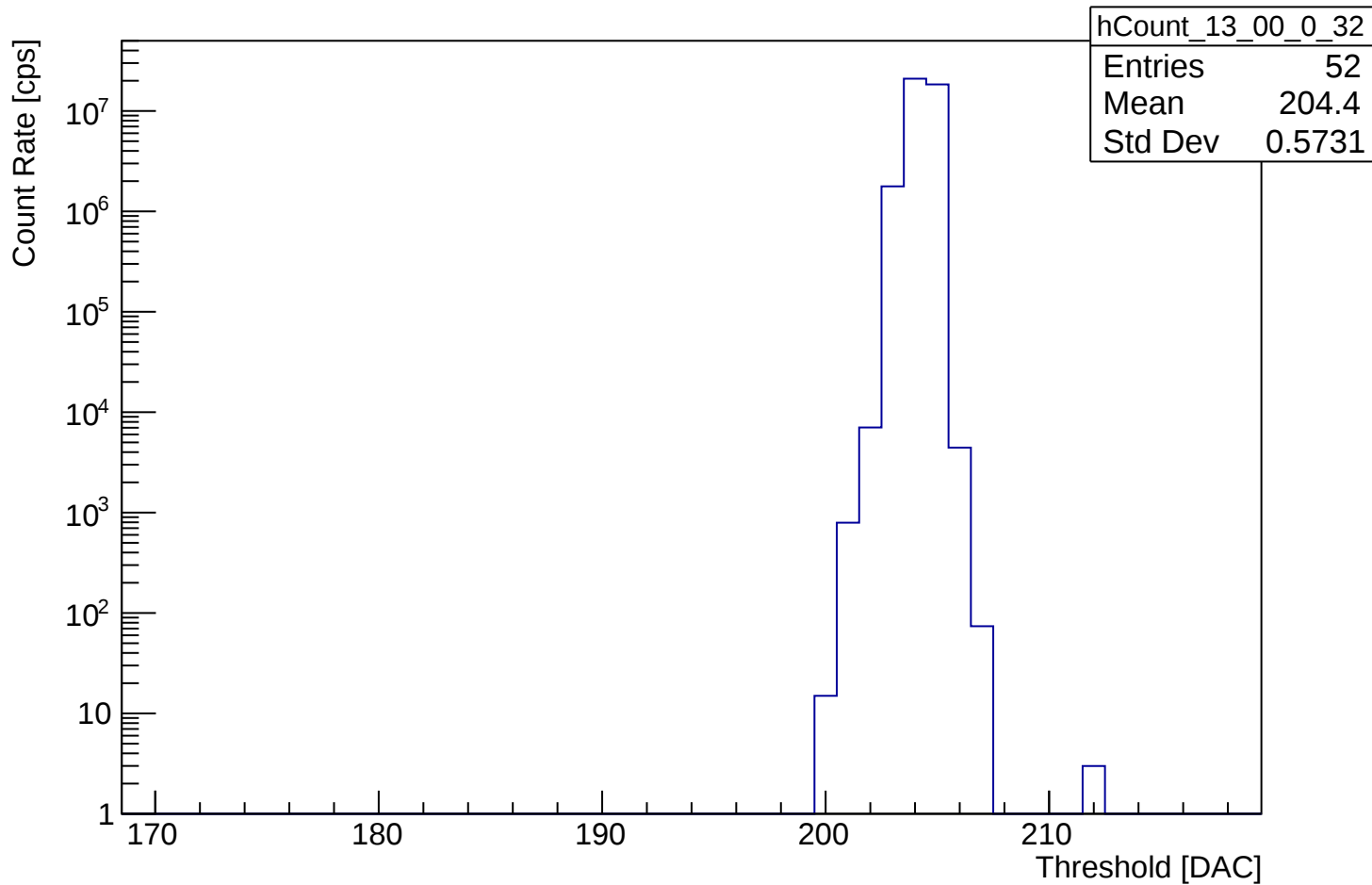
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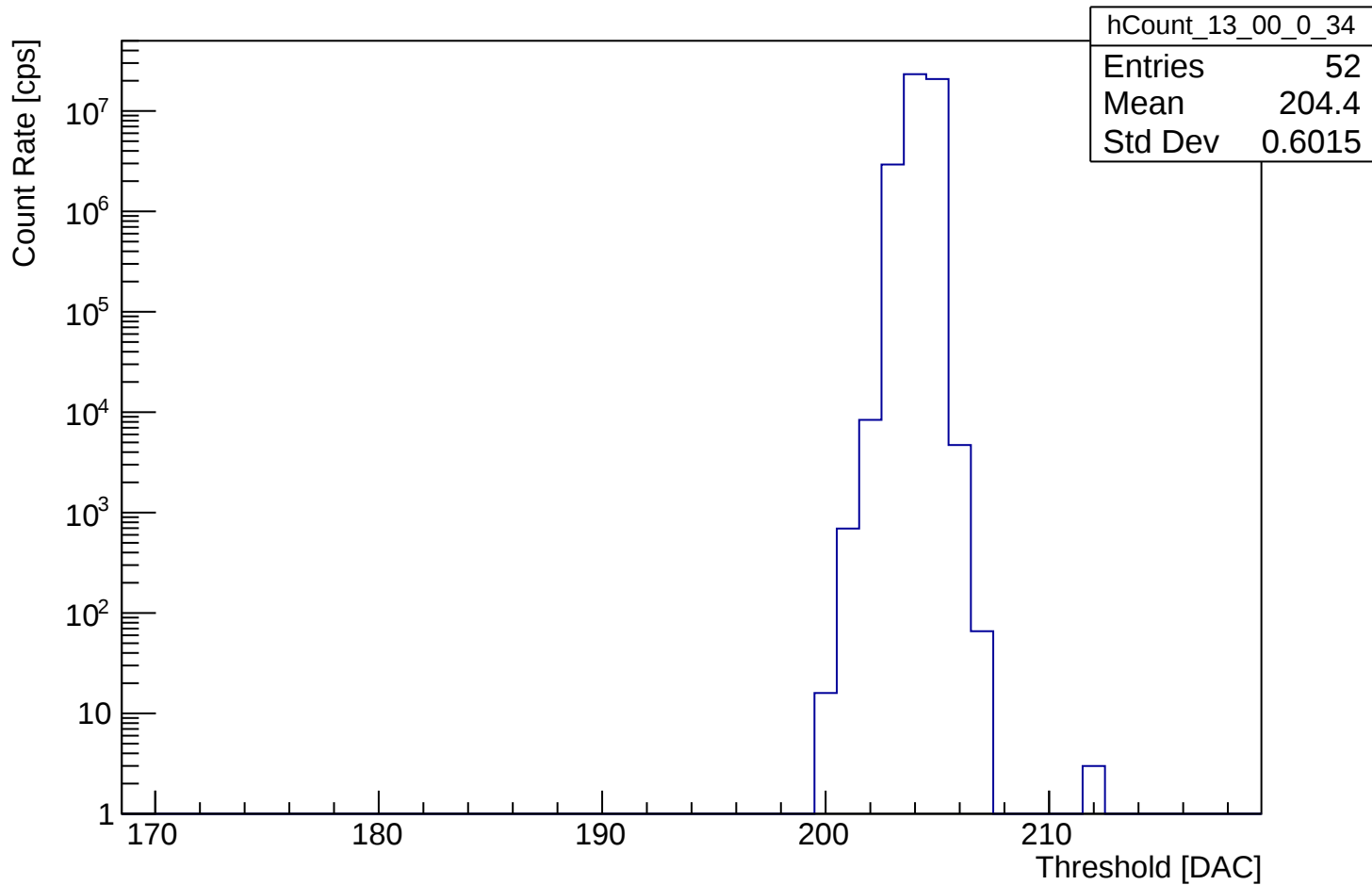
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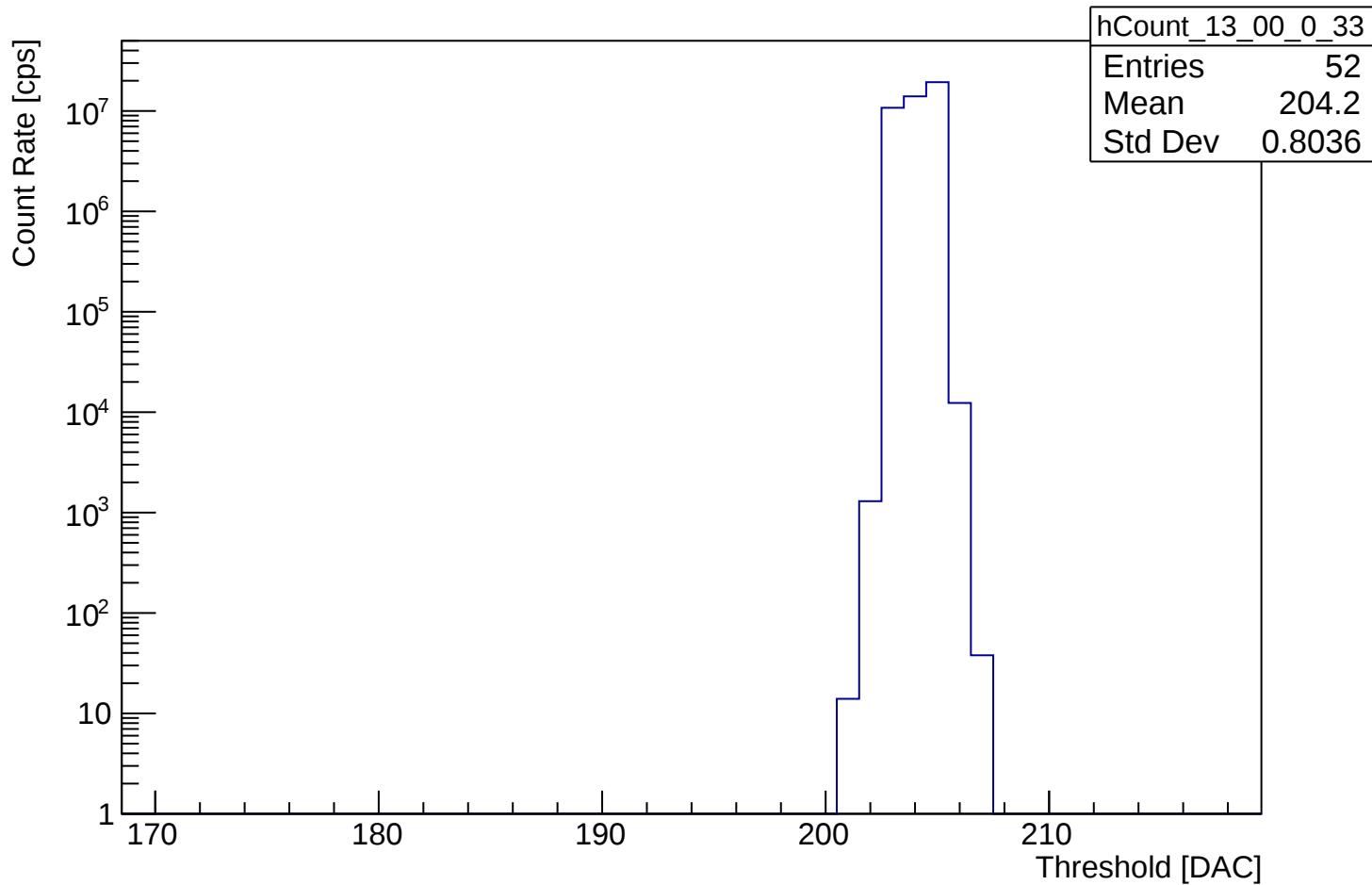
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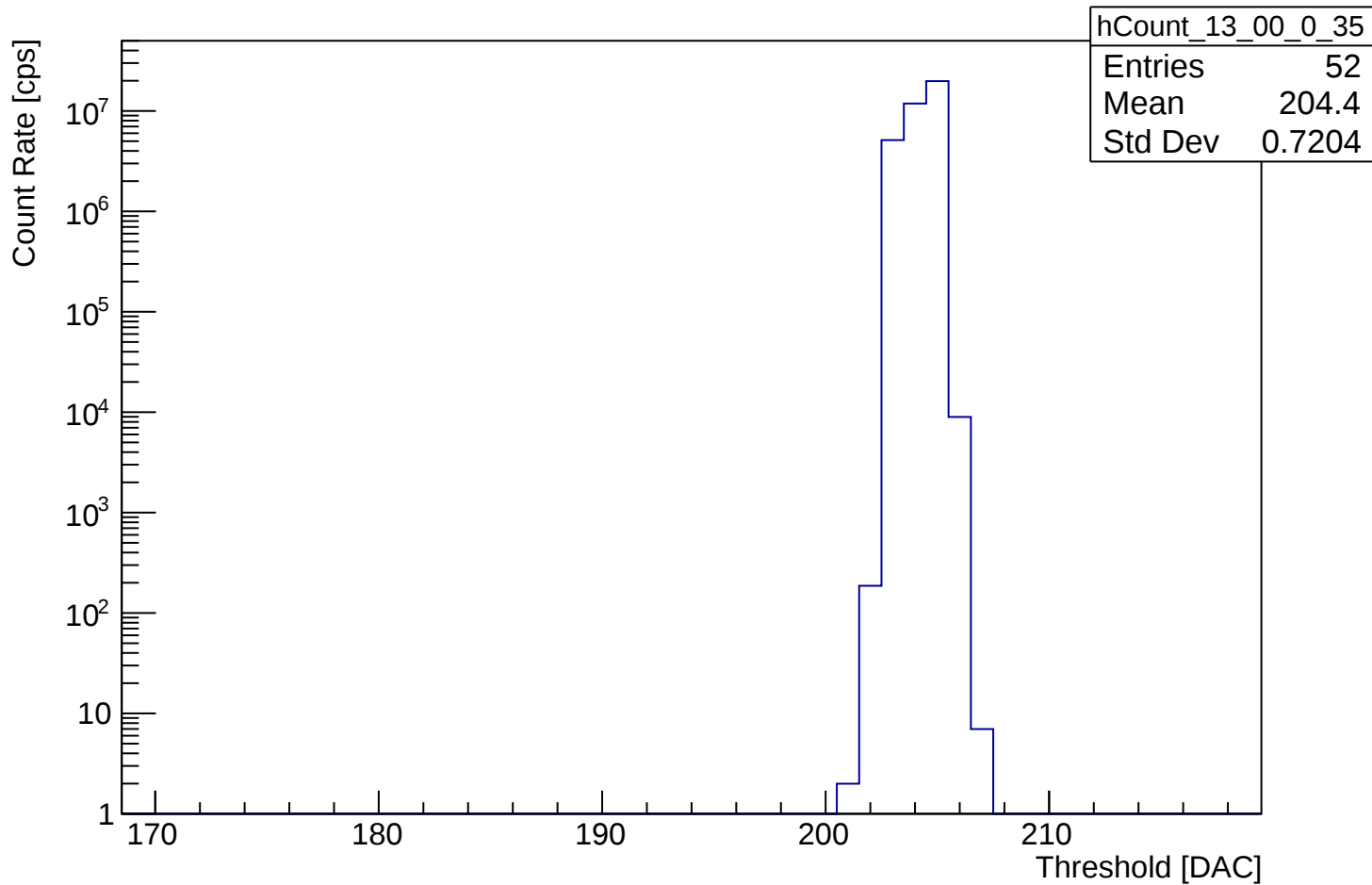
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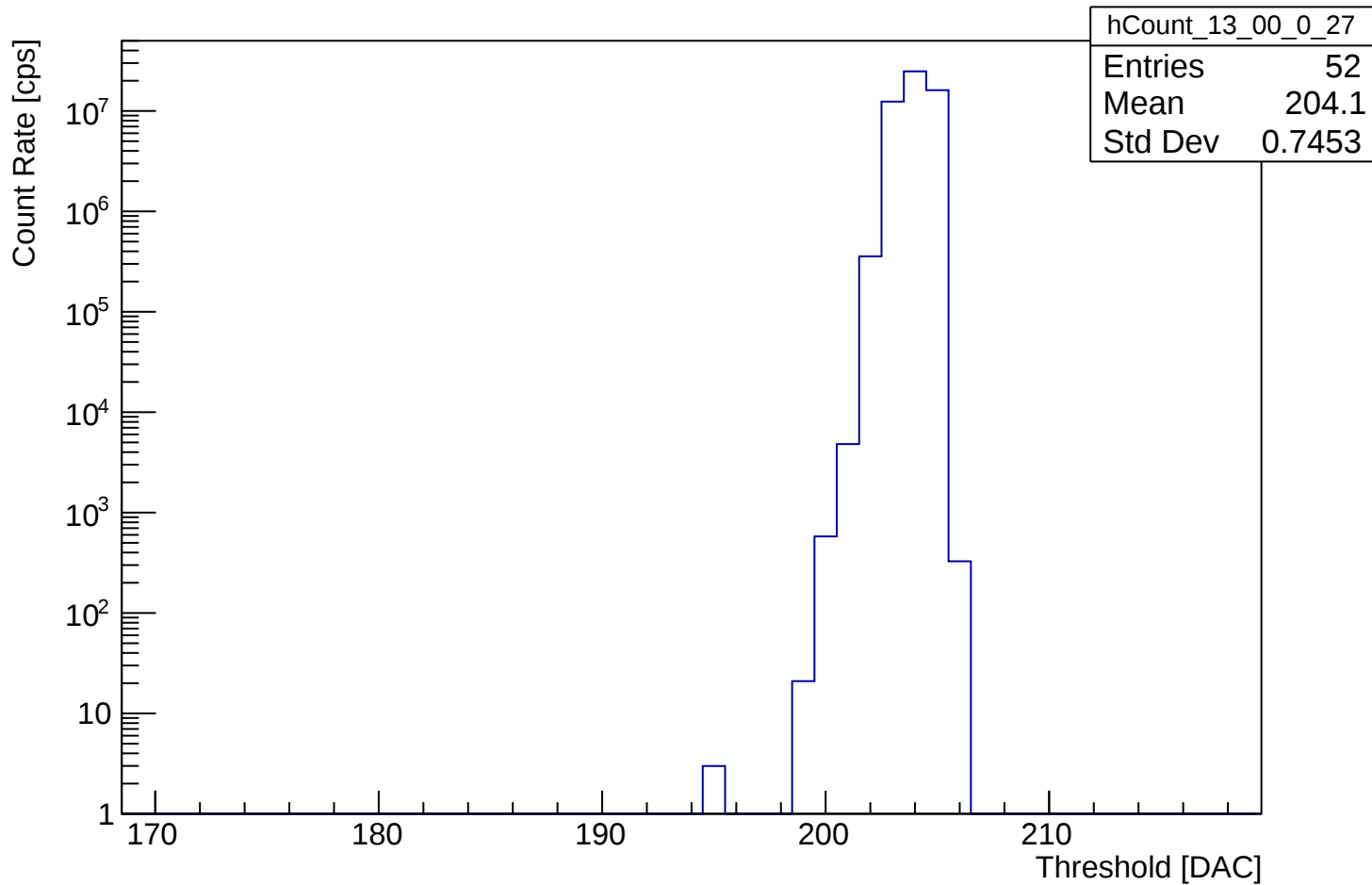
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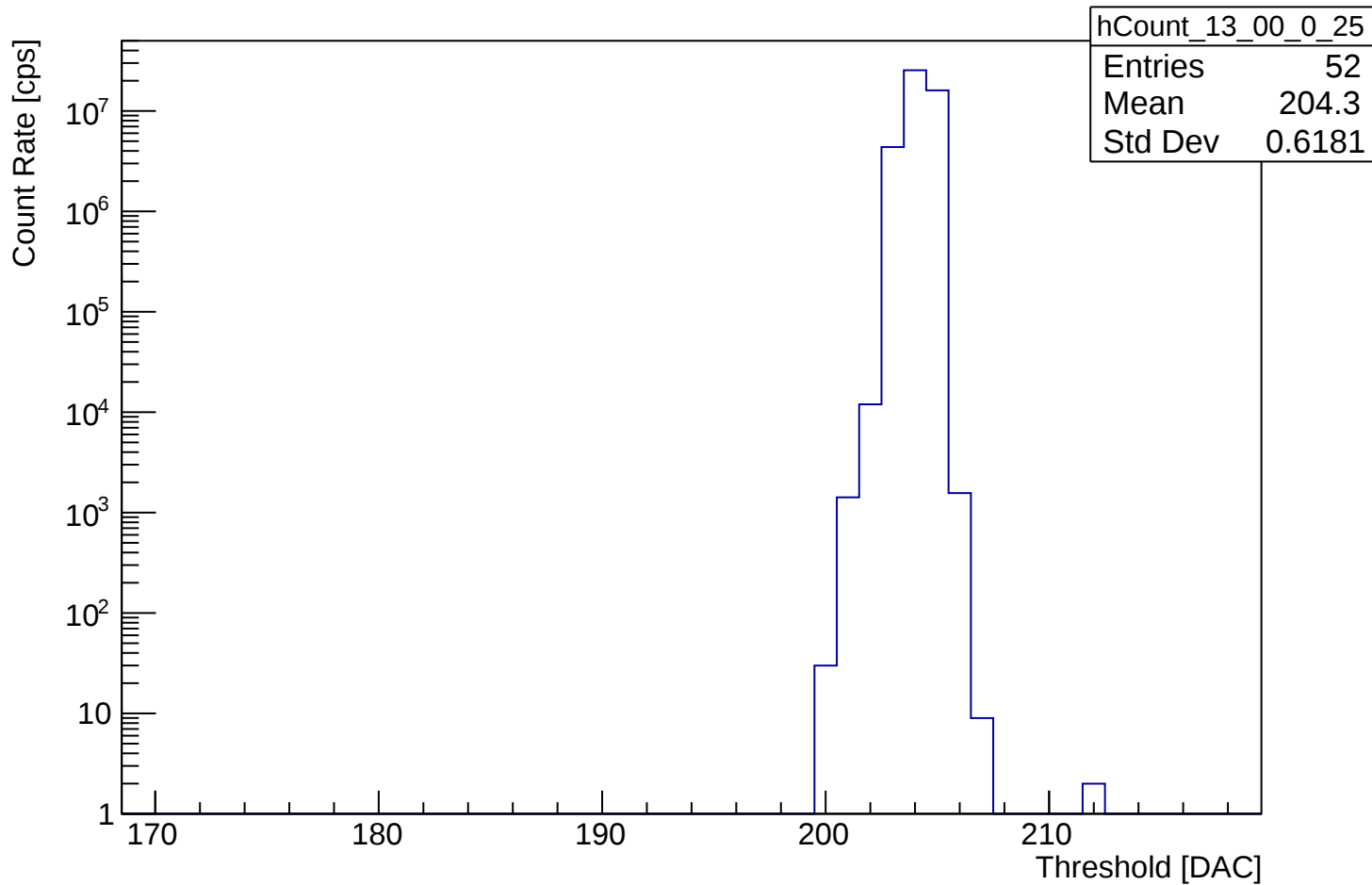
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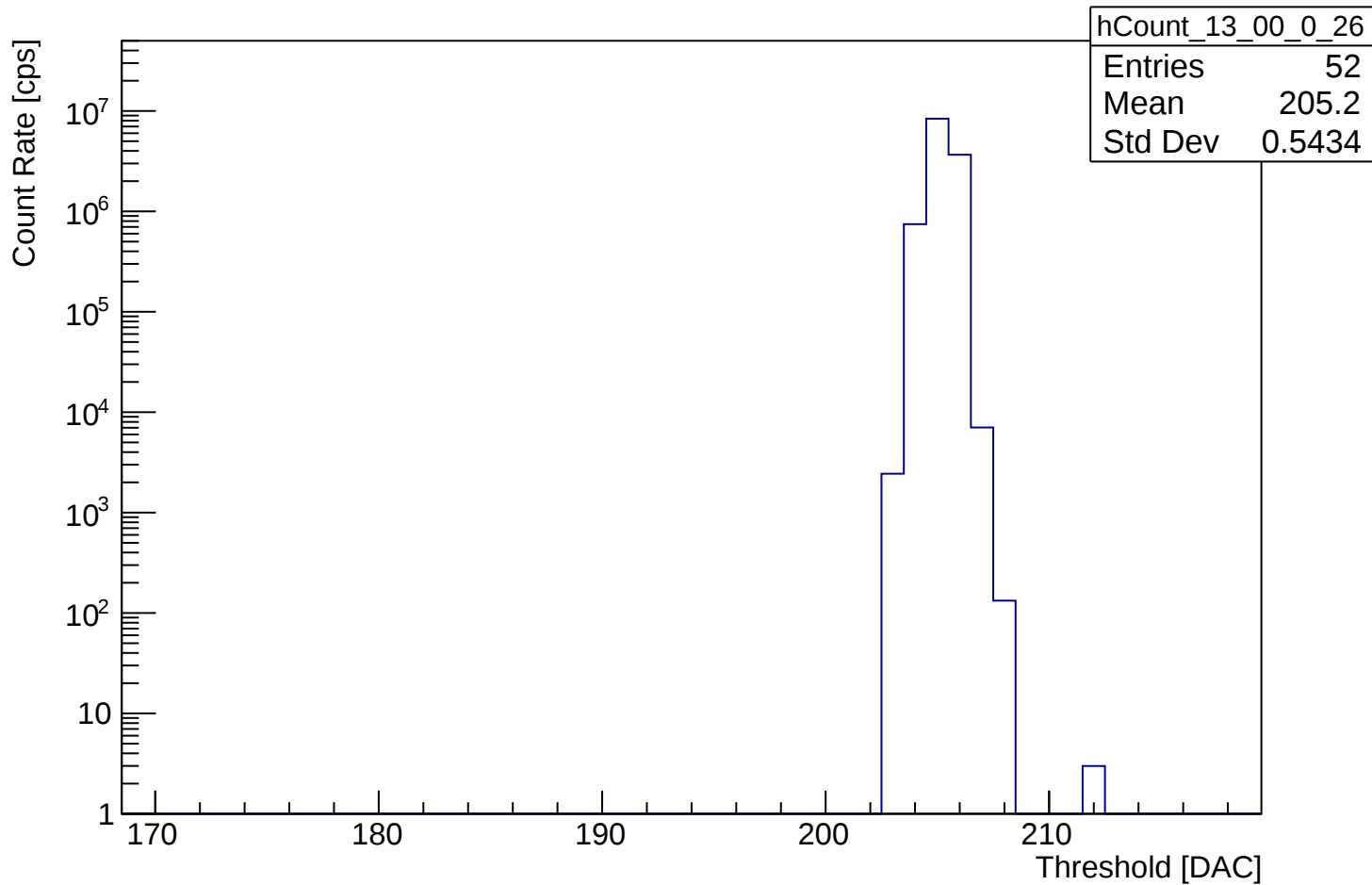
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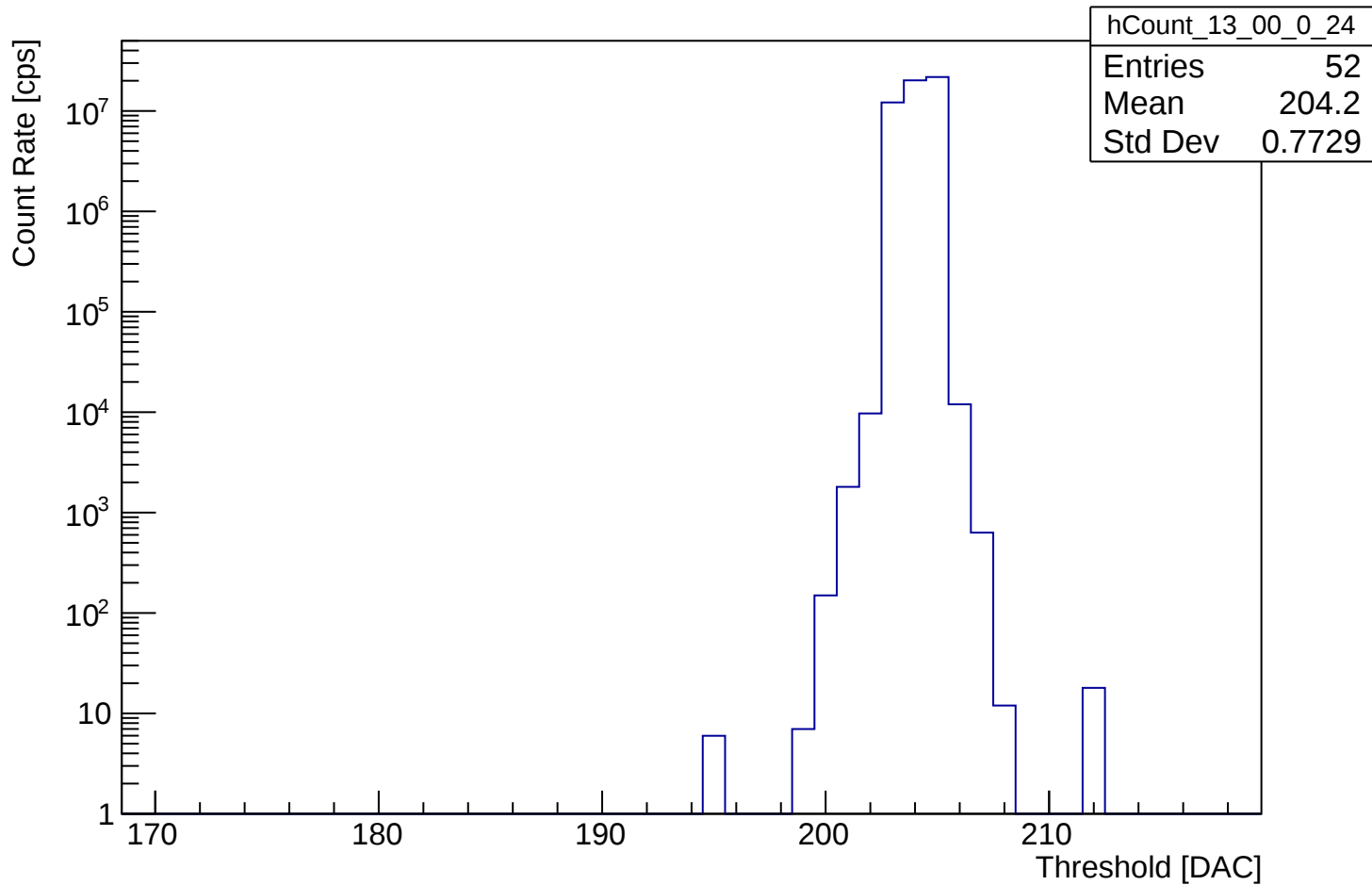
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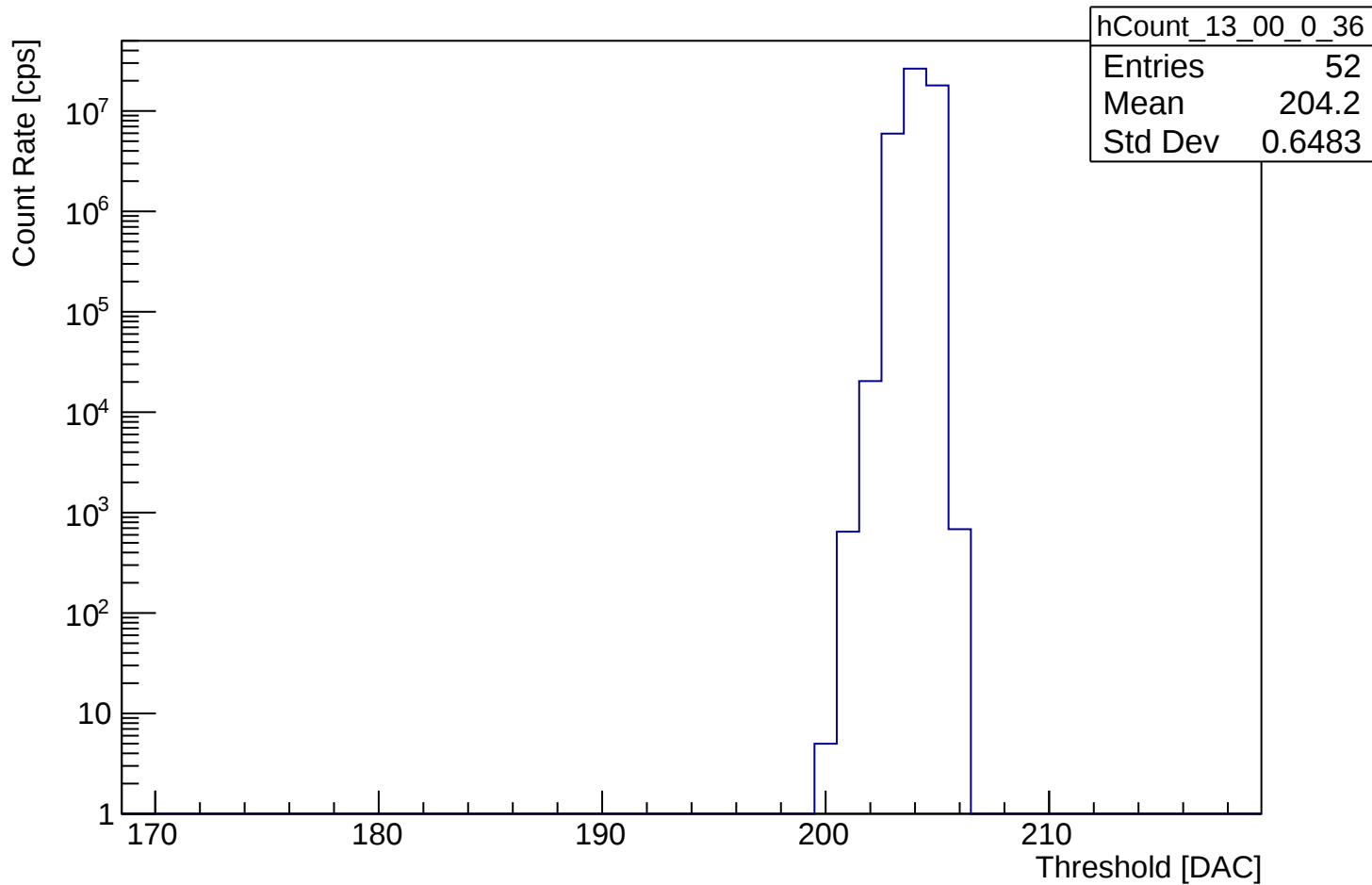
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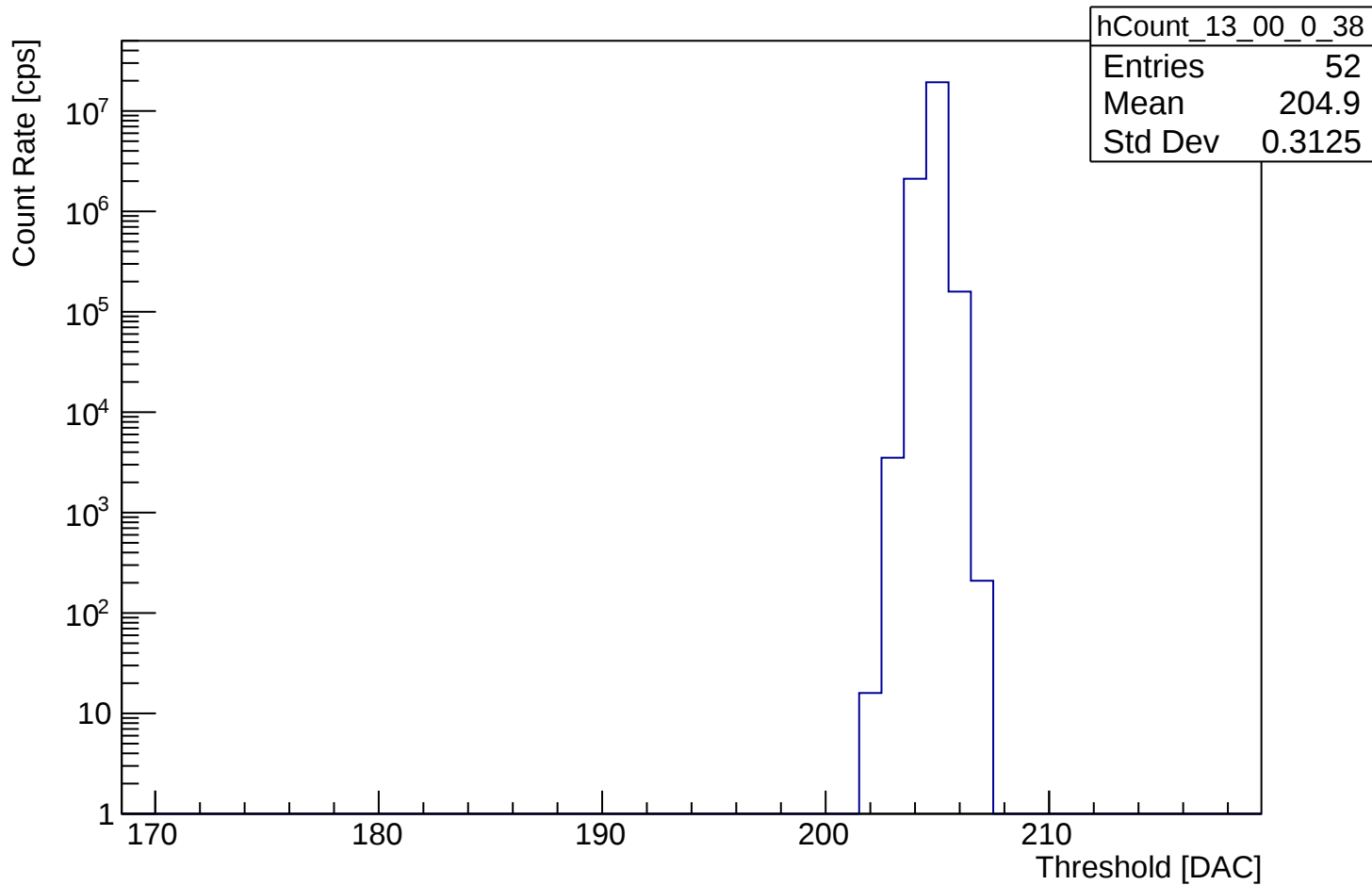
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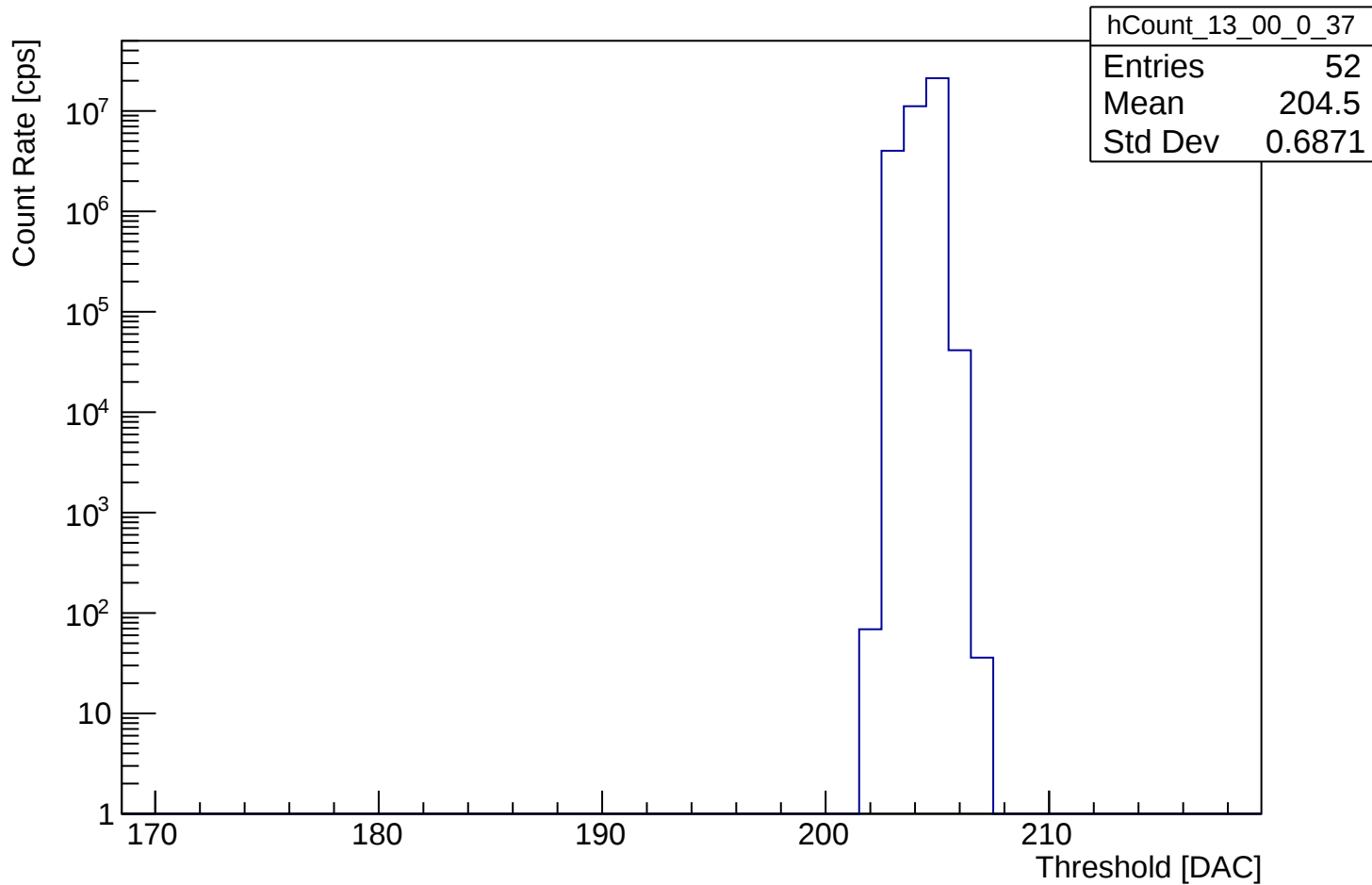
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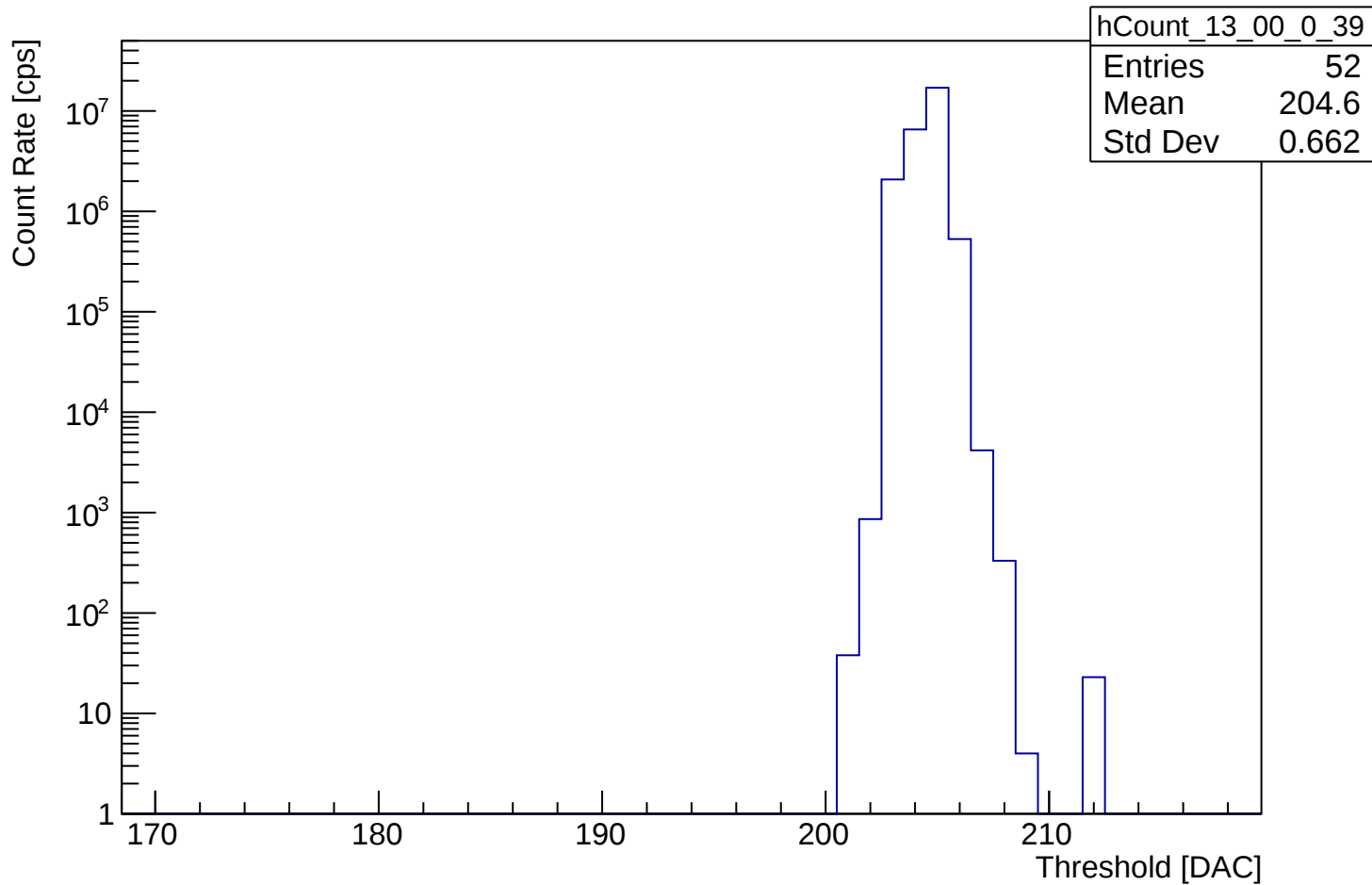
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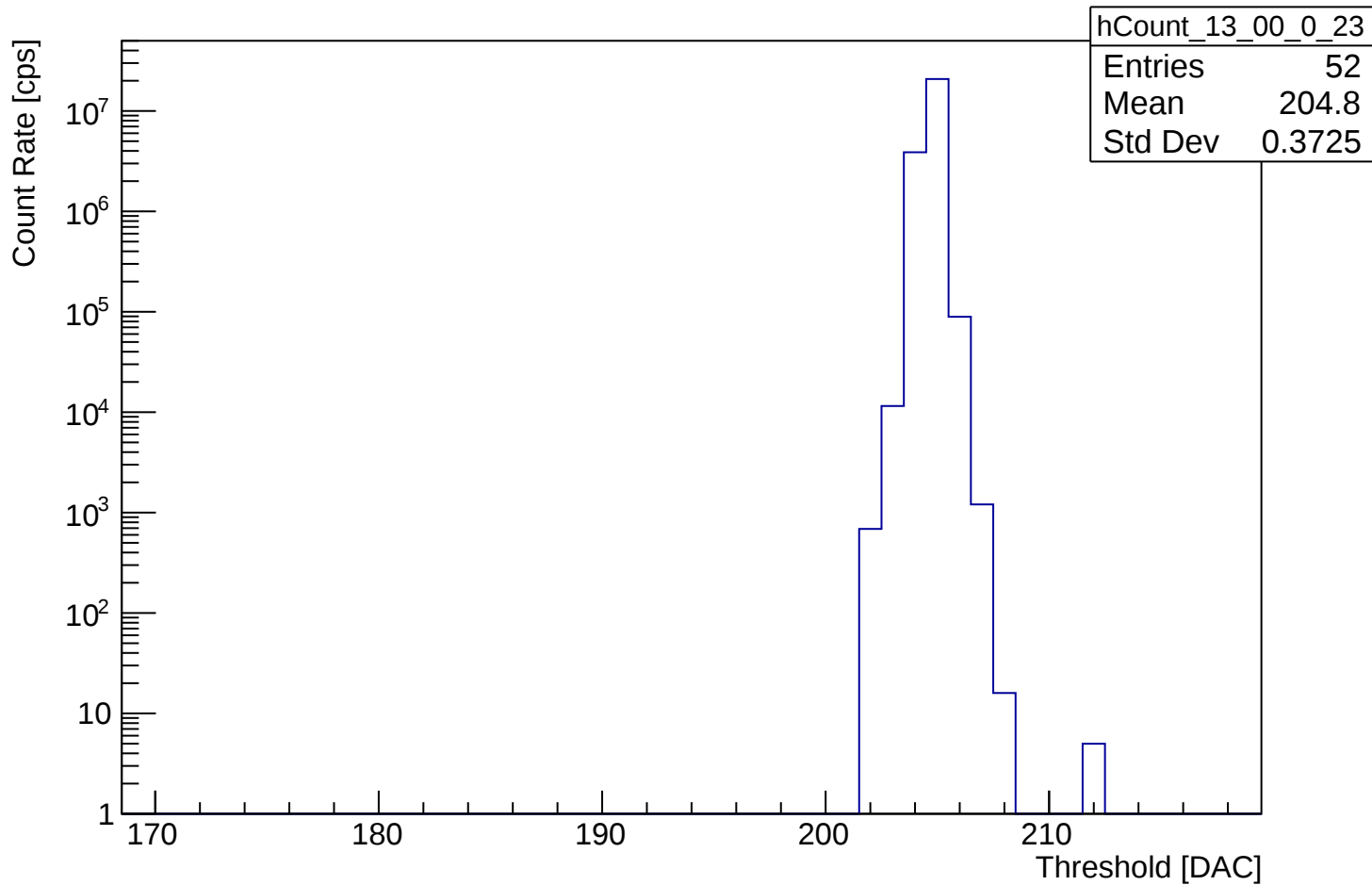
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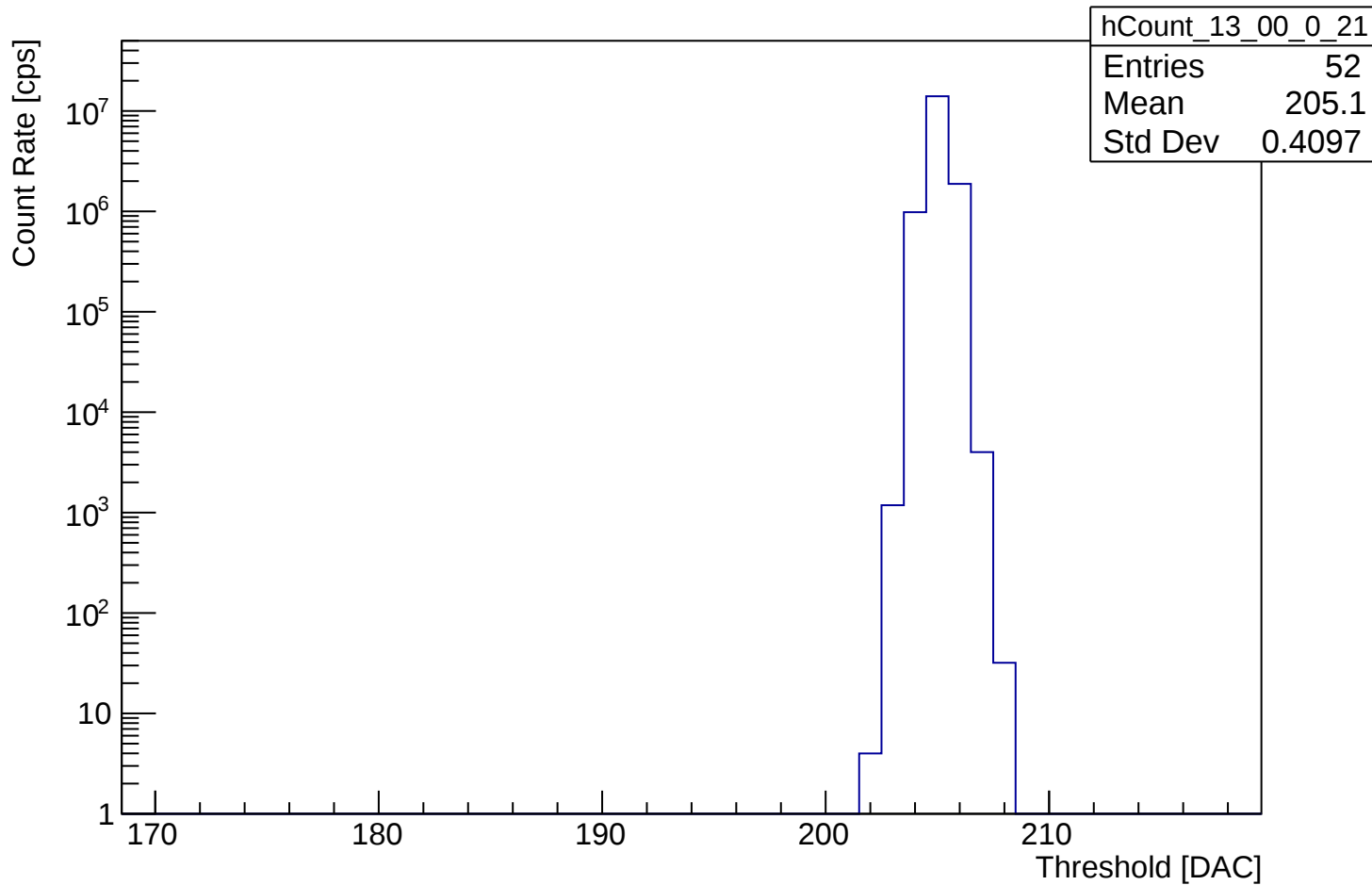
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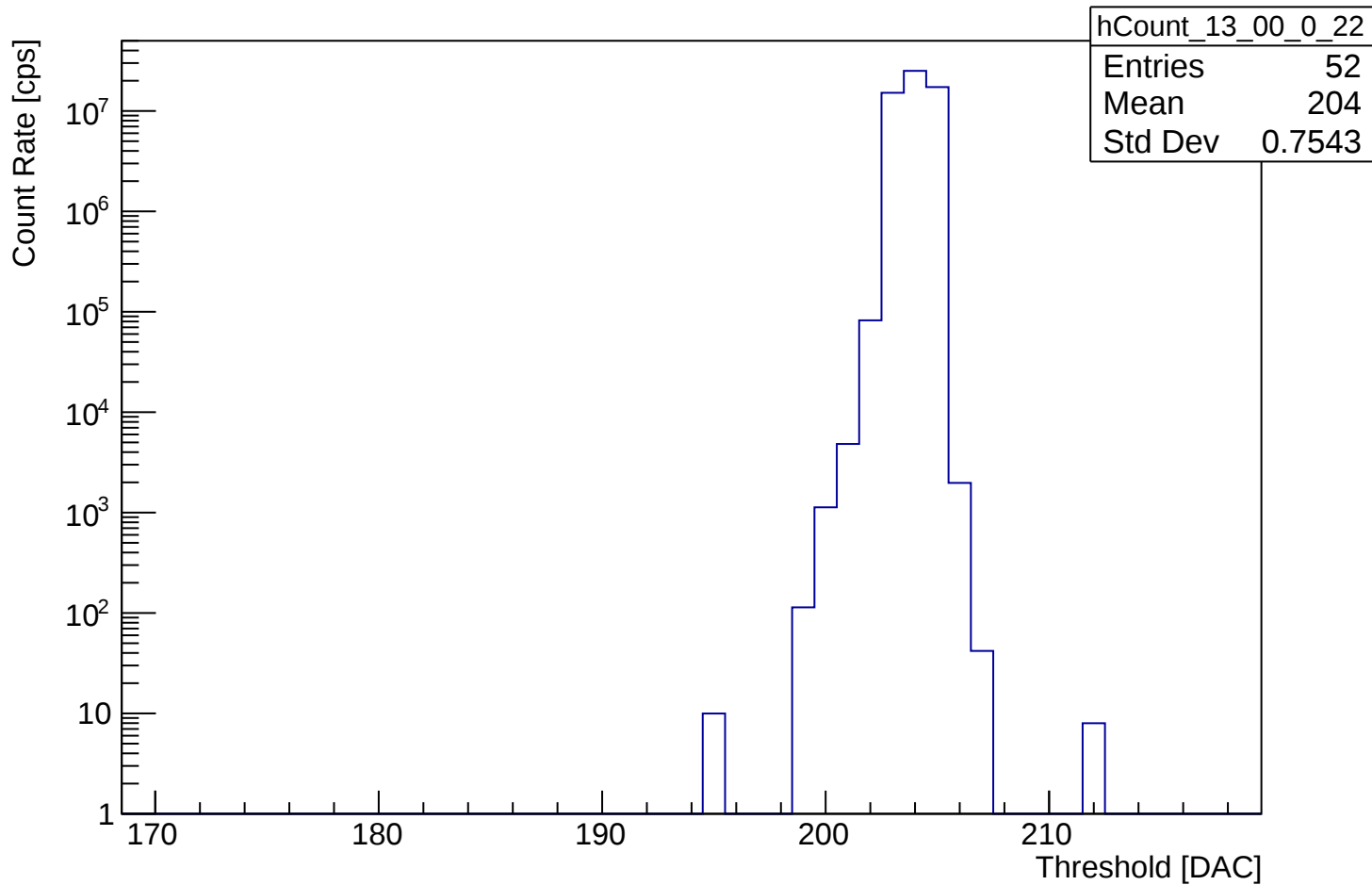
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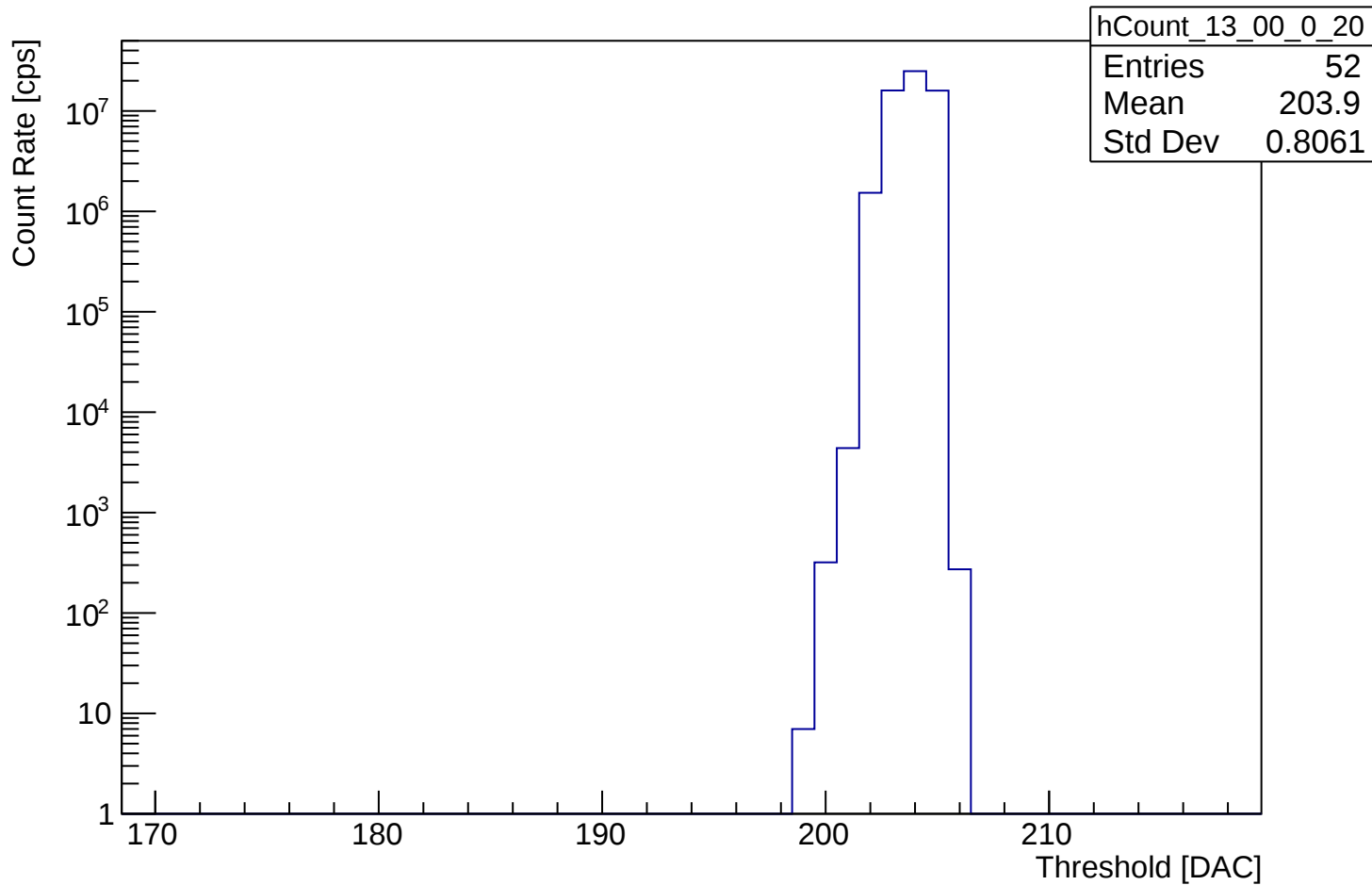
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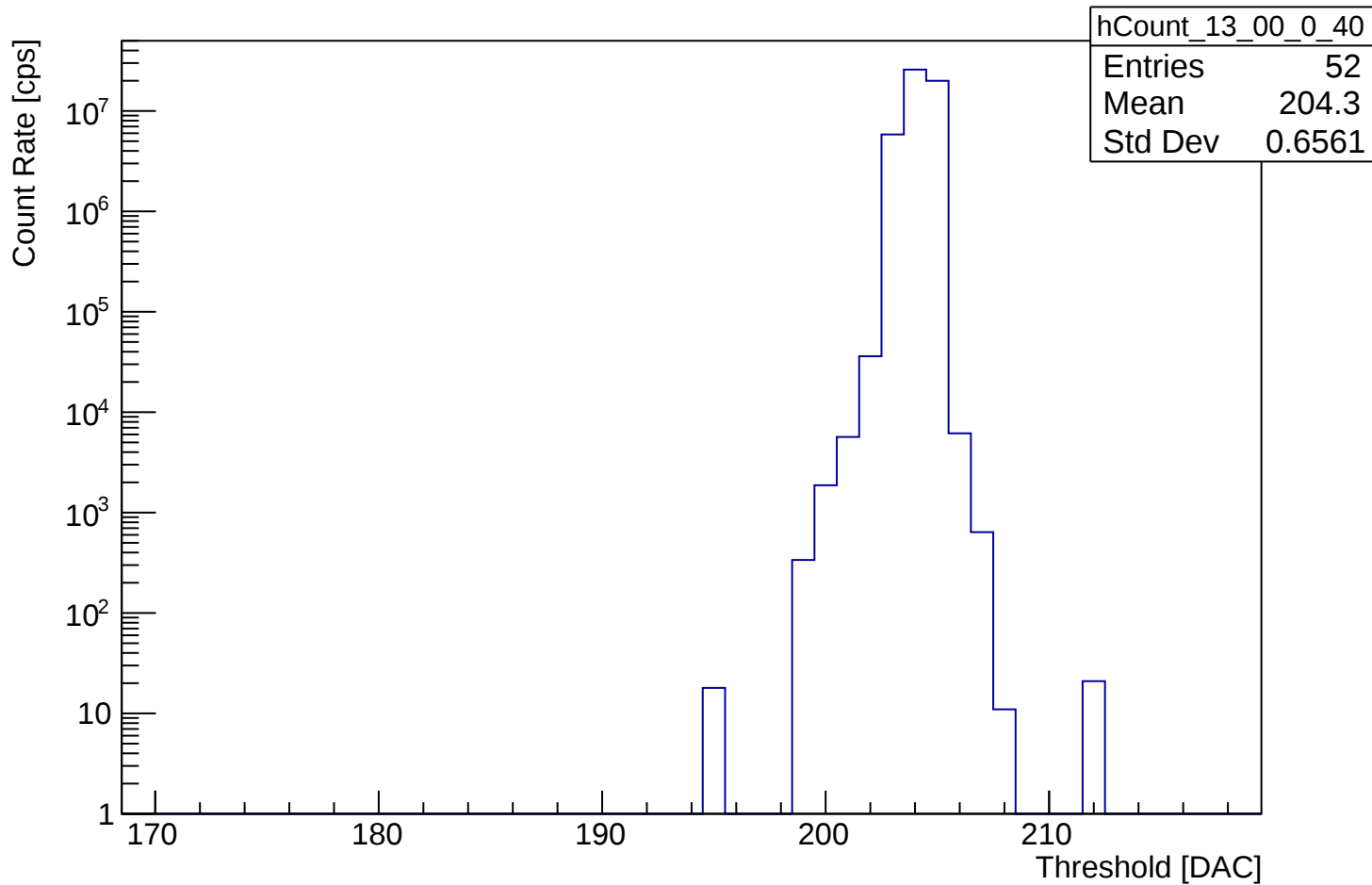
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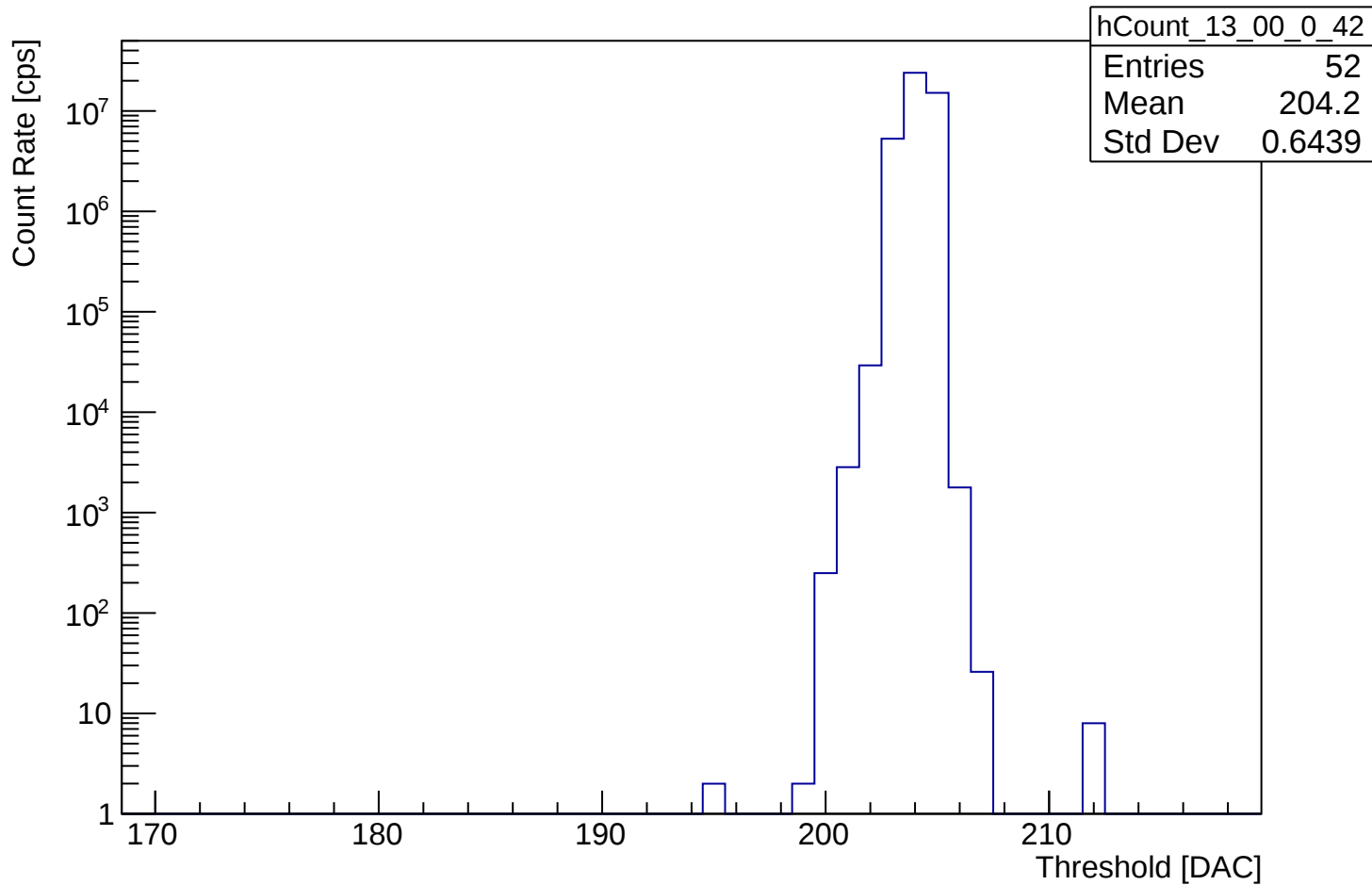
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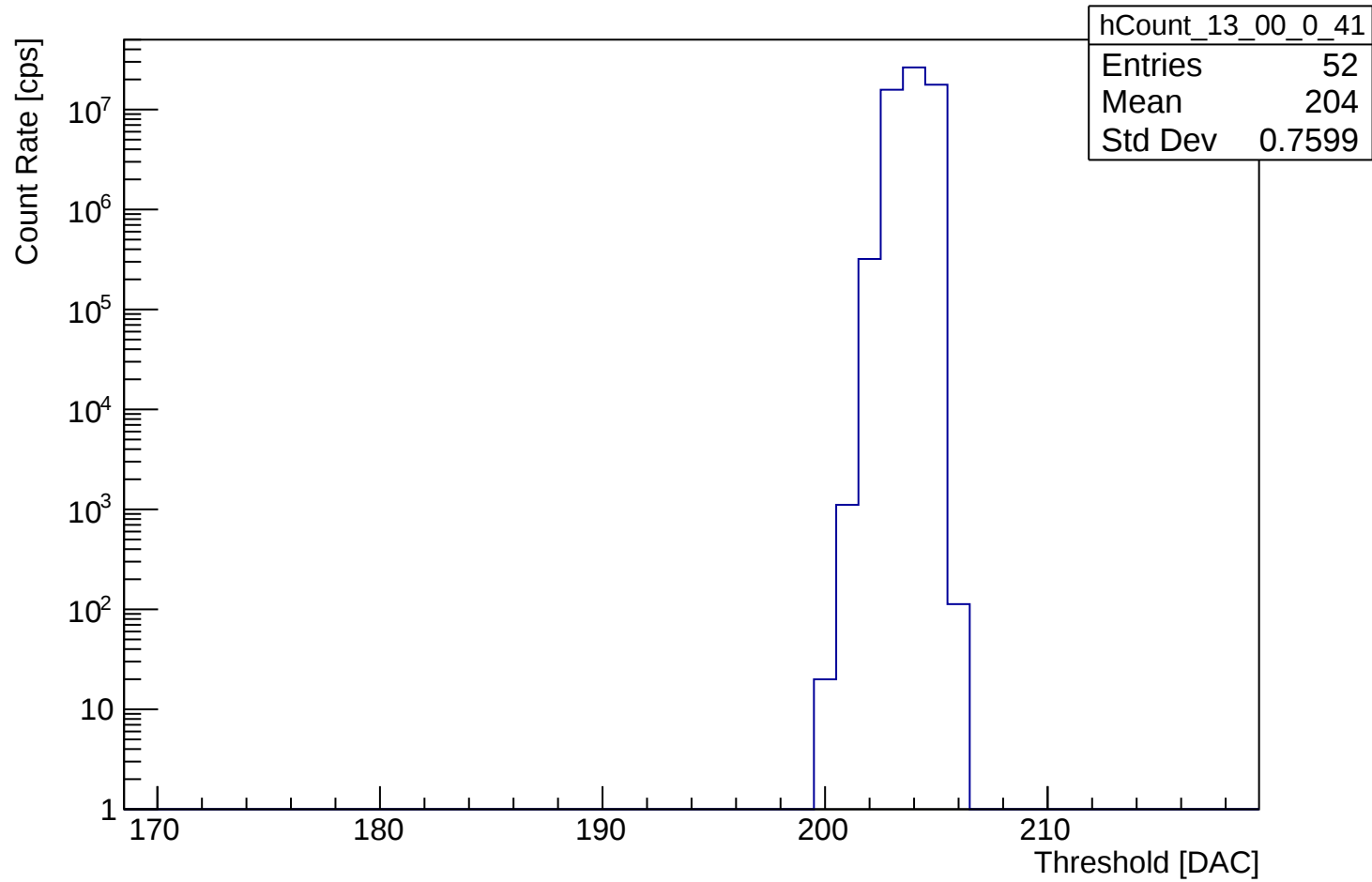


Row 1 Tile 1 Pmt 1 Pixel 21 Slot 13 Fiber 0 Asic 0 Channel 40

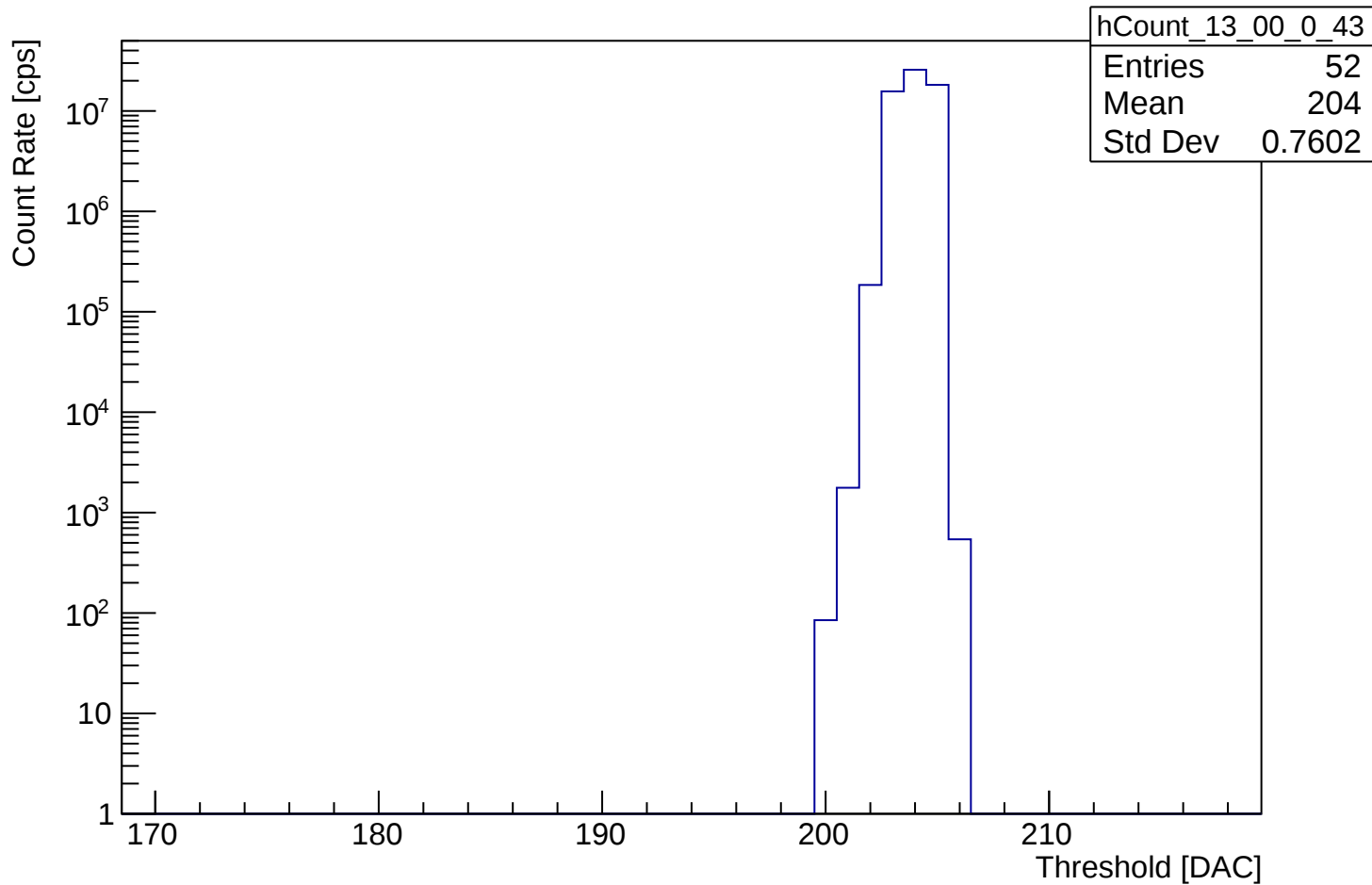


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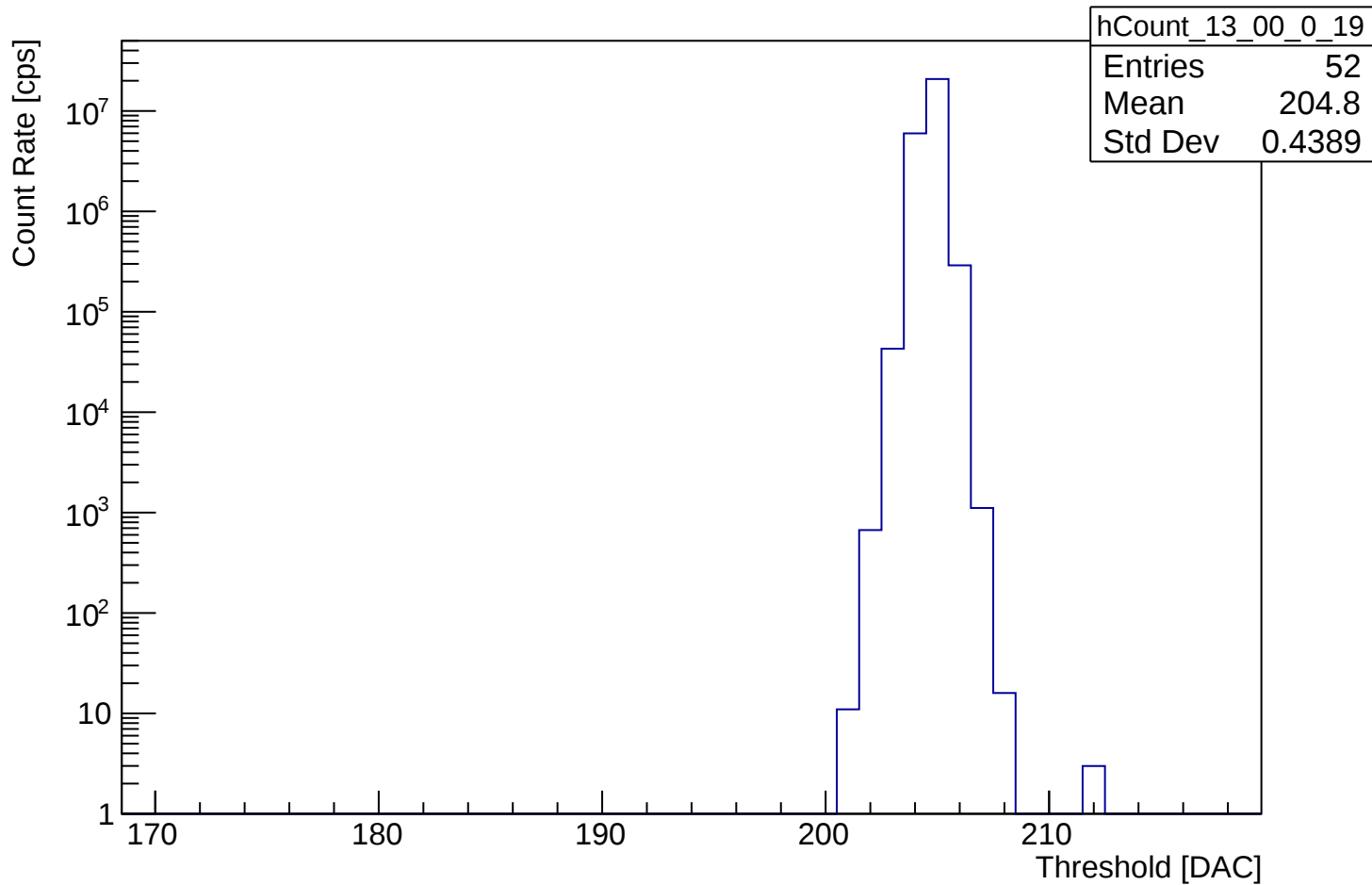




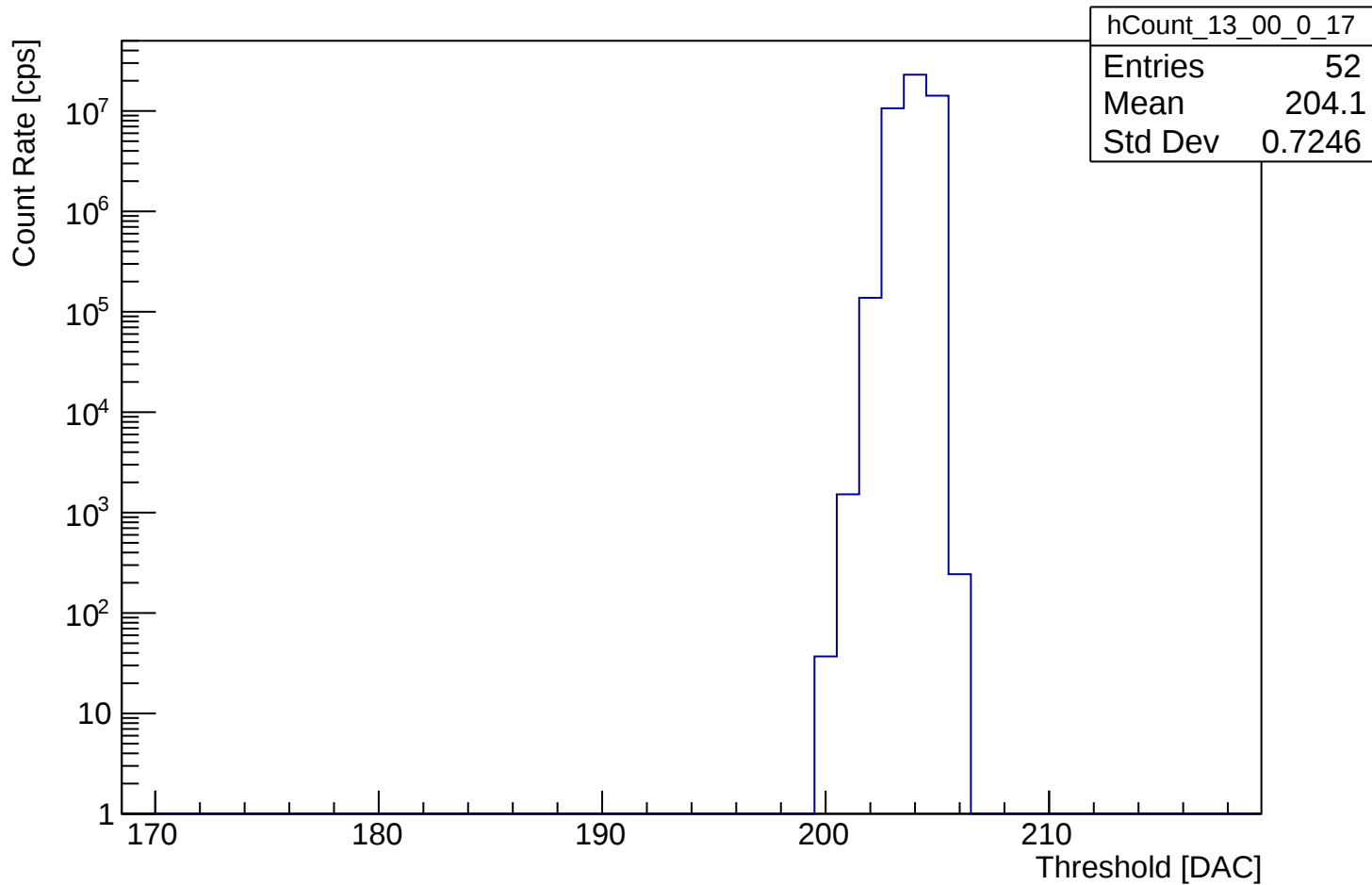
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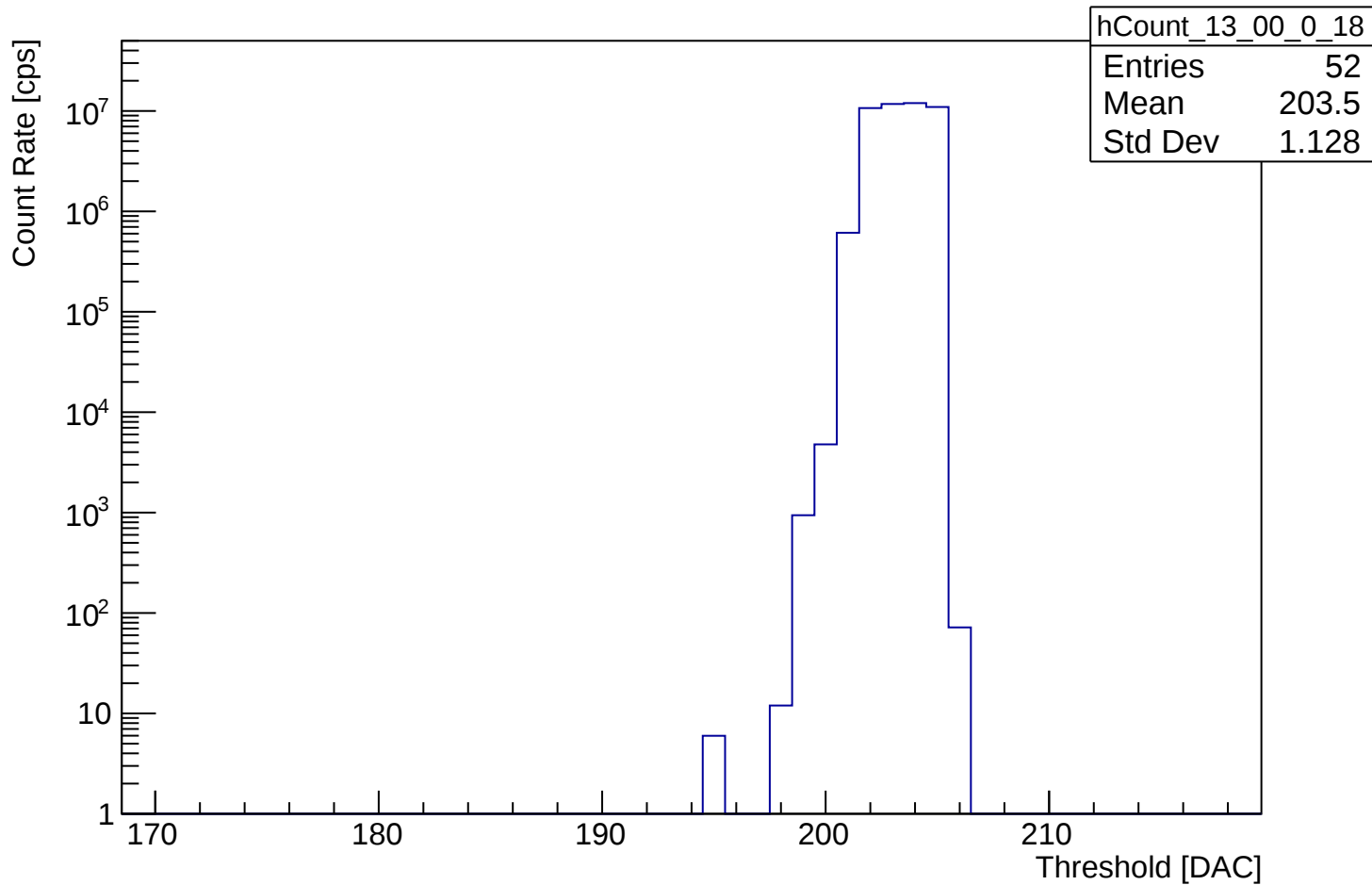
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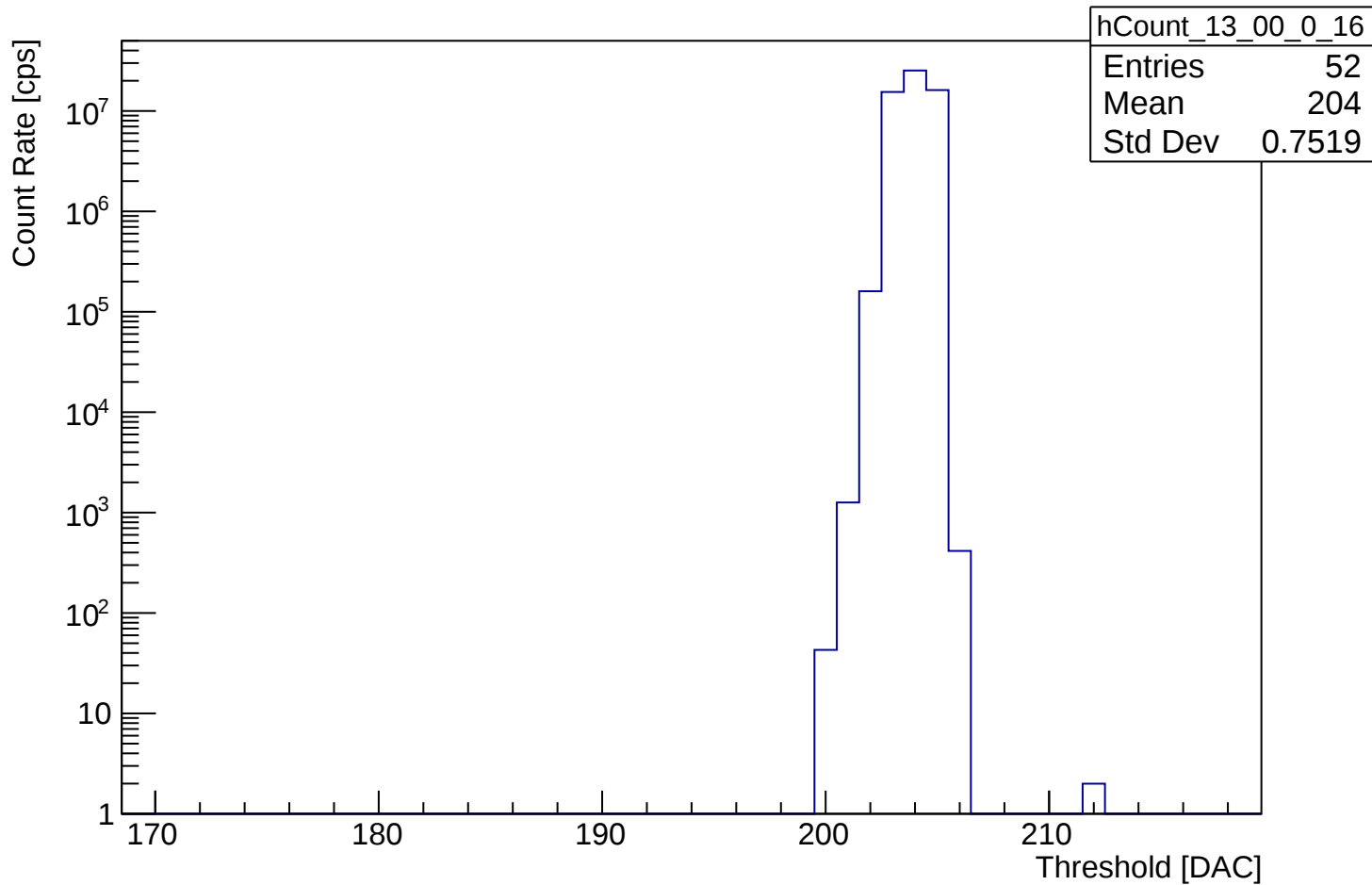
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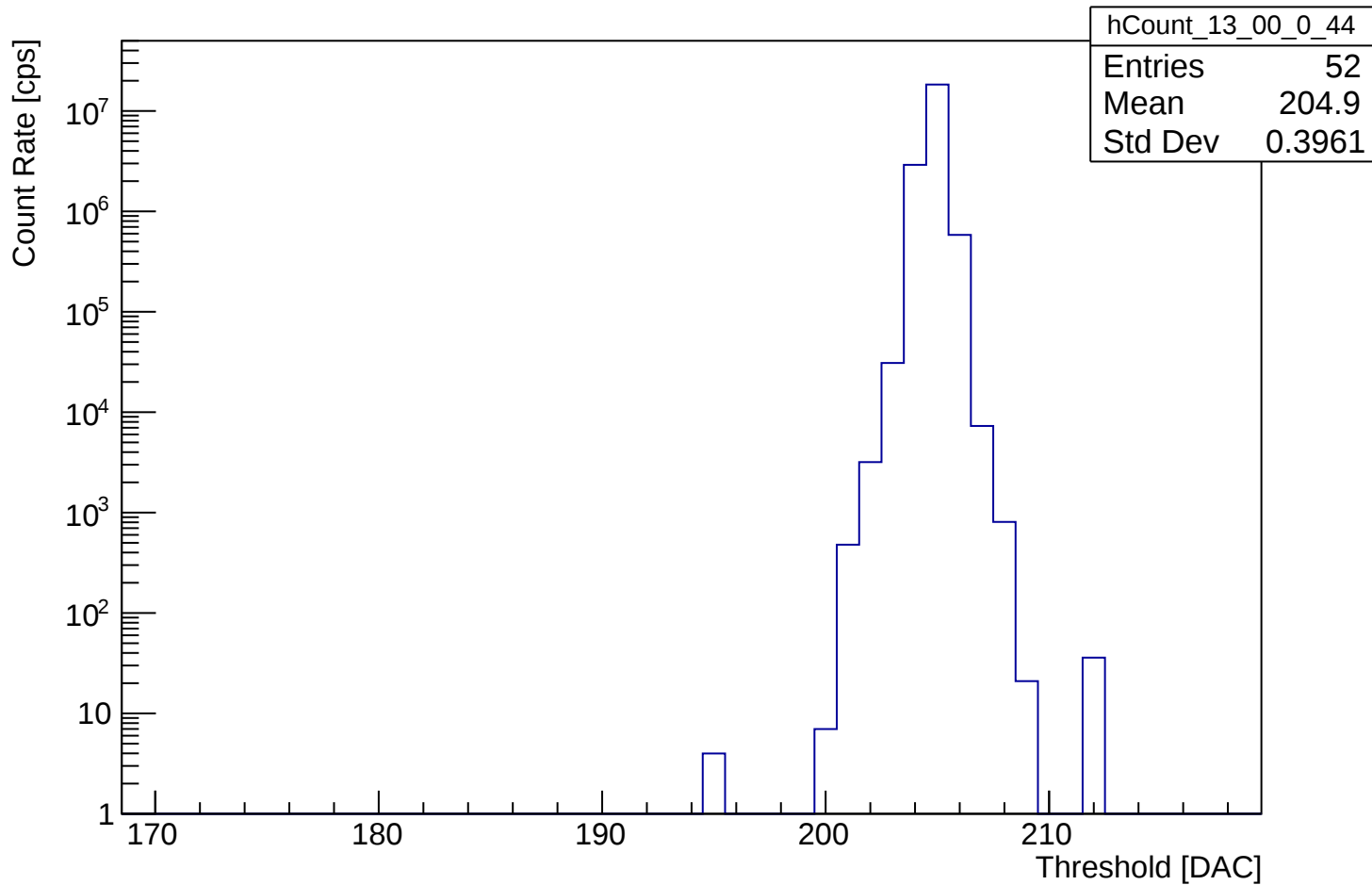
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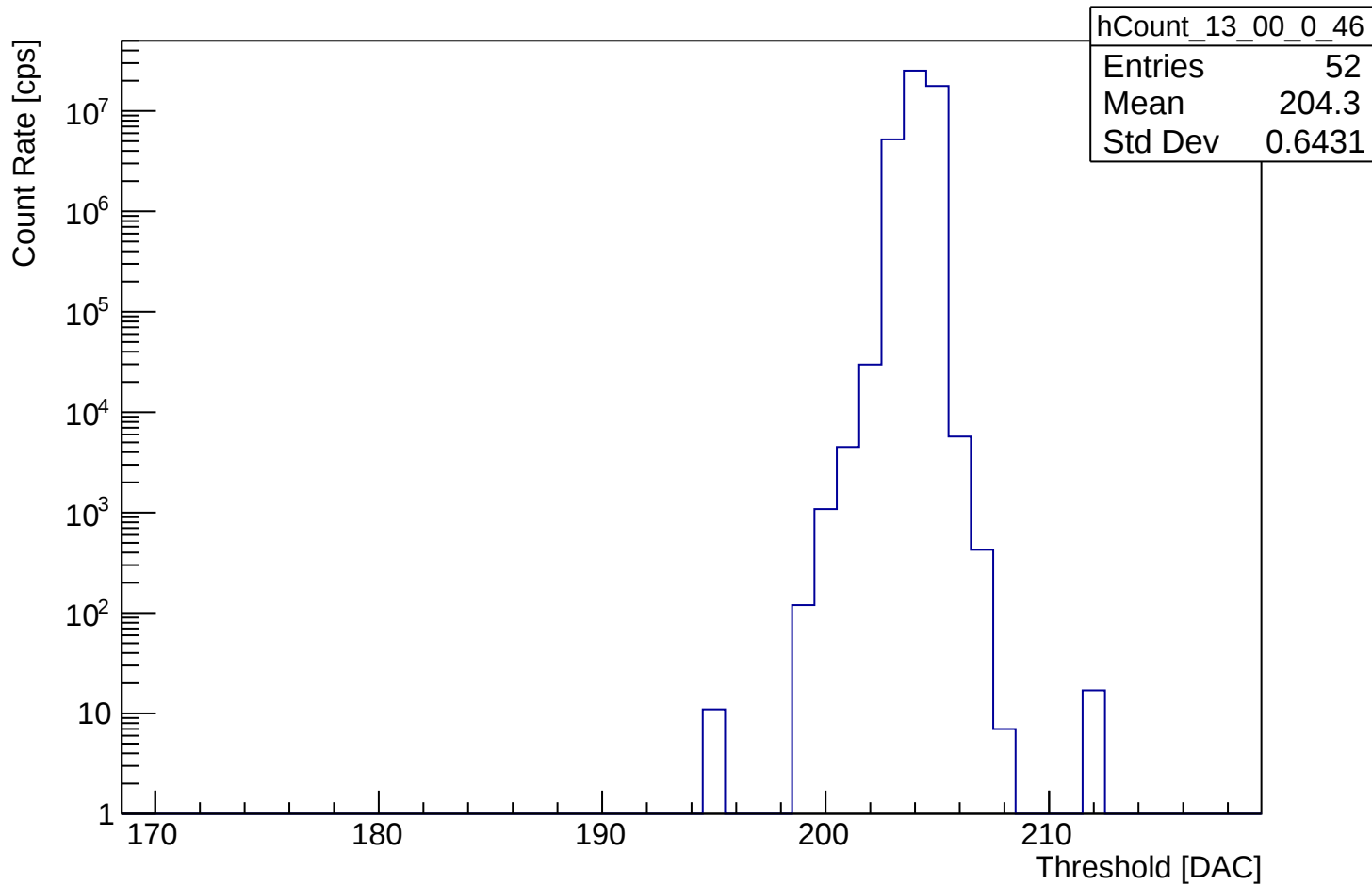
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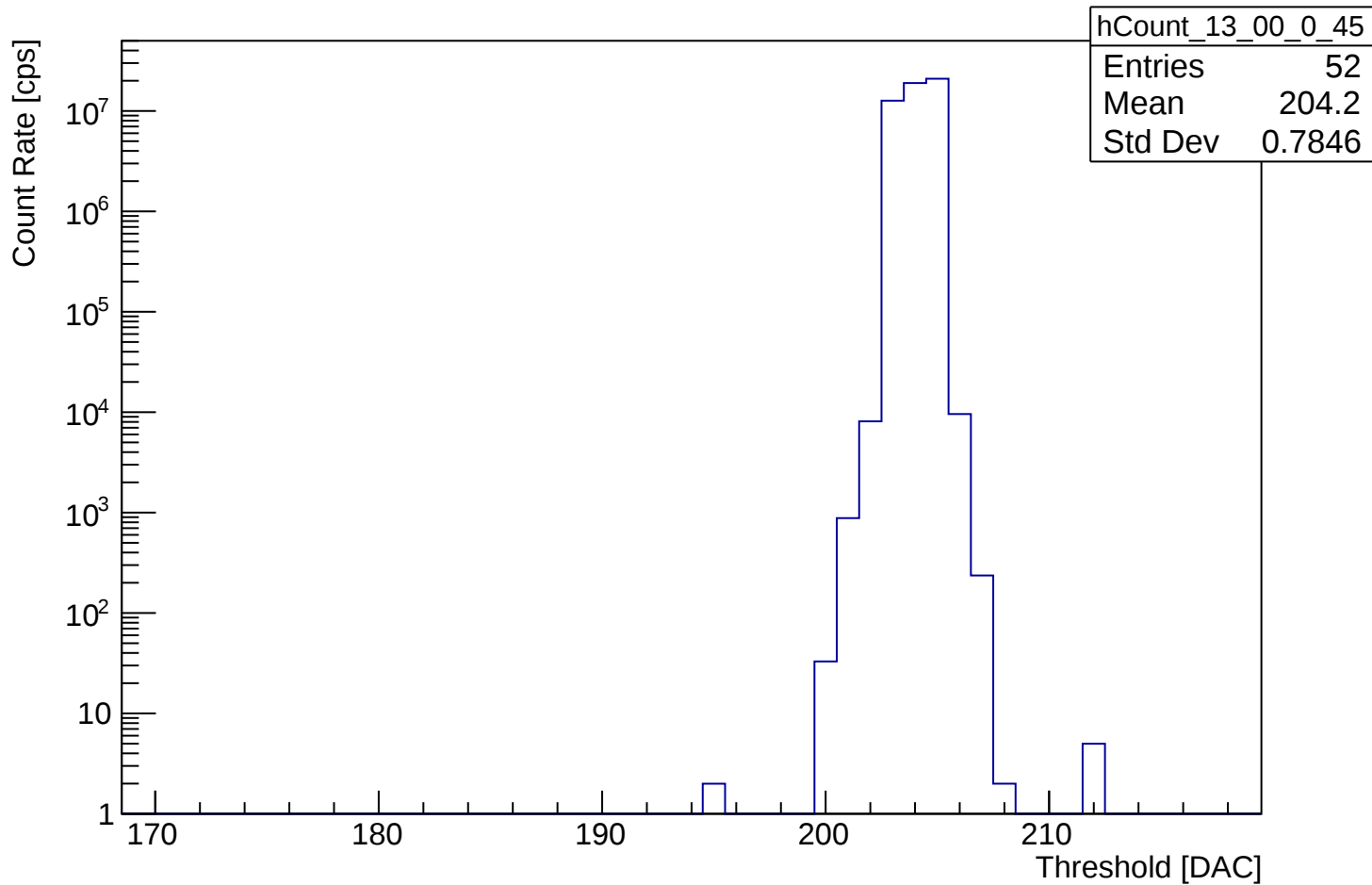
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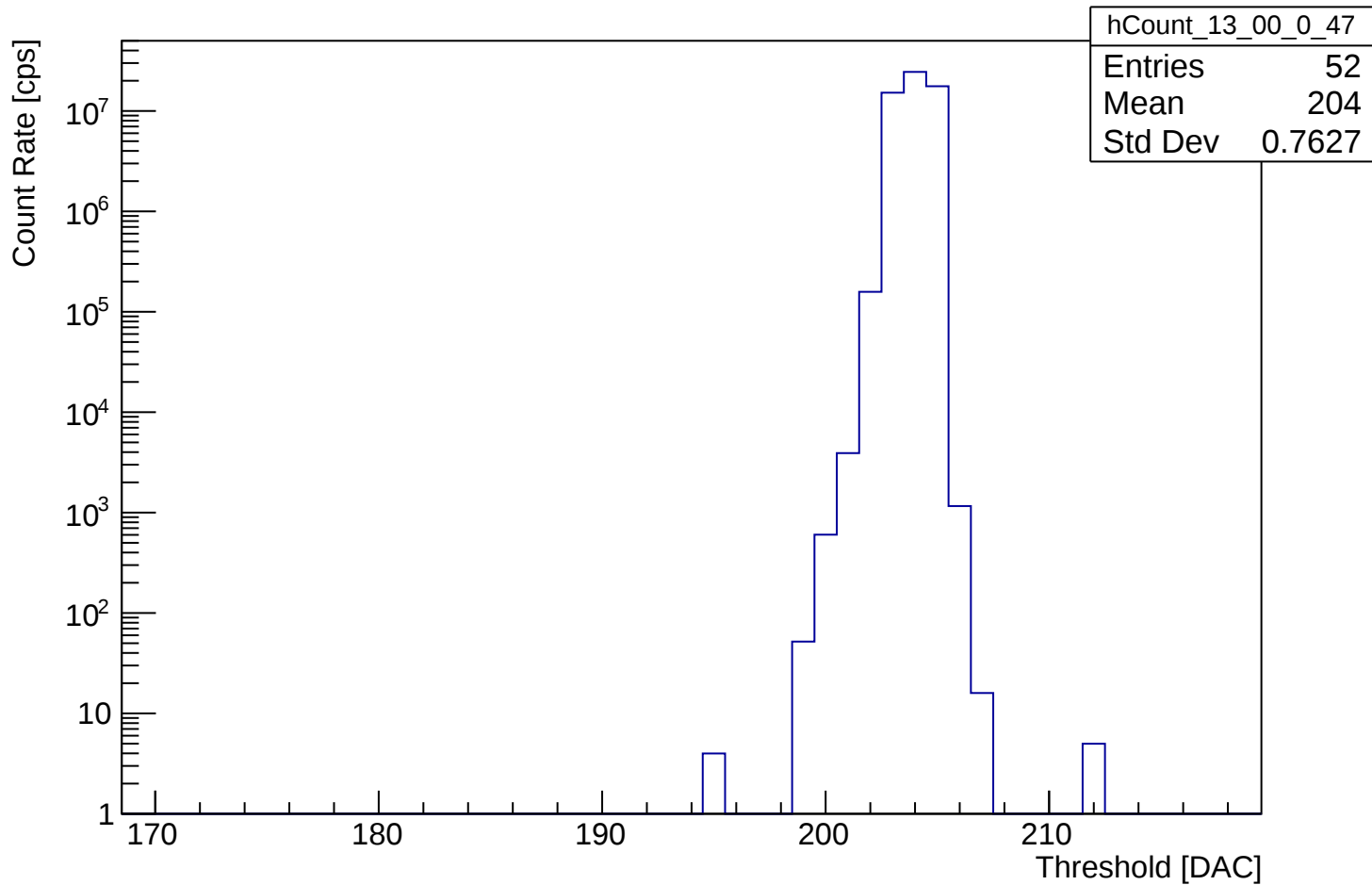
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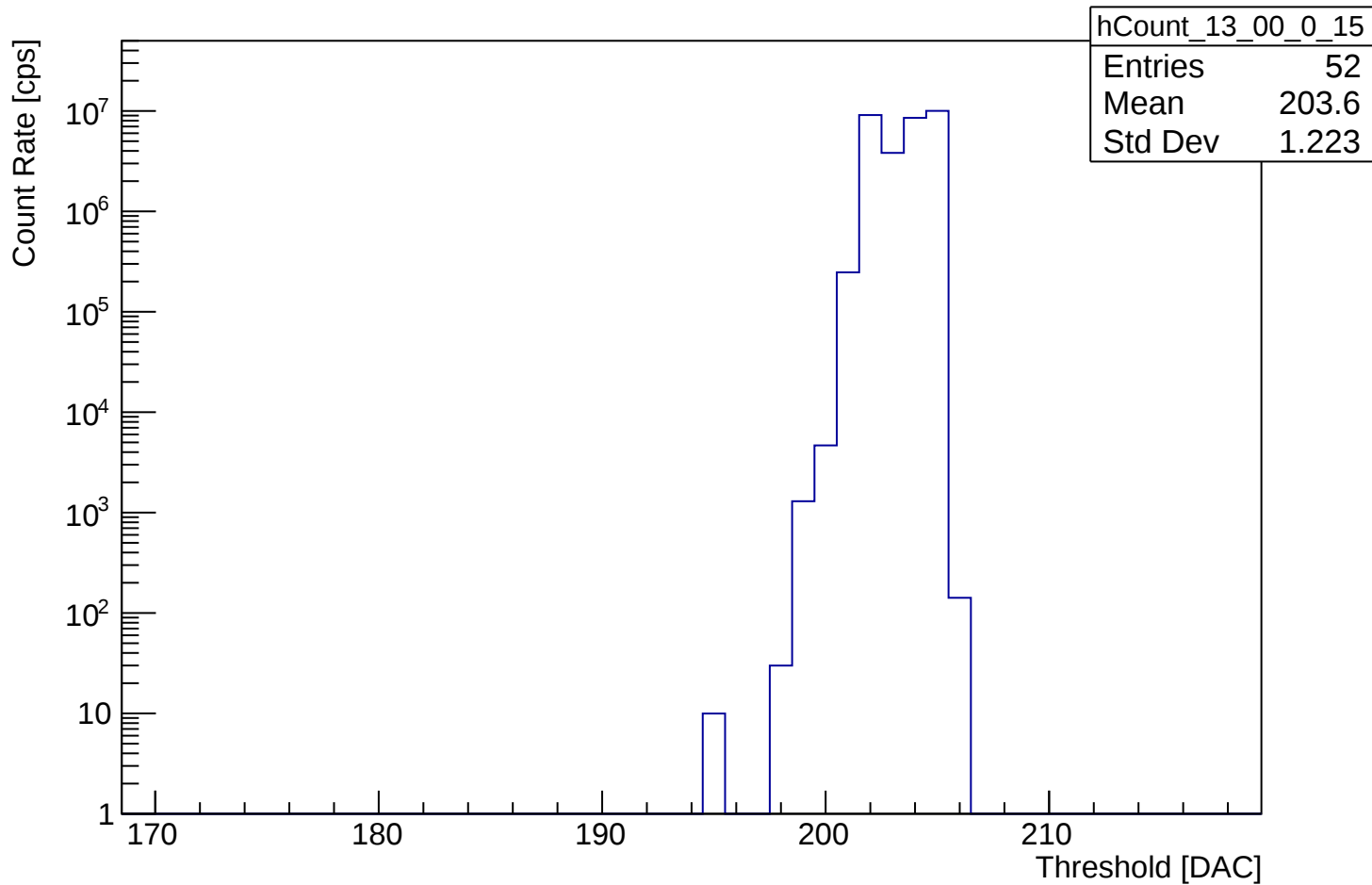
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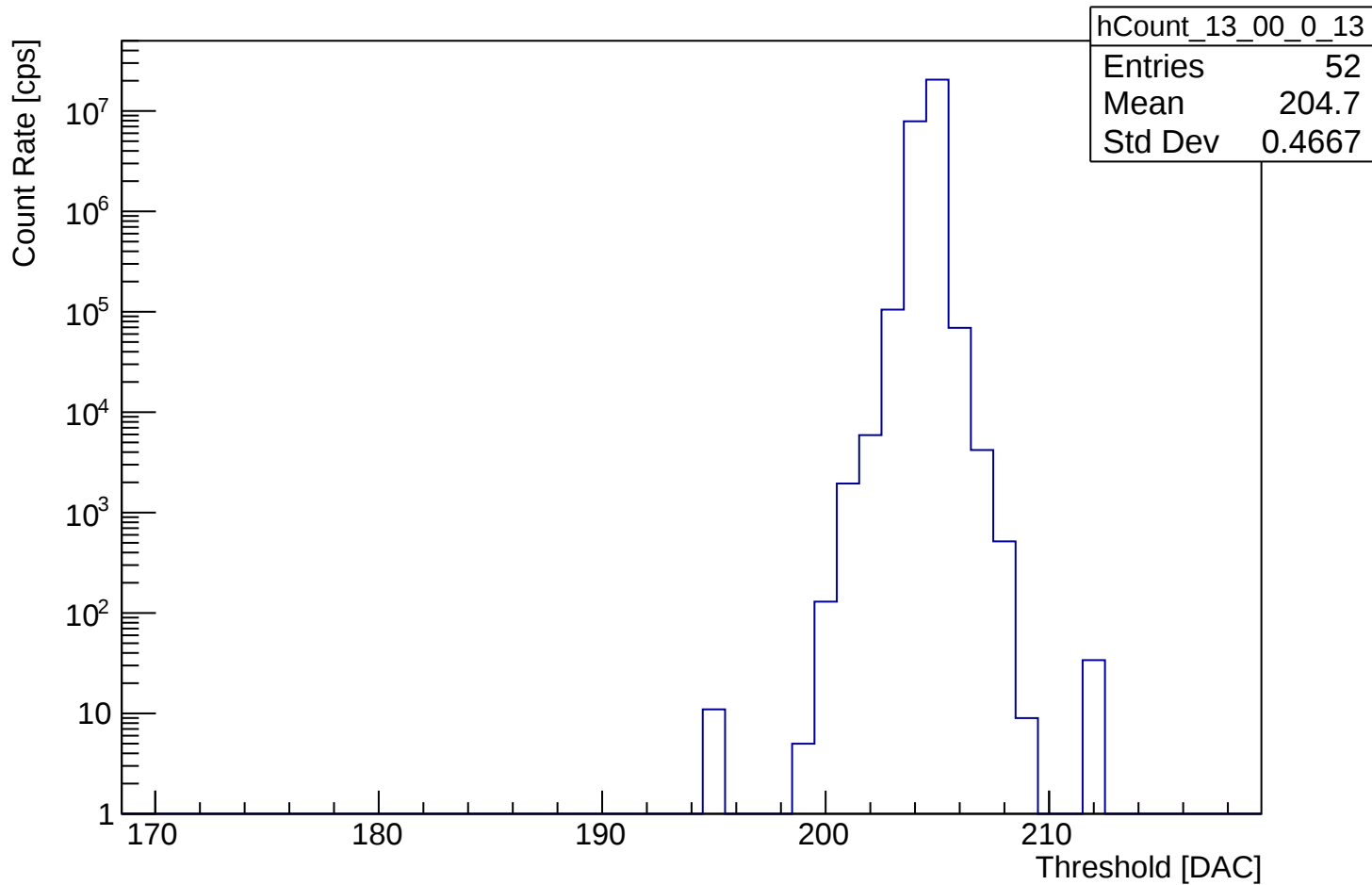
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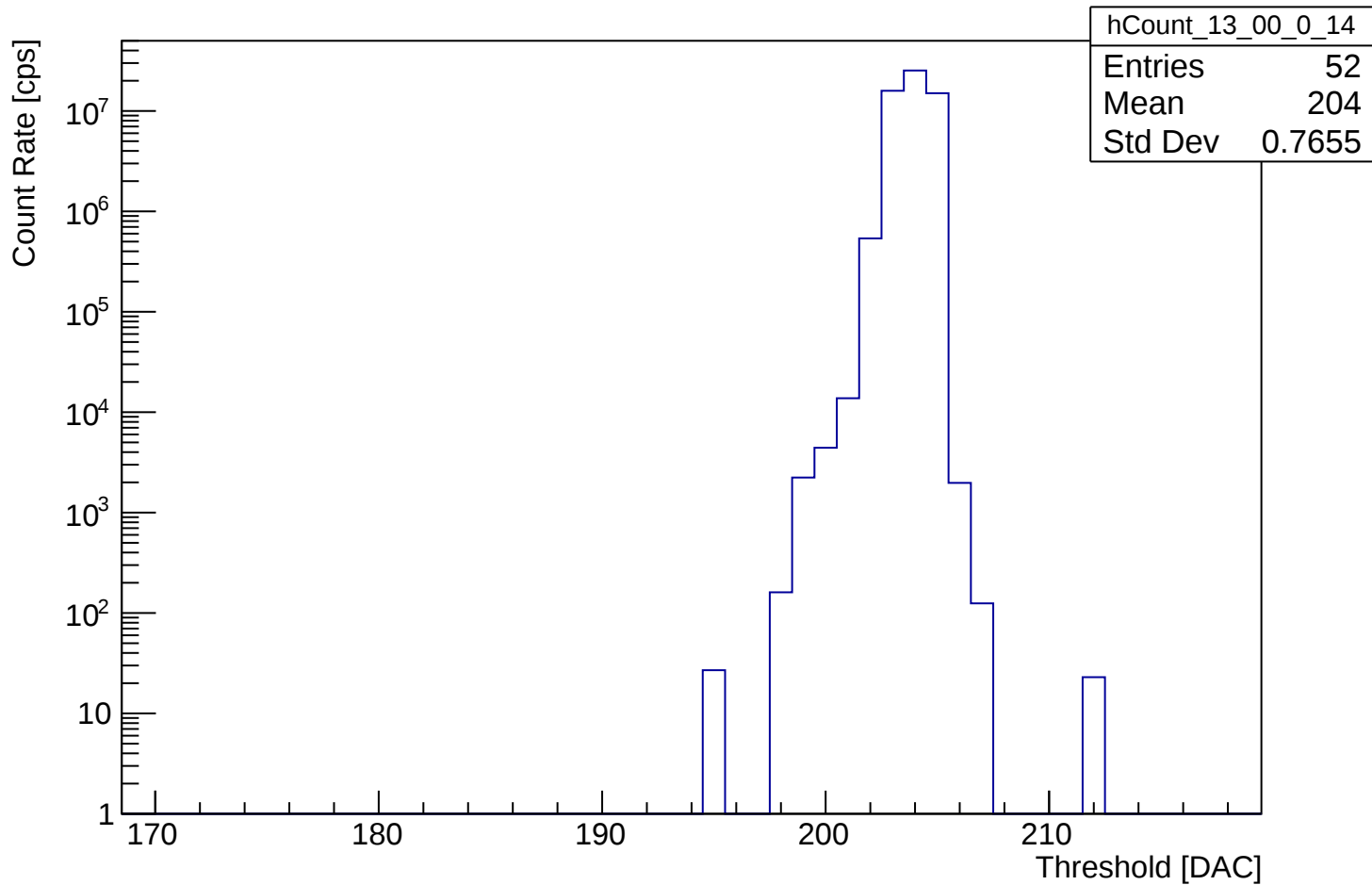
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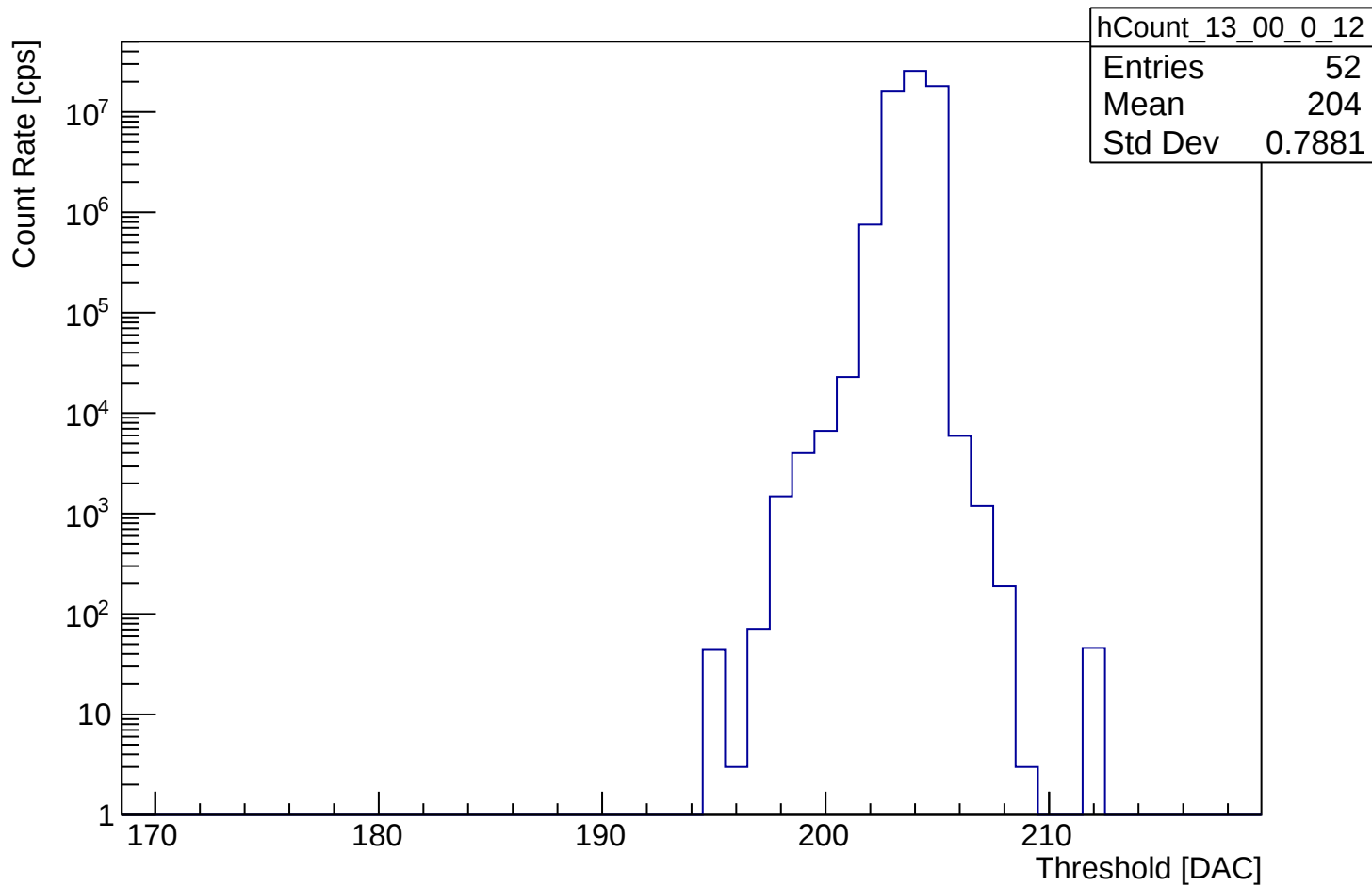
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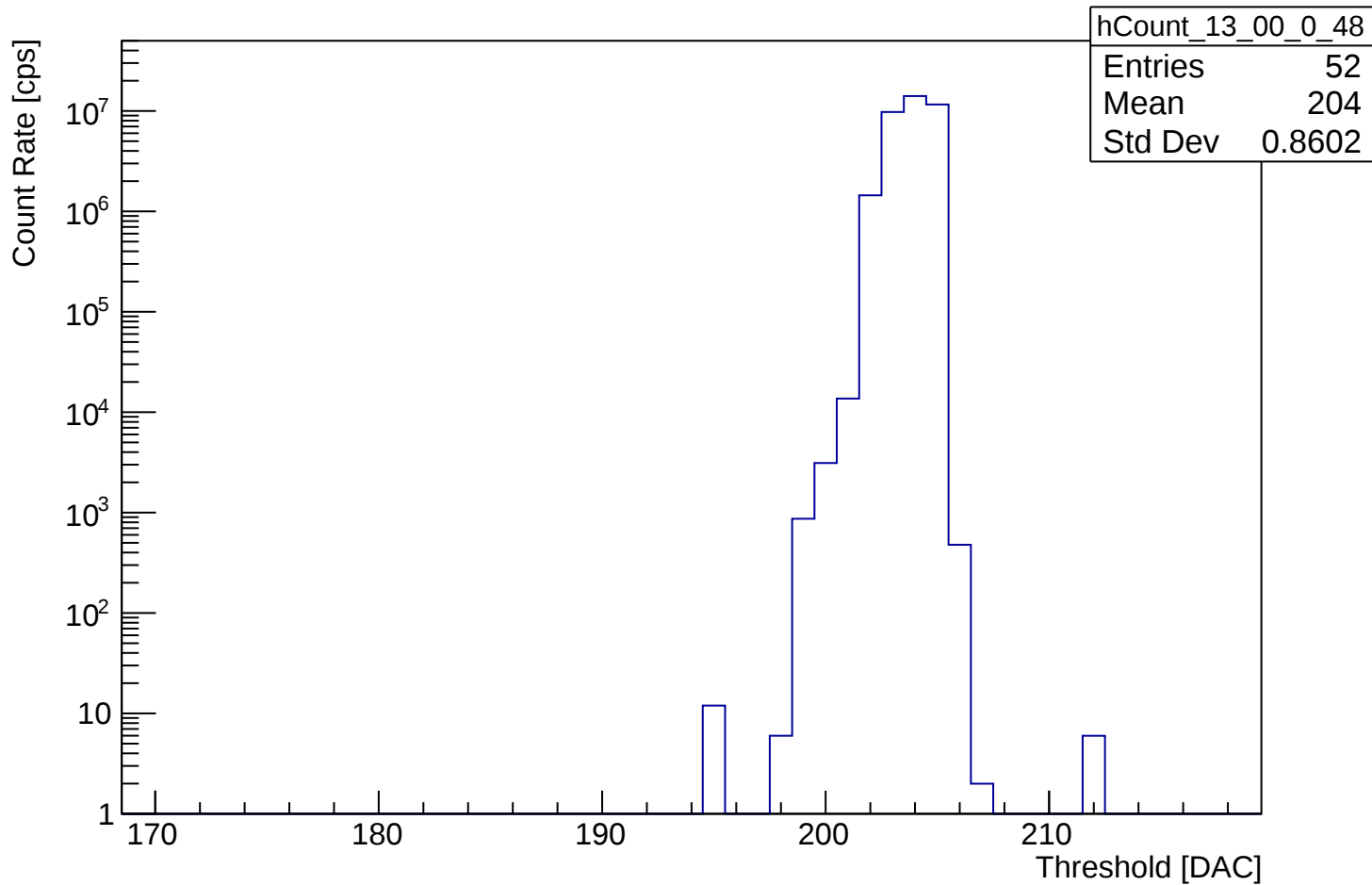
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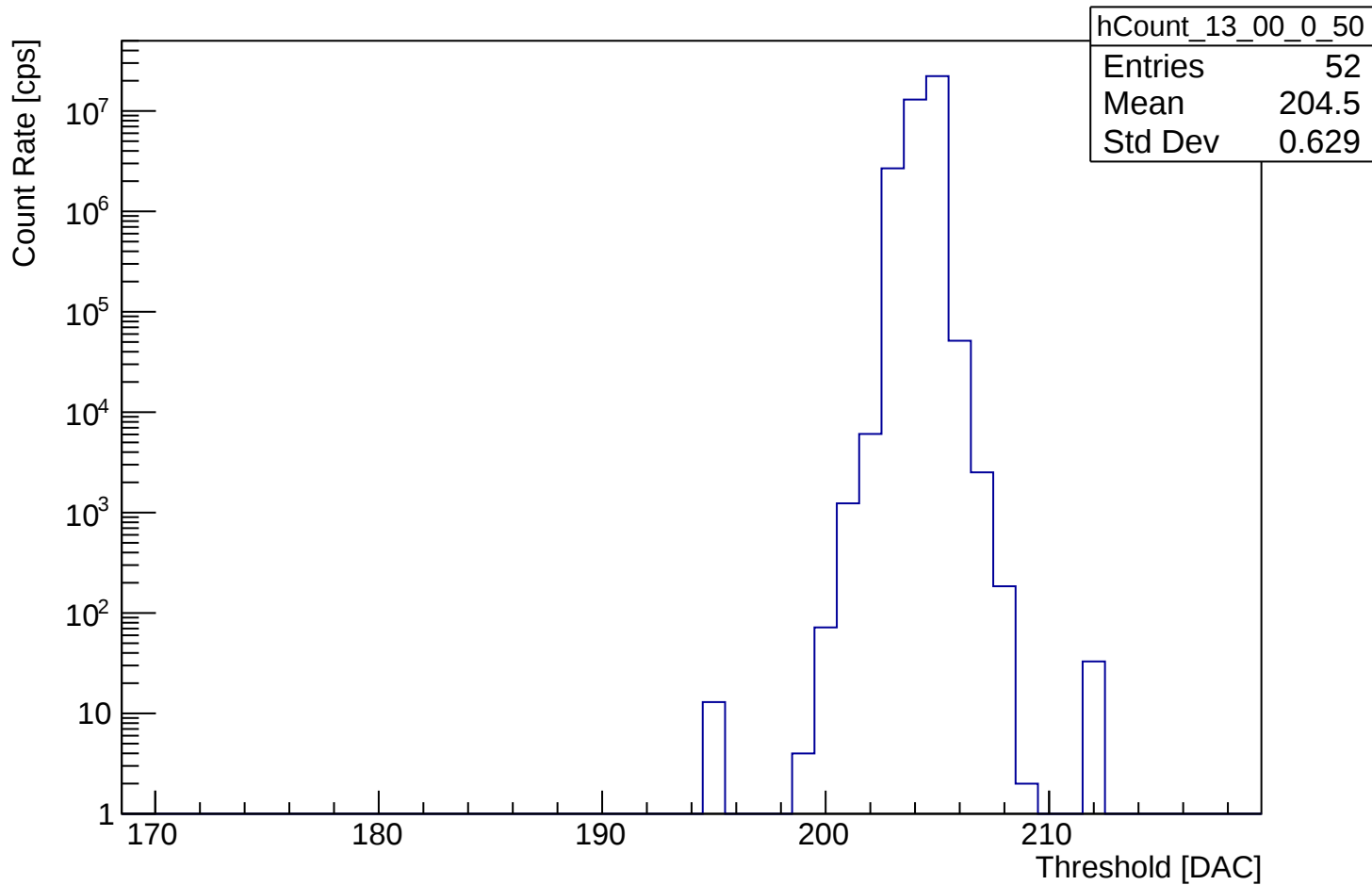
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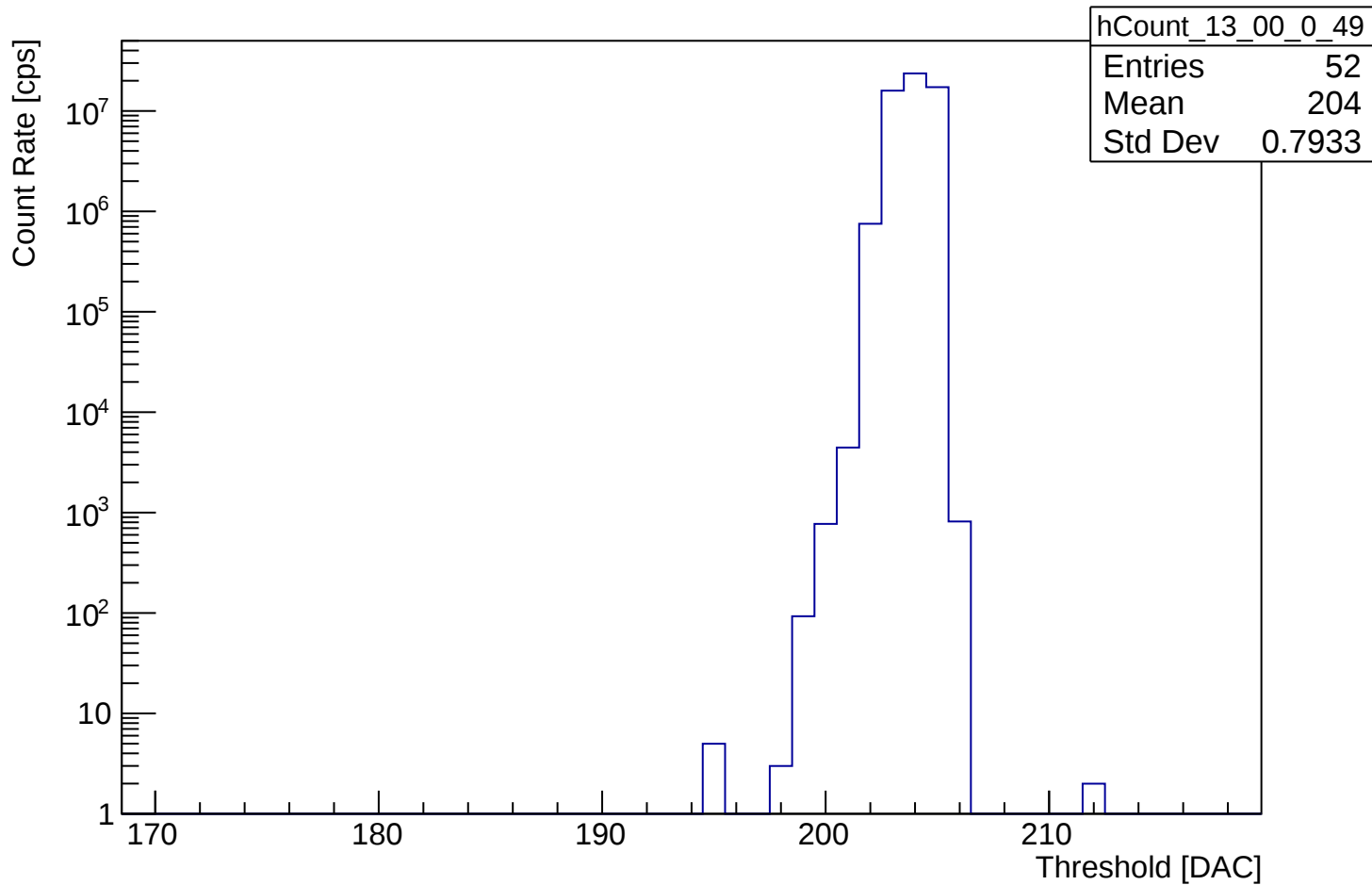
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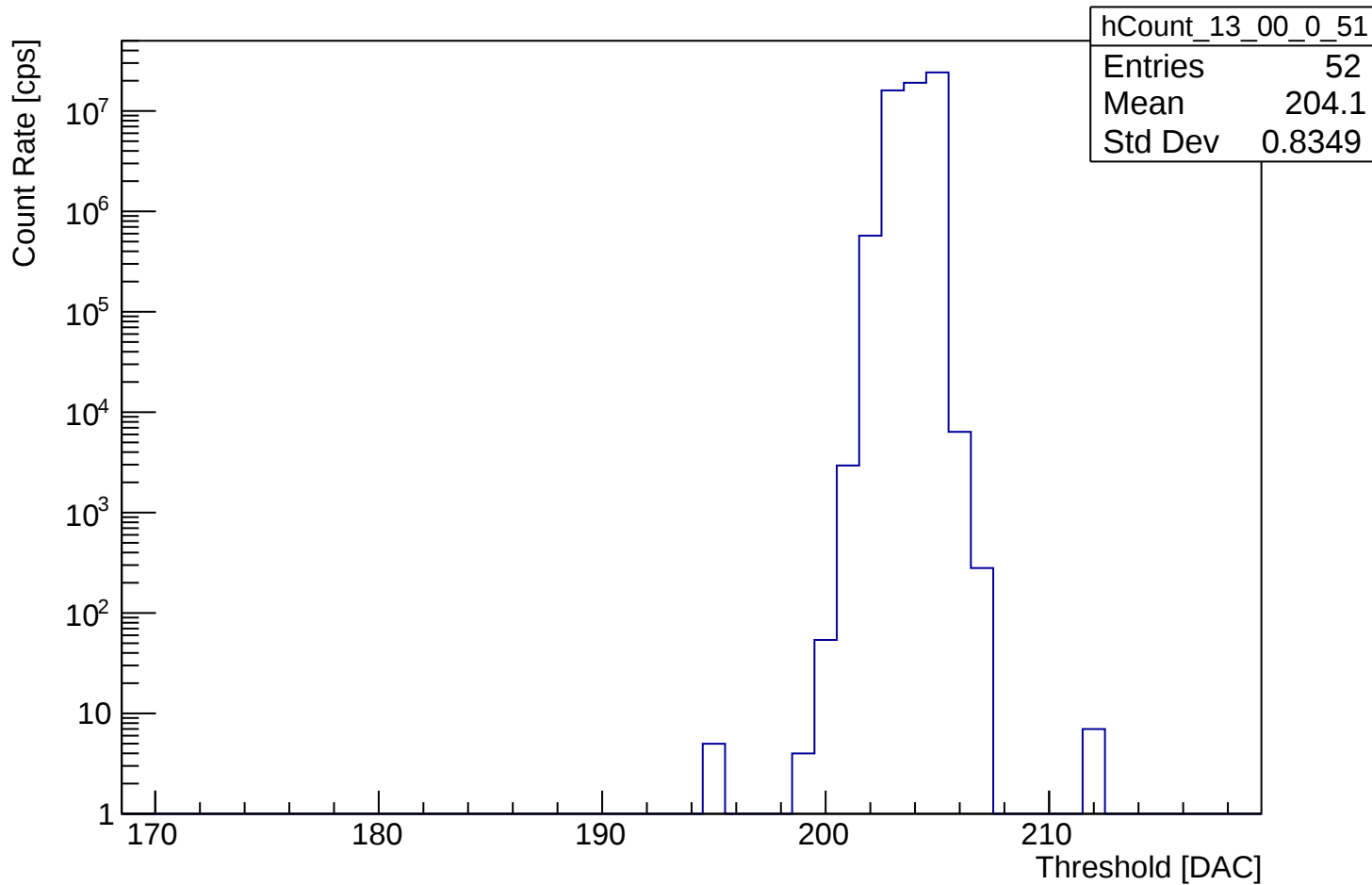
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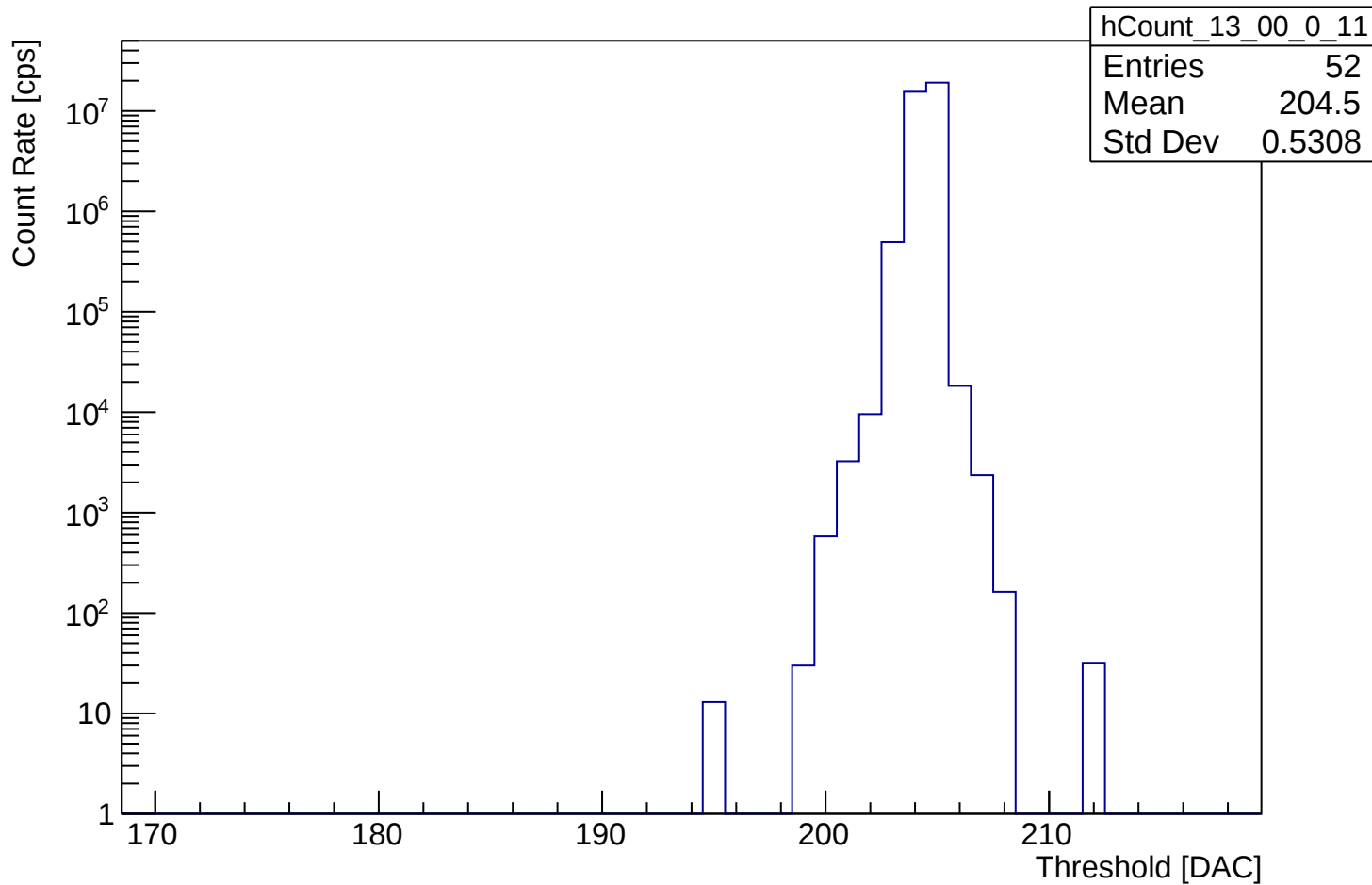
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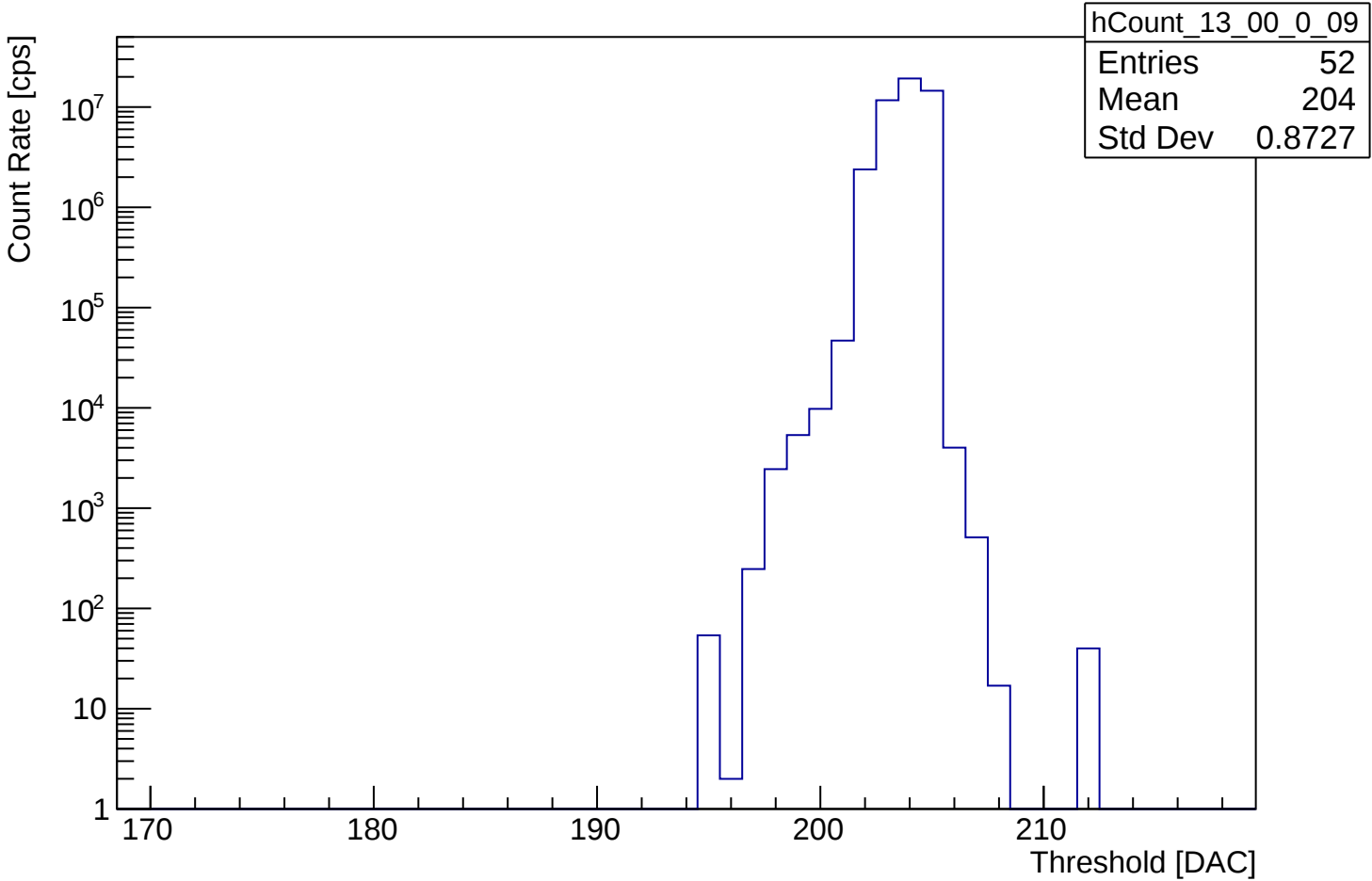


Row 1 Tile 1 Pmt 1 Pixel 40 Slot 13 Fiber 0 Asic 0 Channel 51

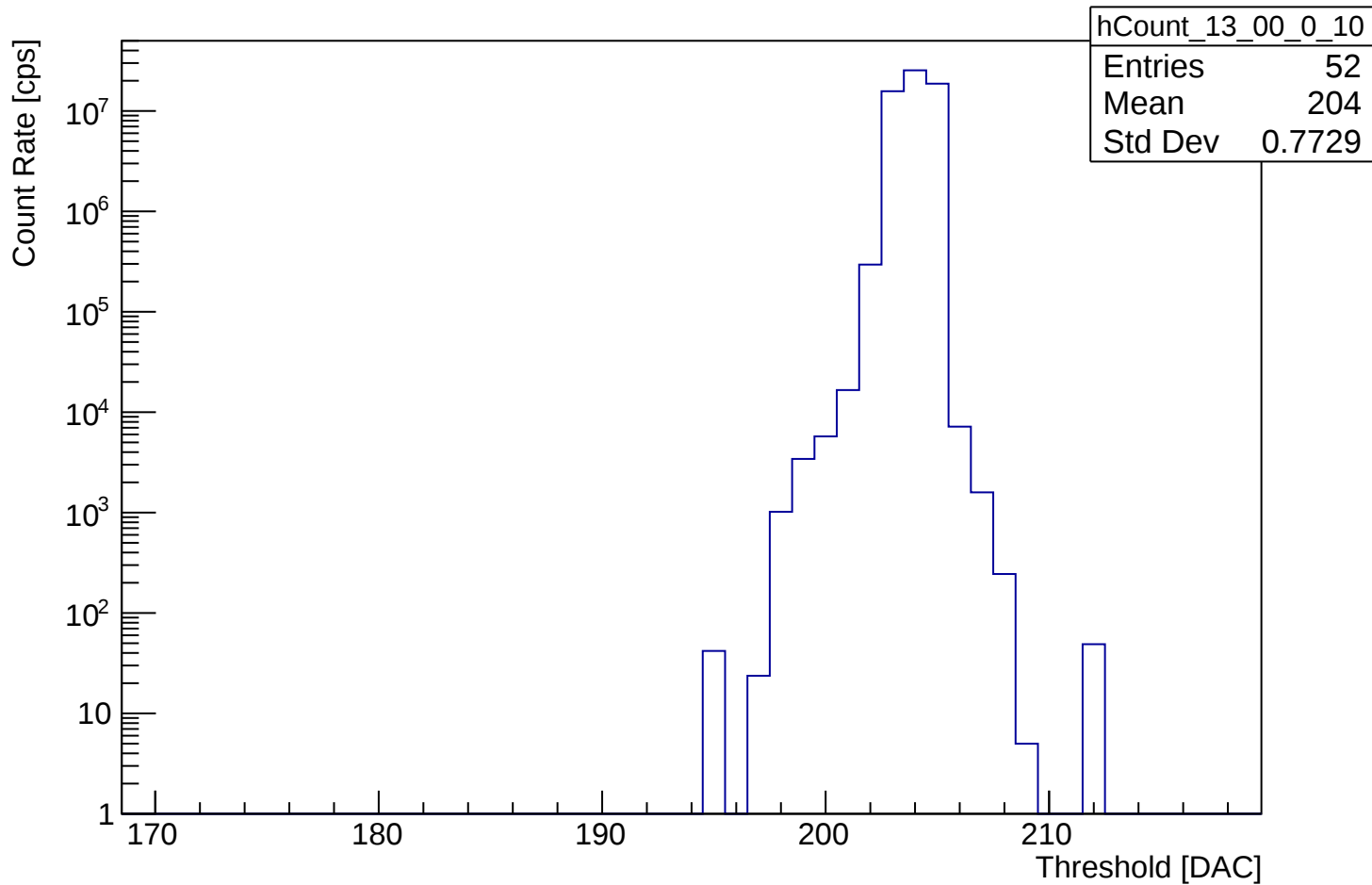


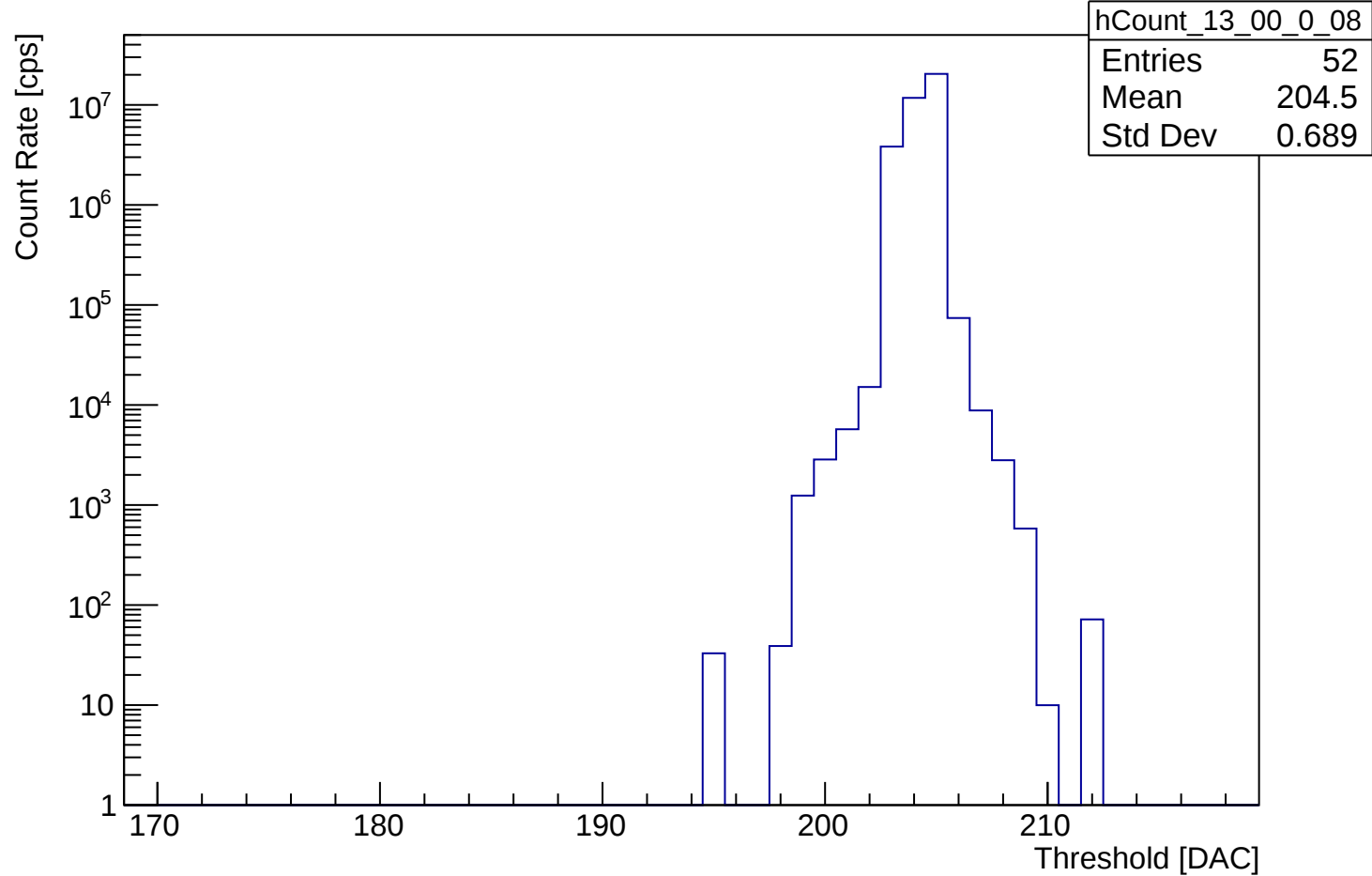
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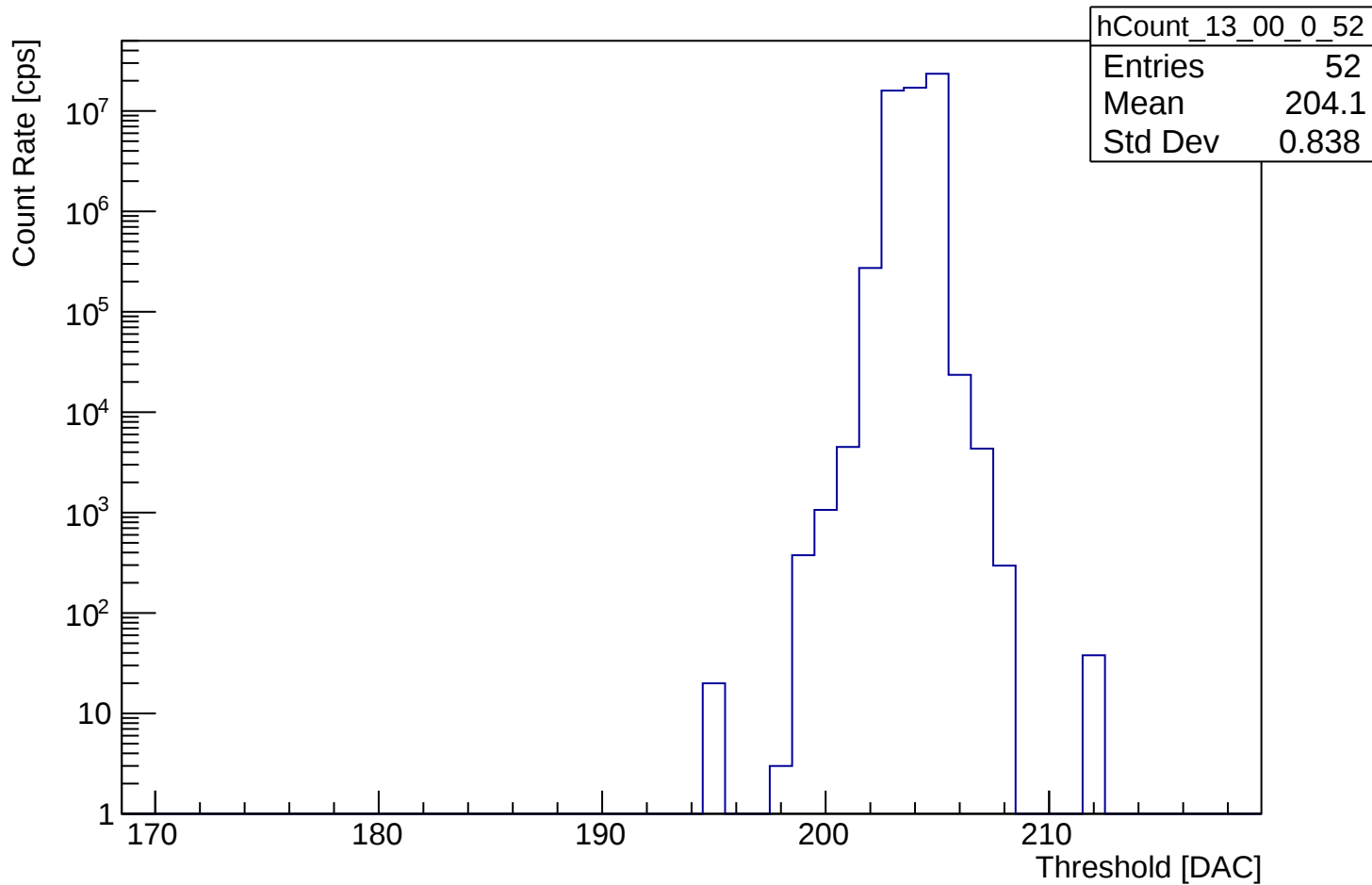


Row 1 Tile 1 Pmt 1 Pixel 43 Slot 13 Fiber 0 Asic 0 Channel 10

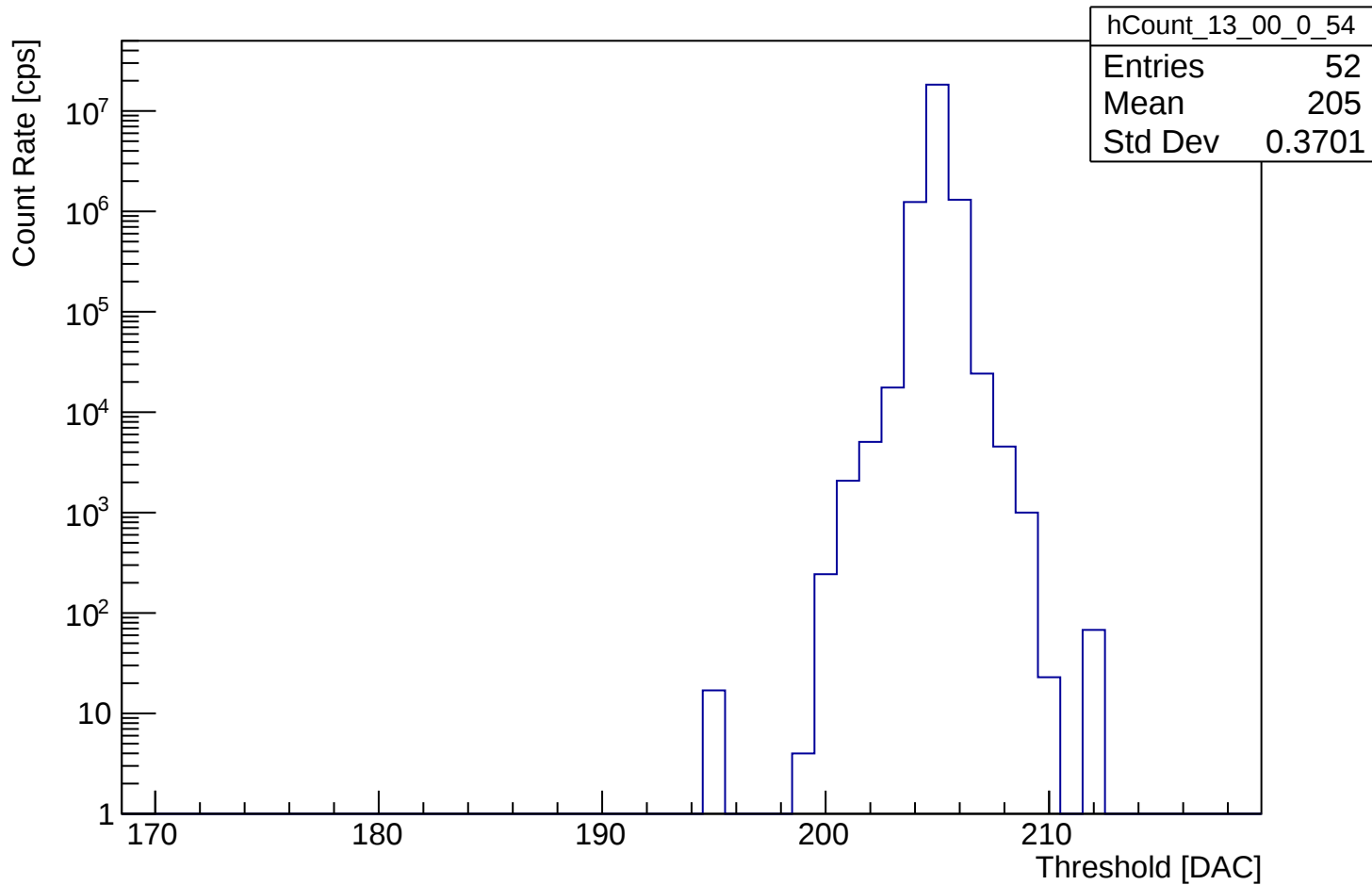




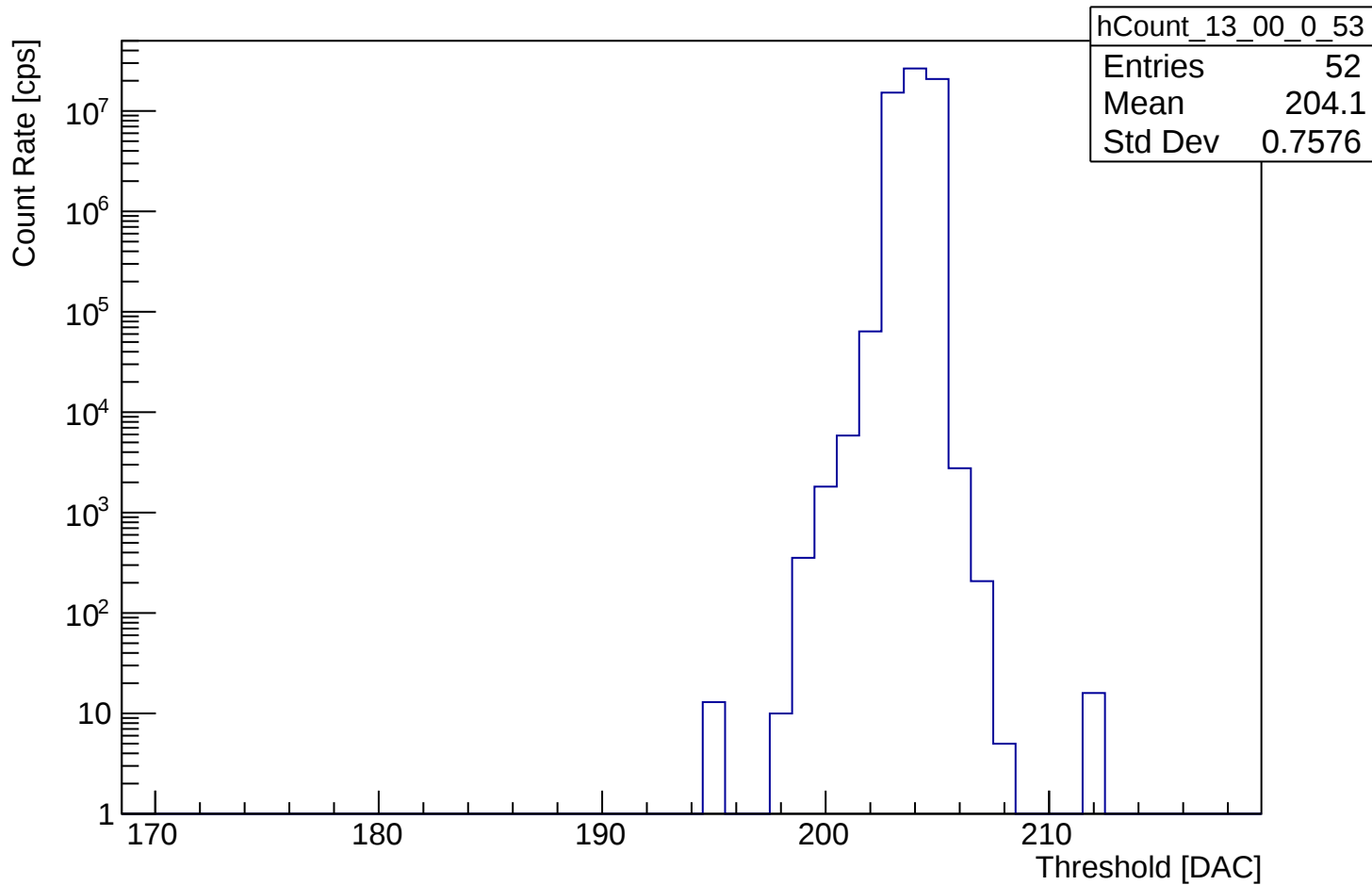
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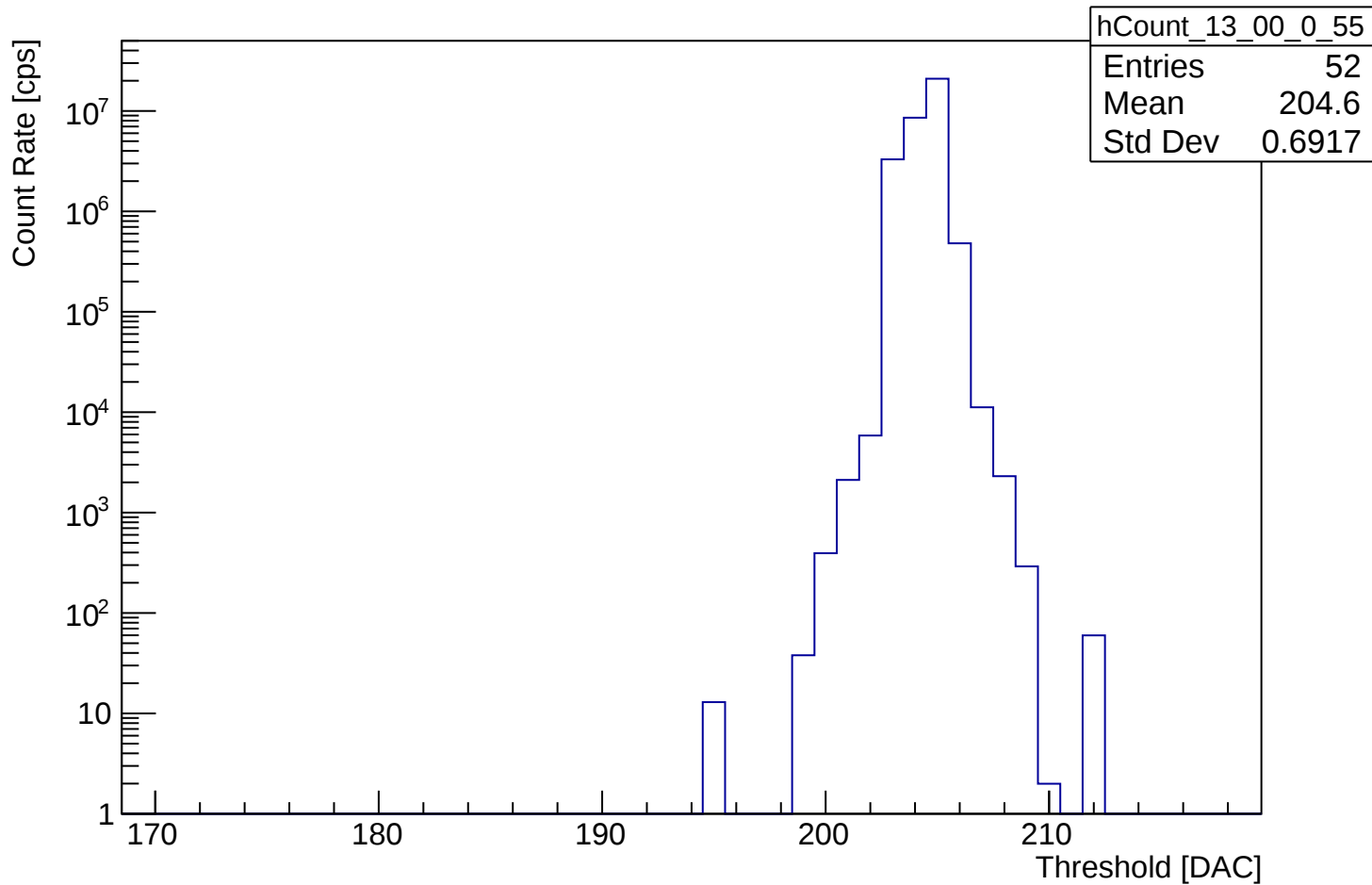
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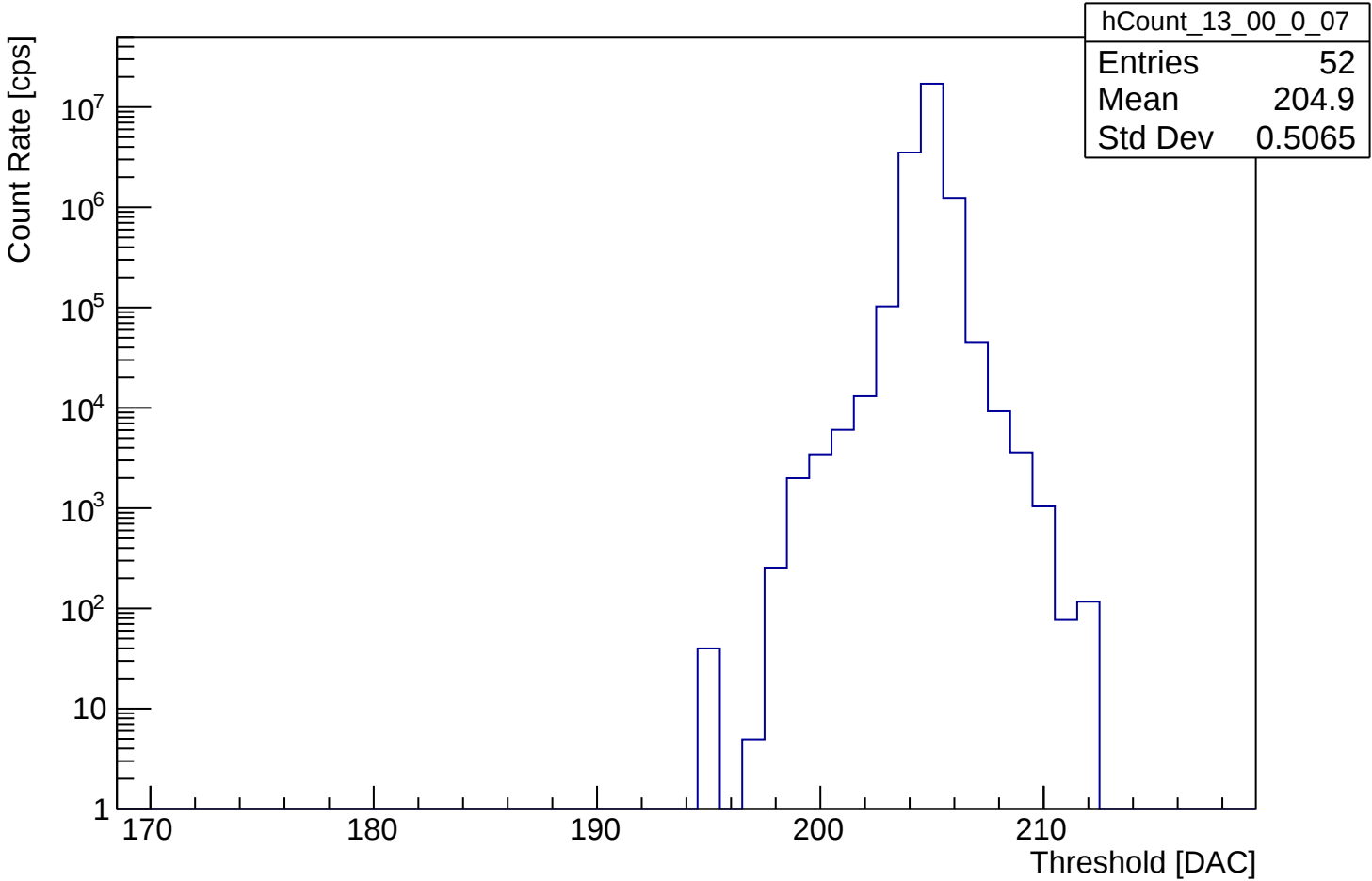


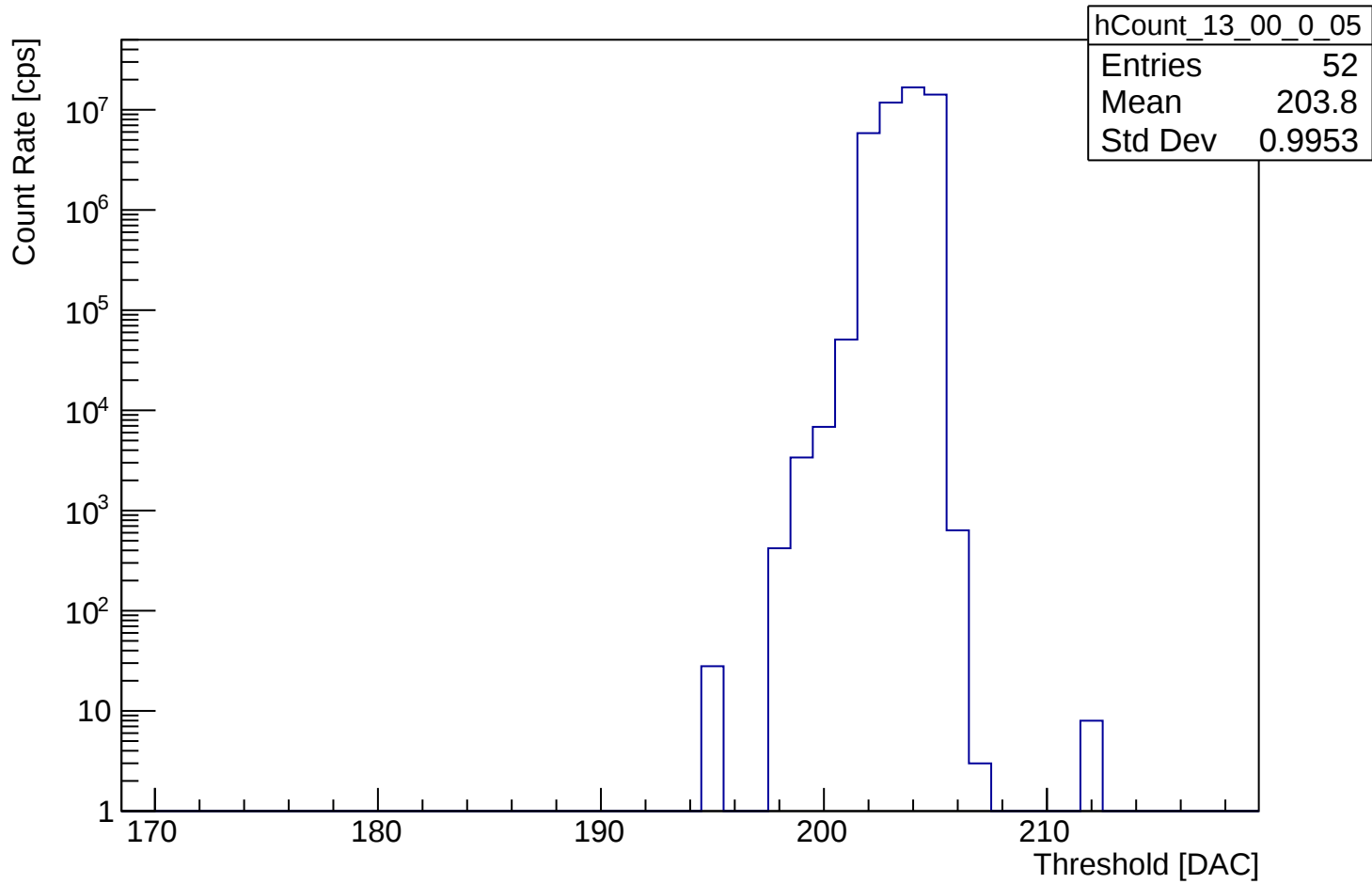
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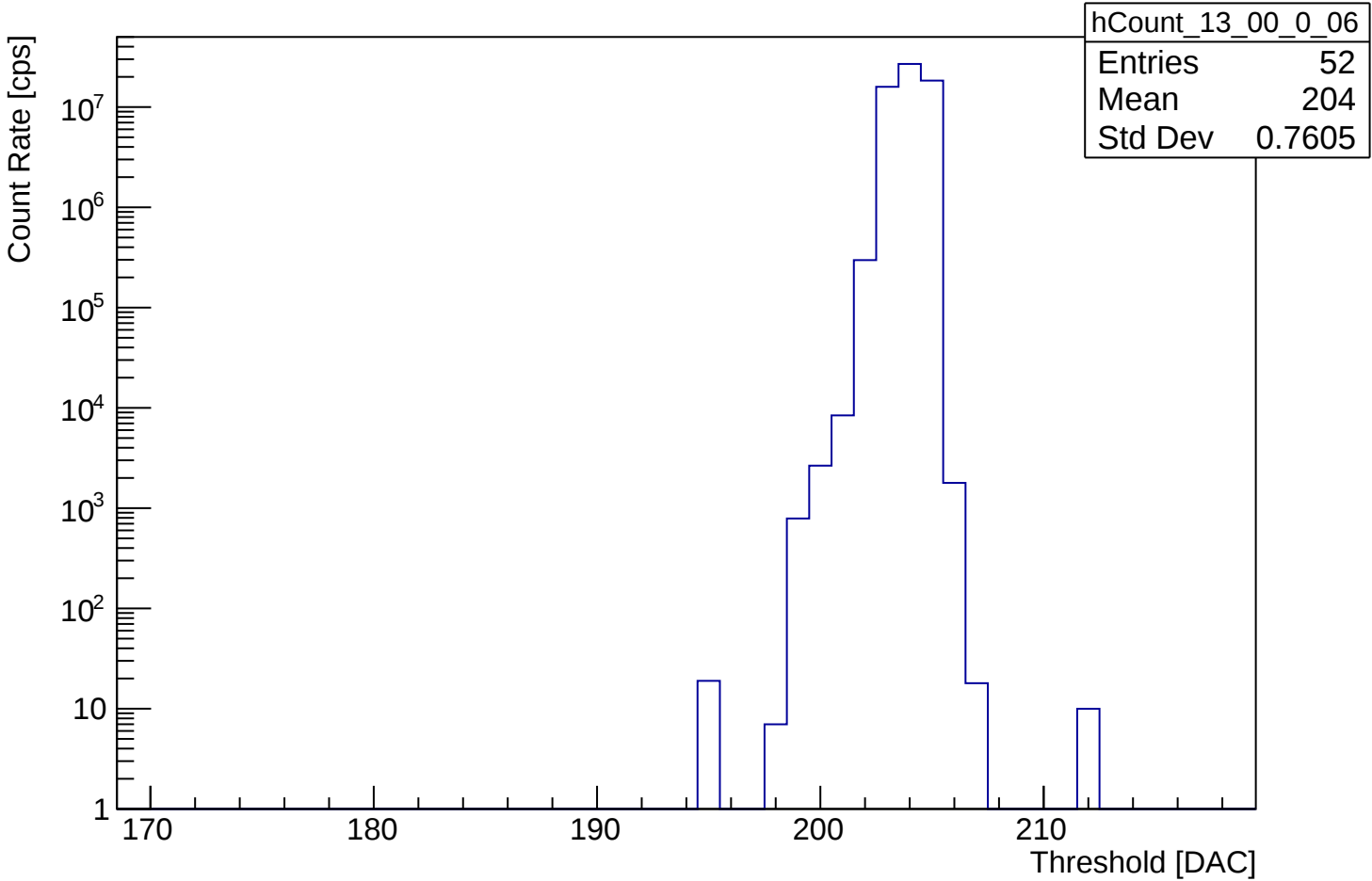


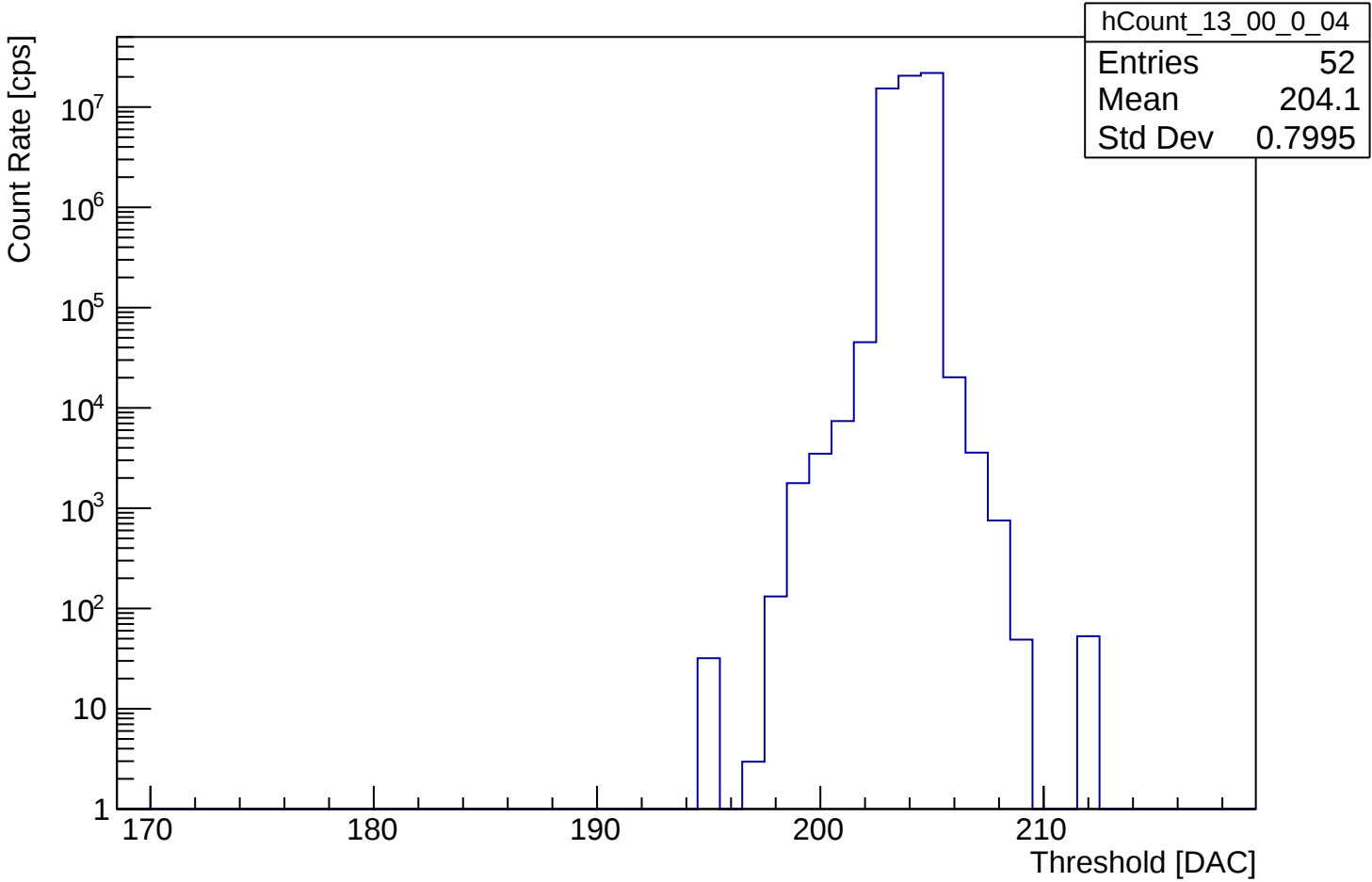
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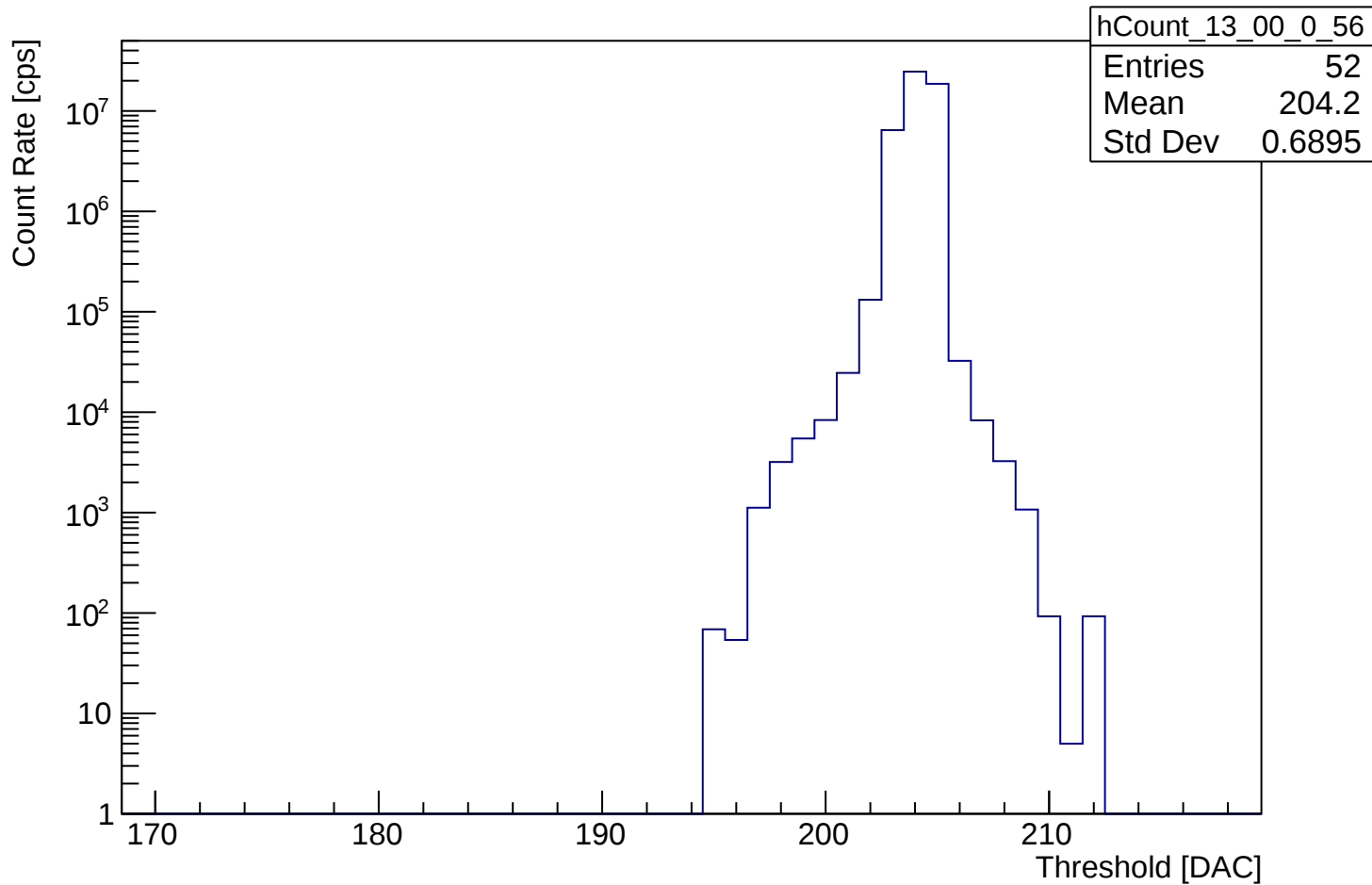




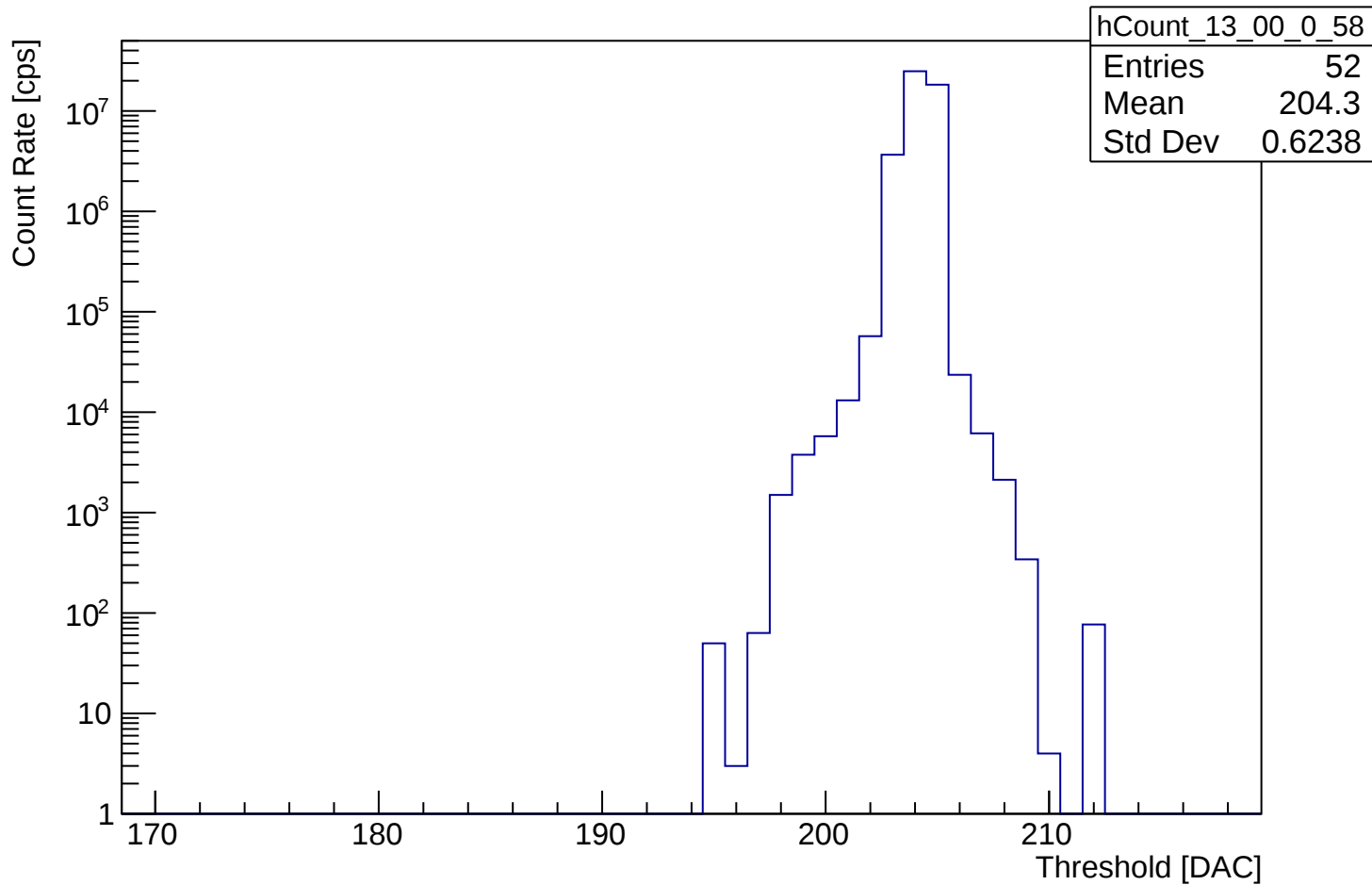




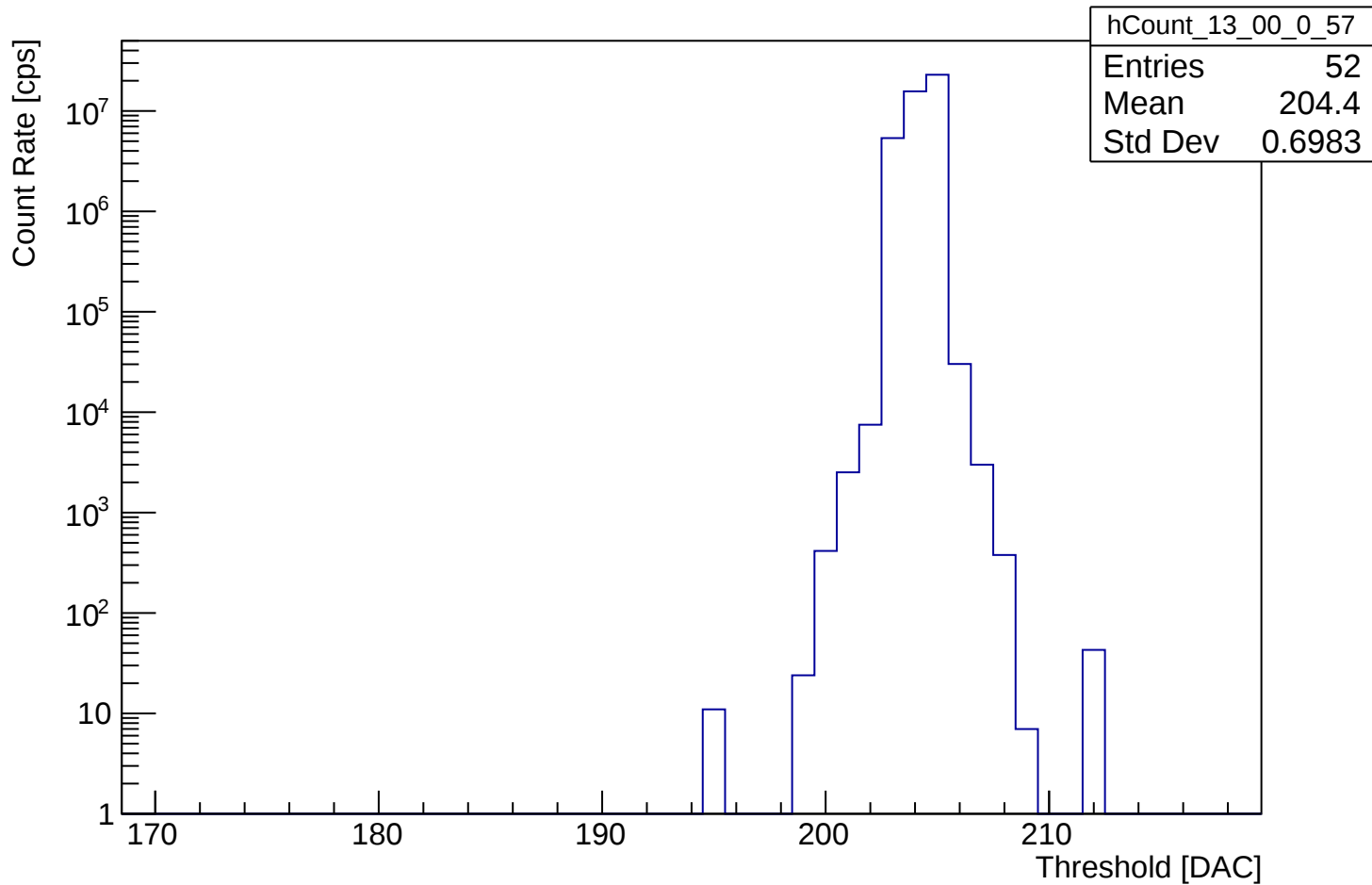
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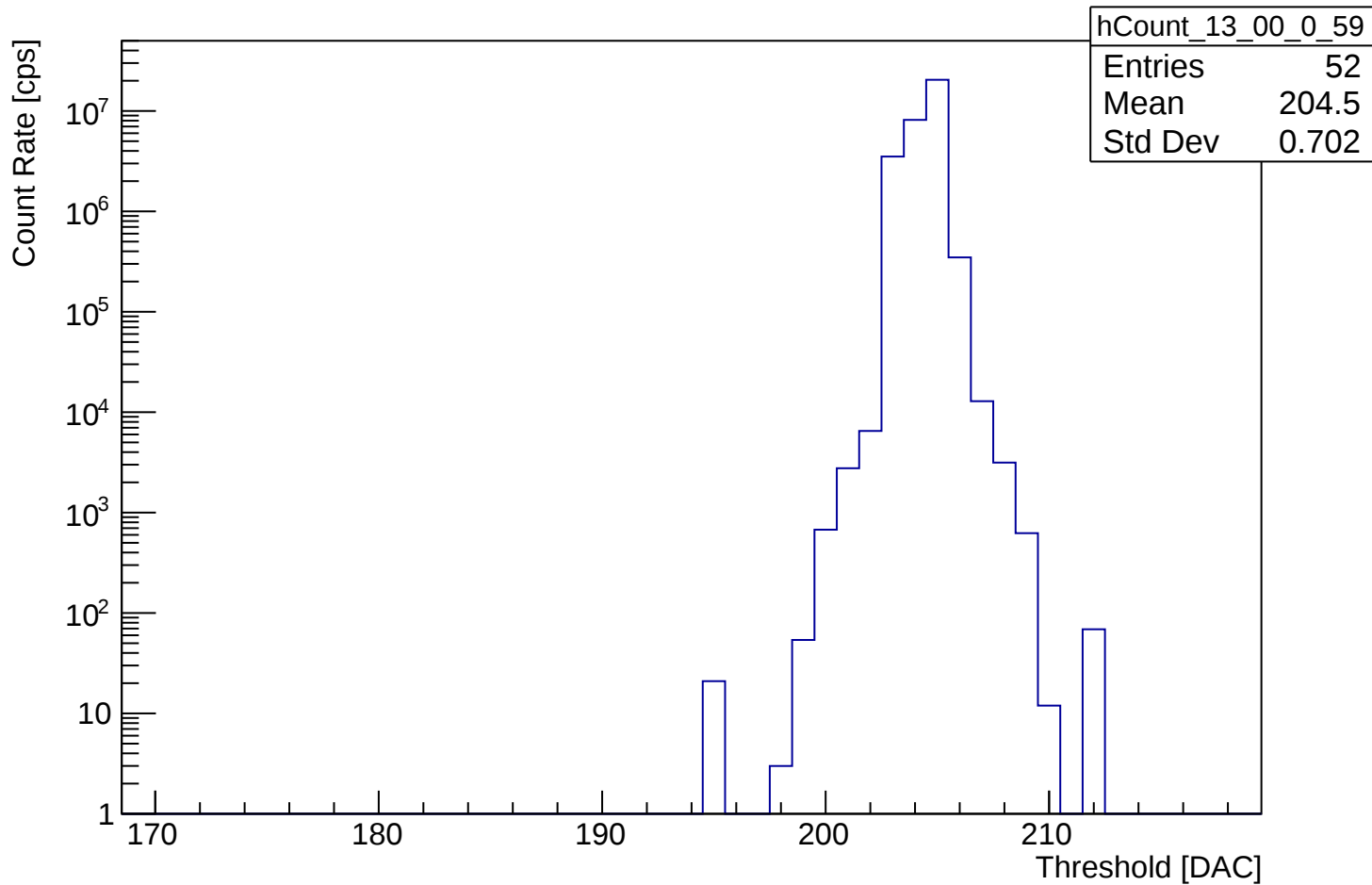
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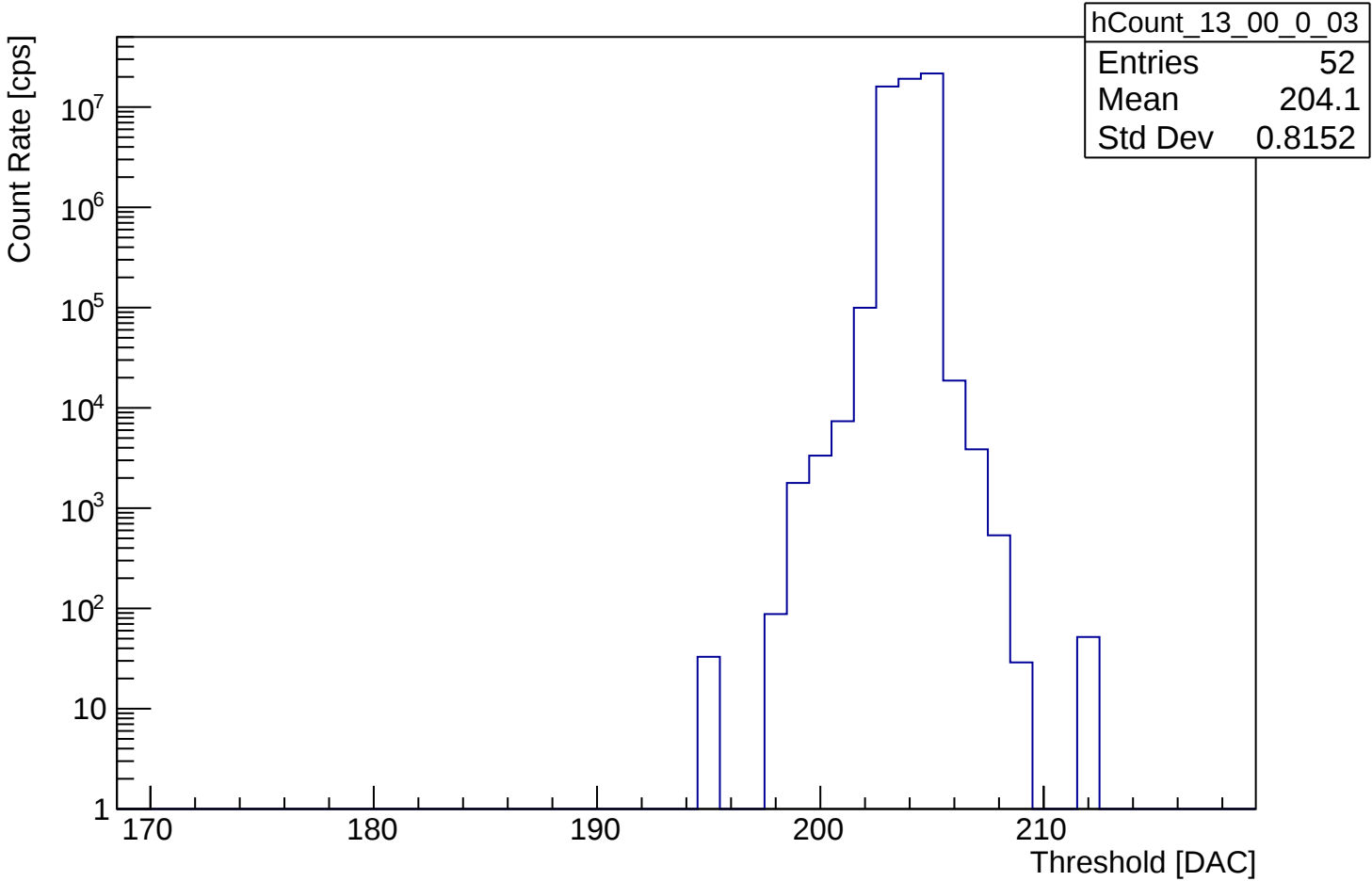


Row 1 Tile 1 Pmt 1 Pixel 55 Slot 13 Fiber 0 Asic 0 Channel 57

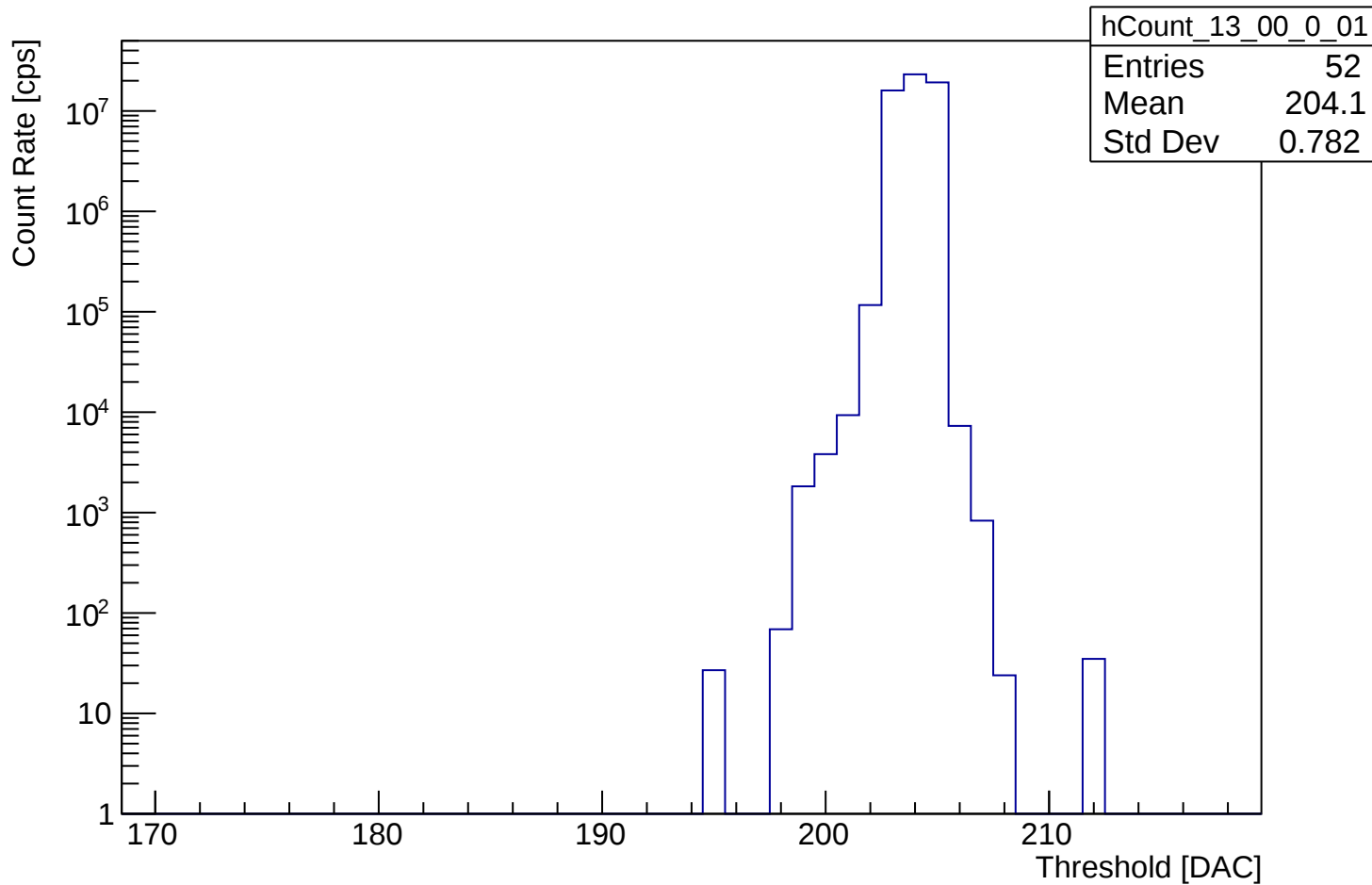


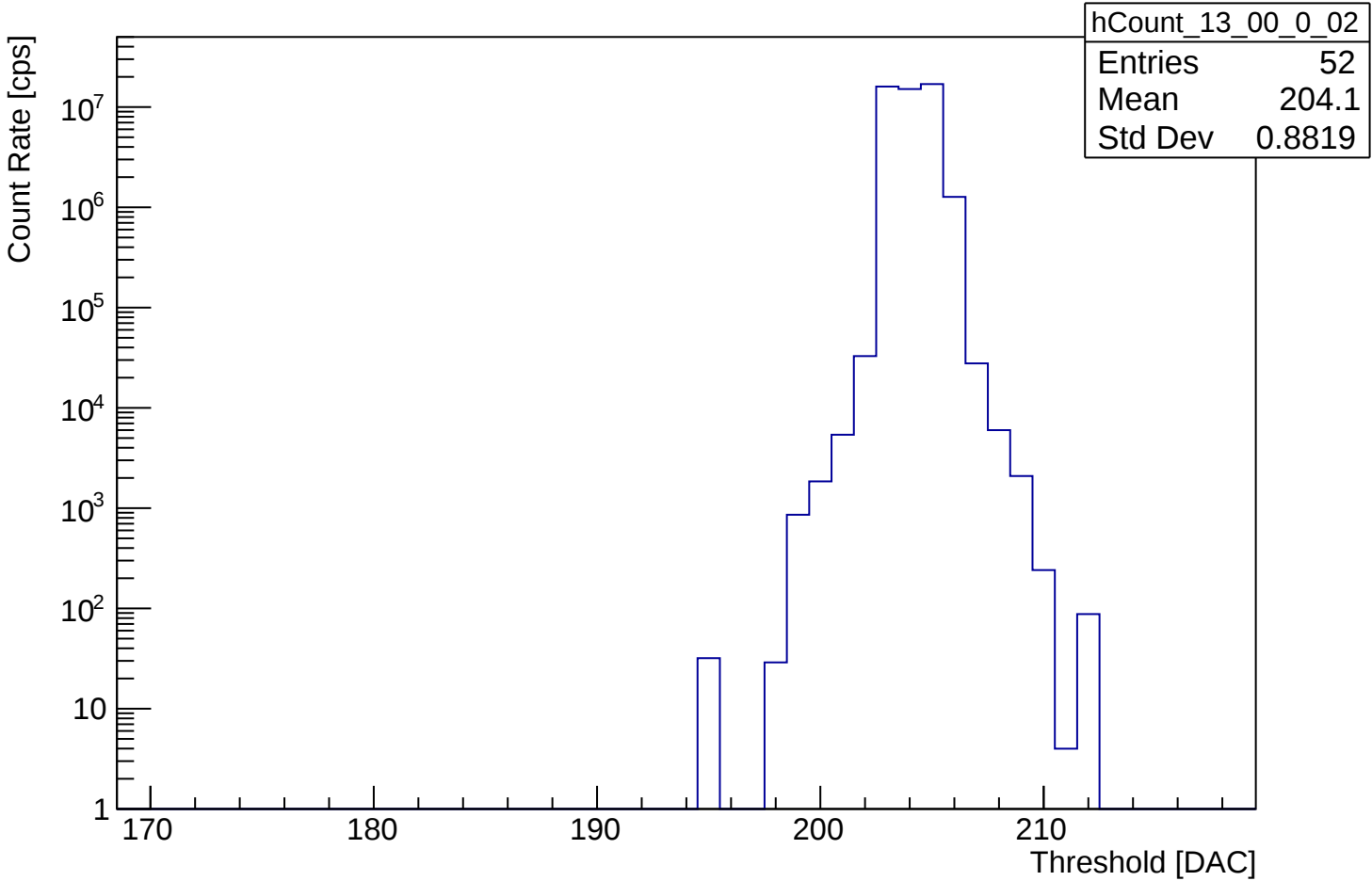
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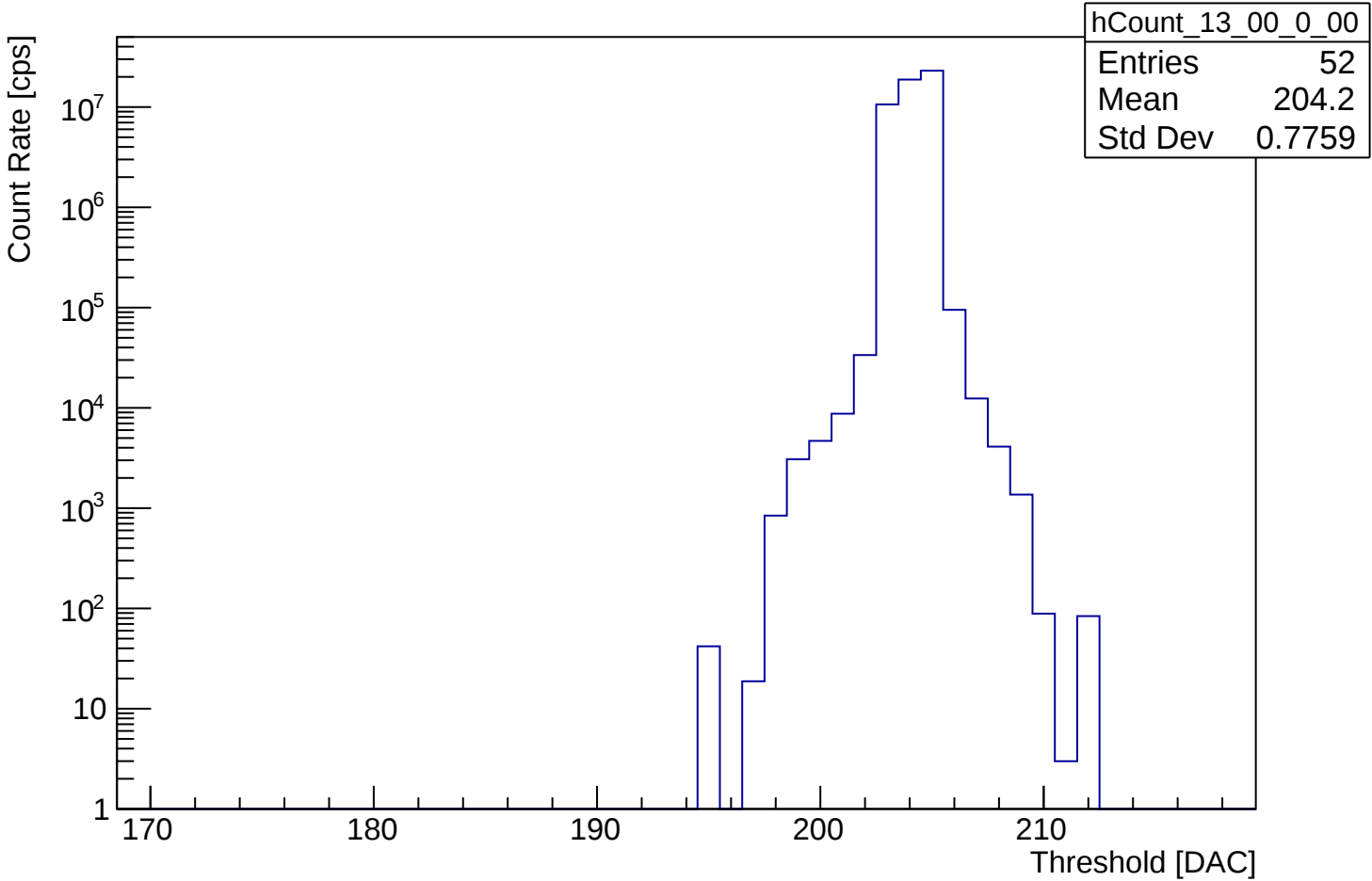




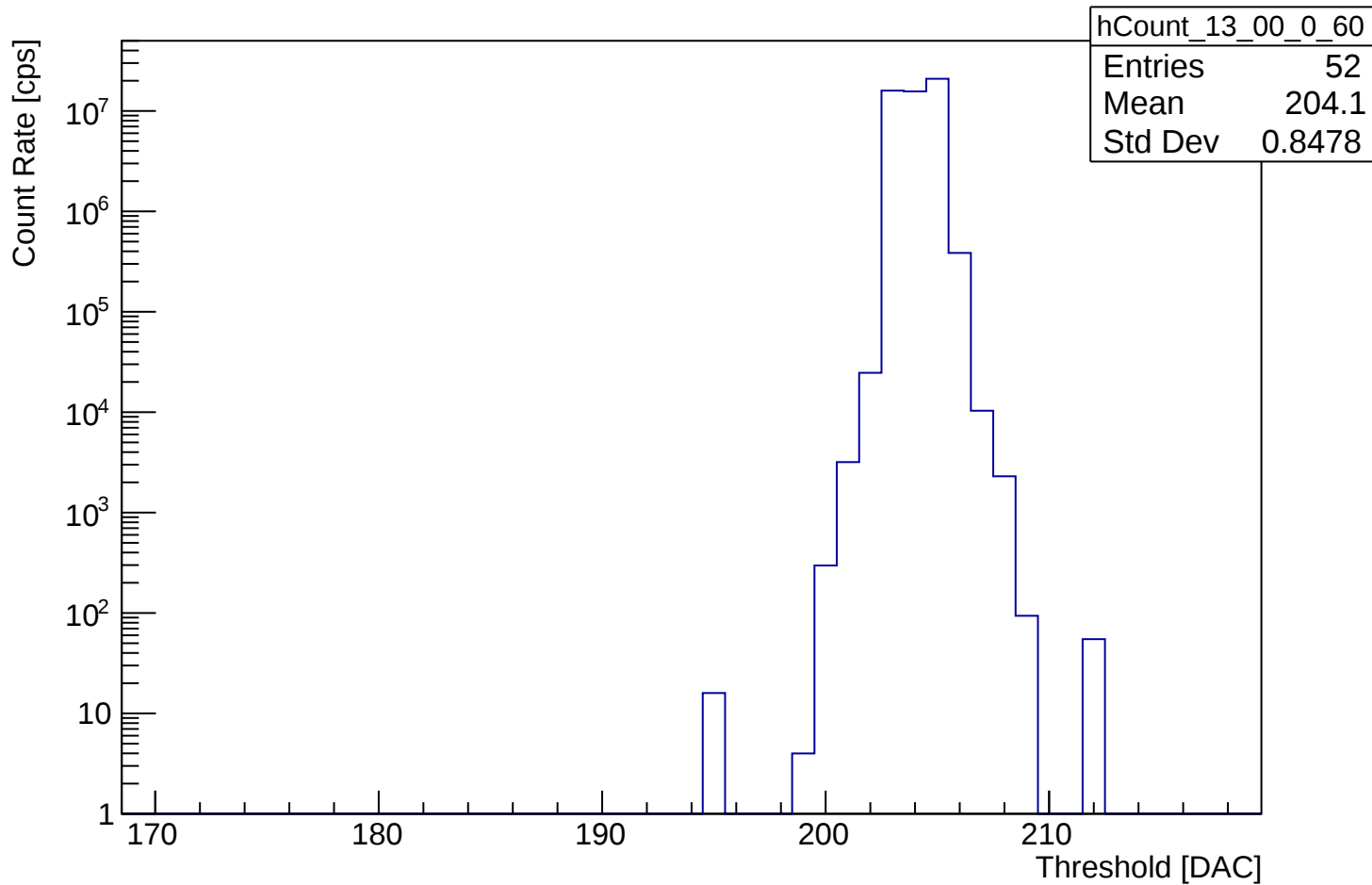
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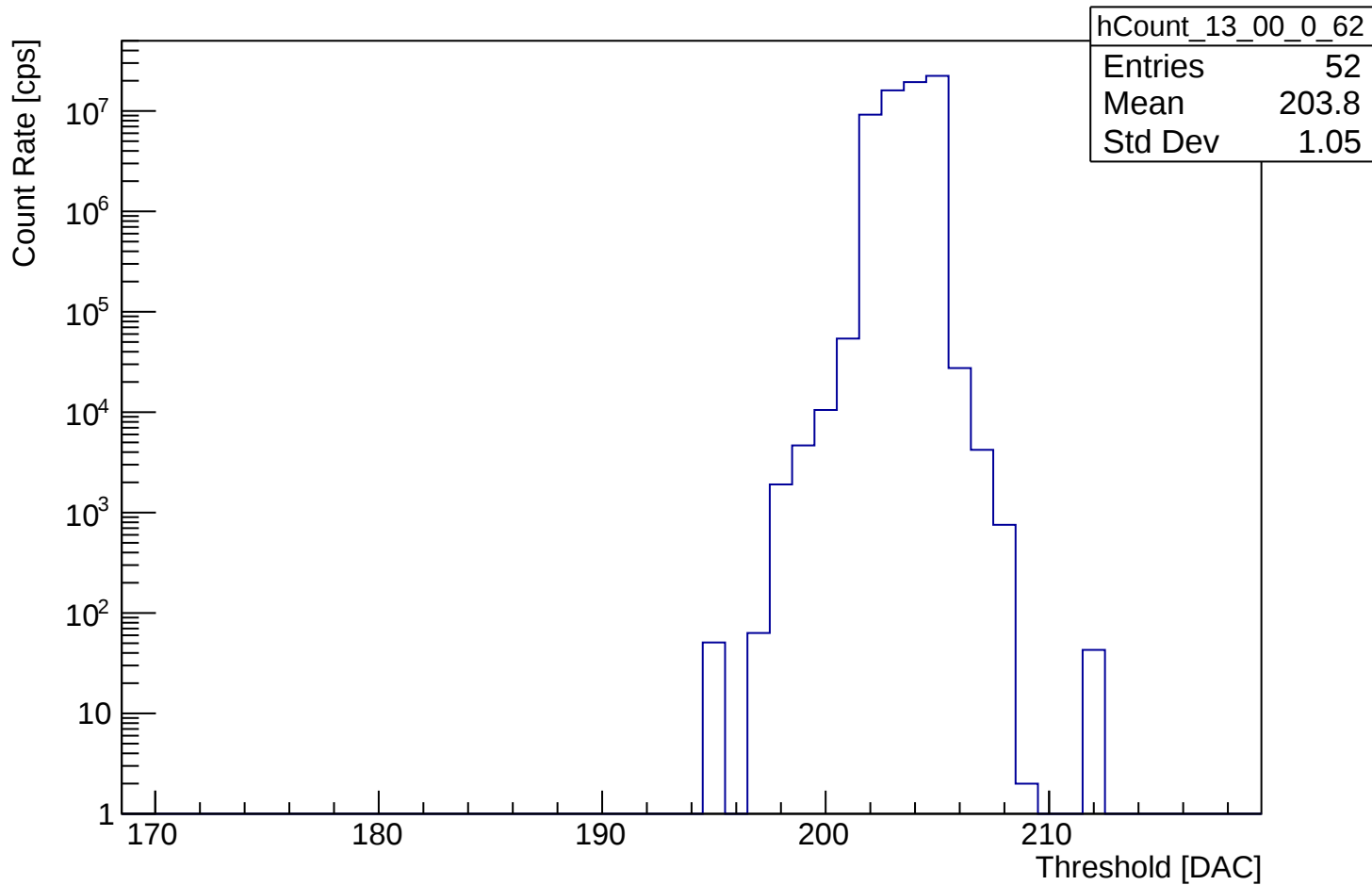




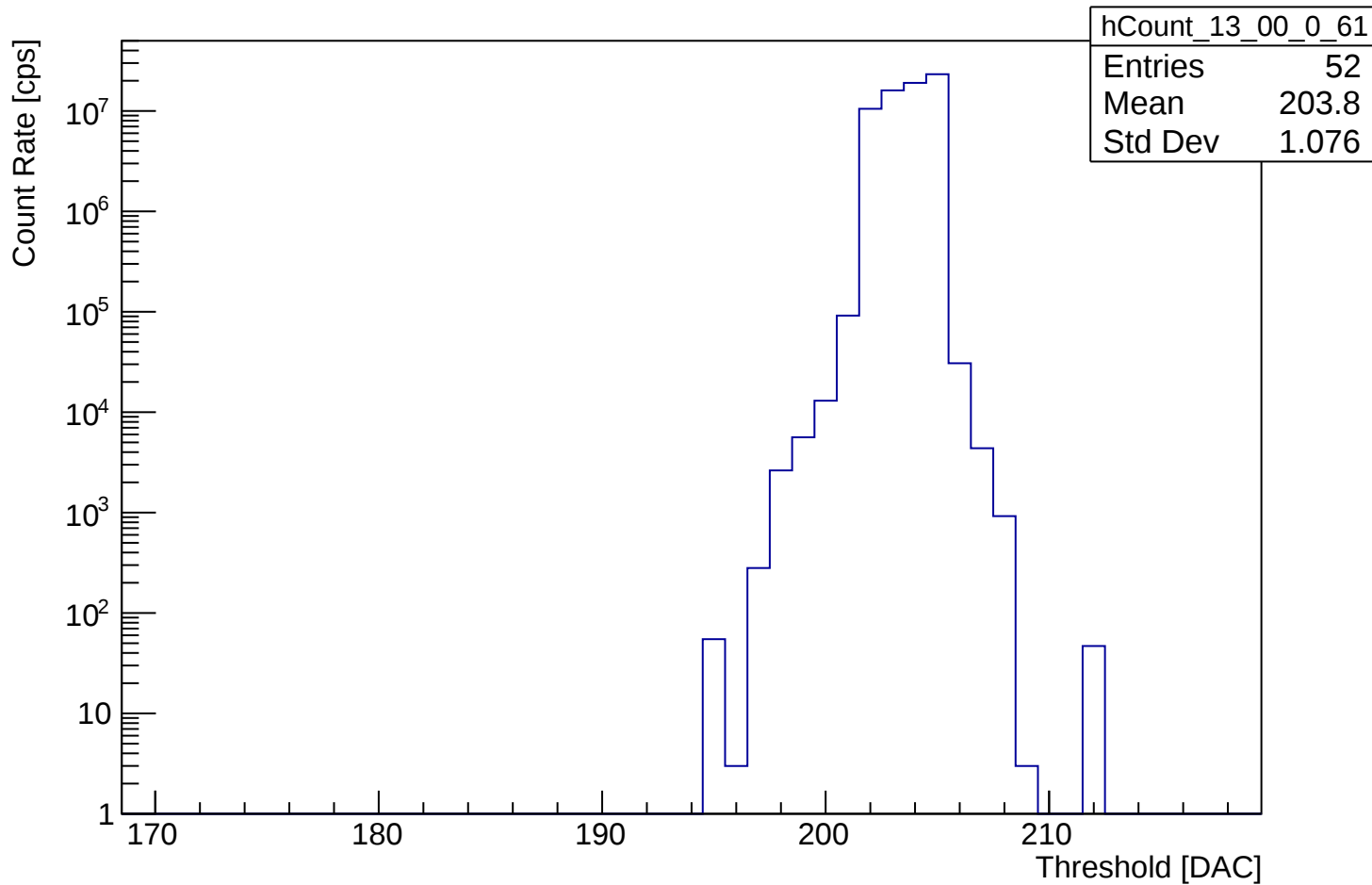
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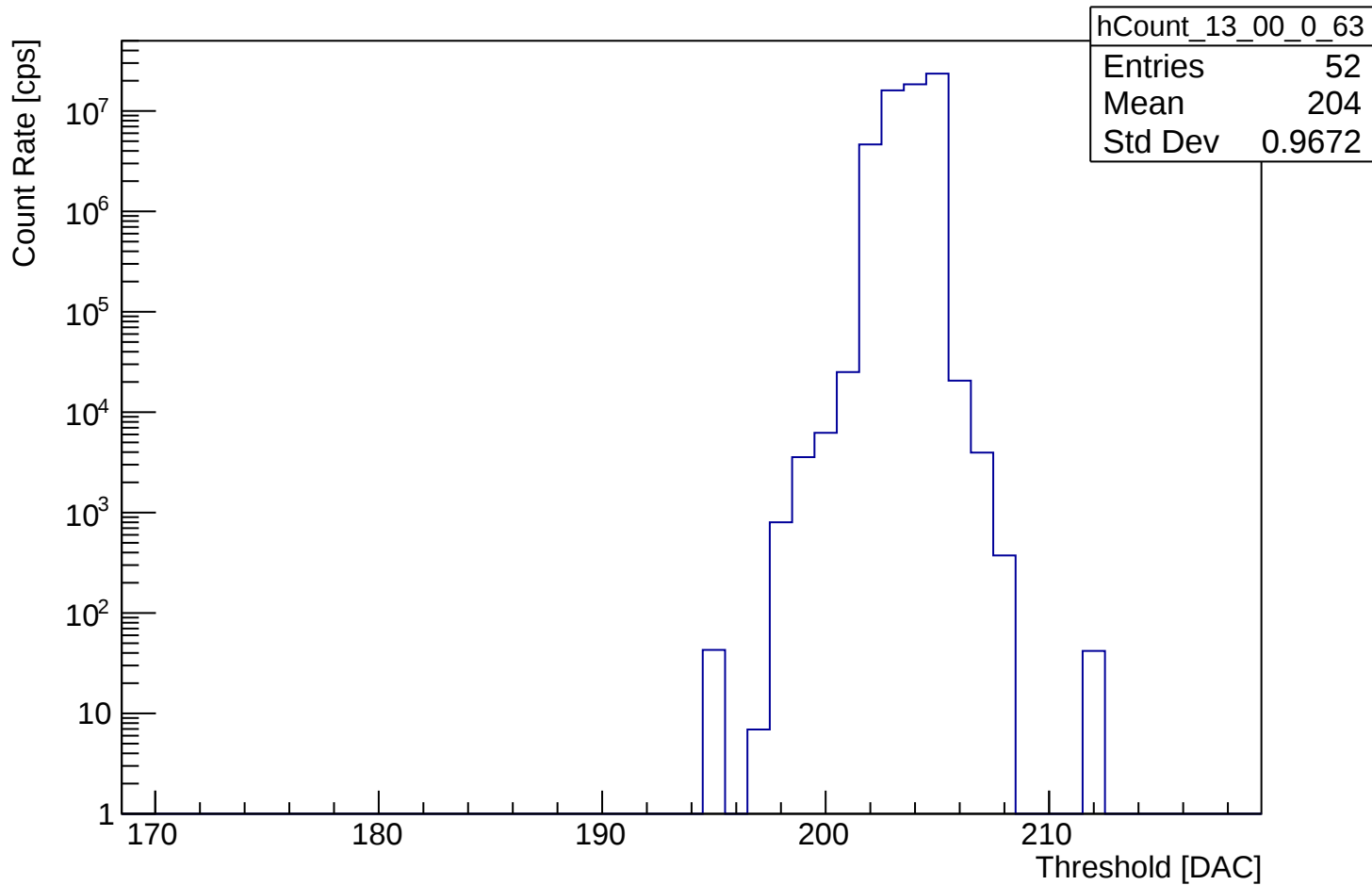
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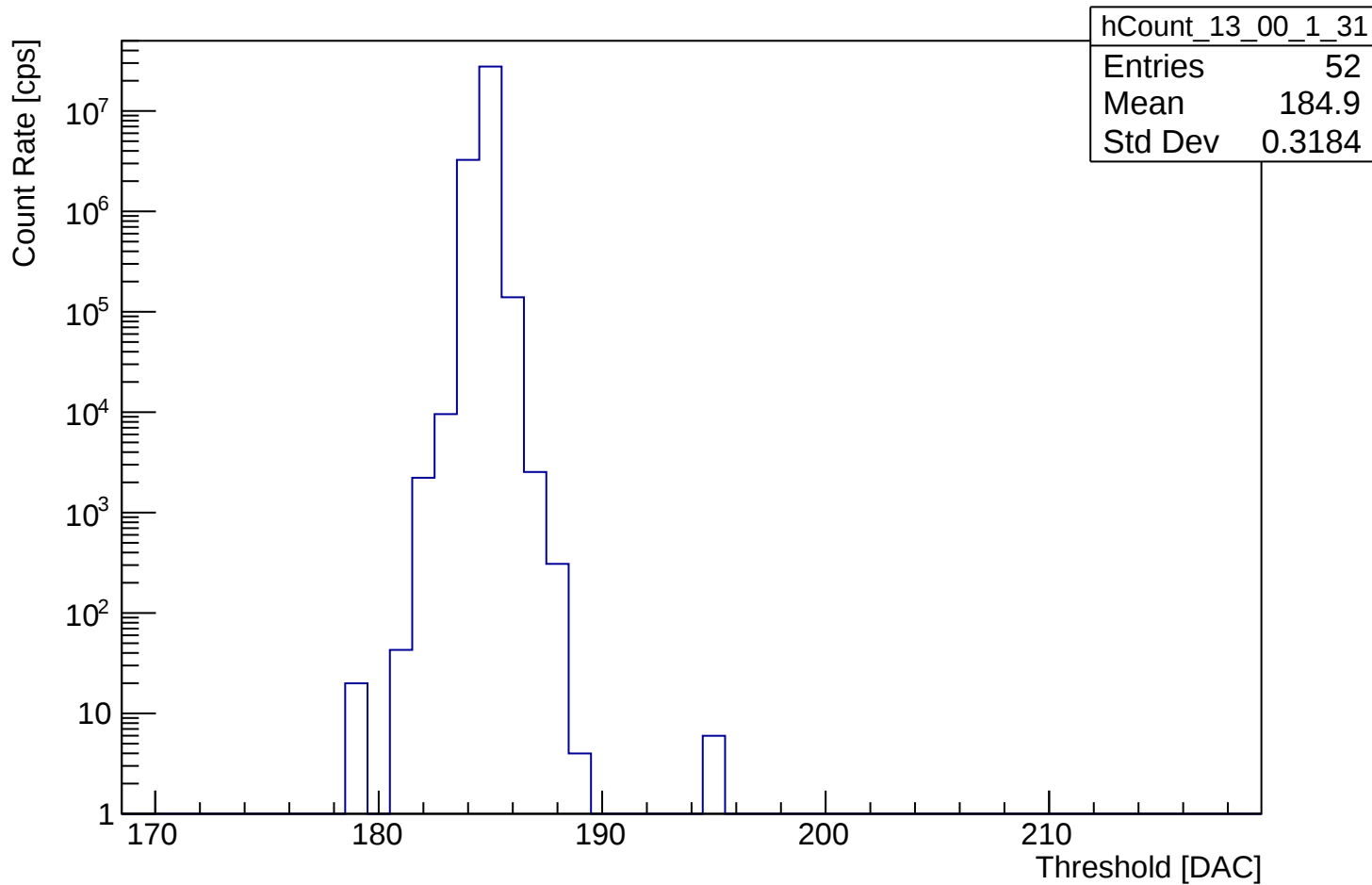
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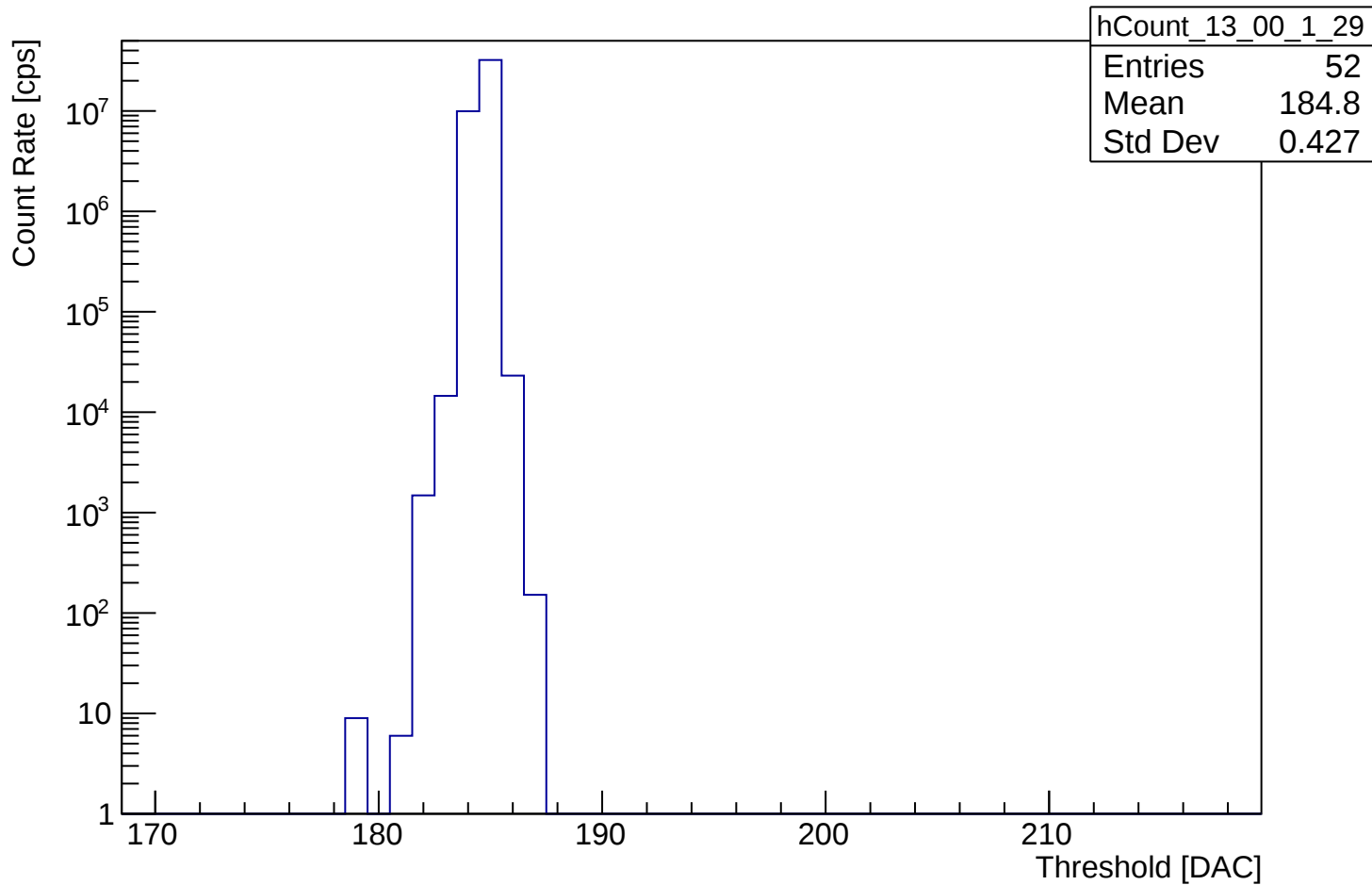
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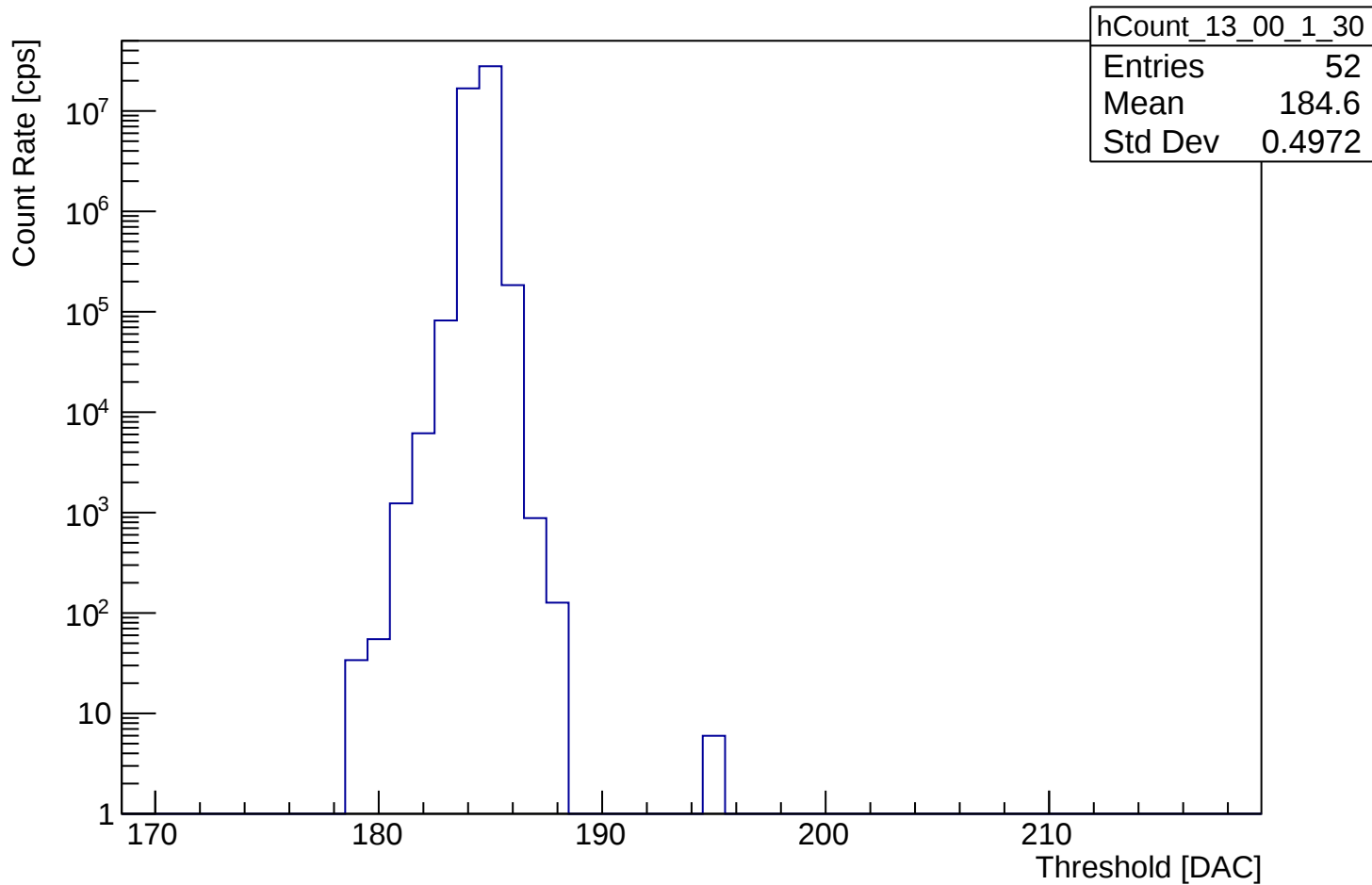
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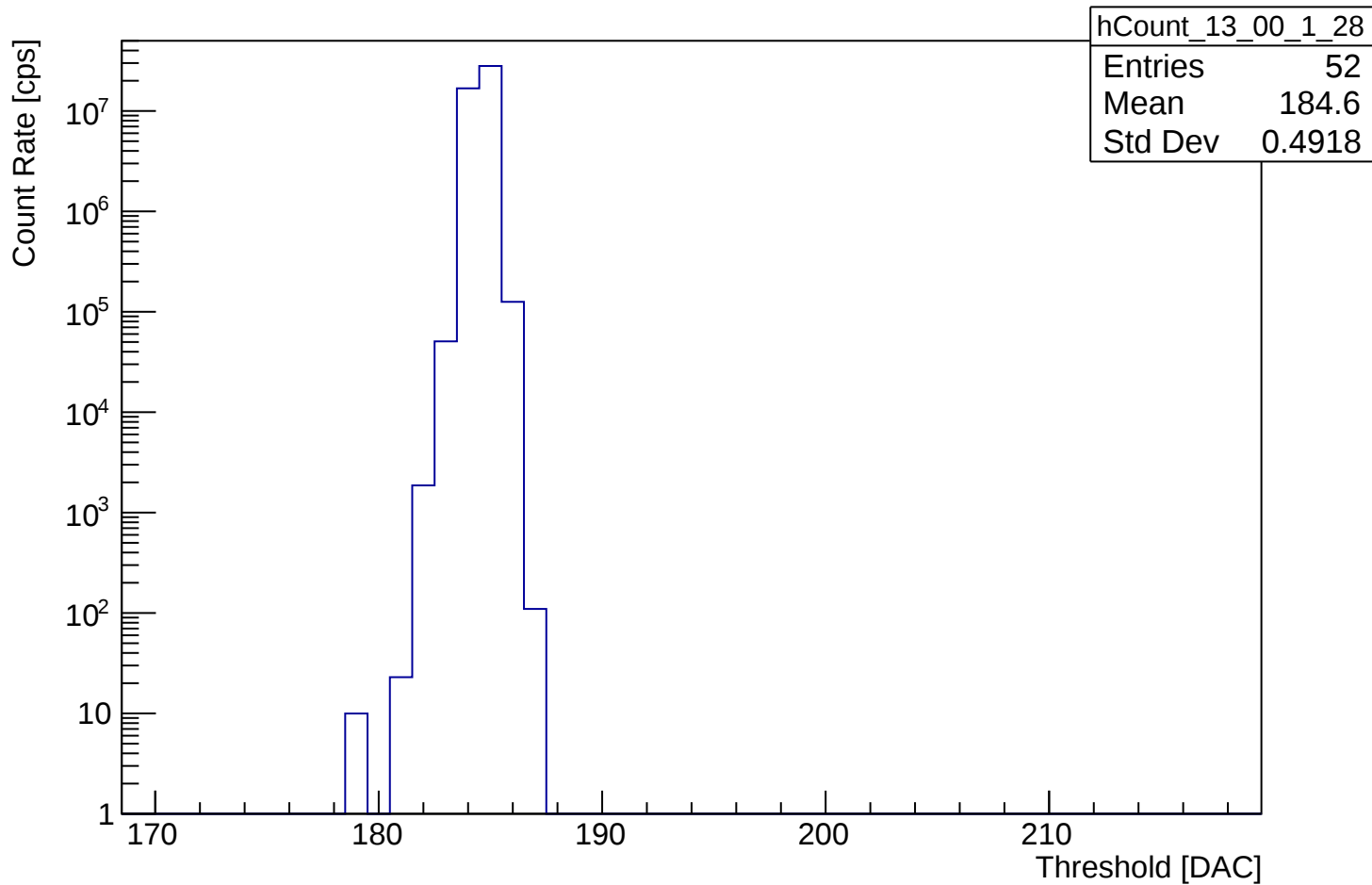
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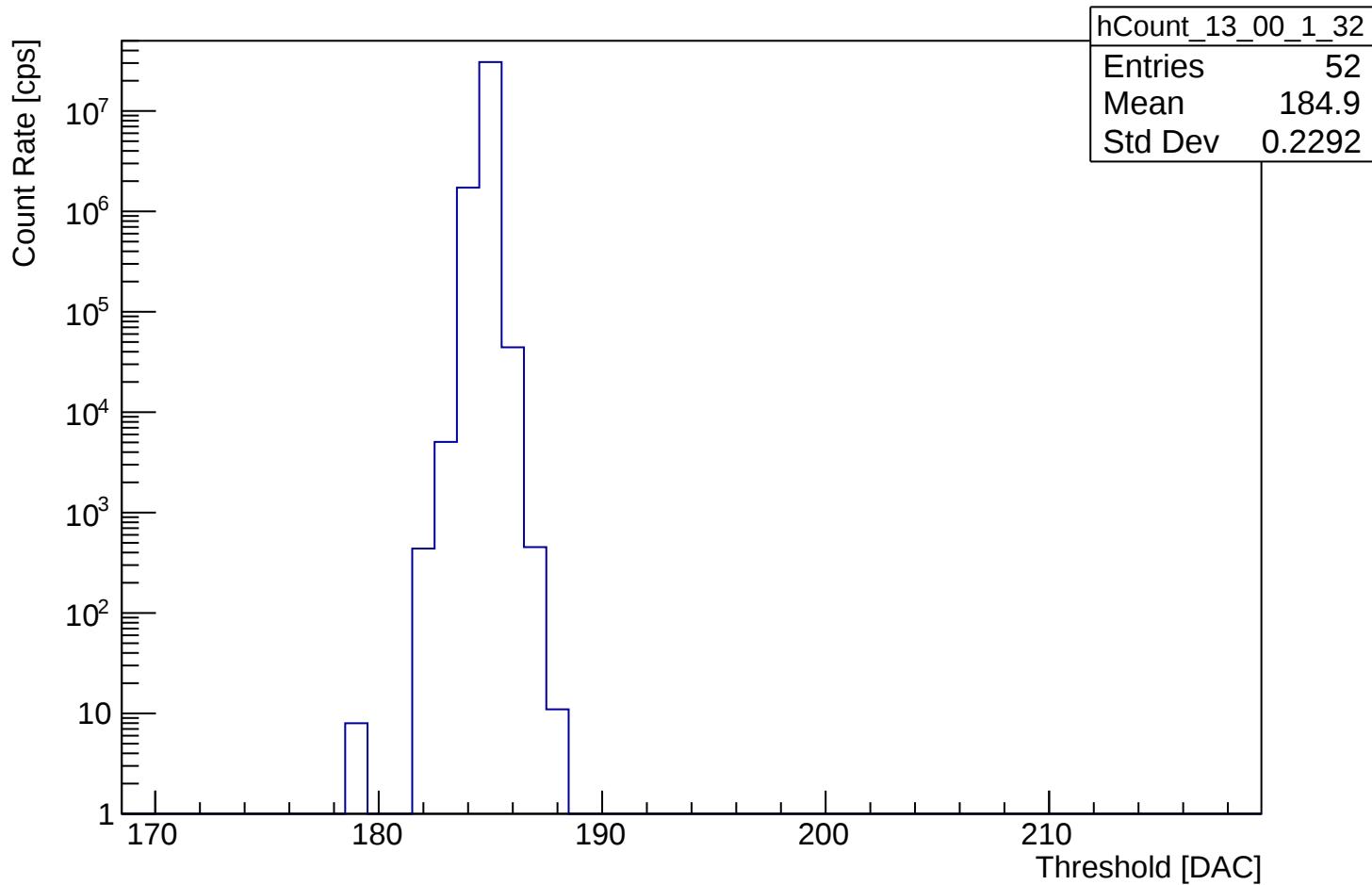
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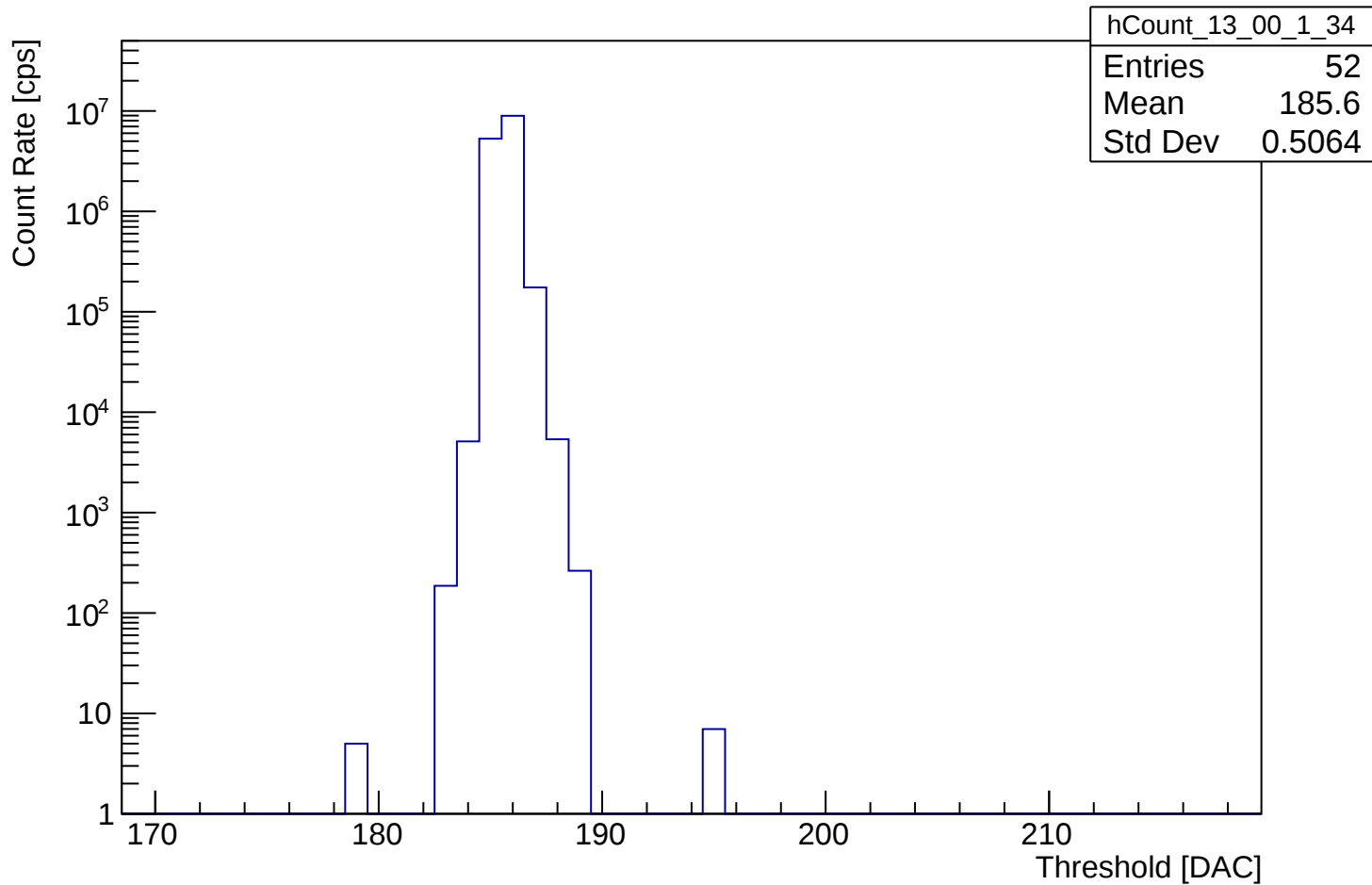
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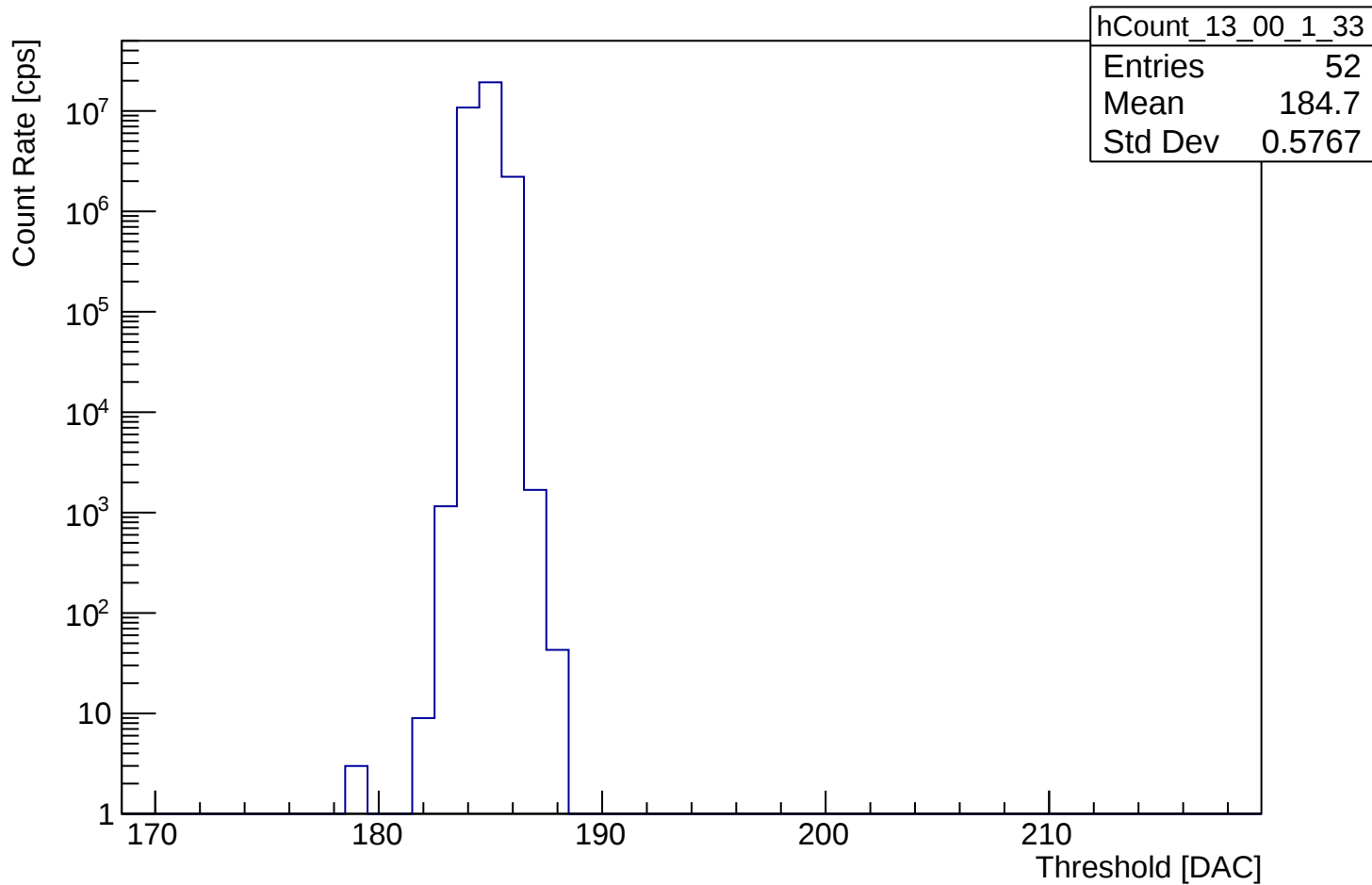
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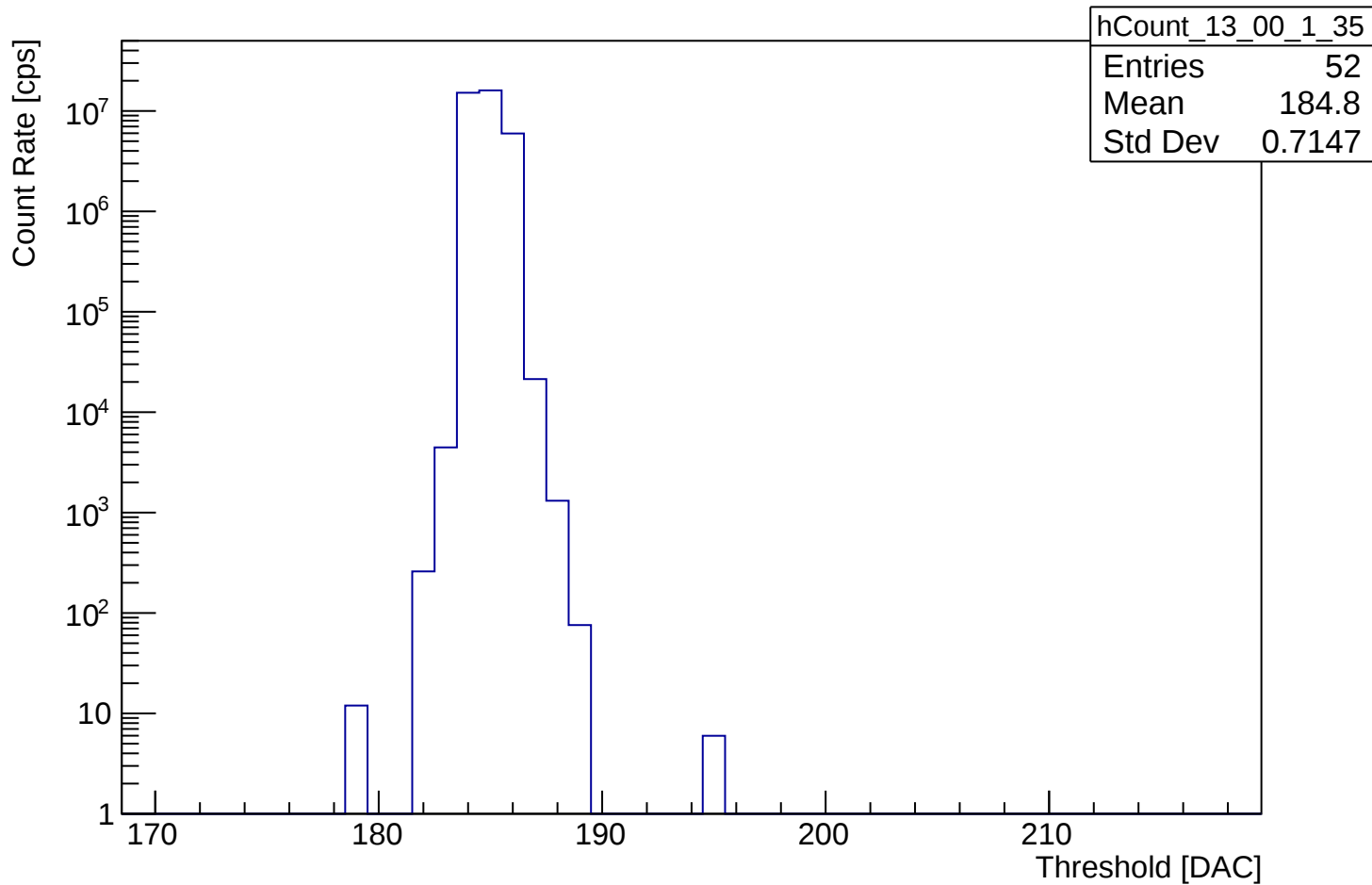
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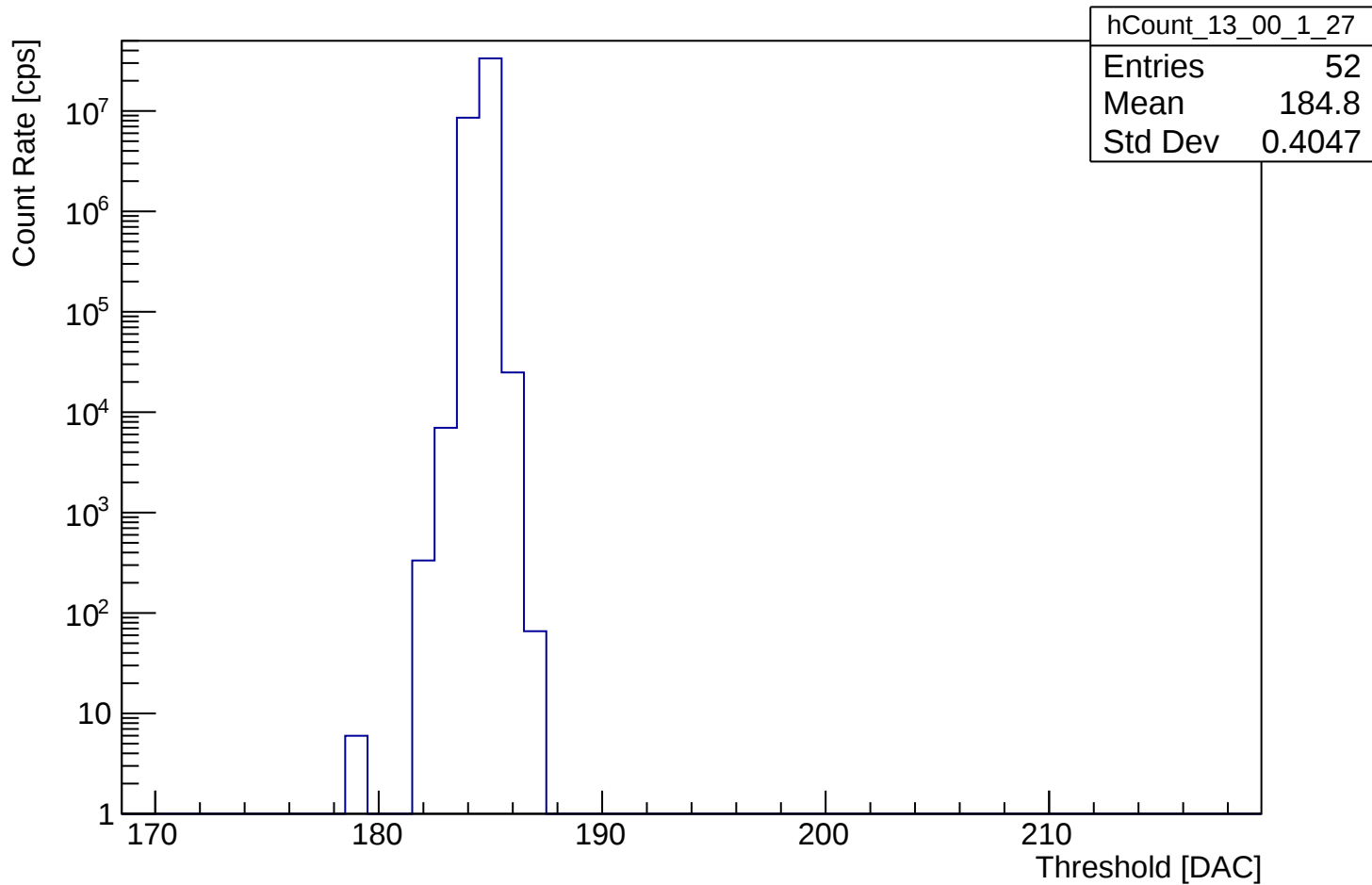
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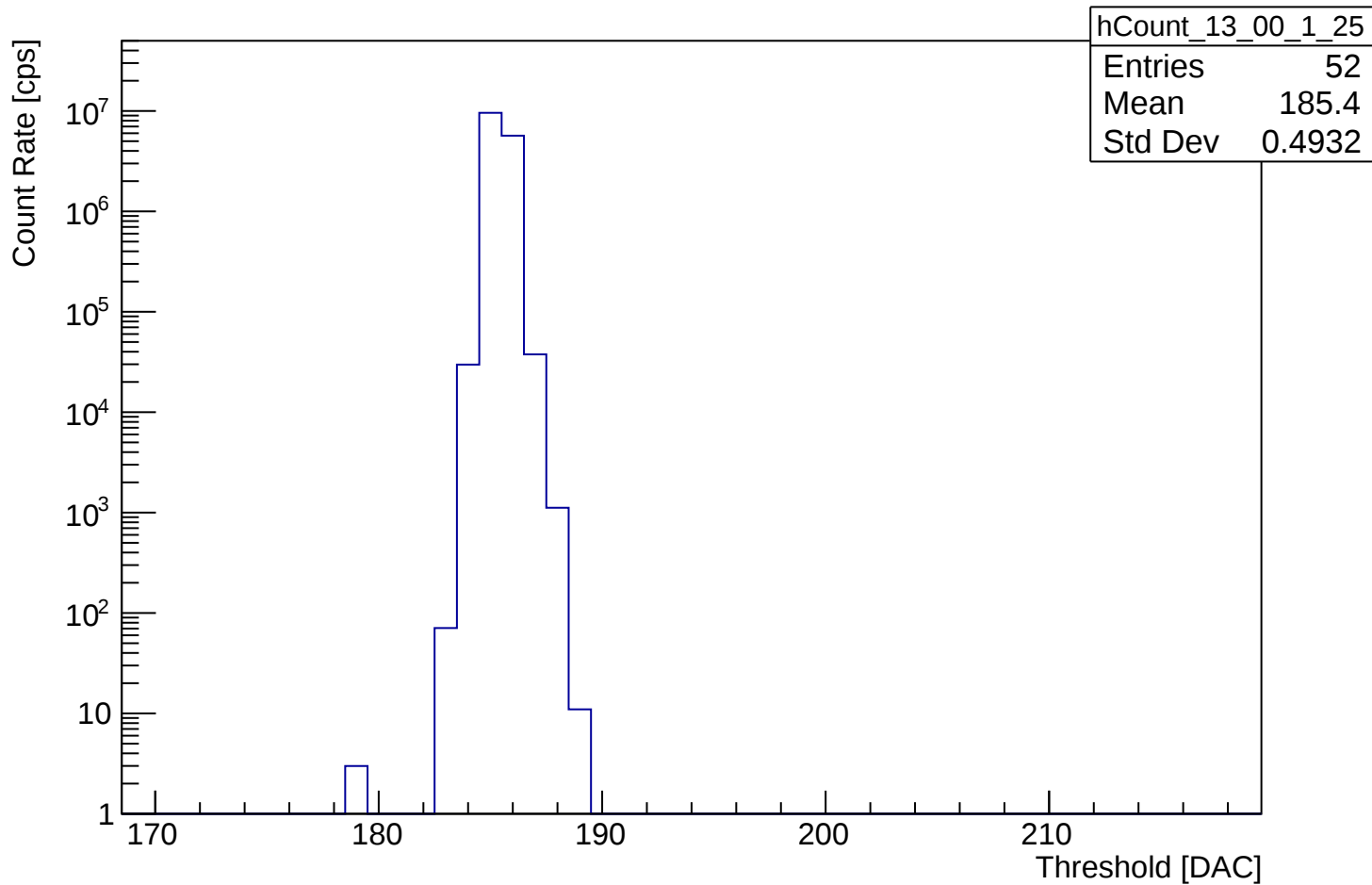
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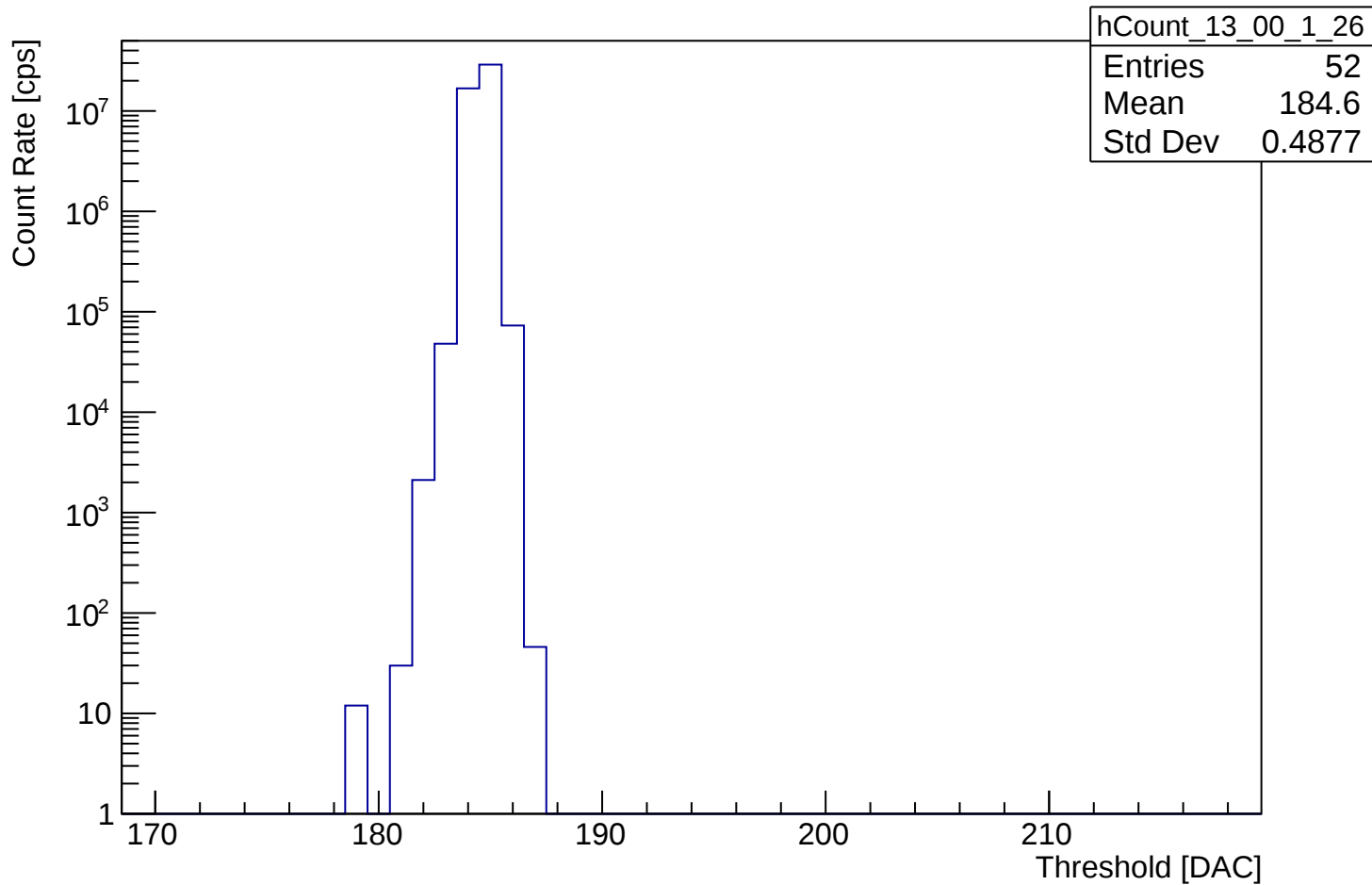
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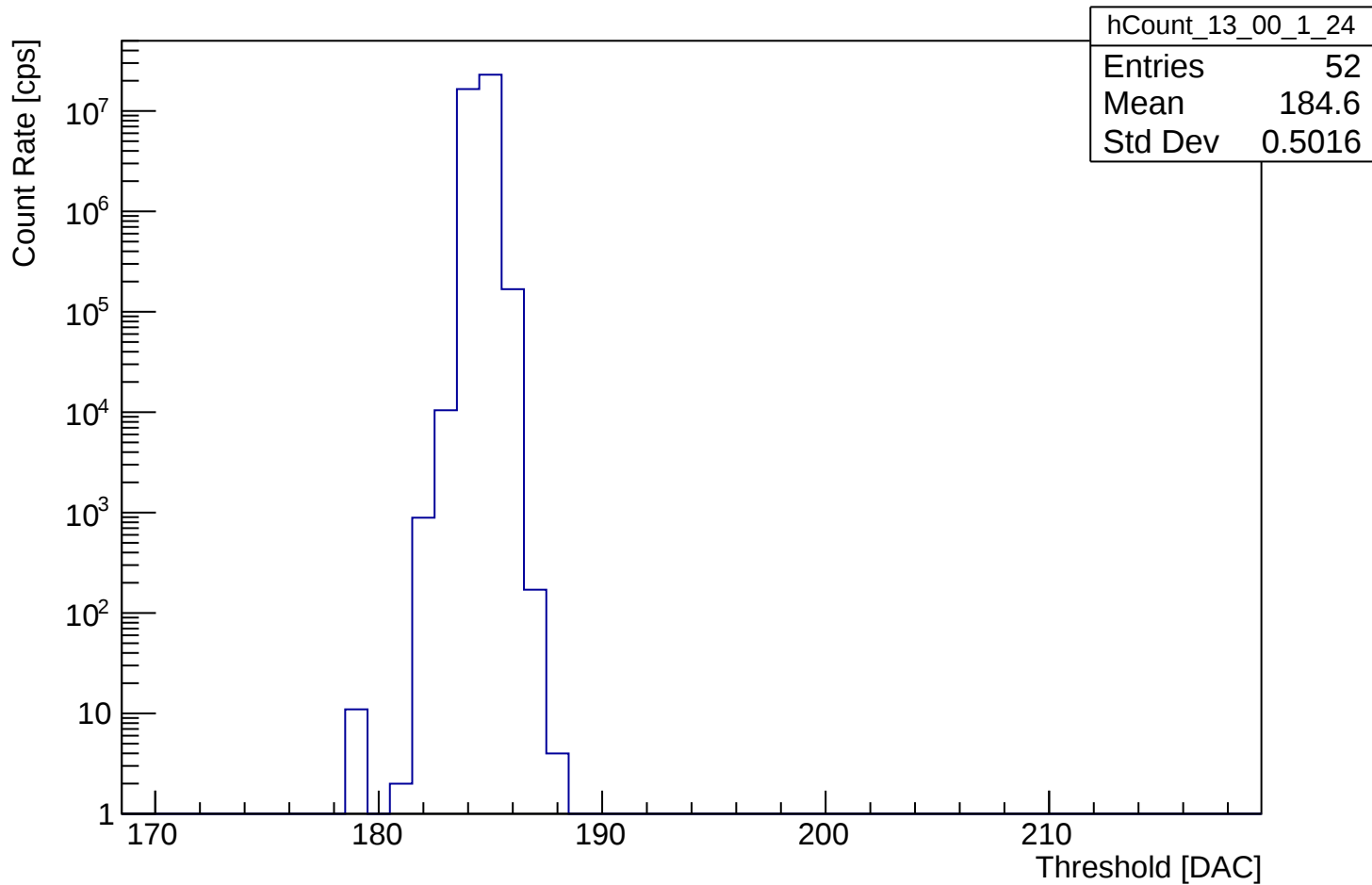
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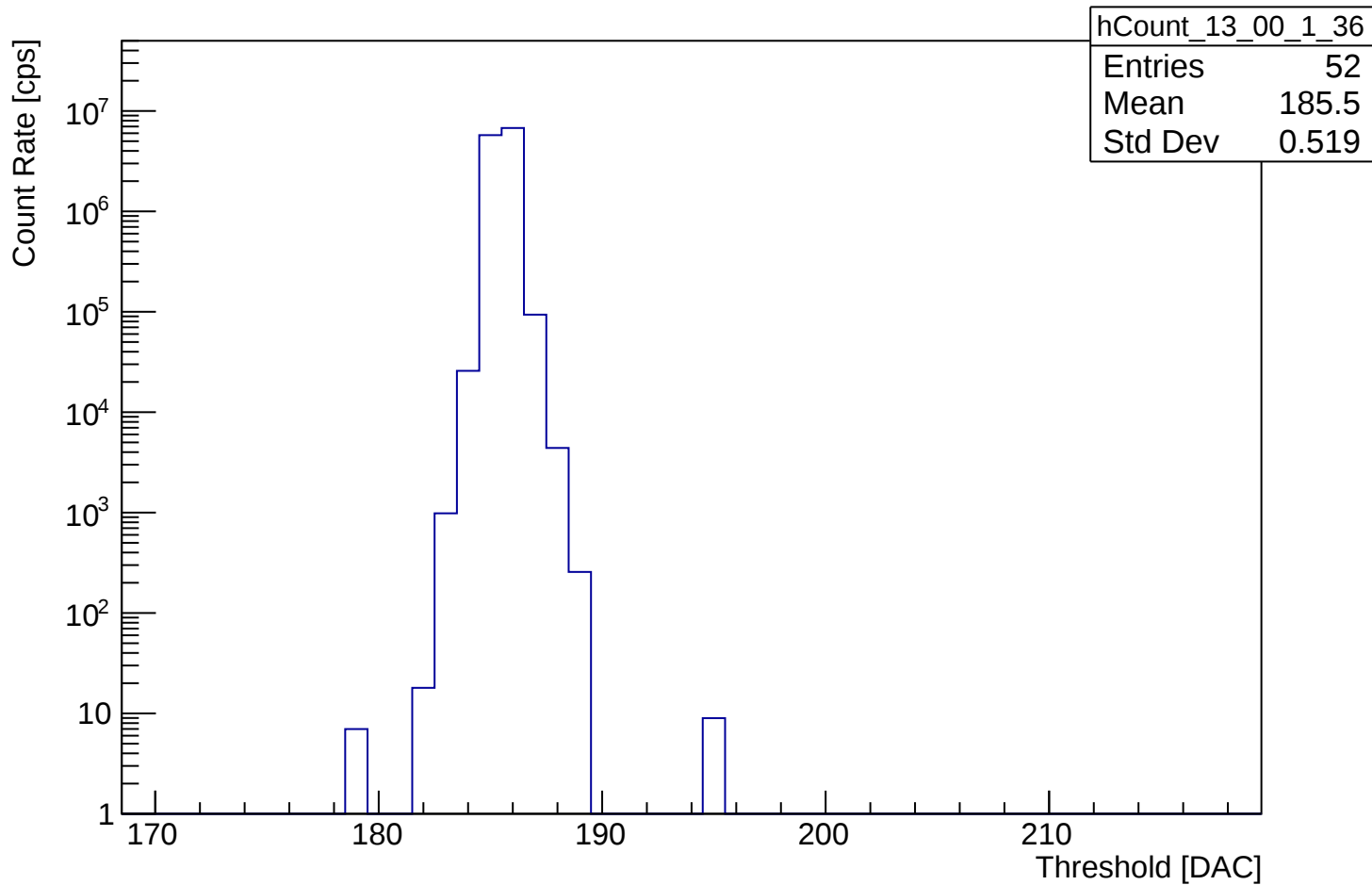
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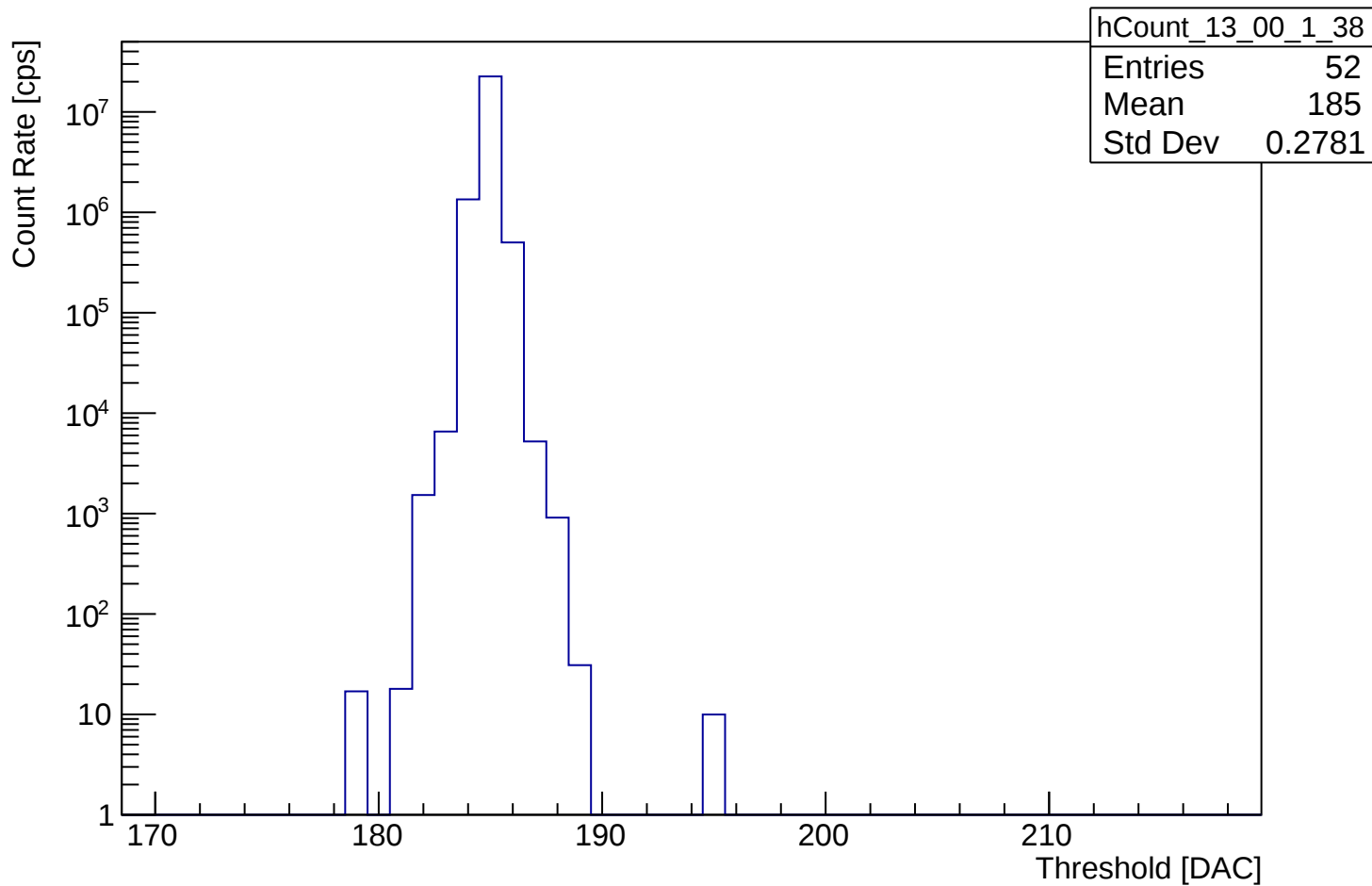
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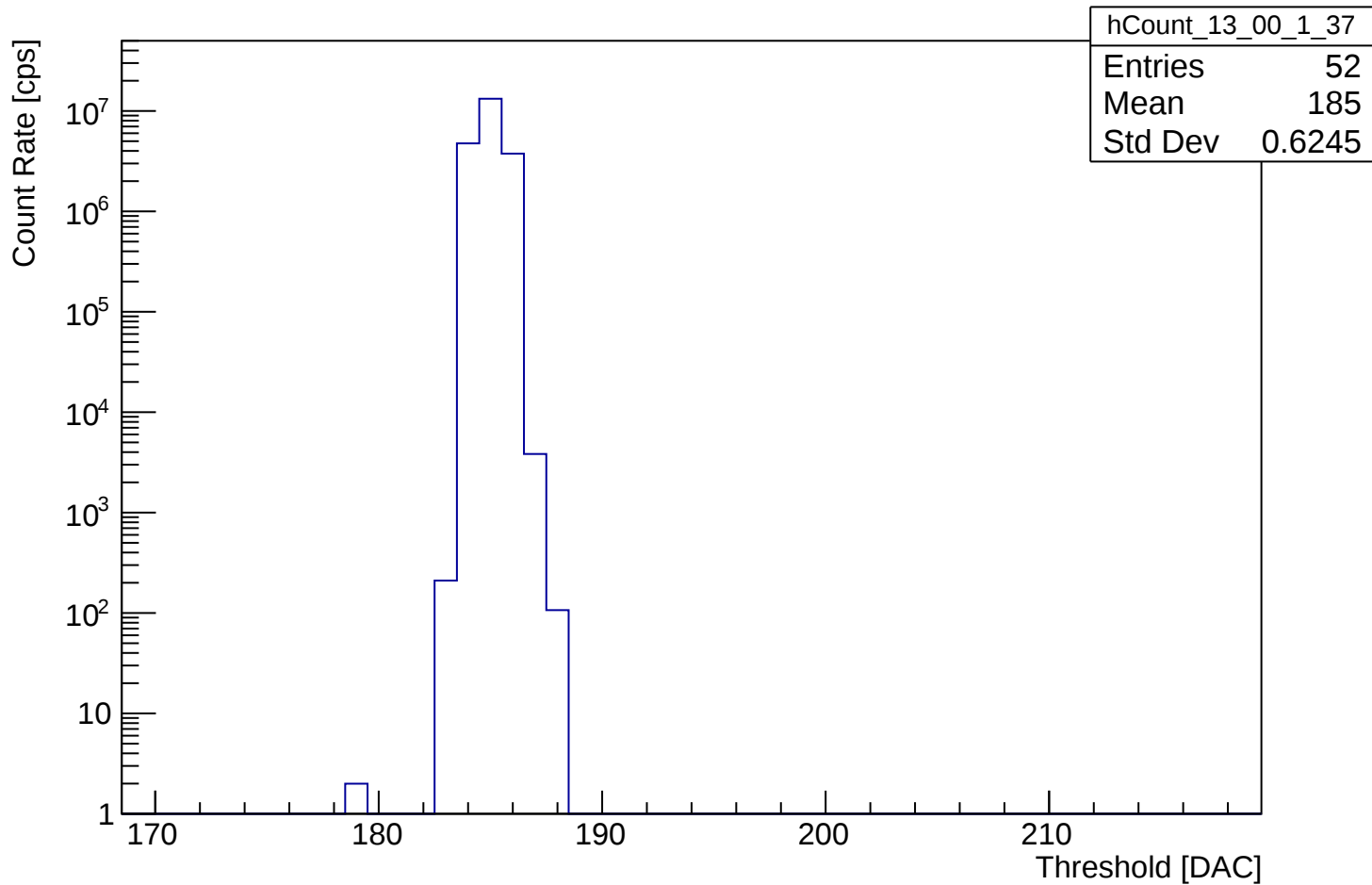
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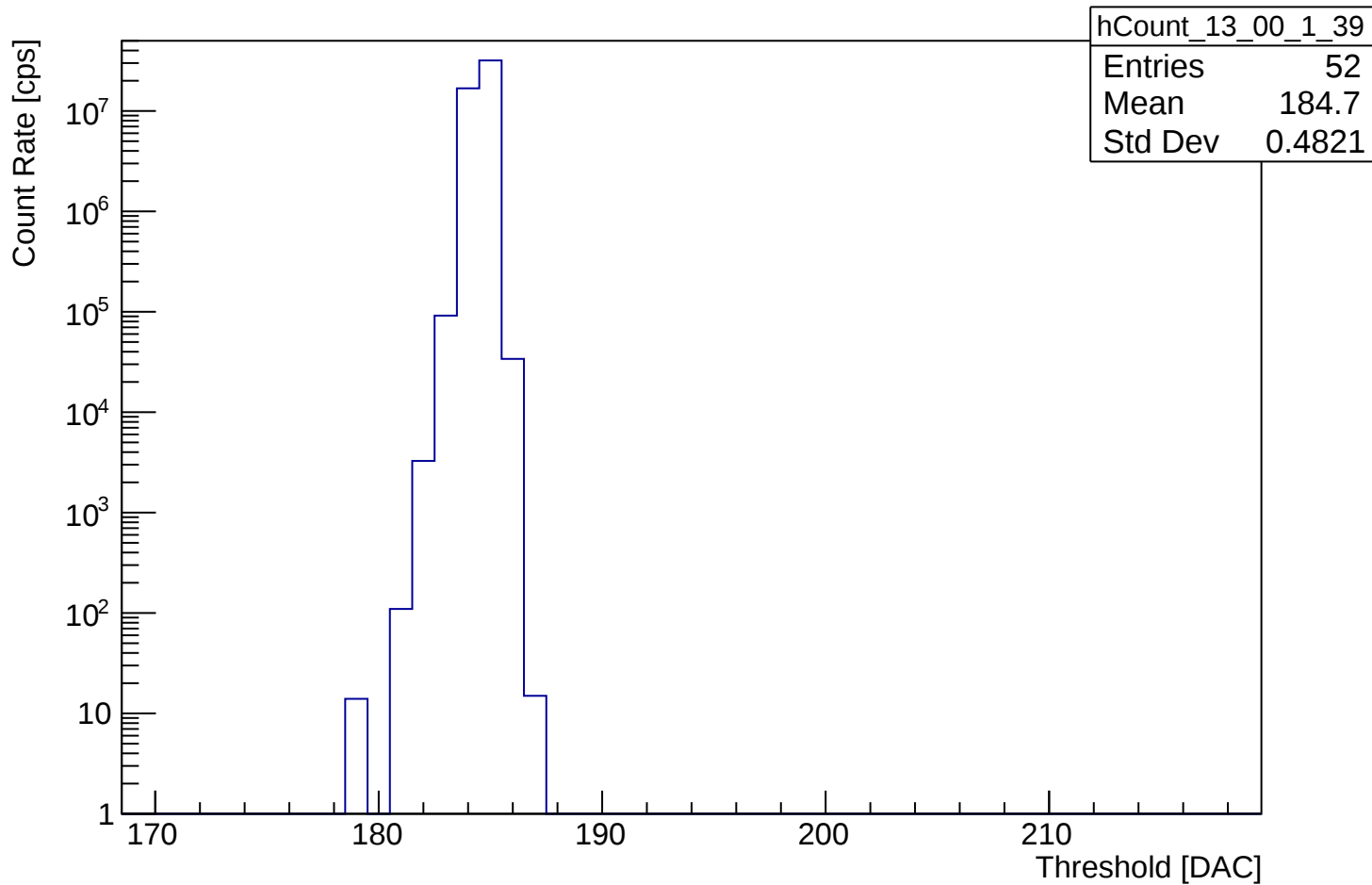
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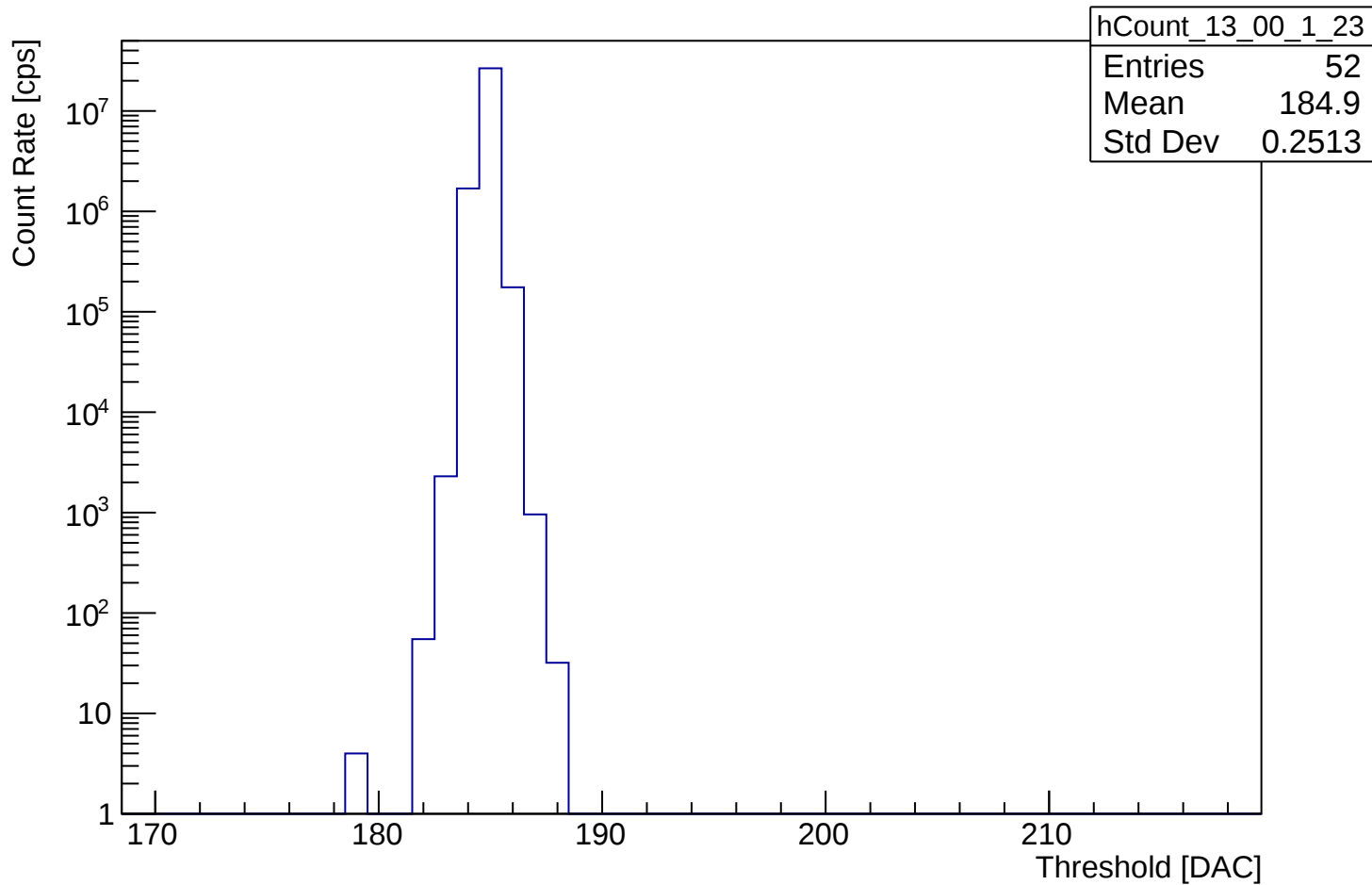
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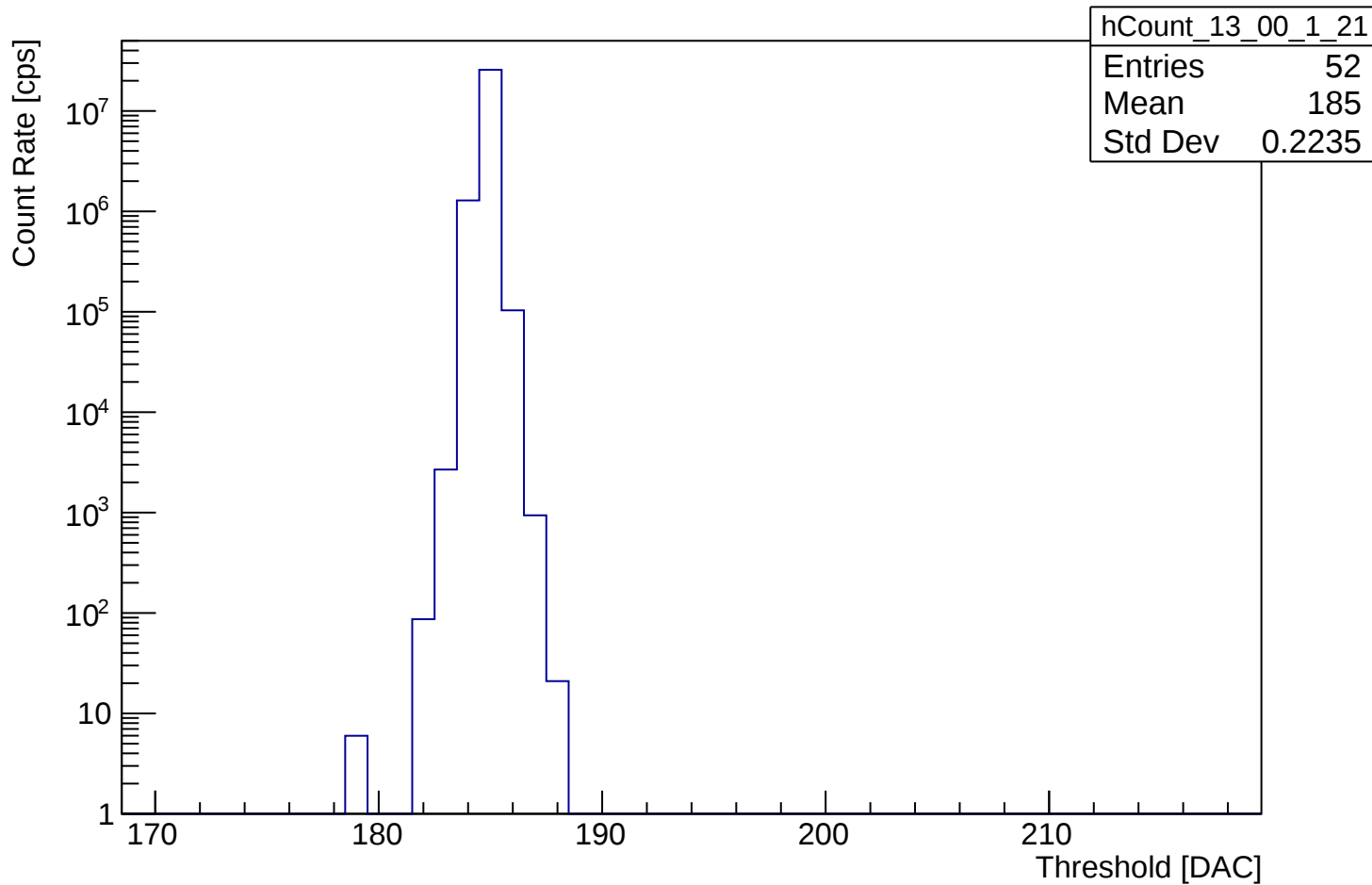
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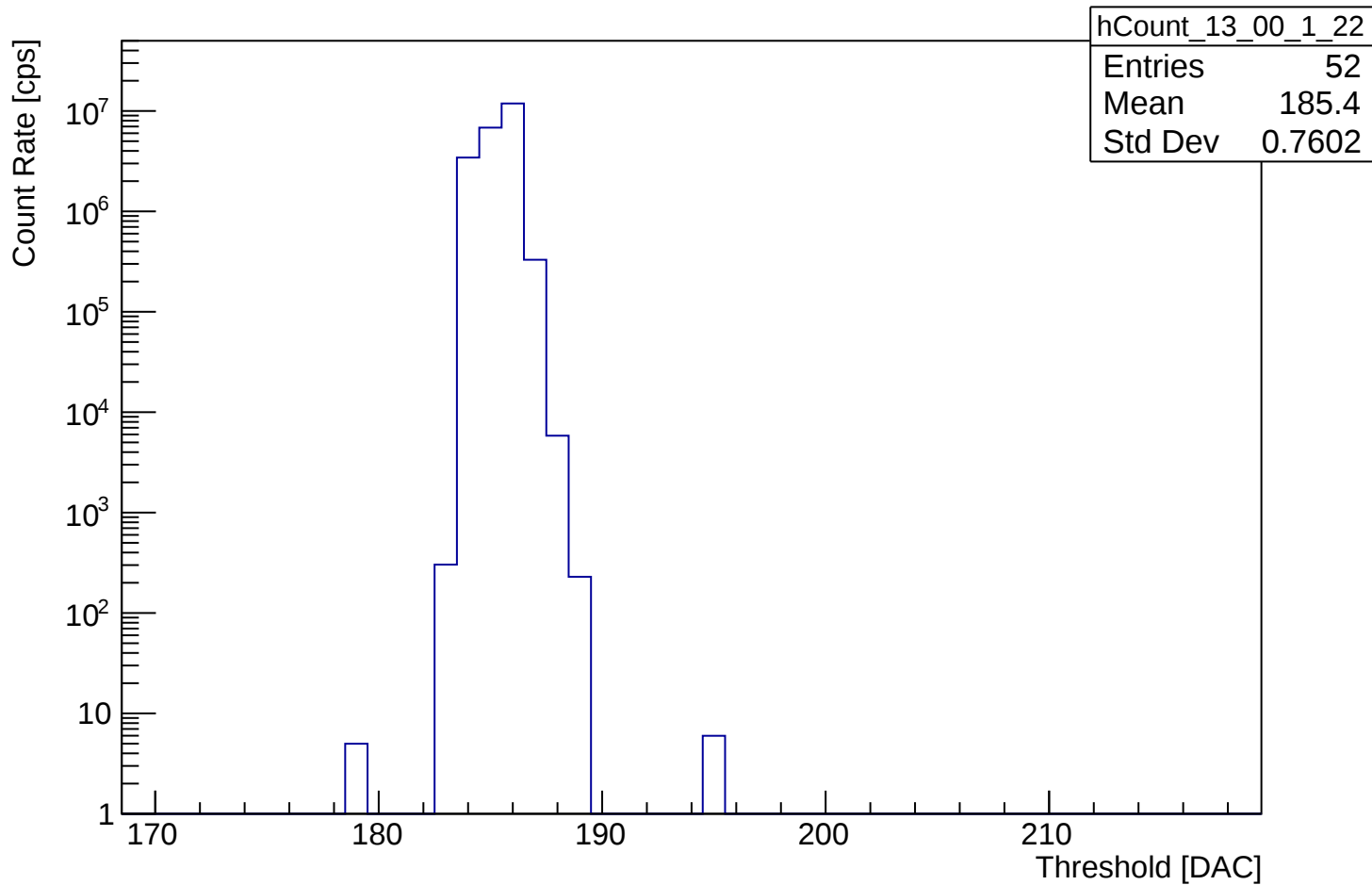
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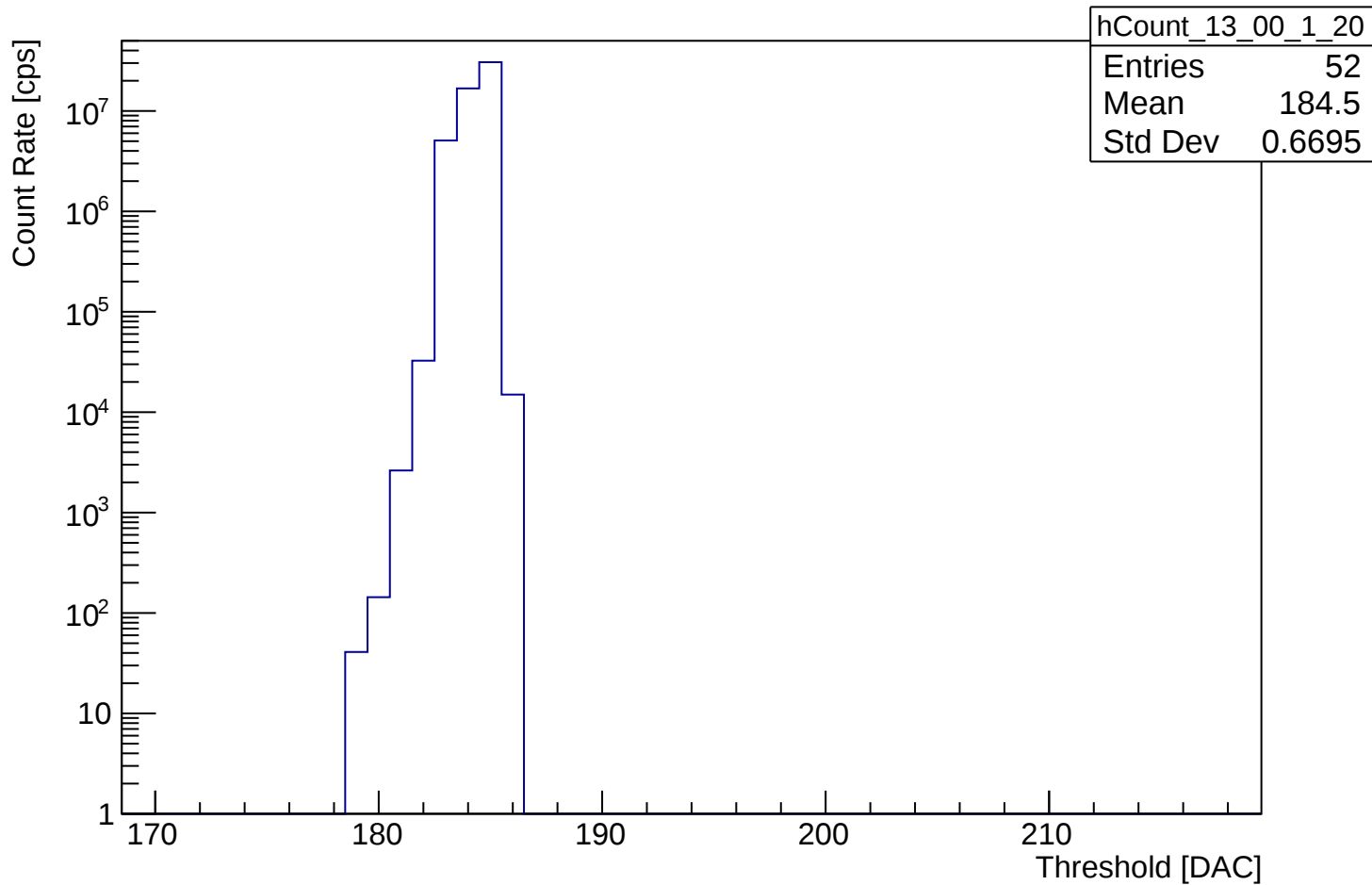
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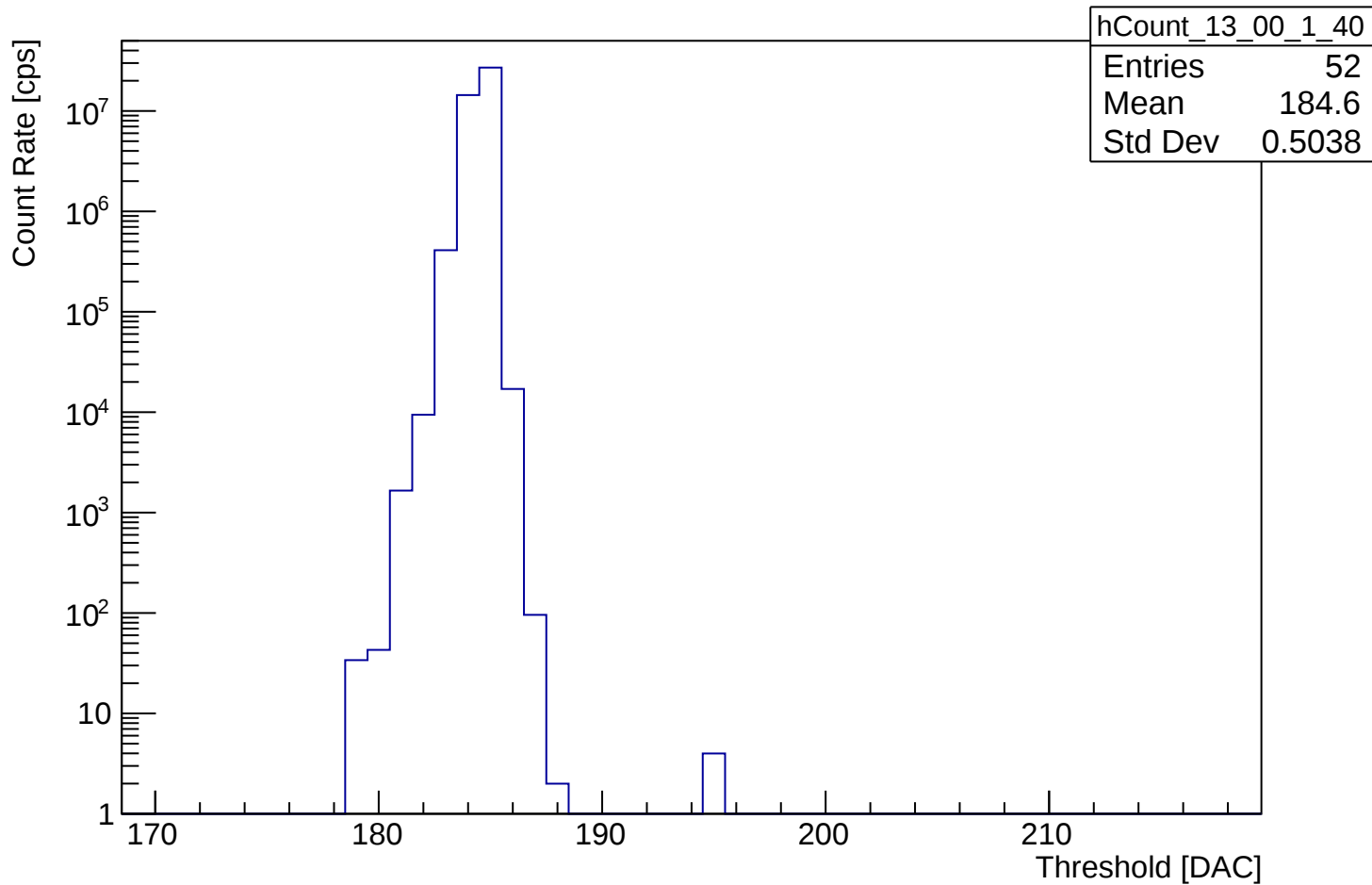
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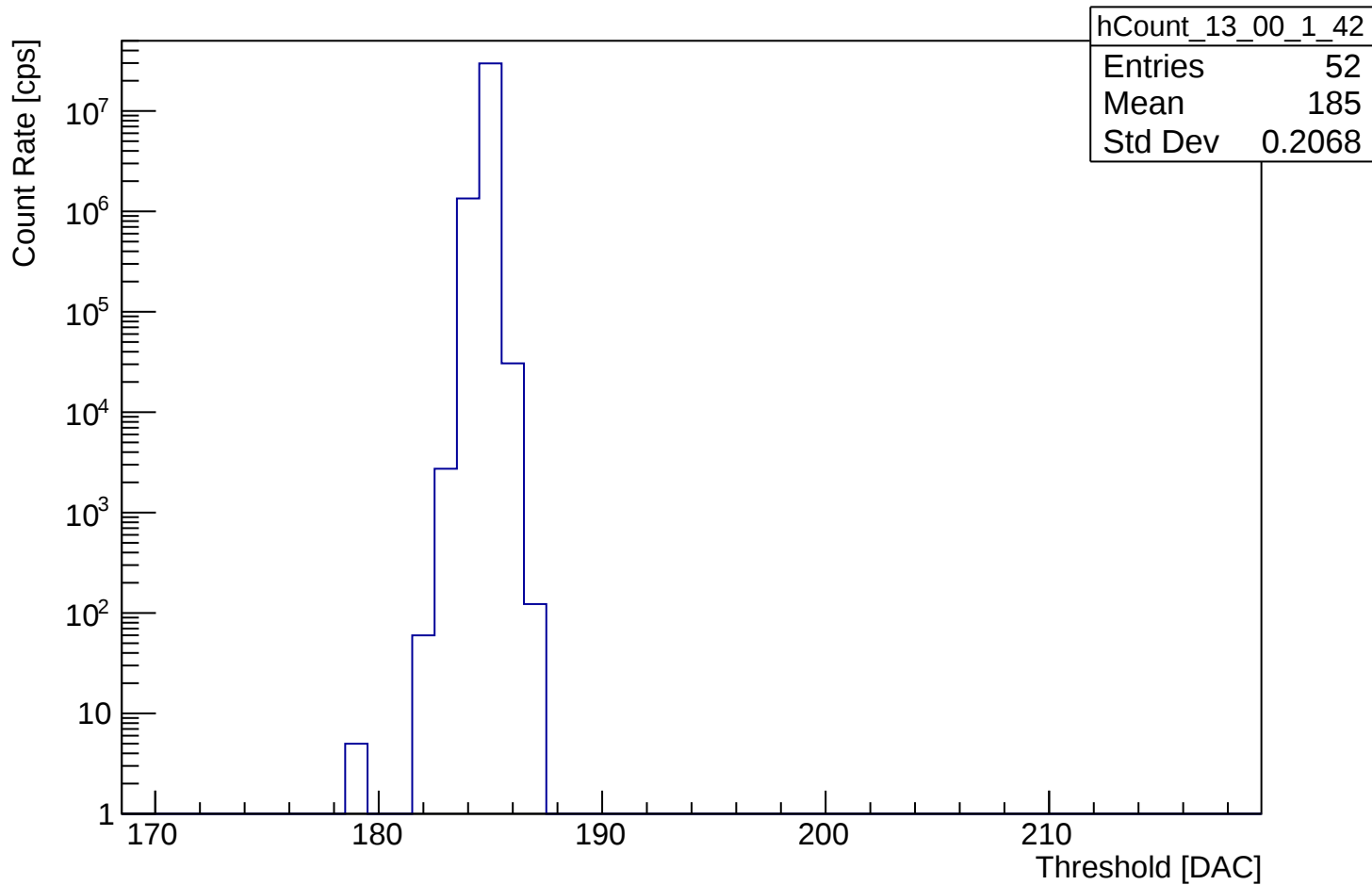
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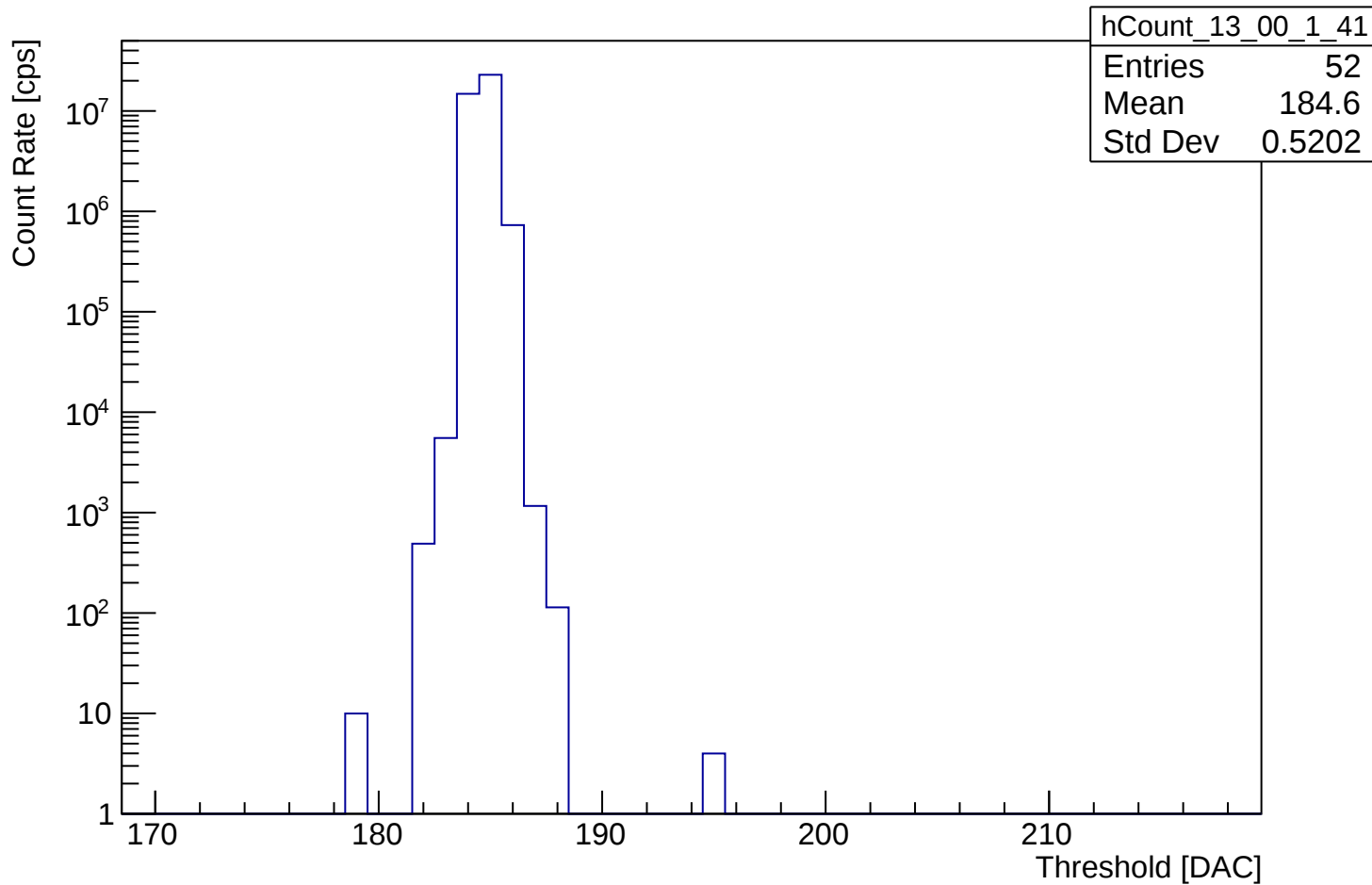
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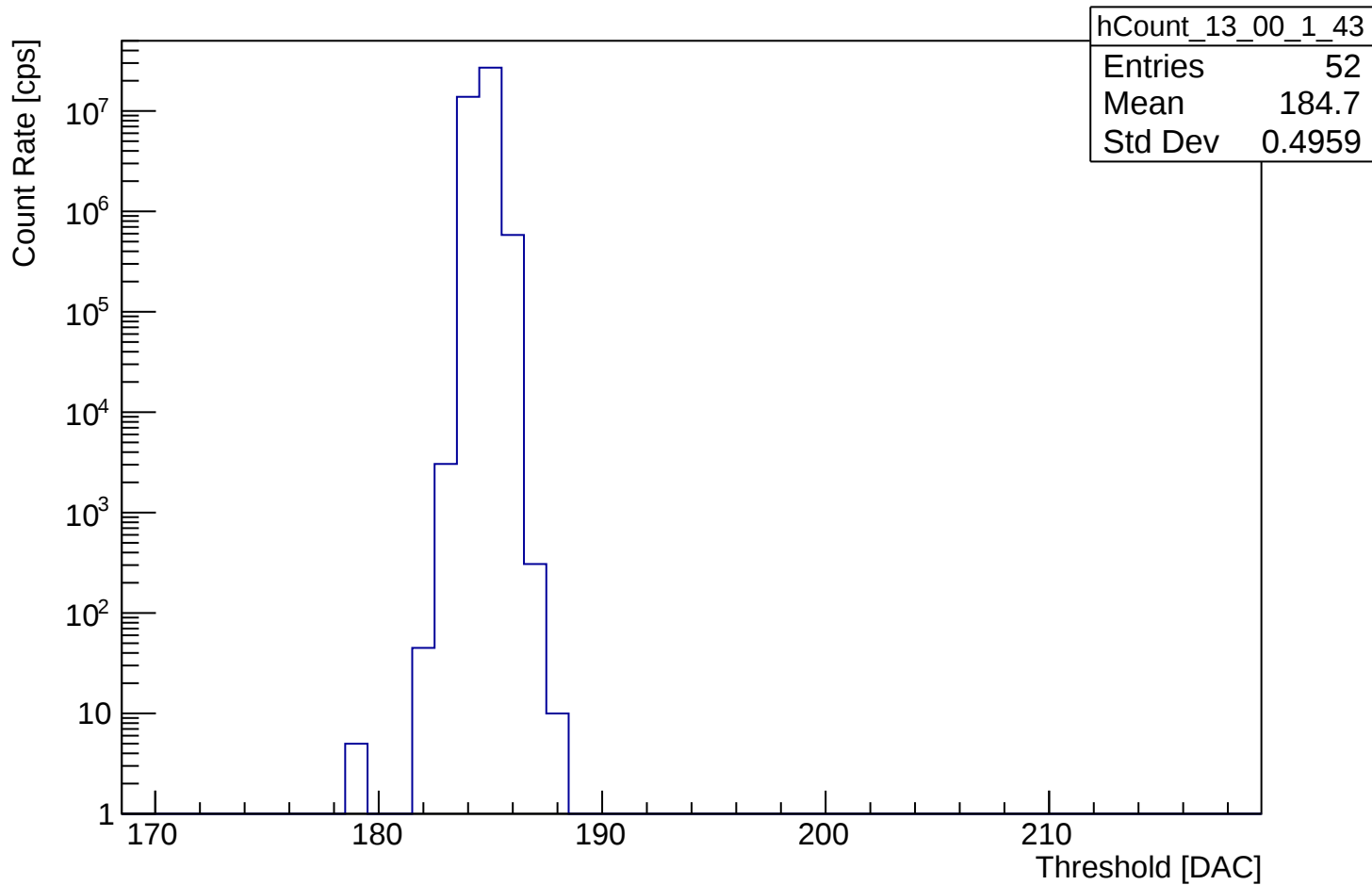
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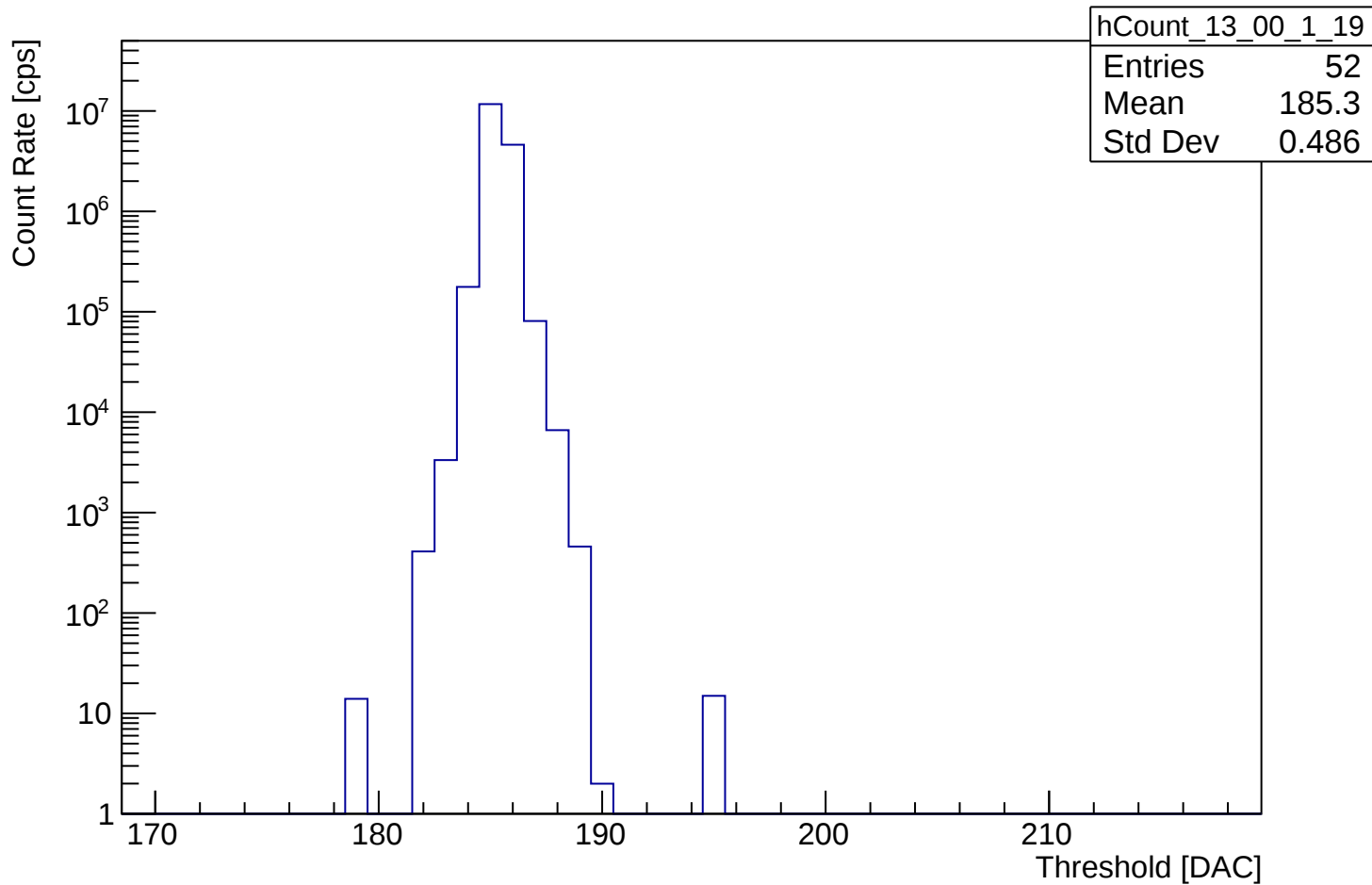
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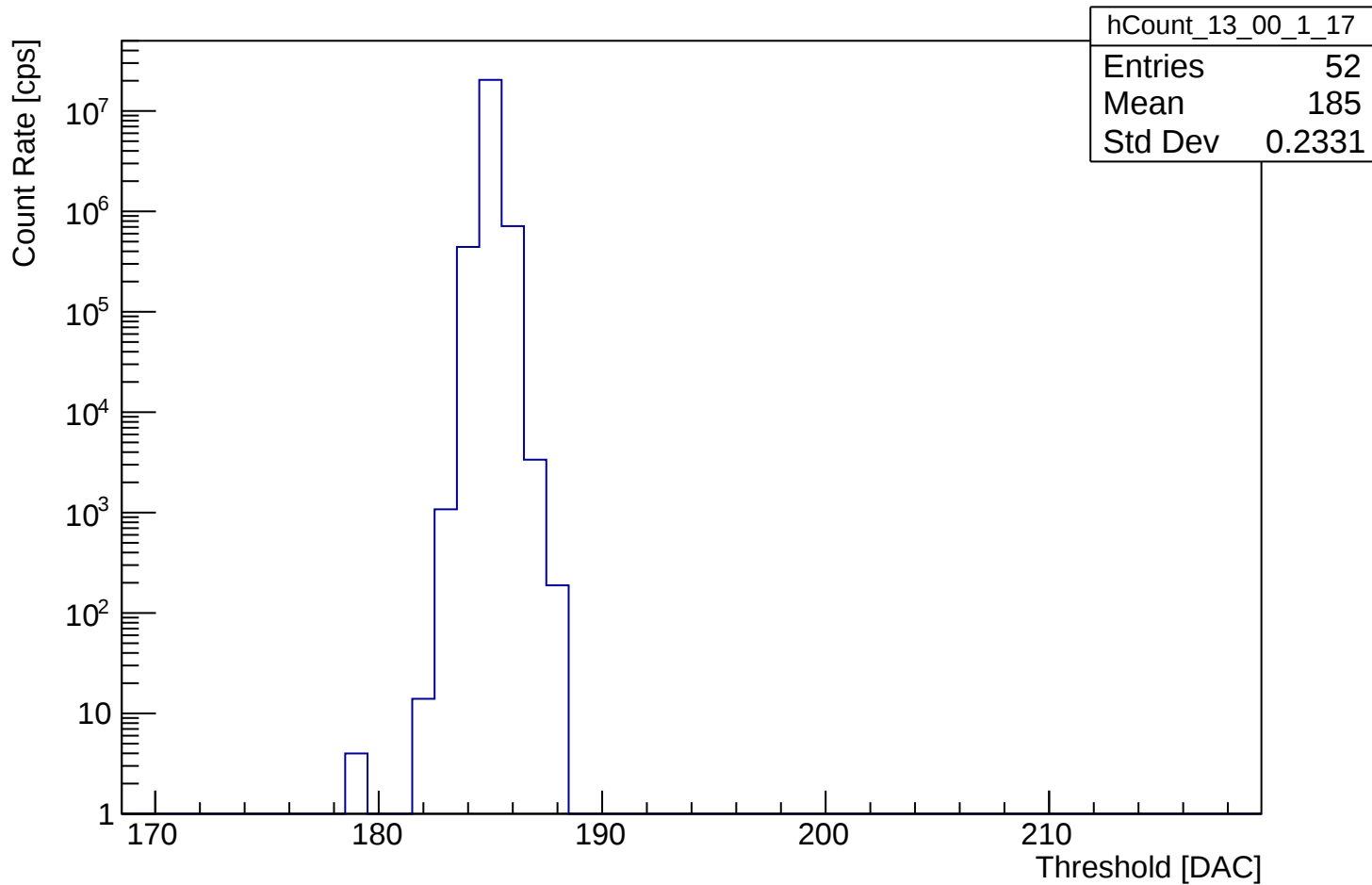
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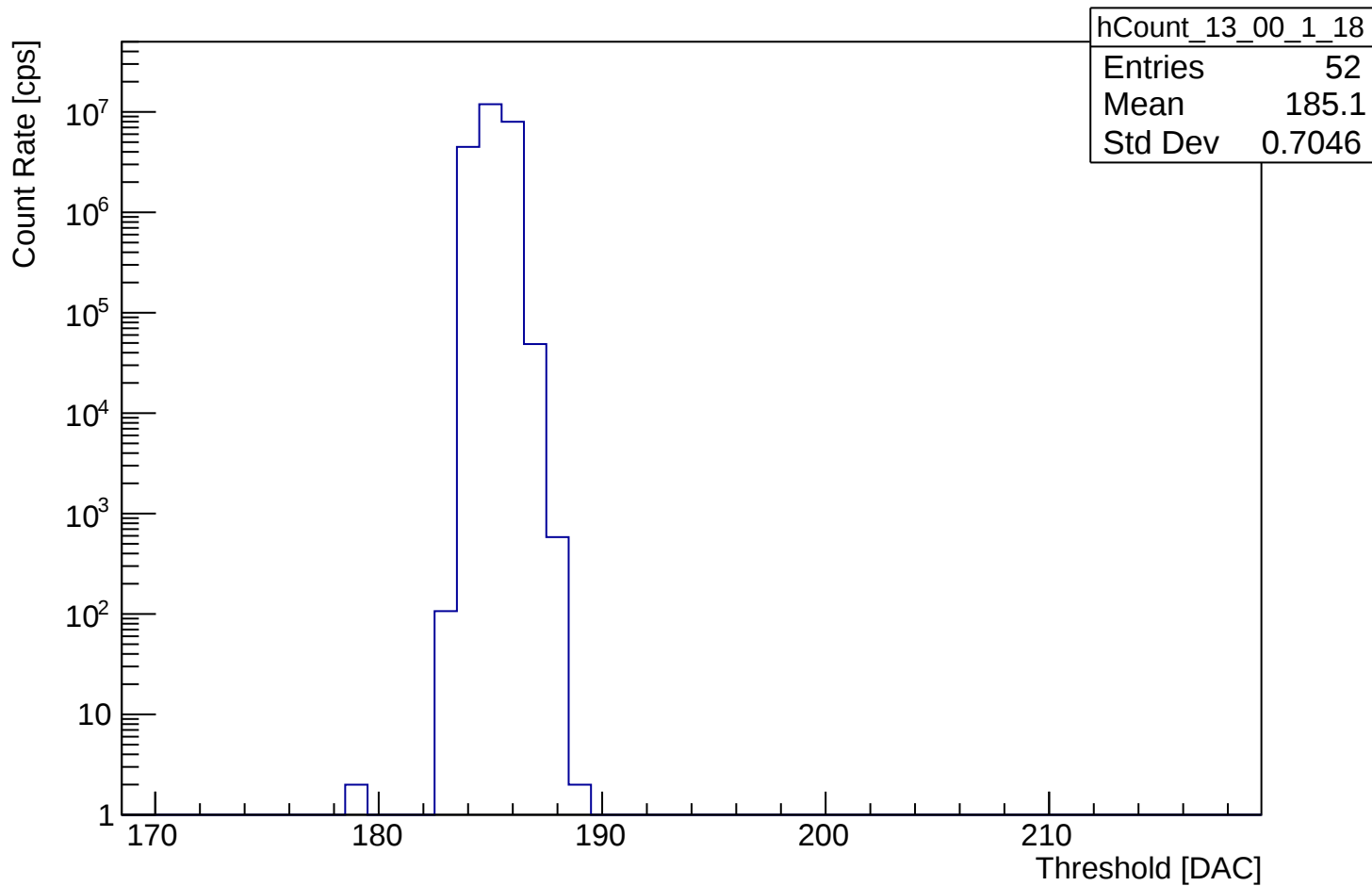
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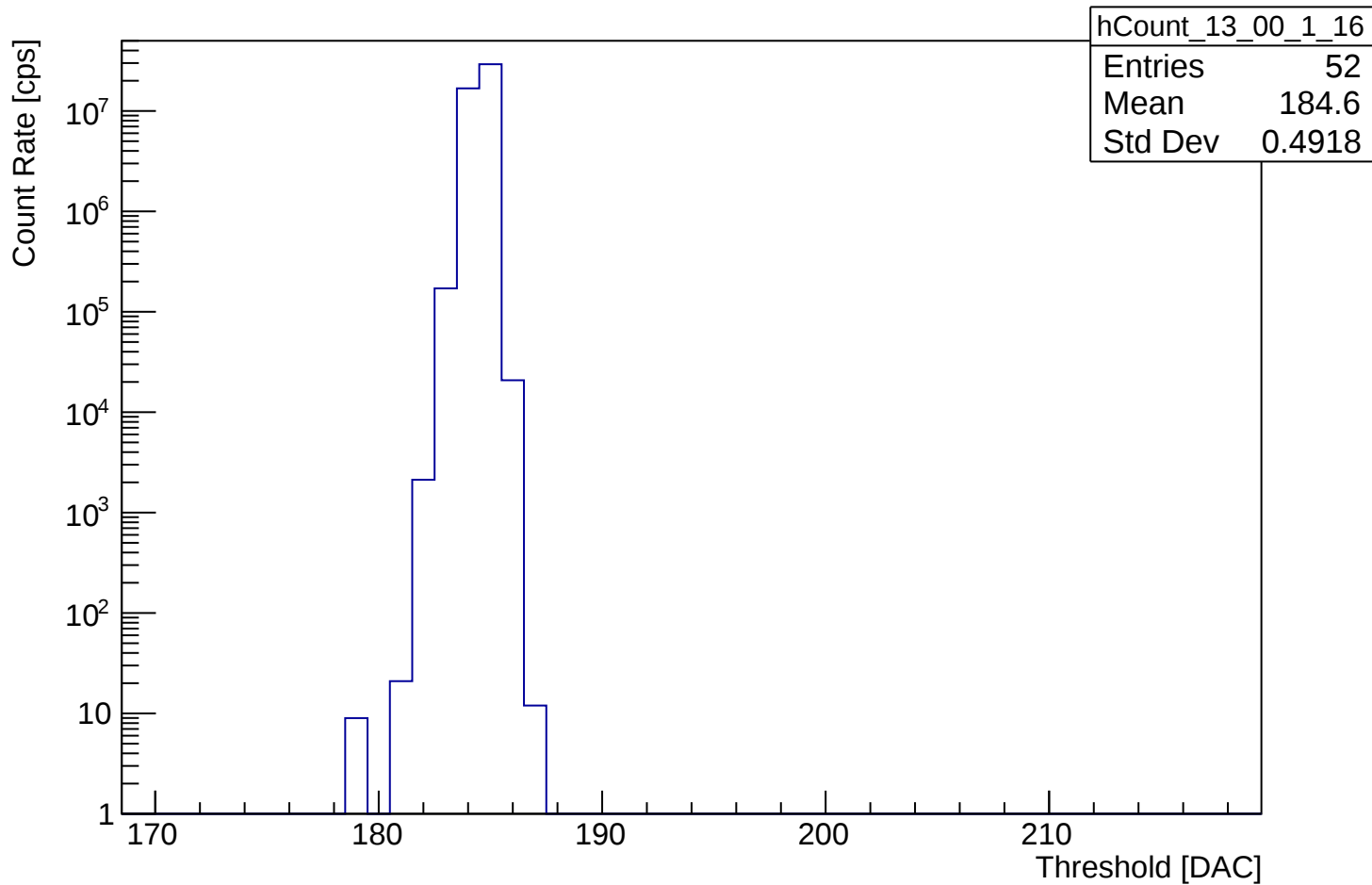
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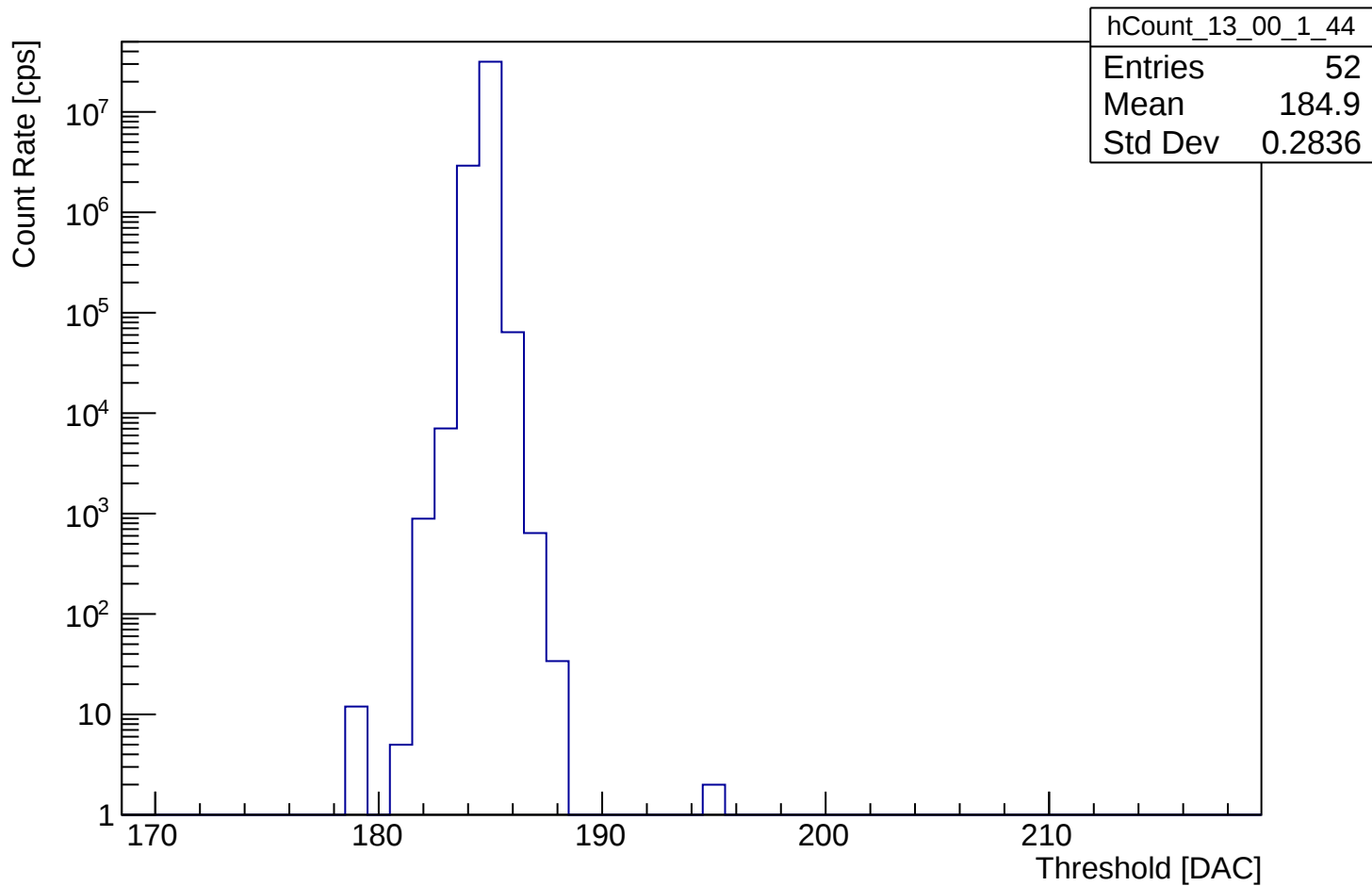
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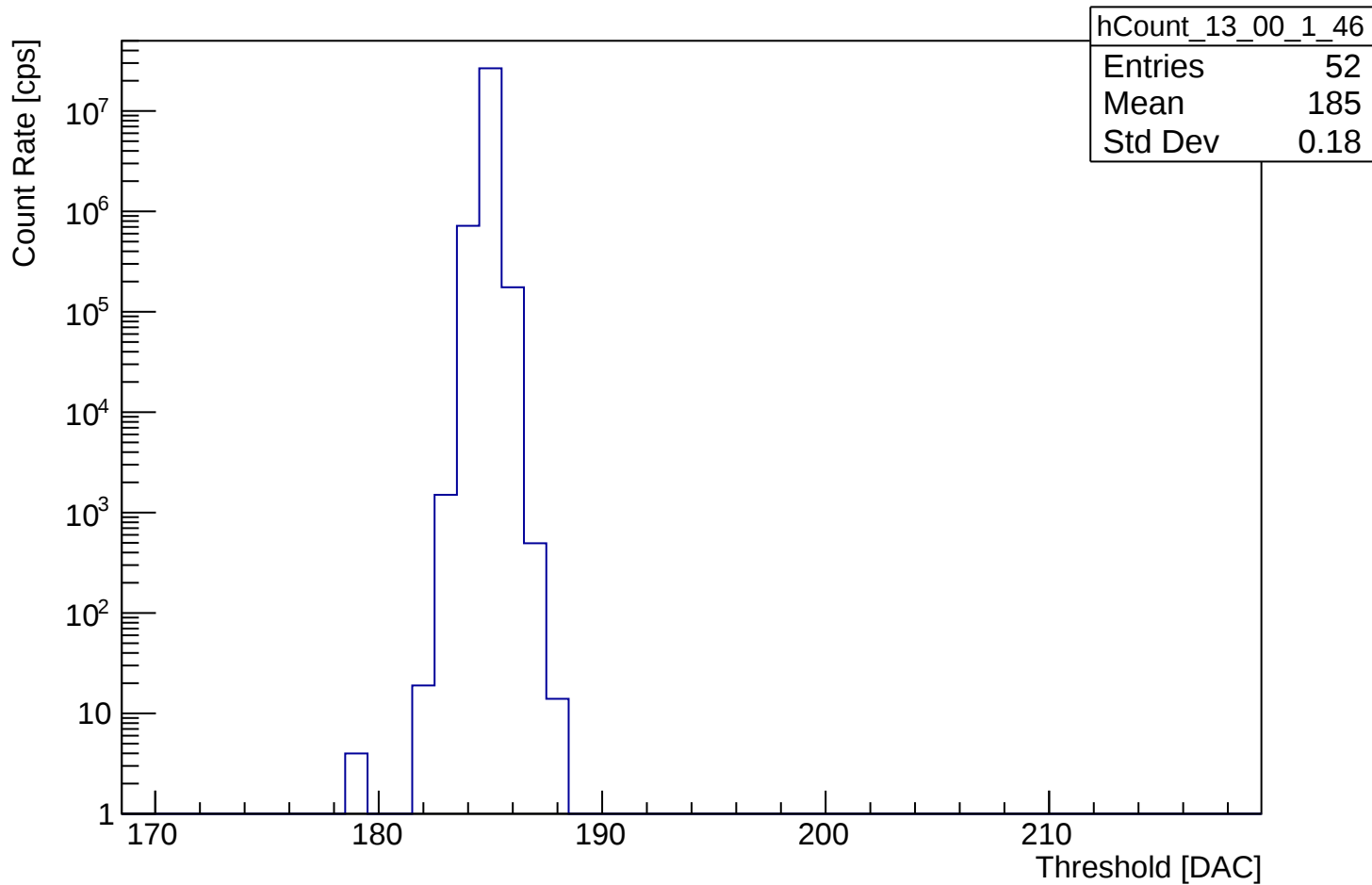
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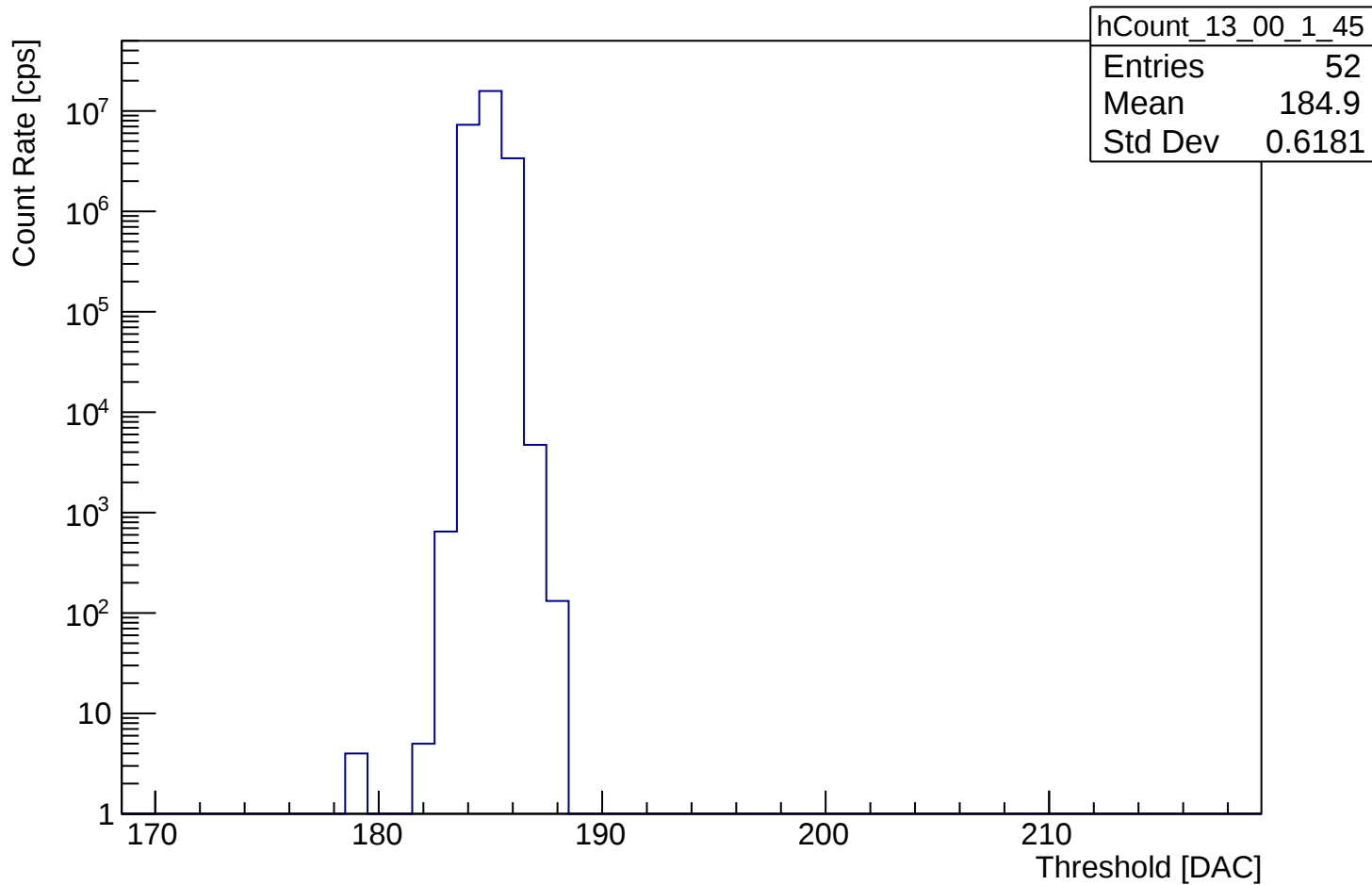
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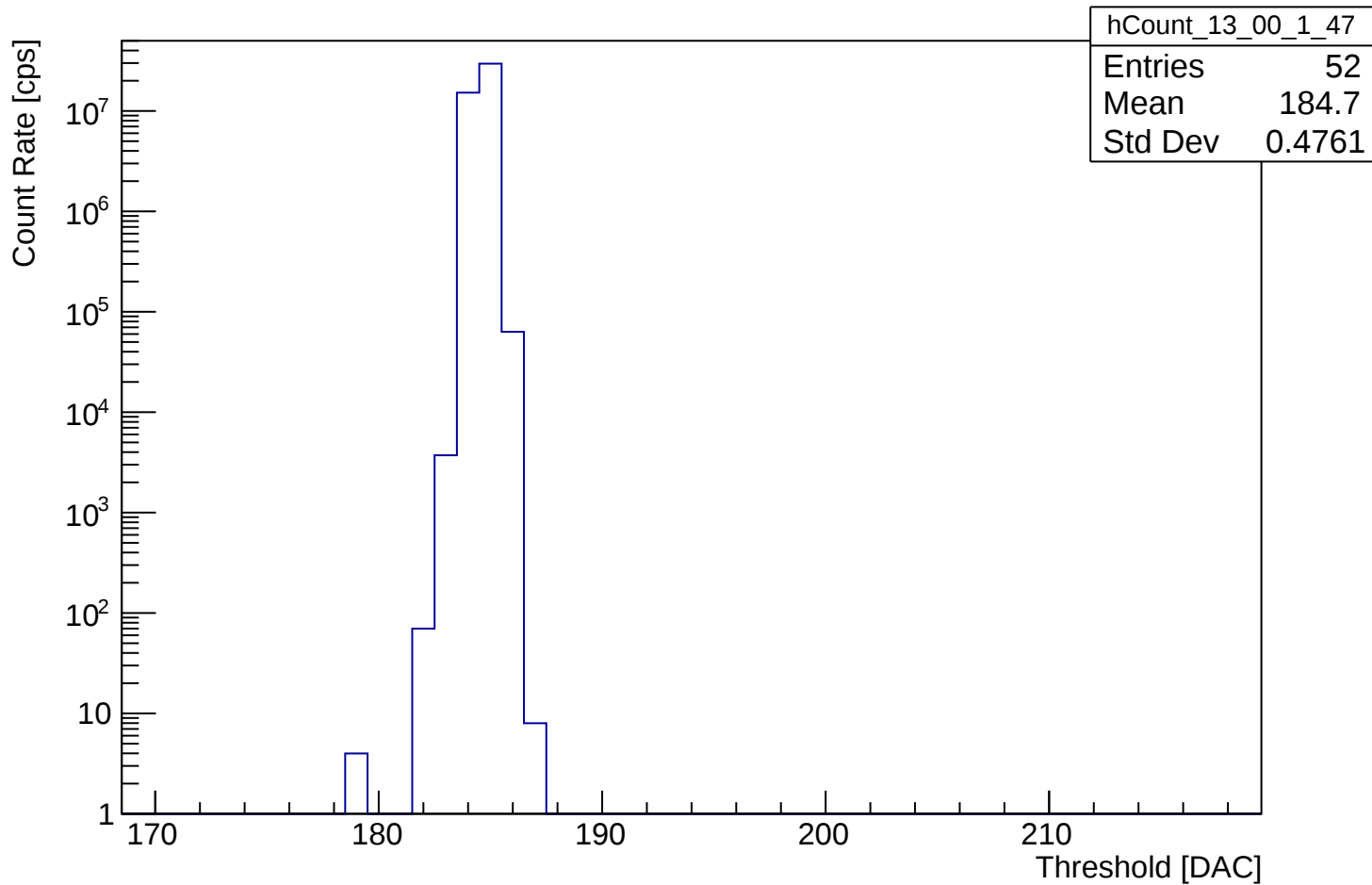
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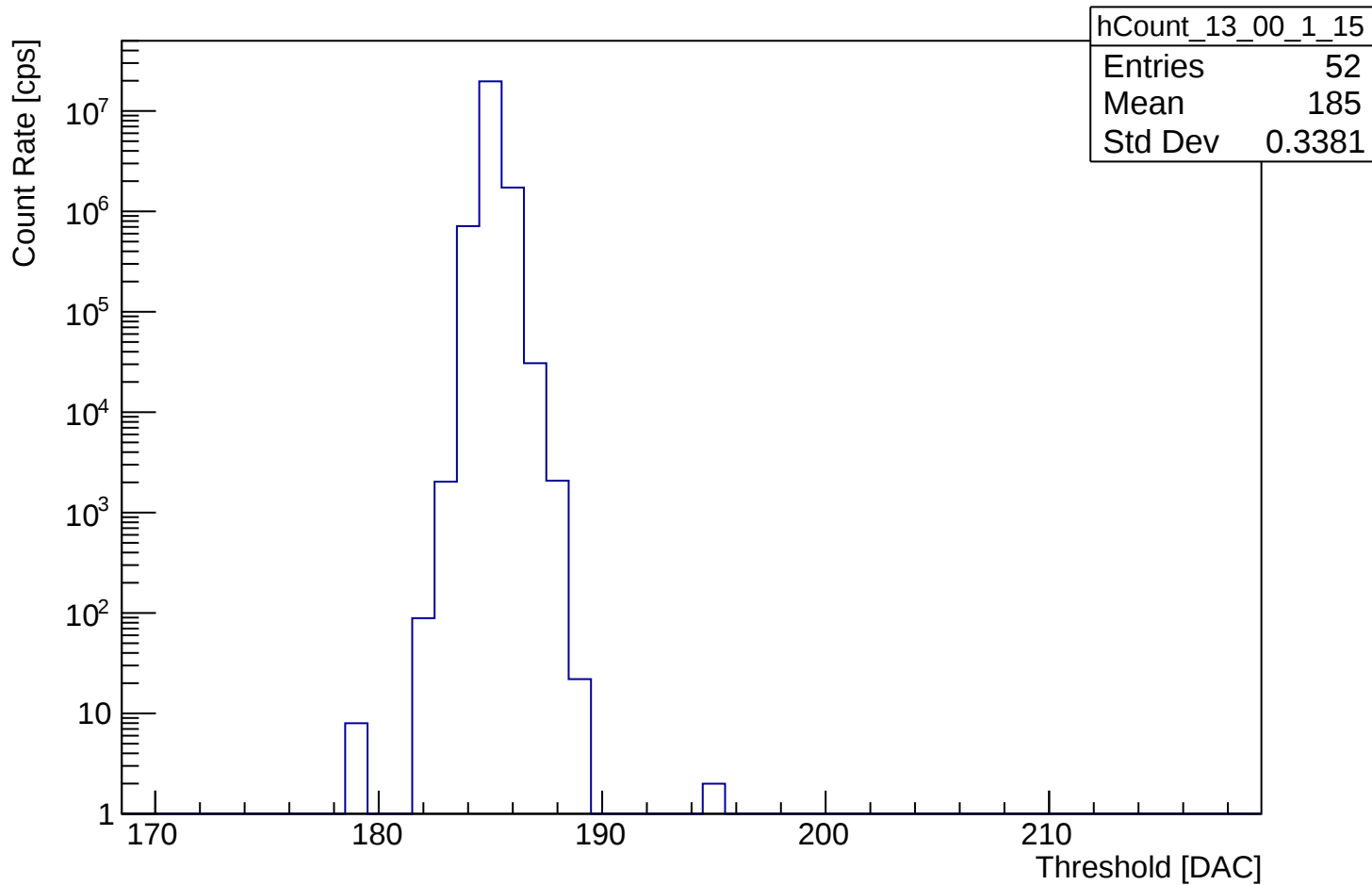
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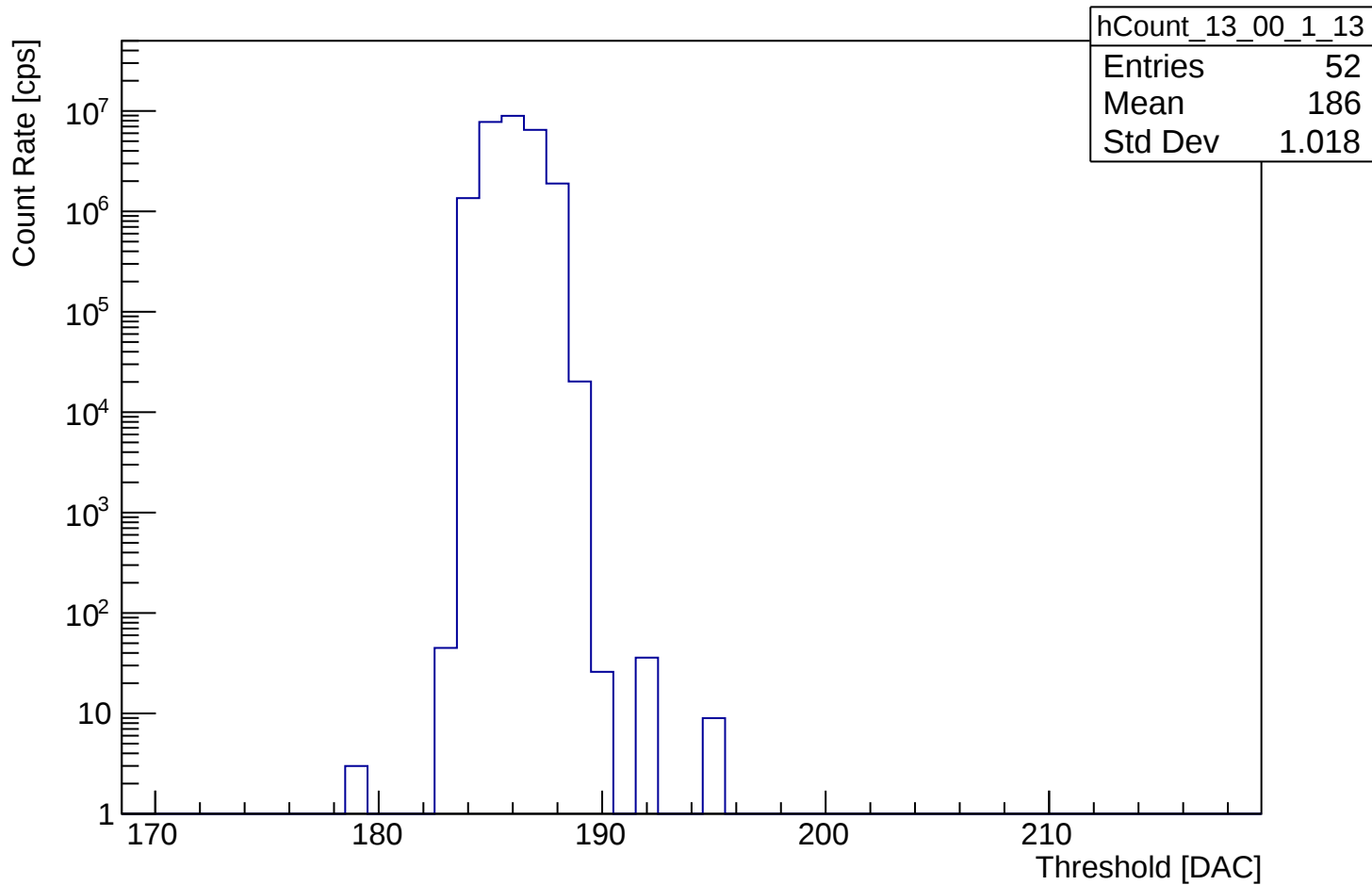
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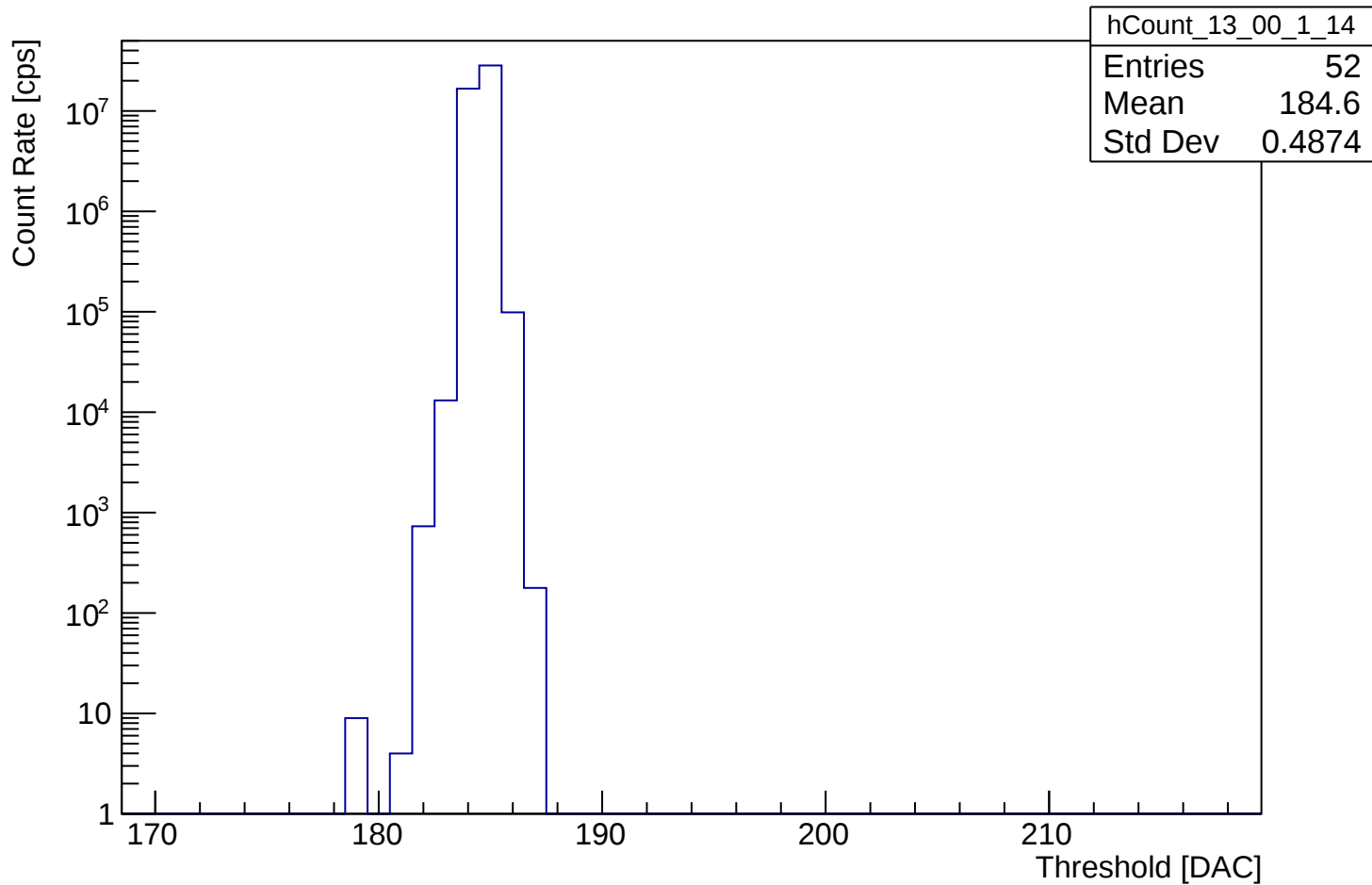
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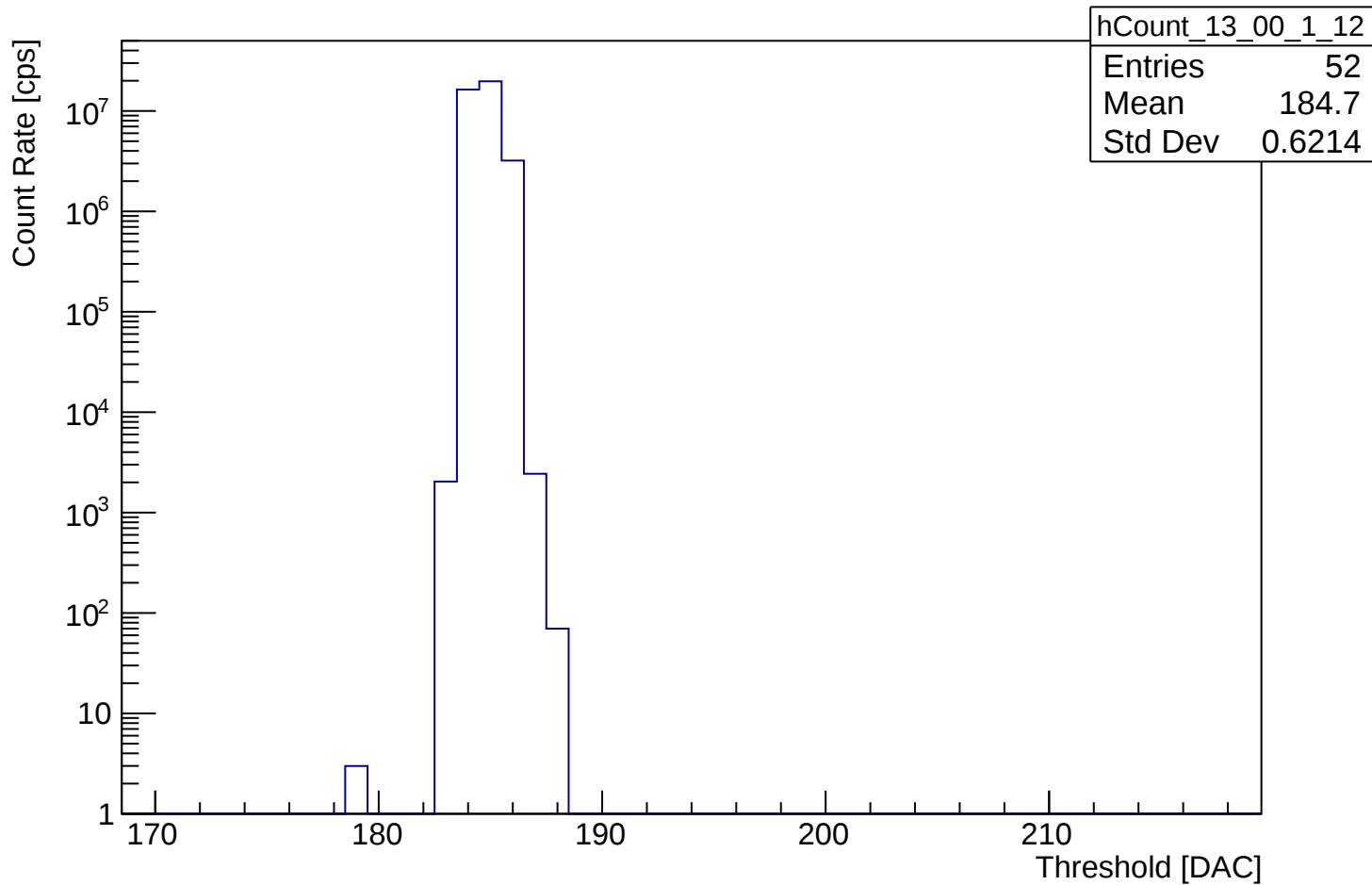
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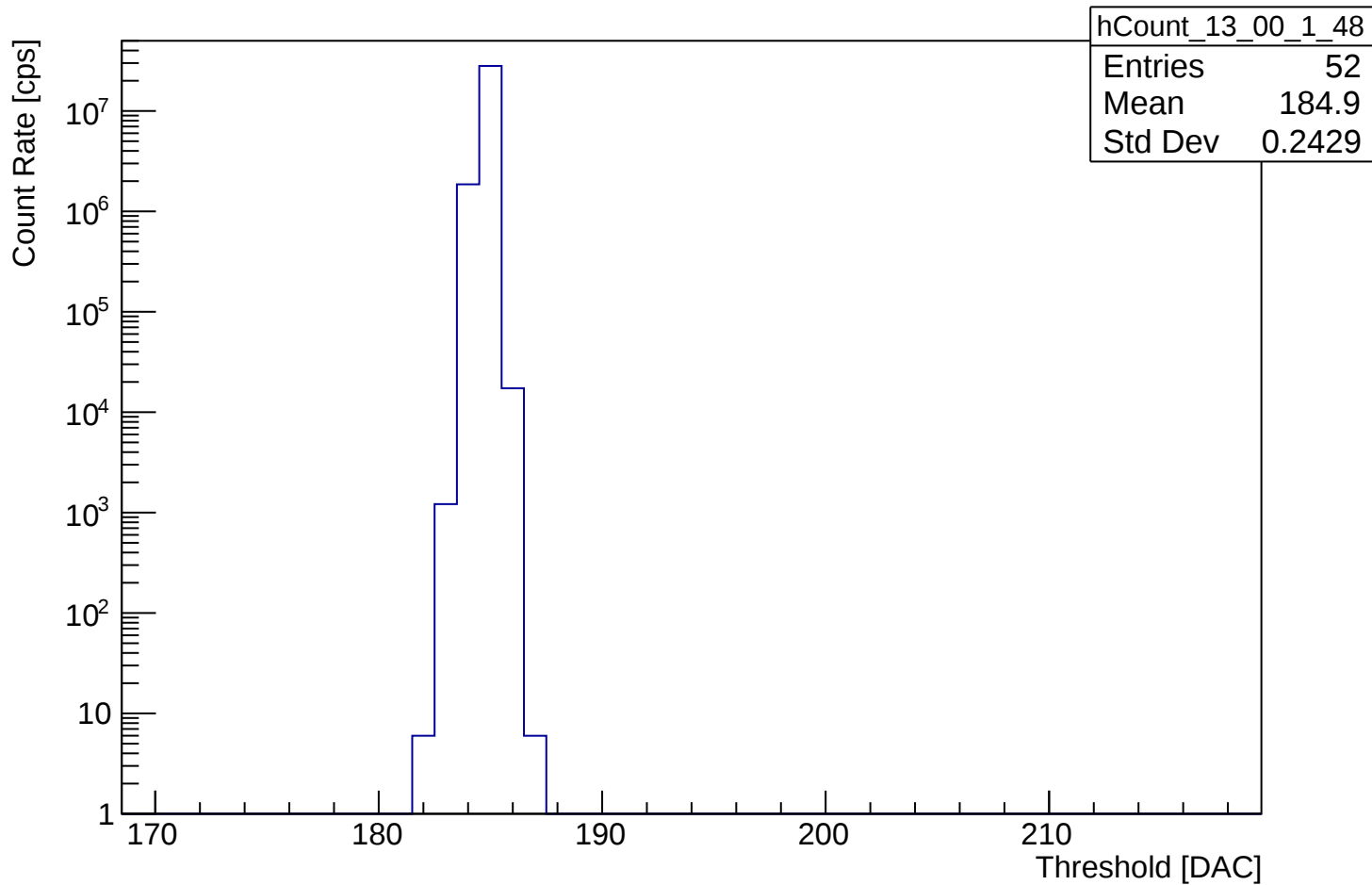
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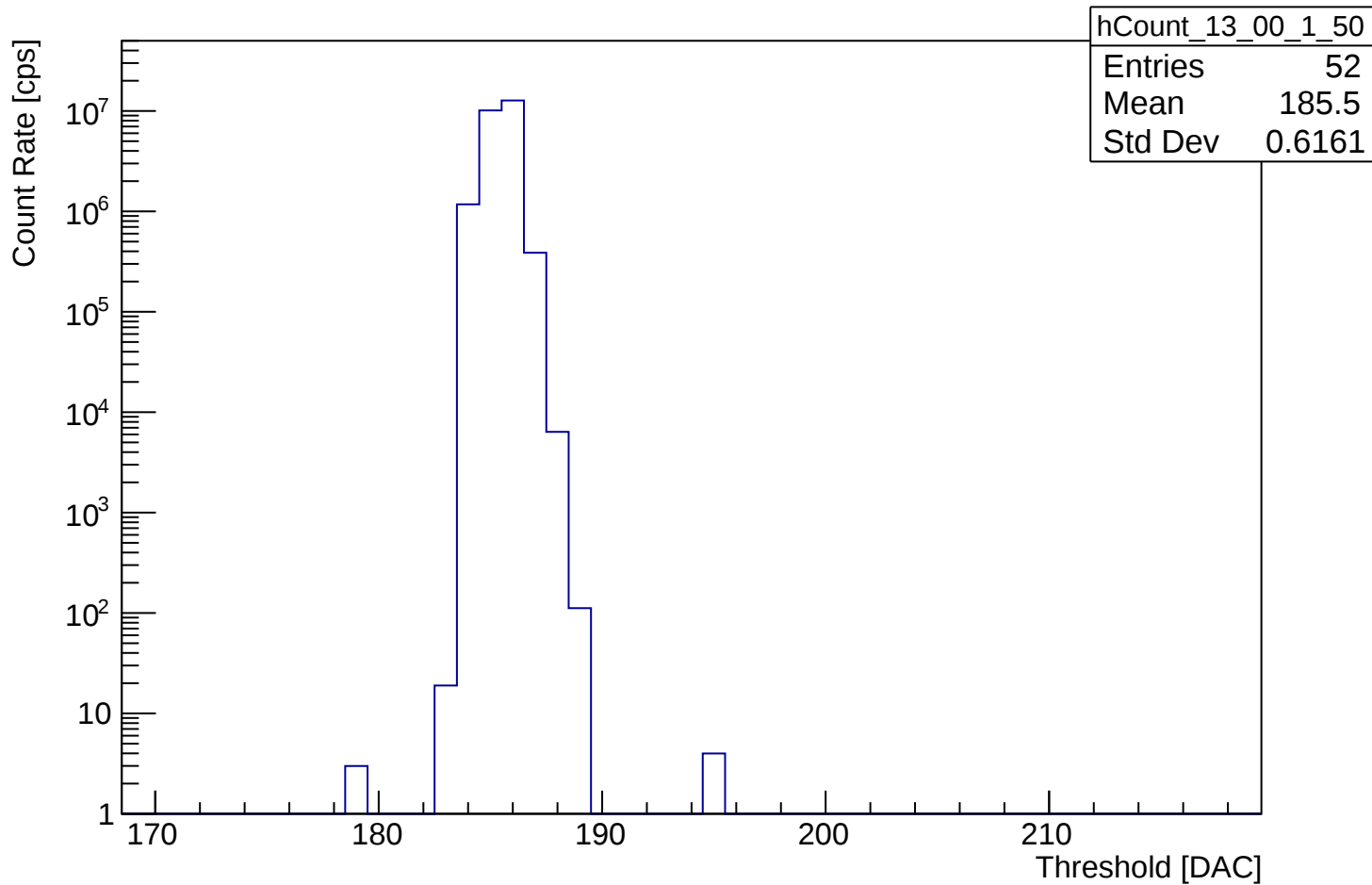
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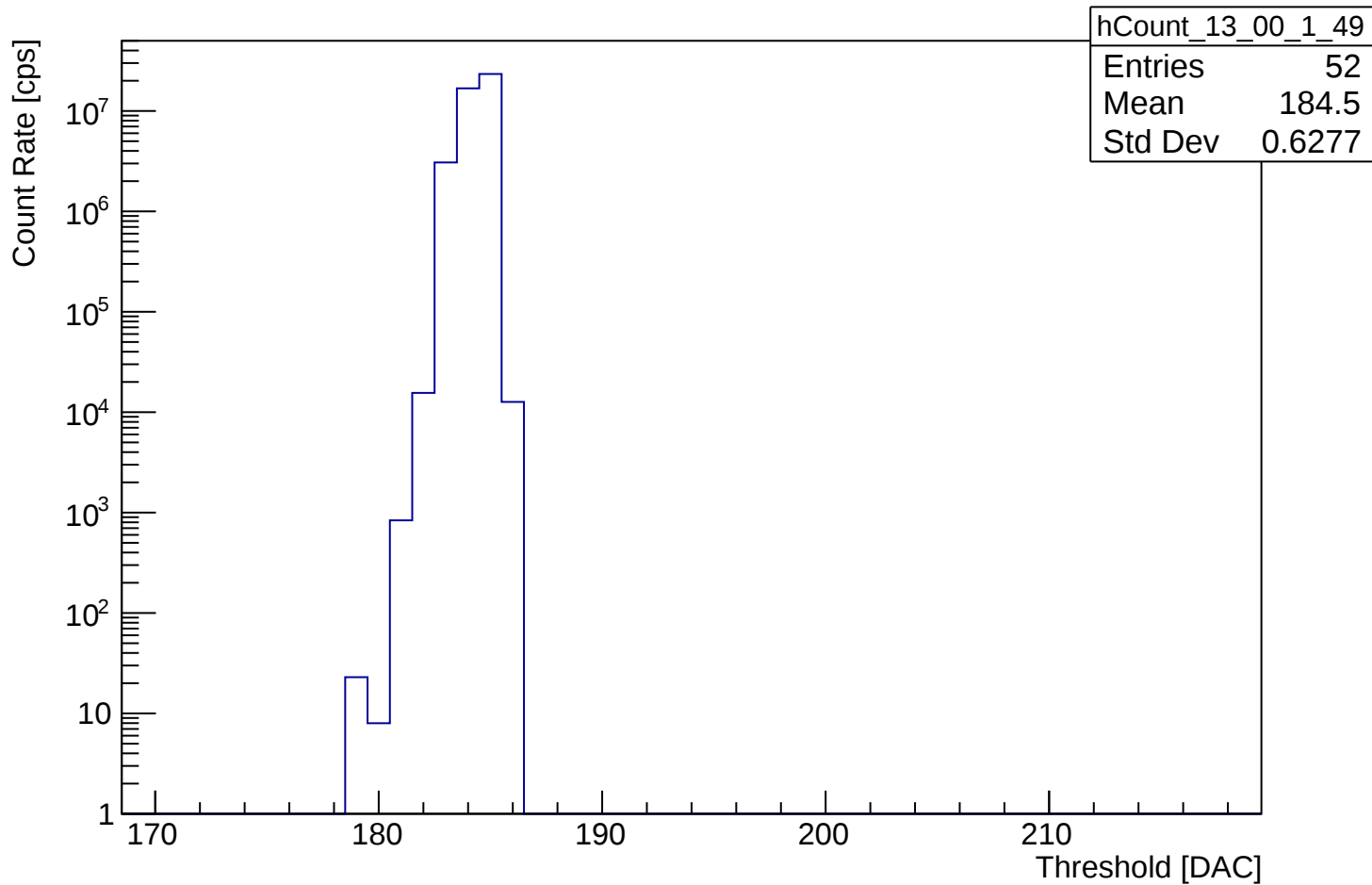
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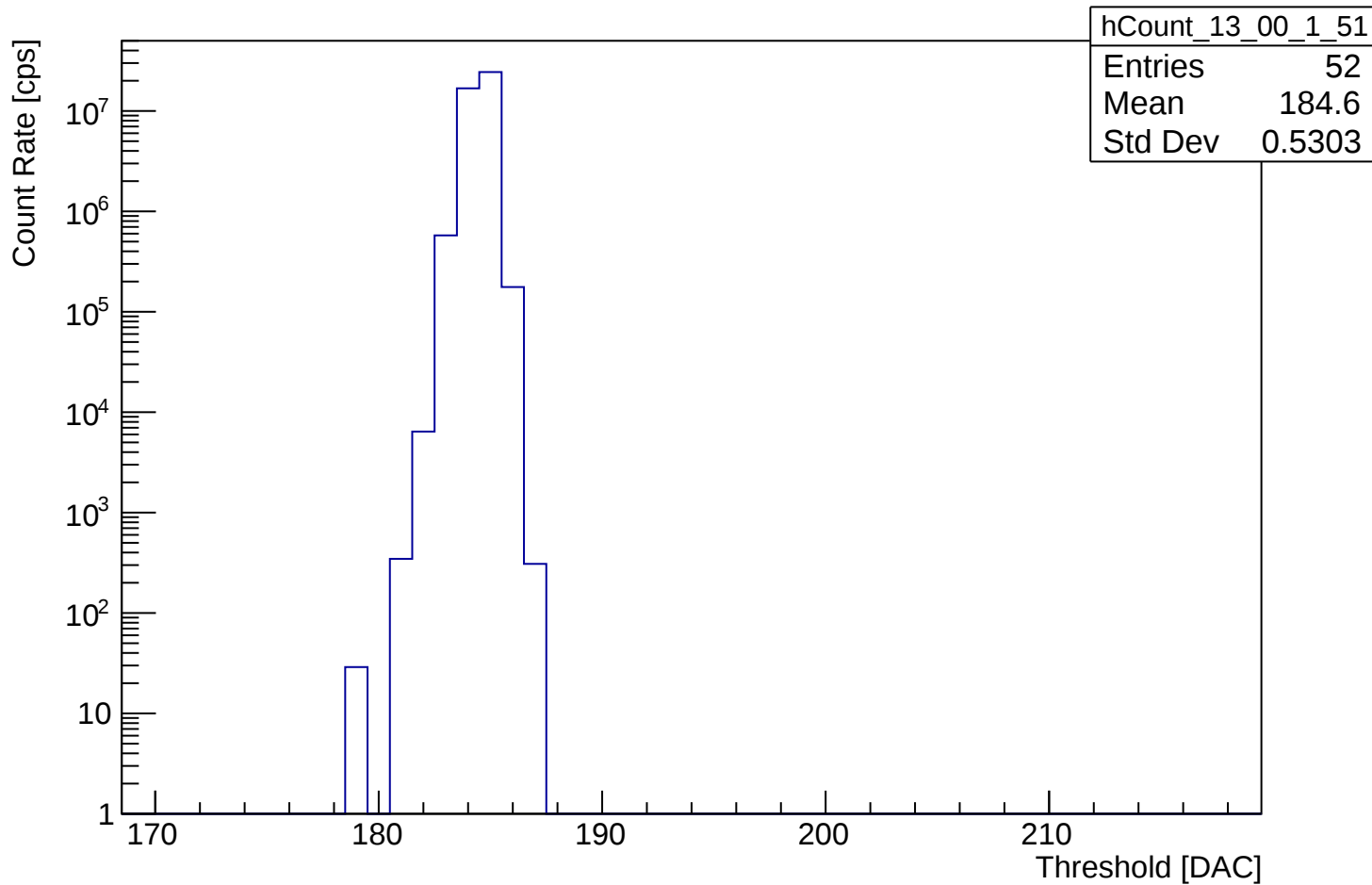
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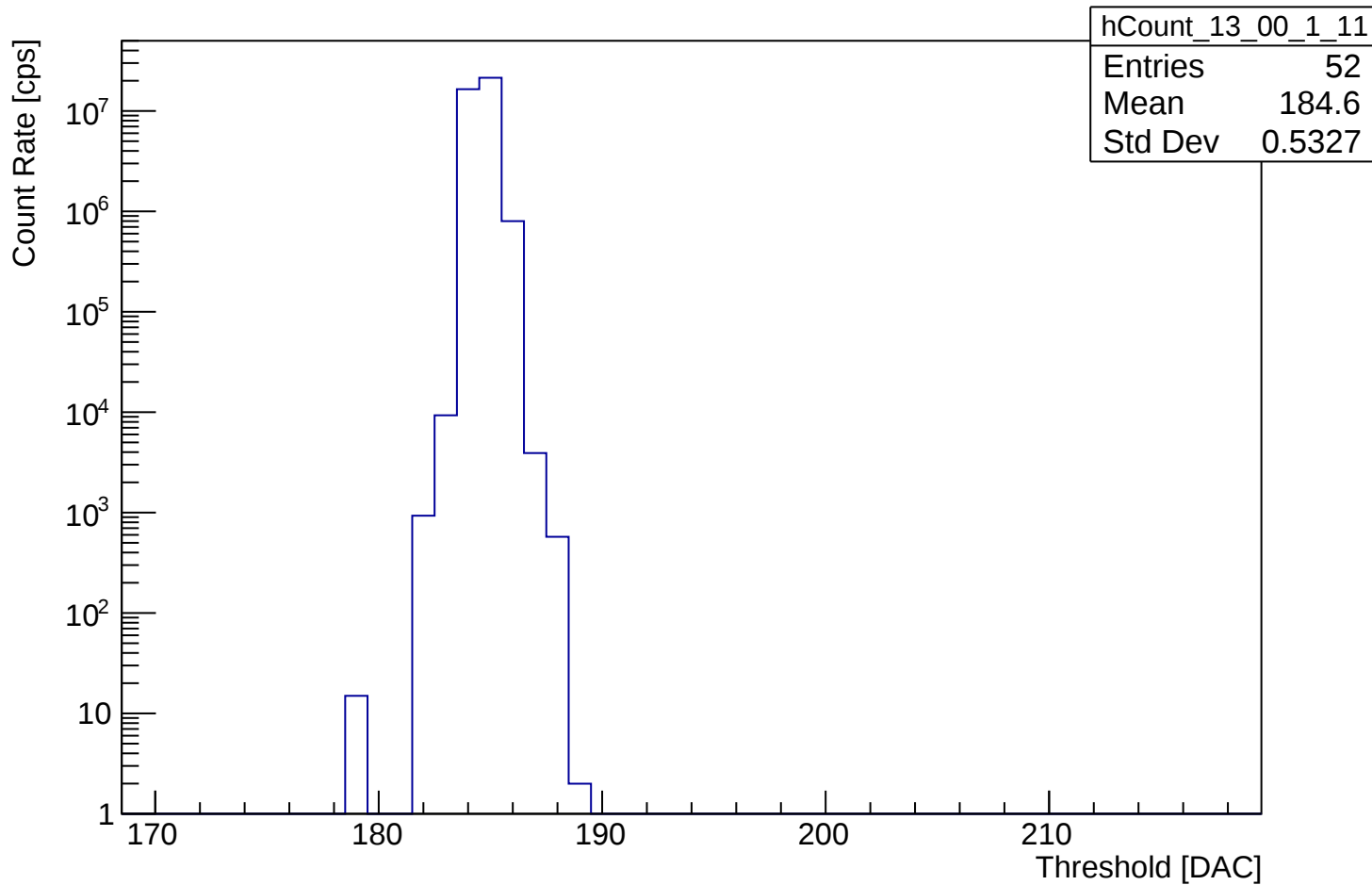
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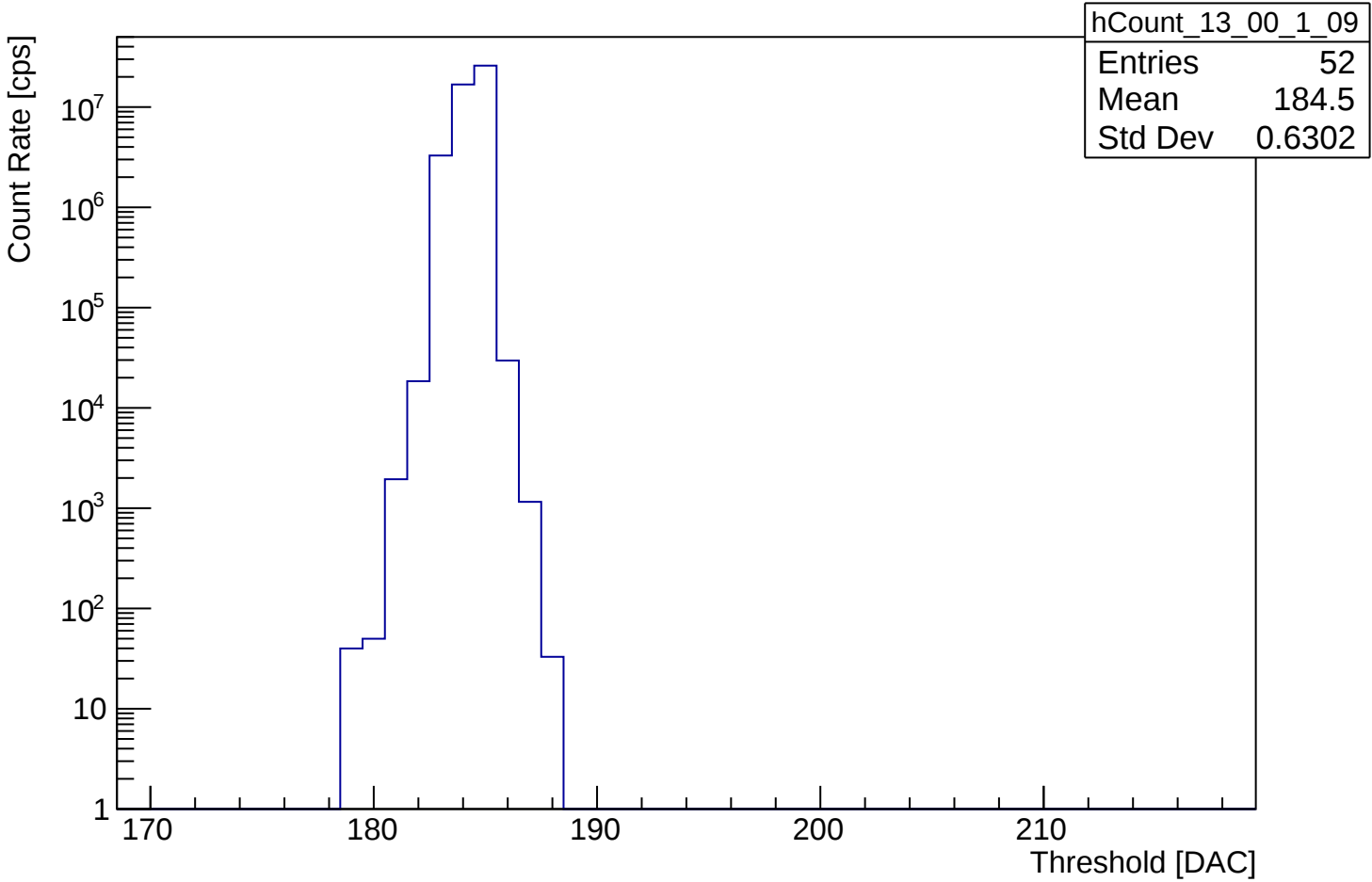


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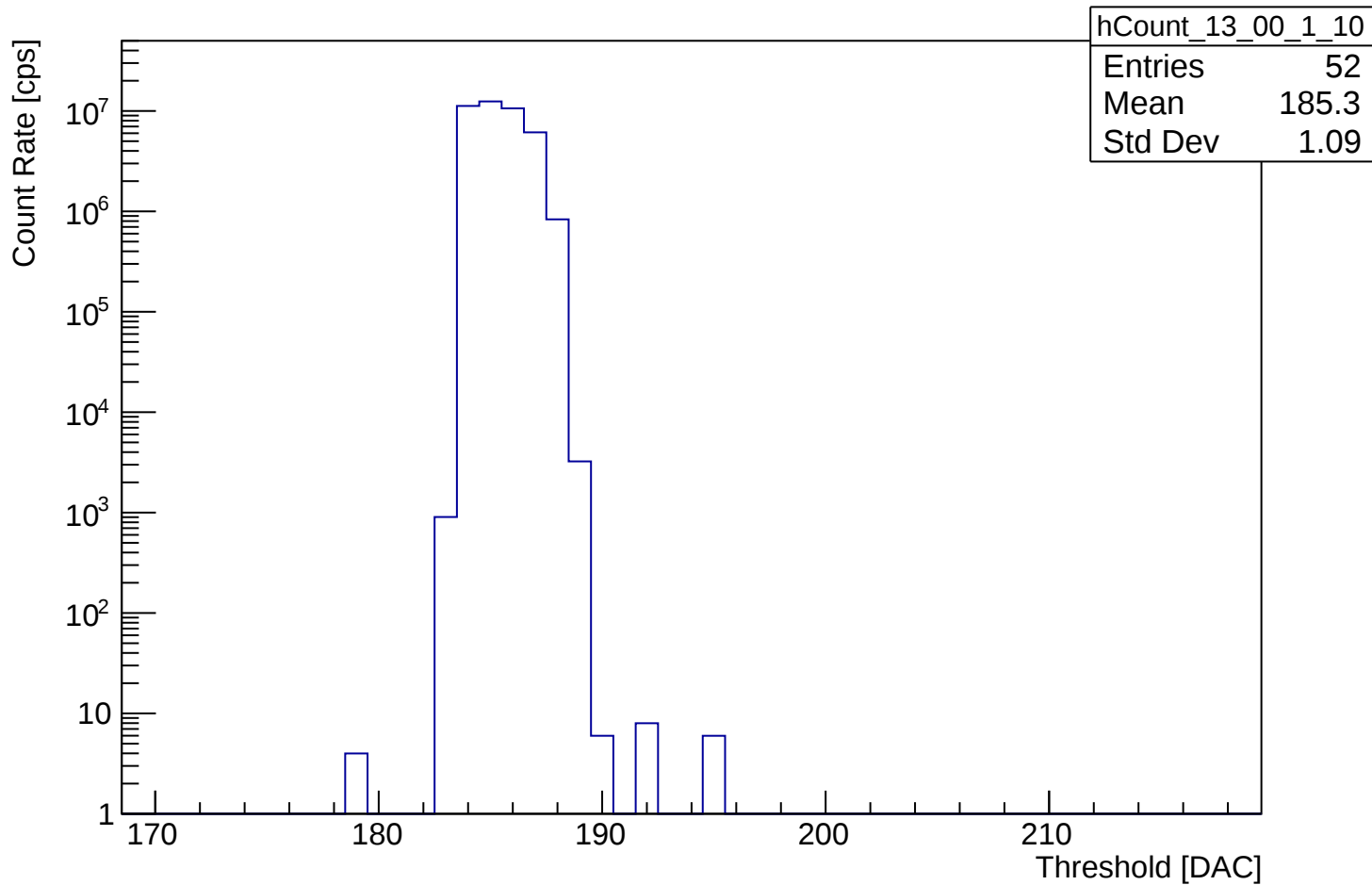


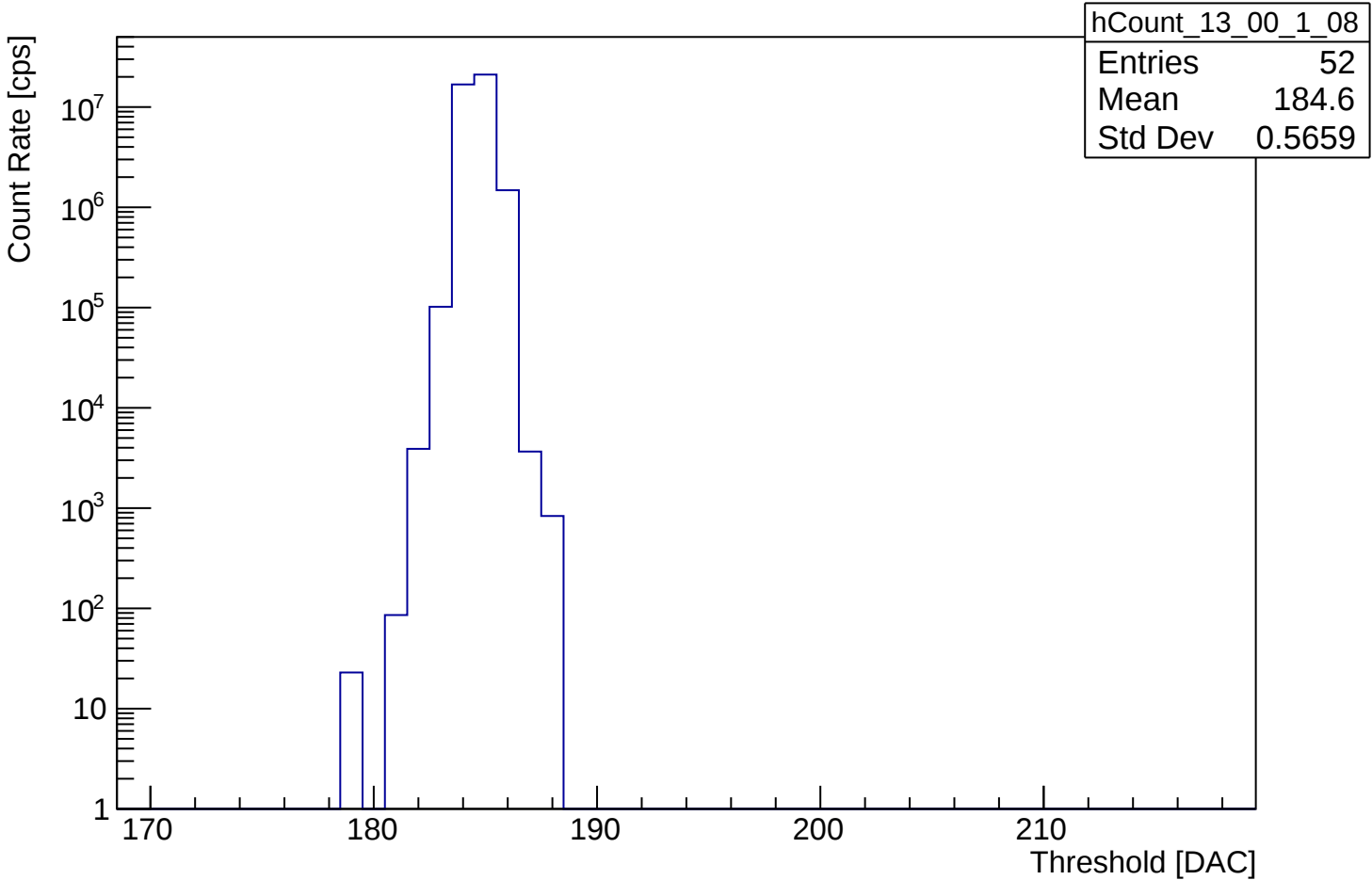
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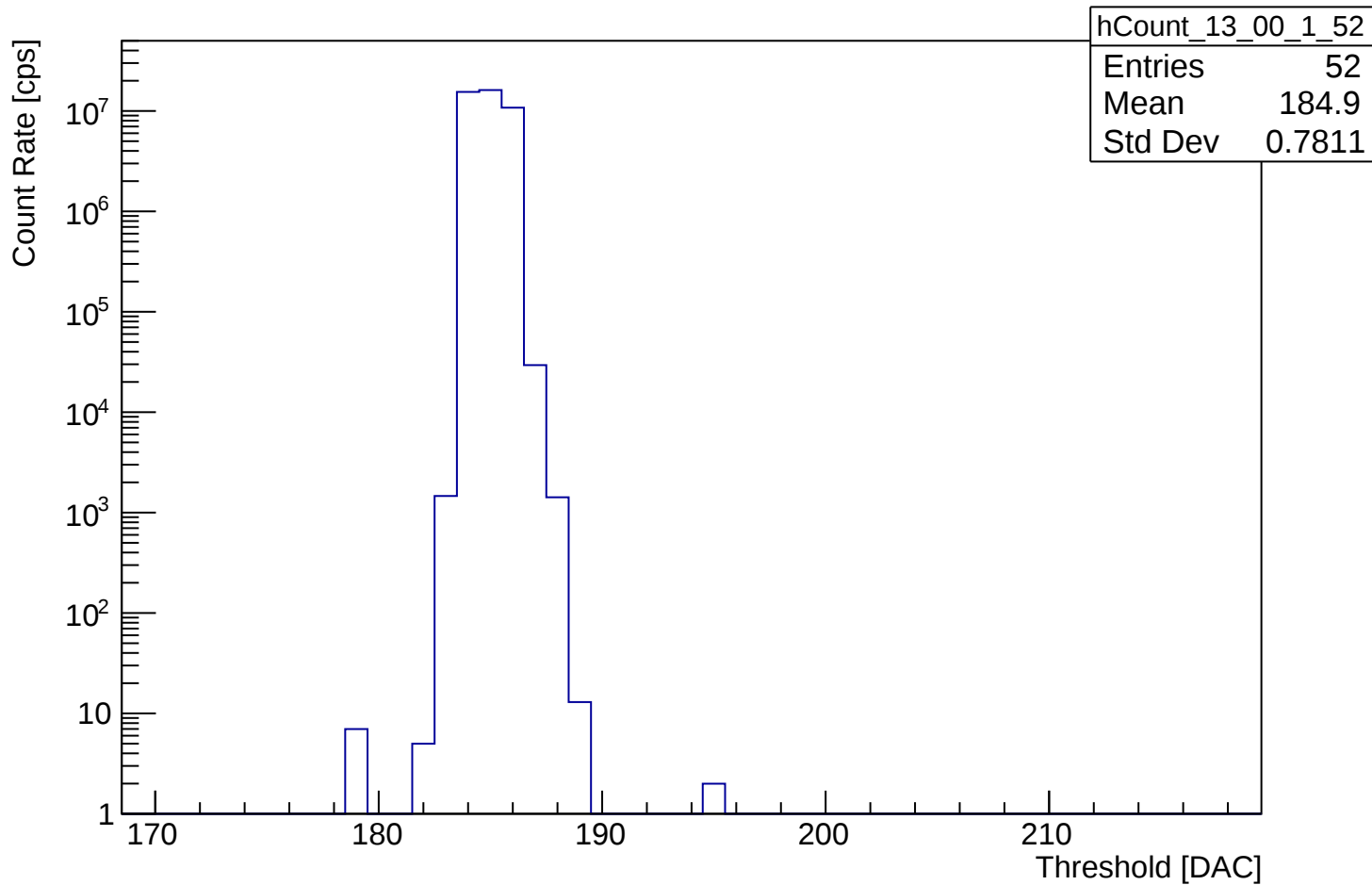


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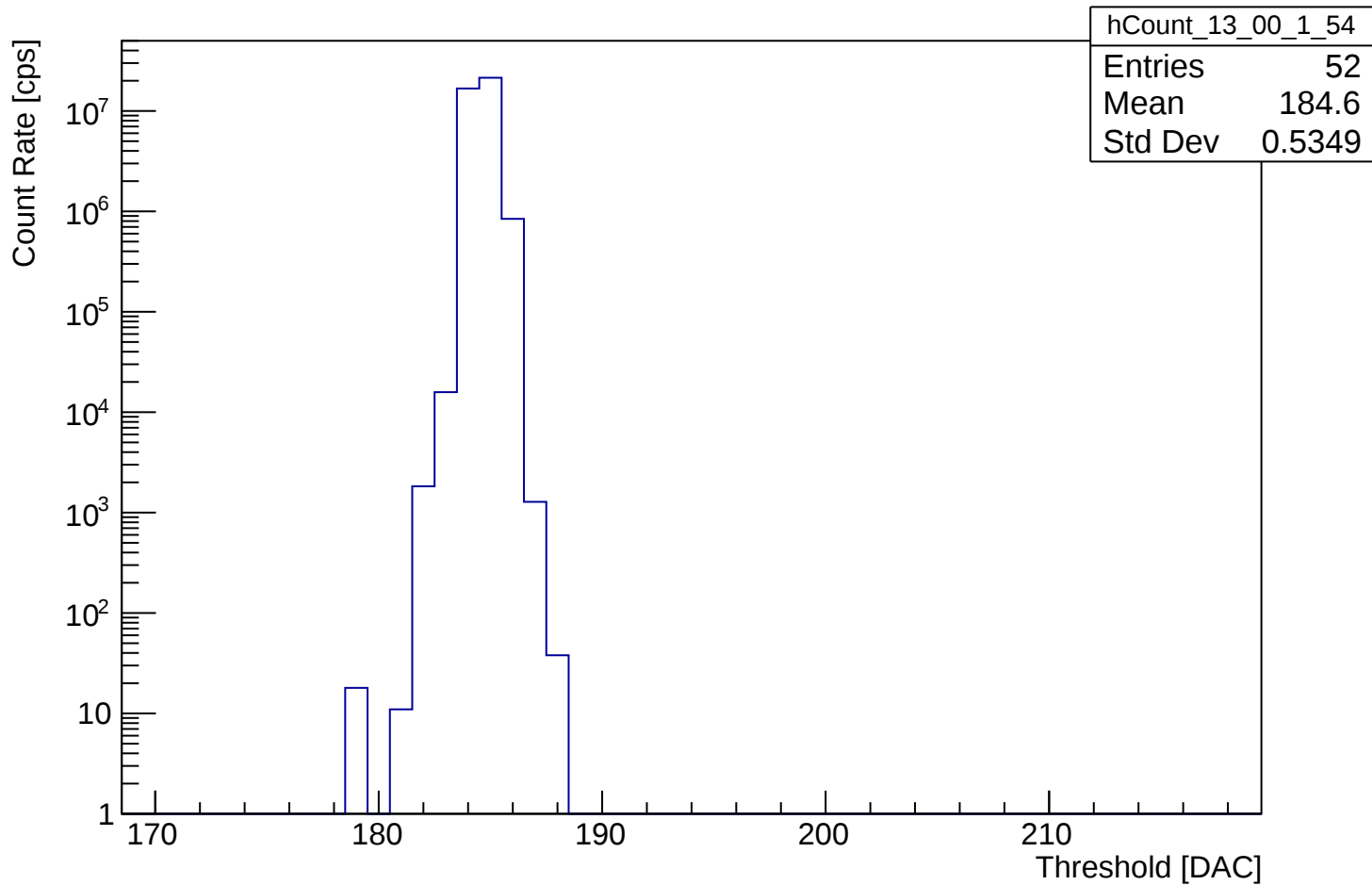




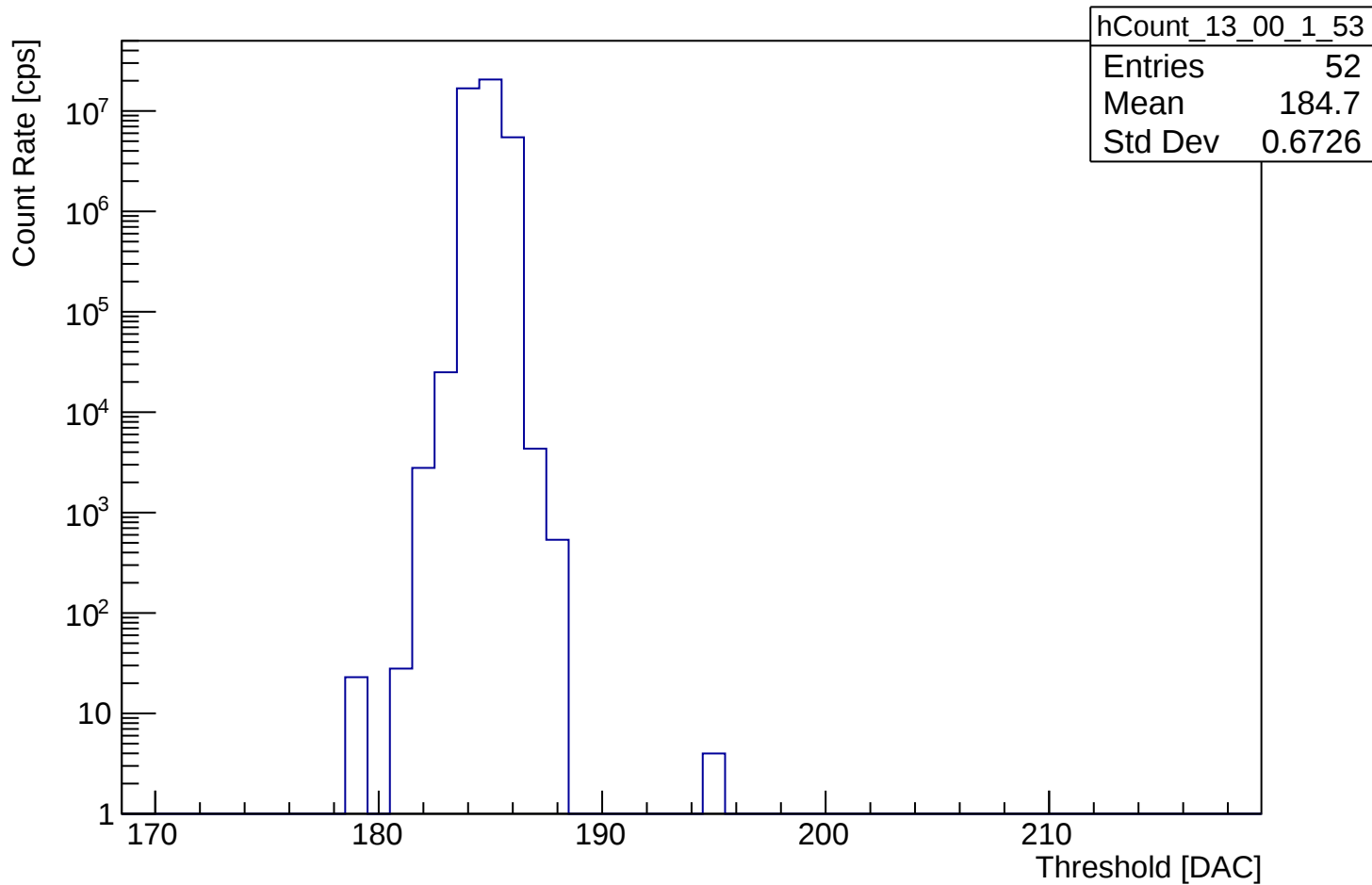
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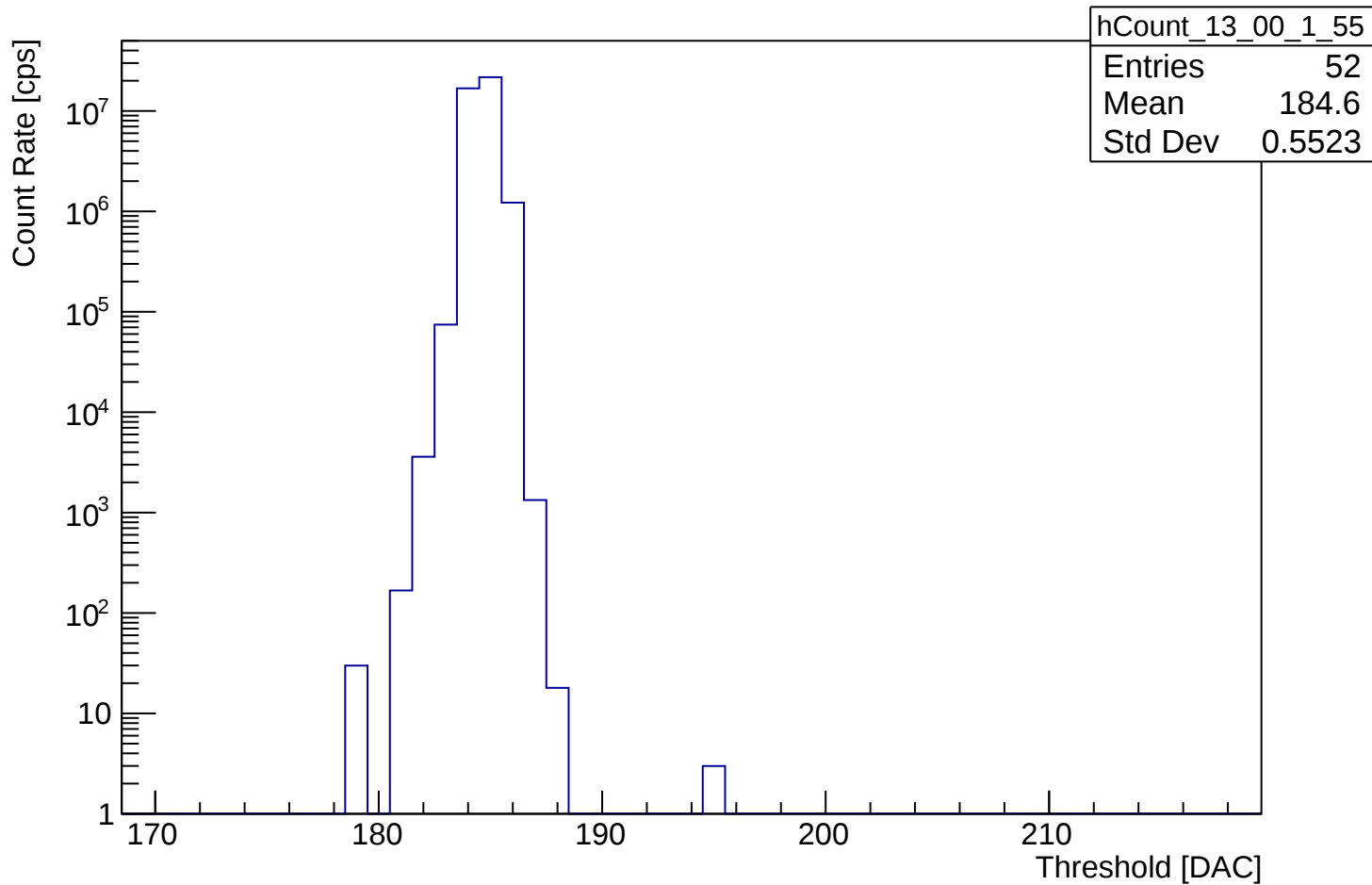
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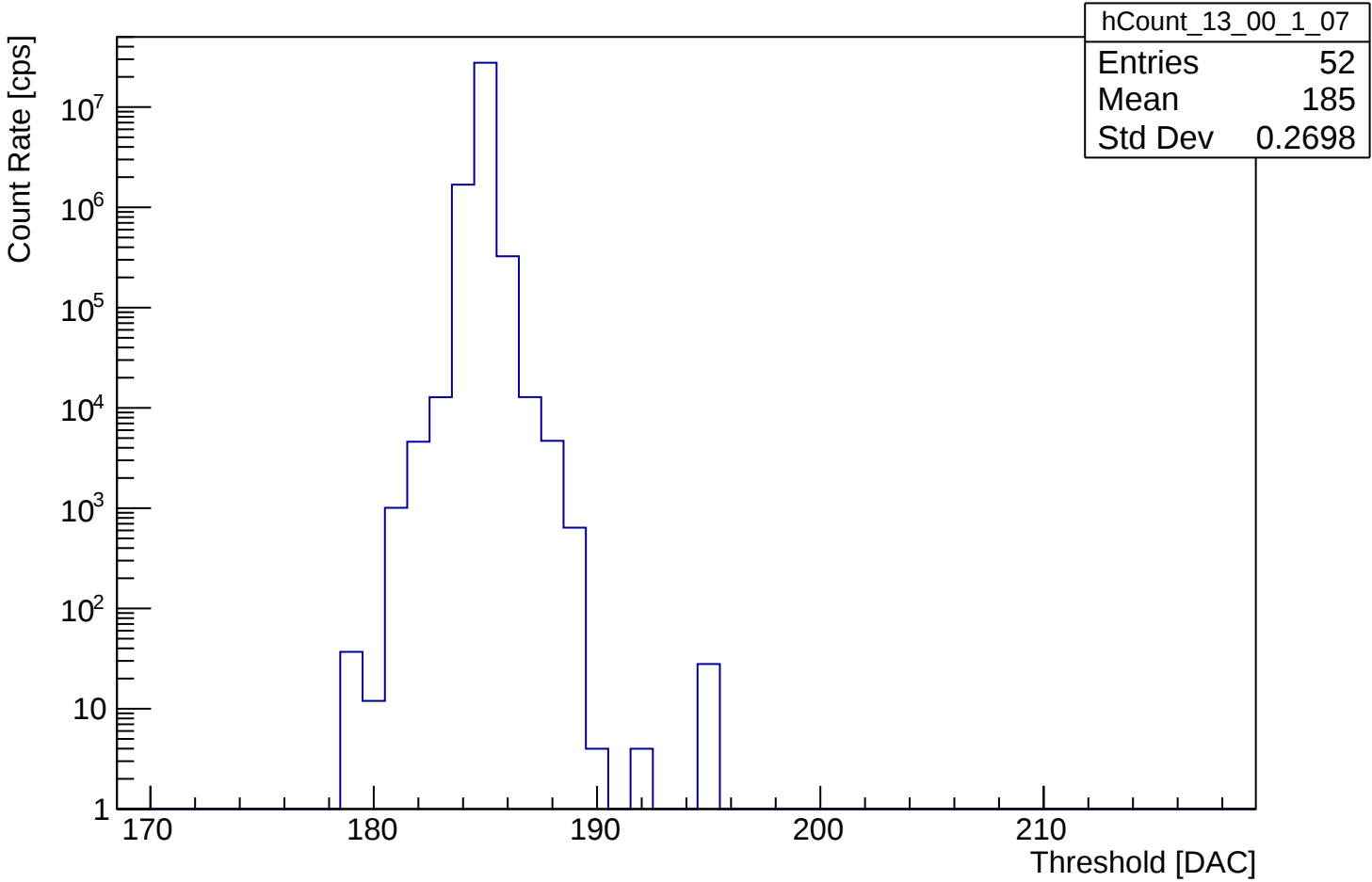


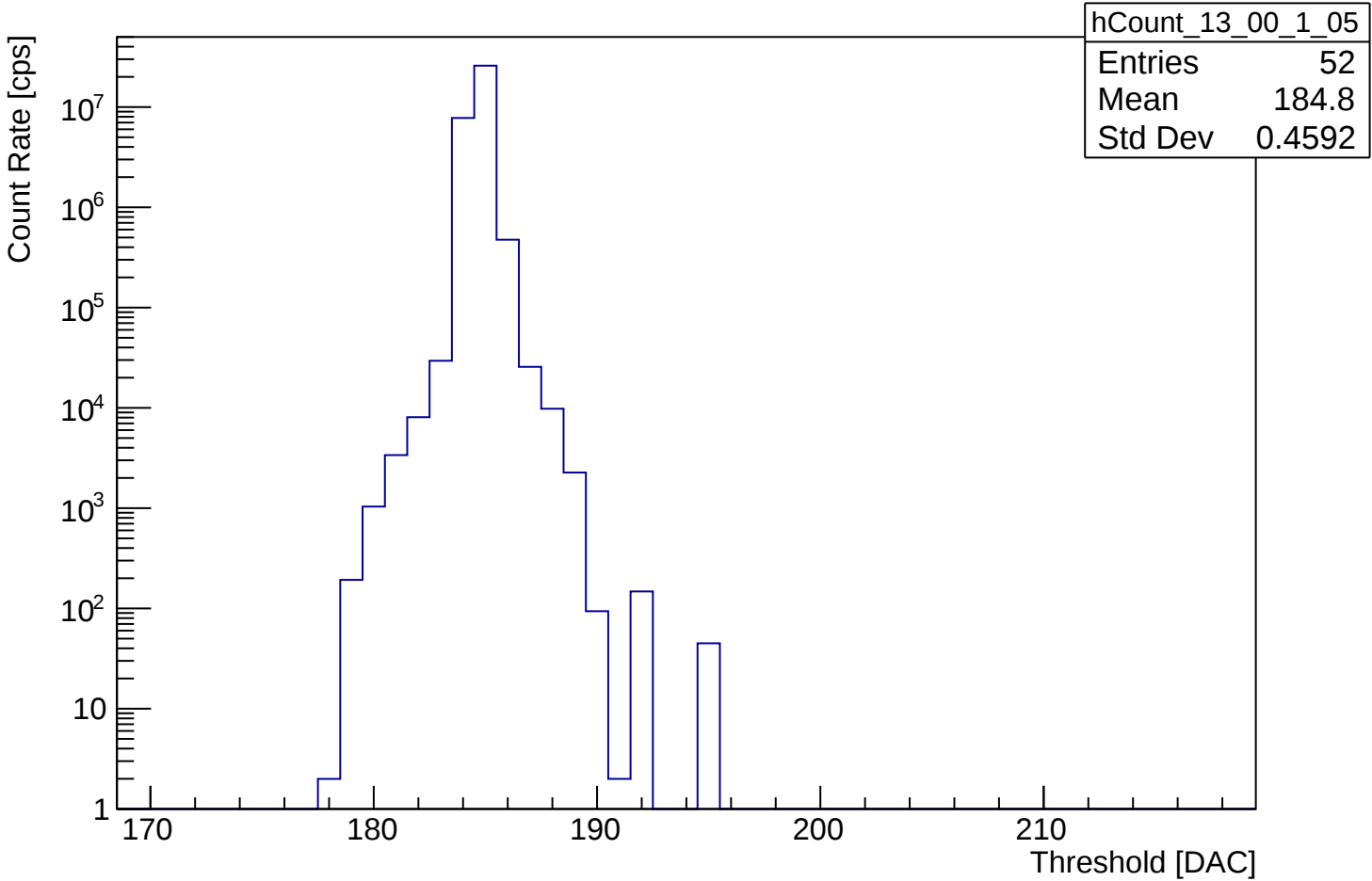
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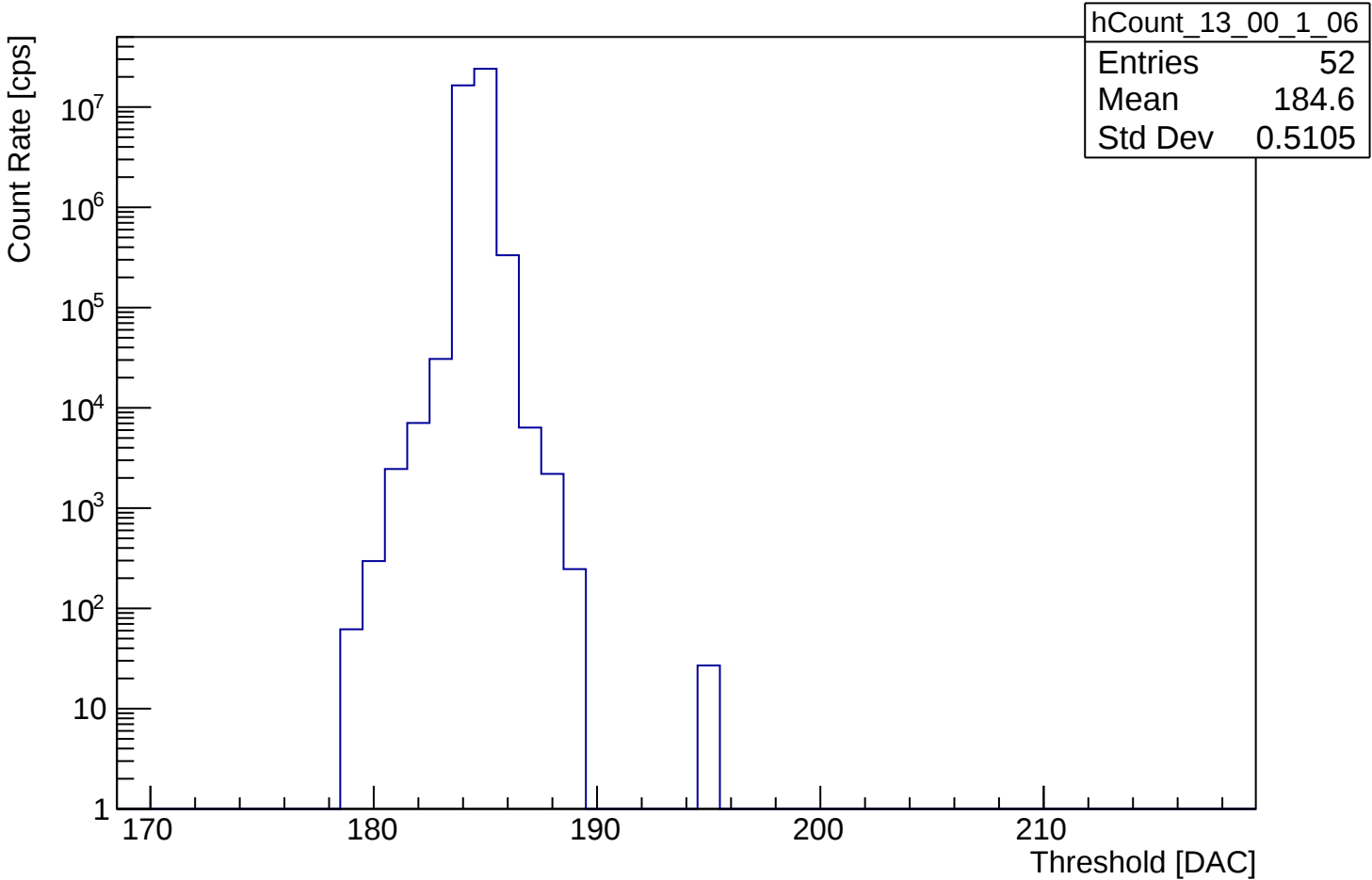


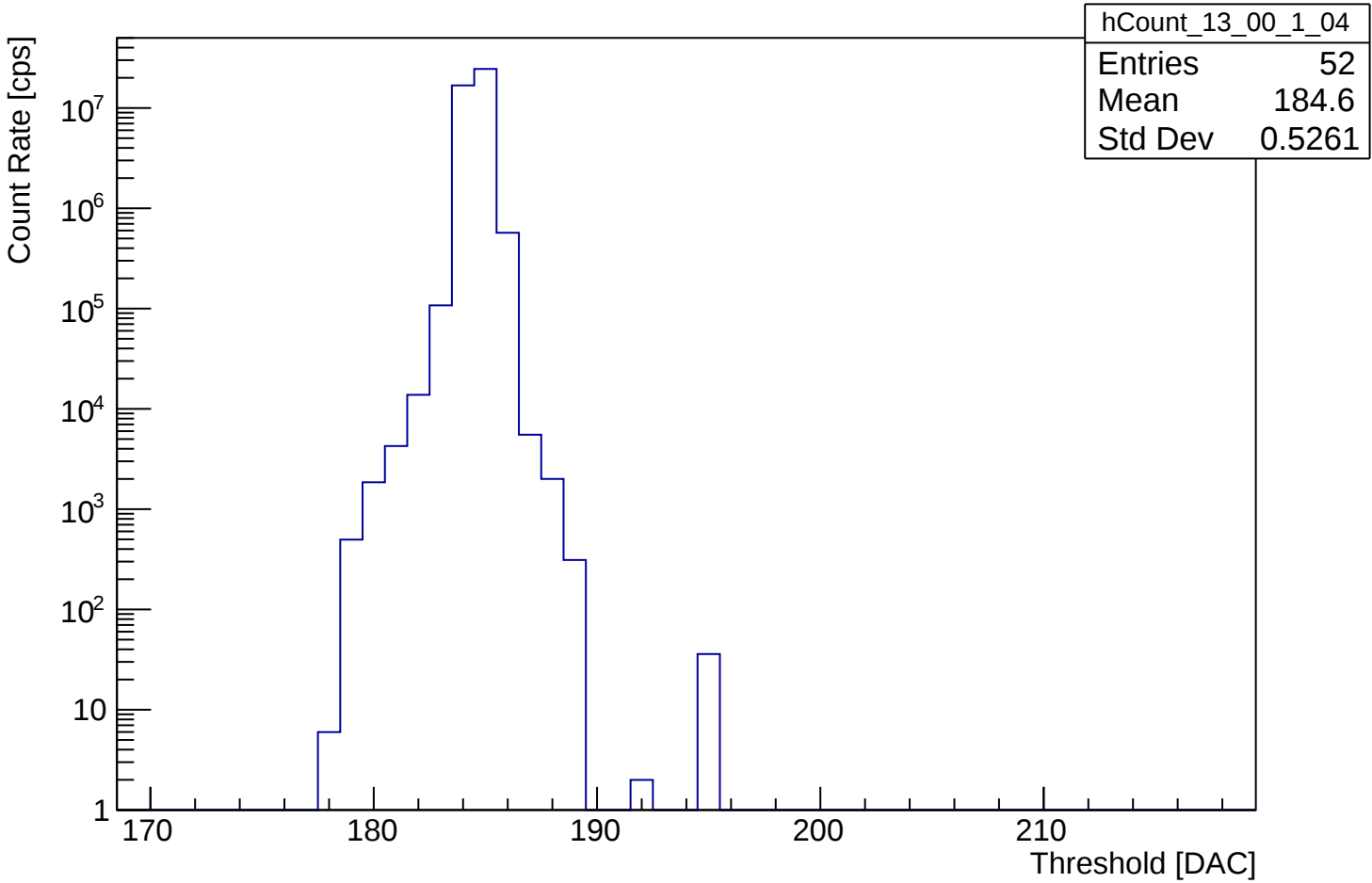
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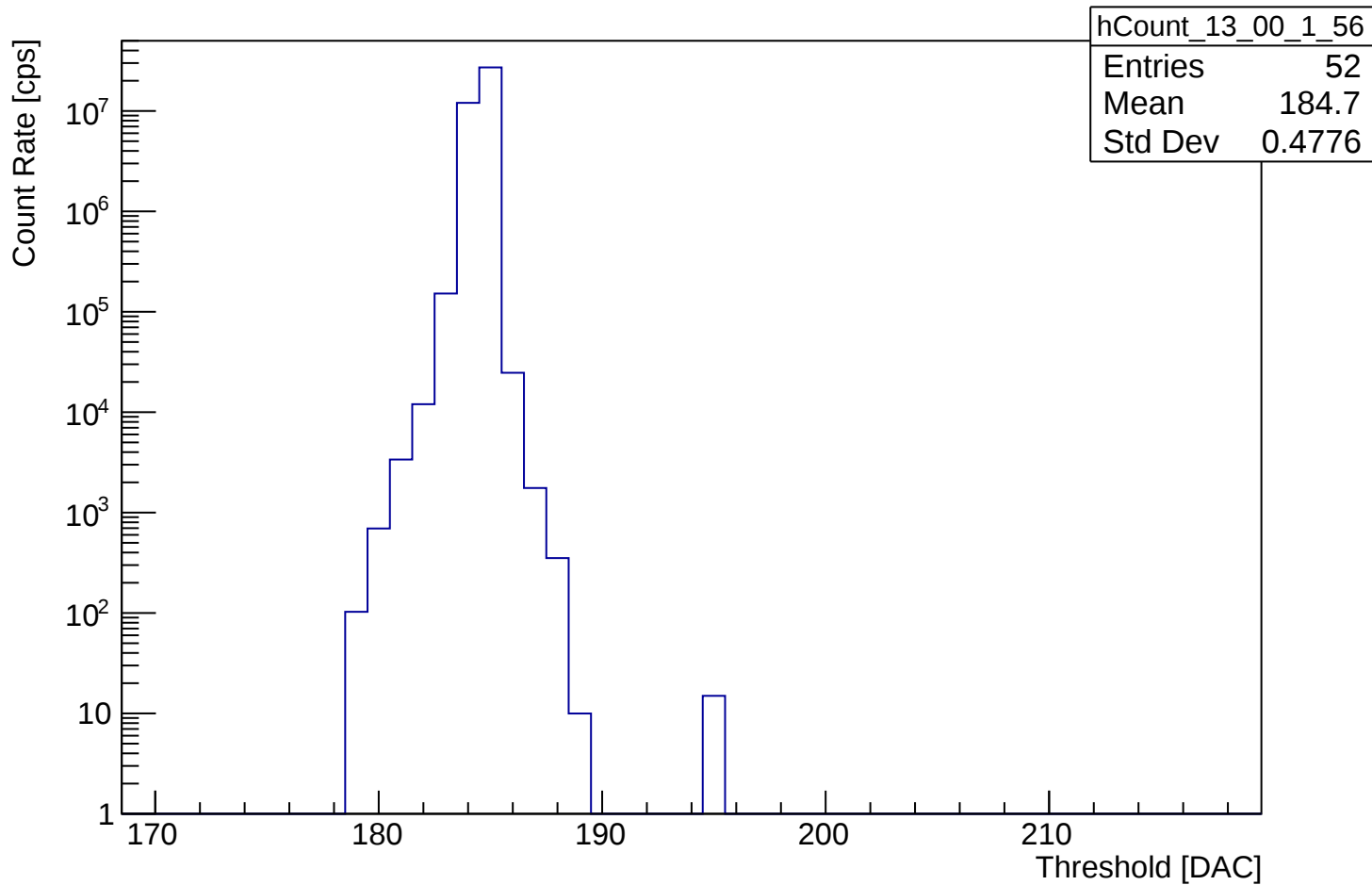




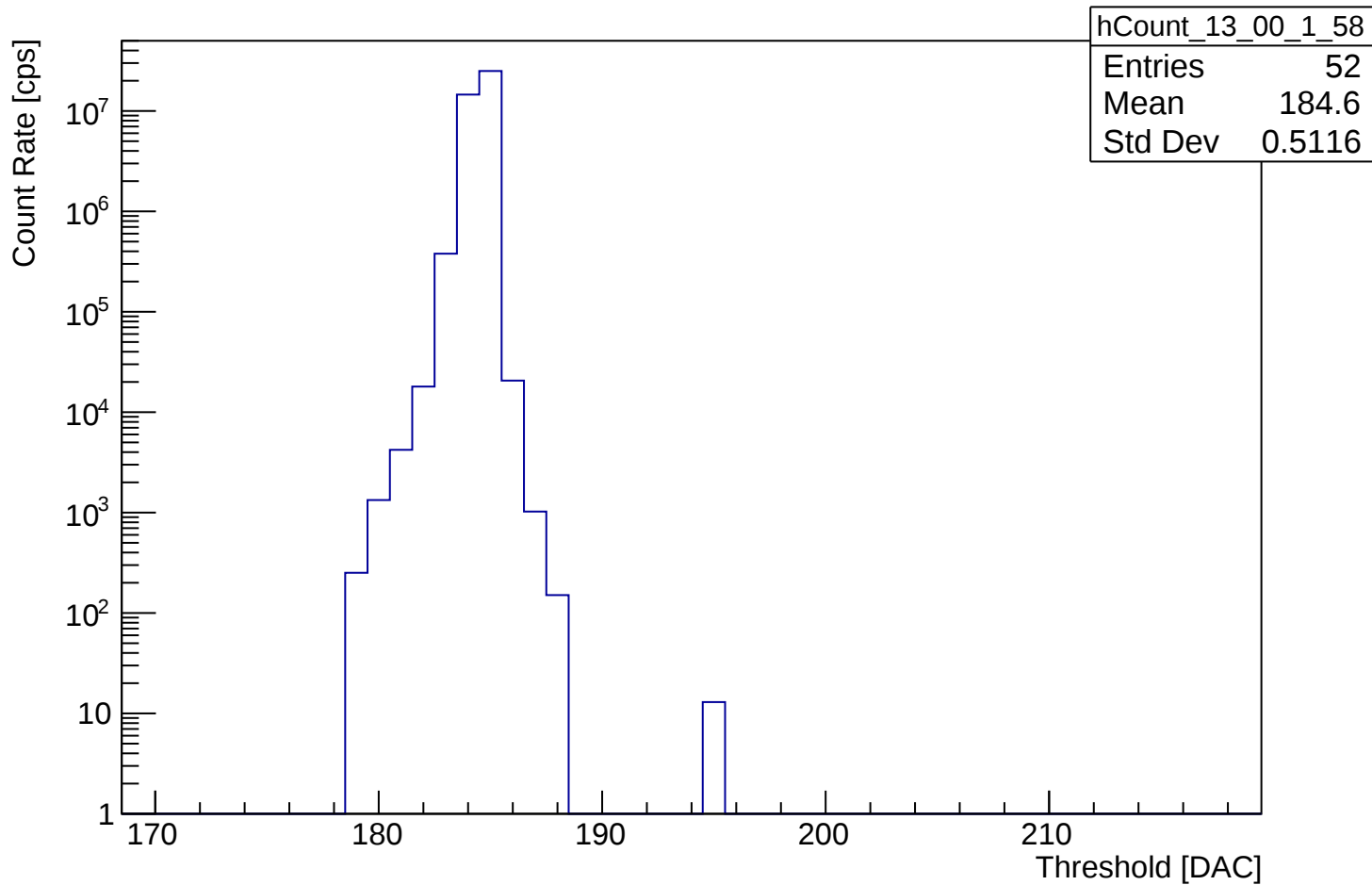




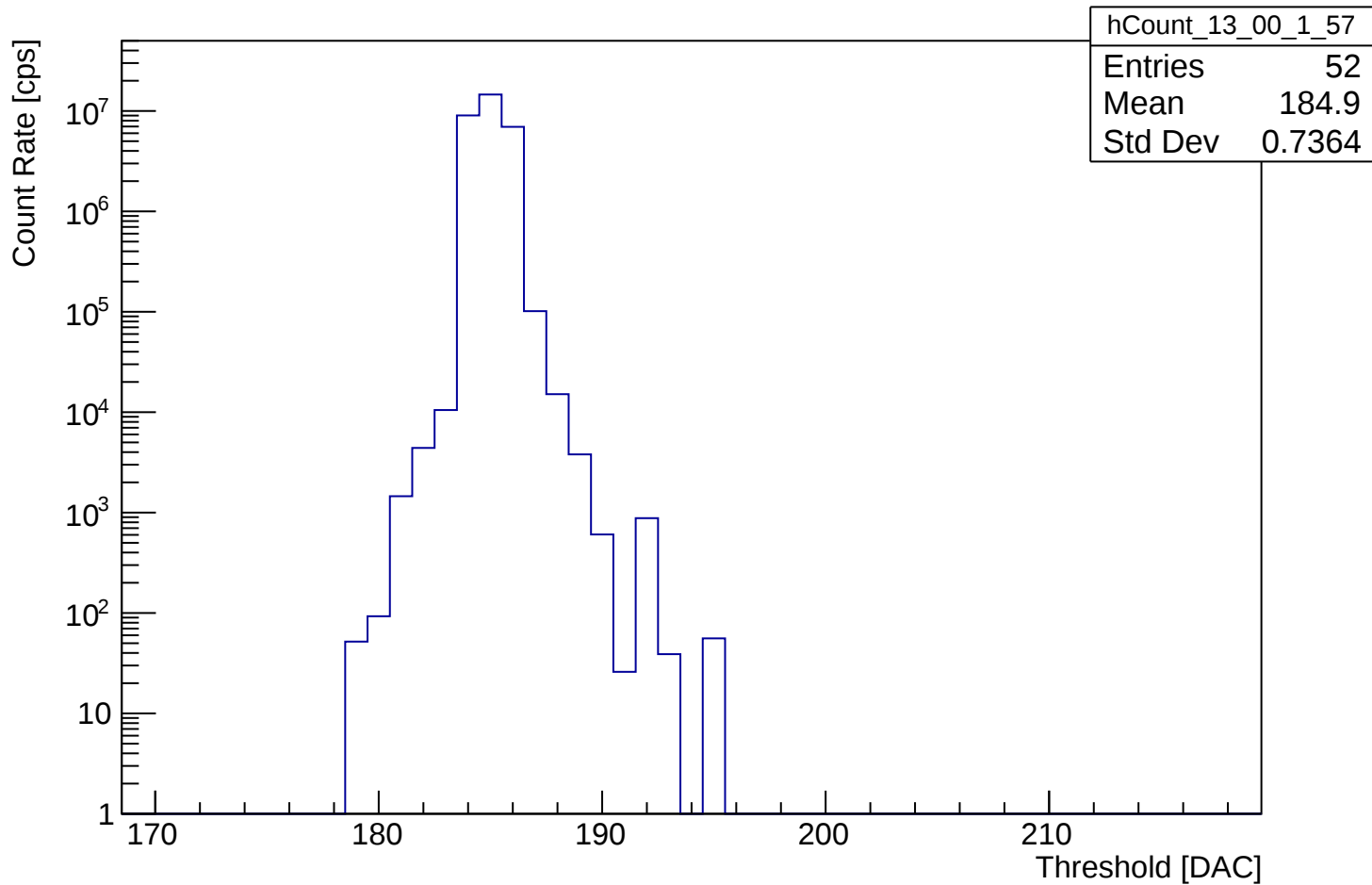
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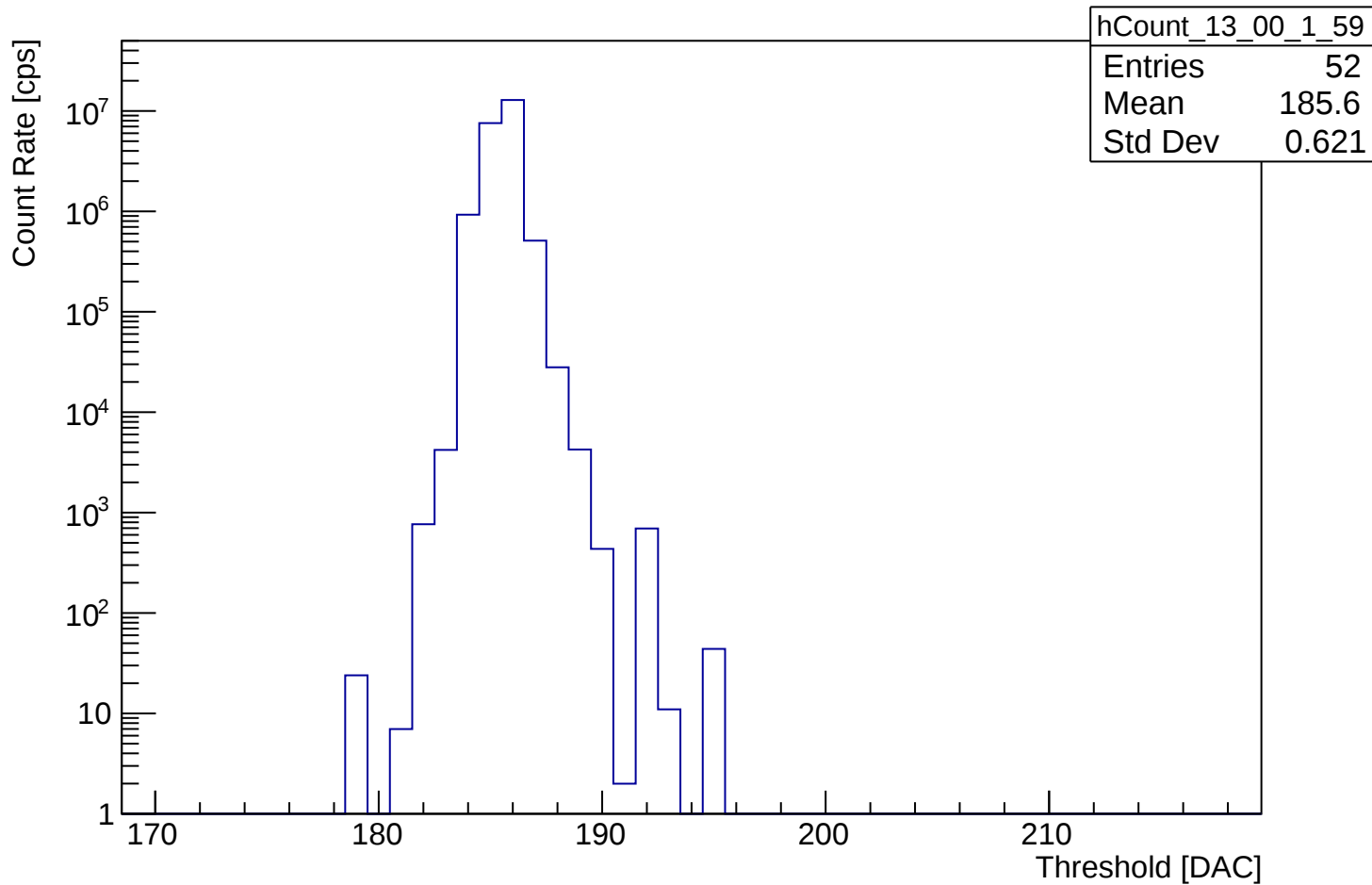
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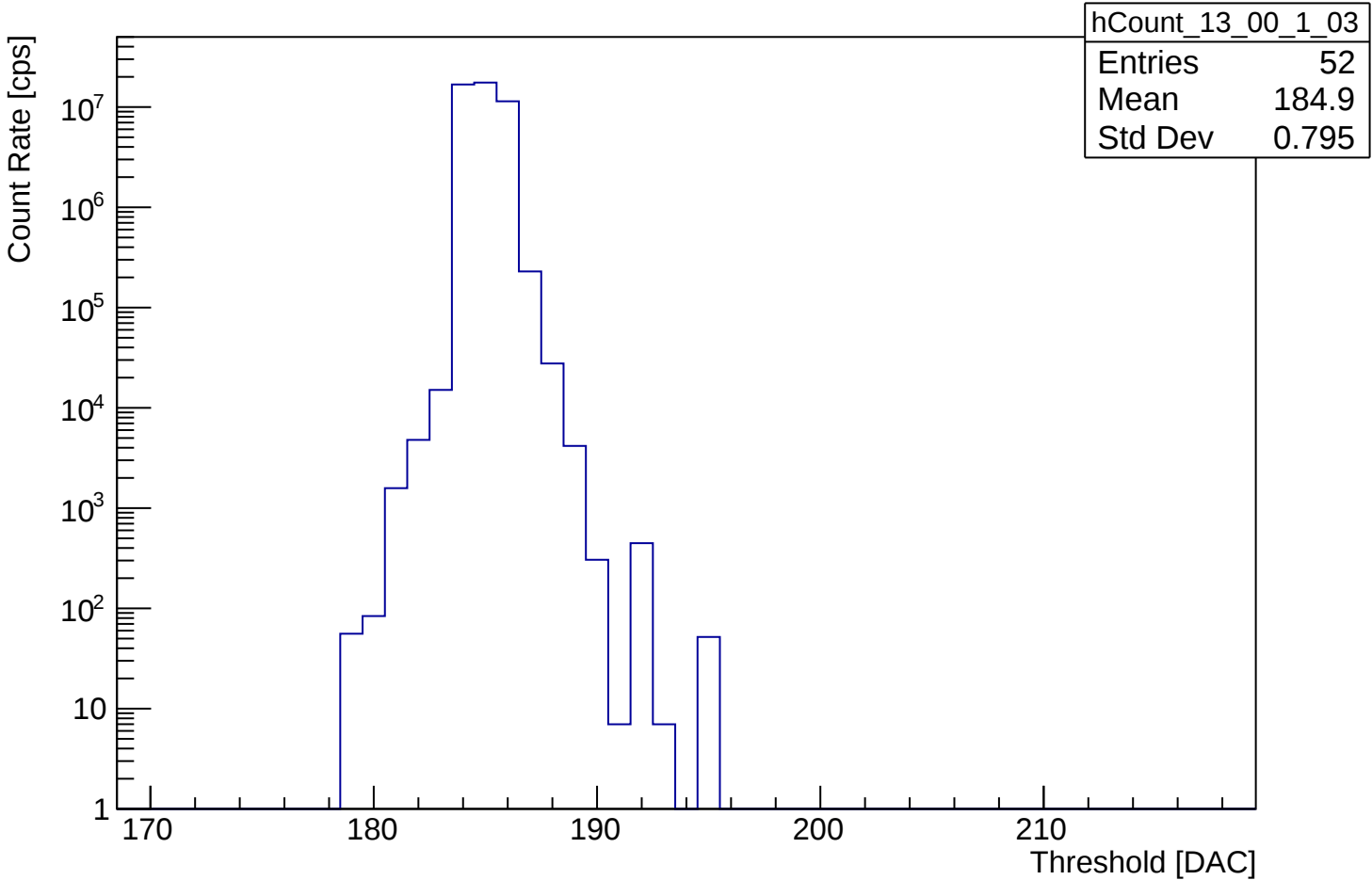


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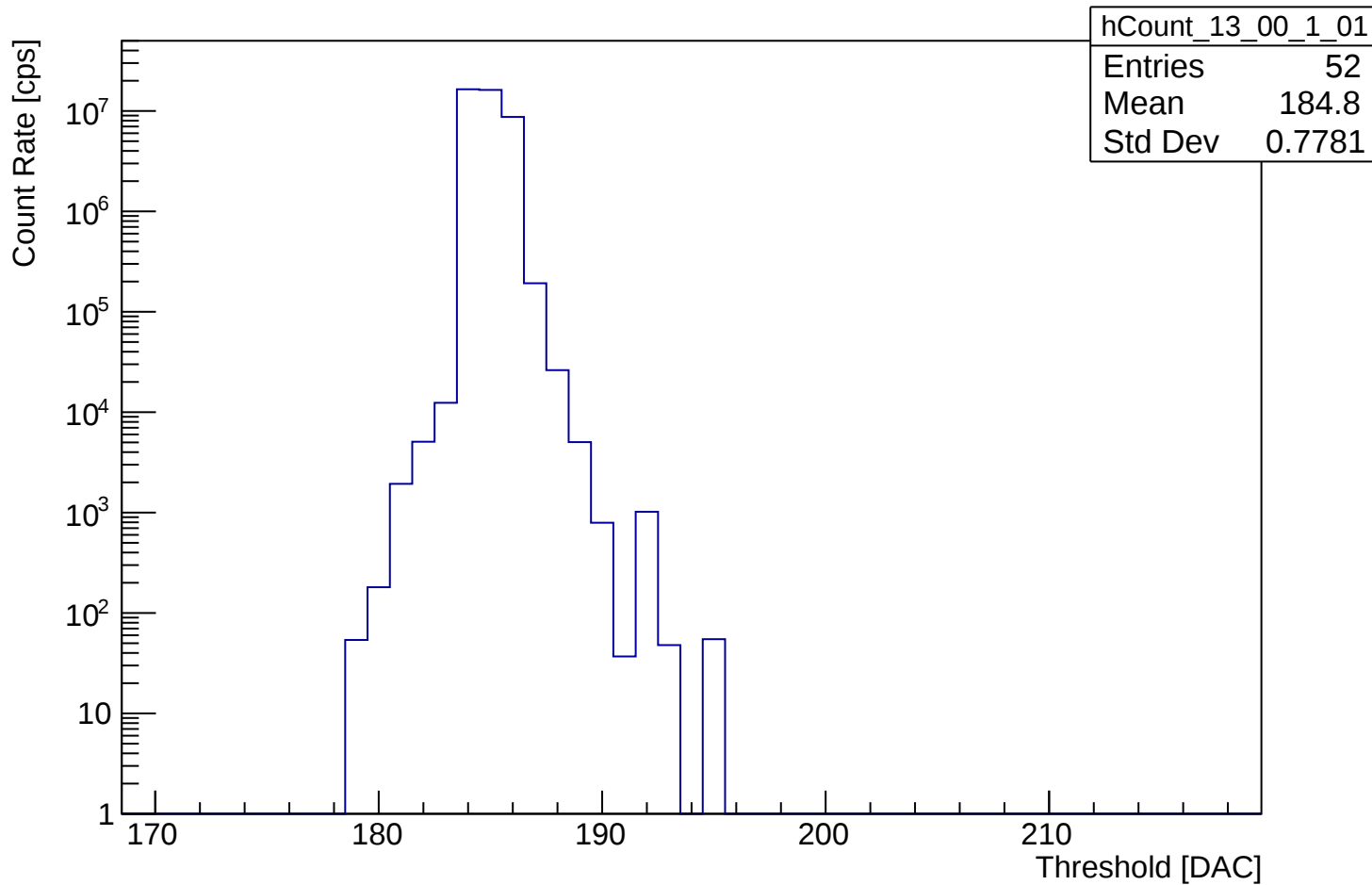


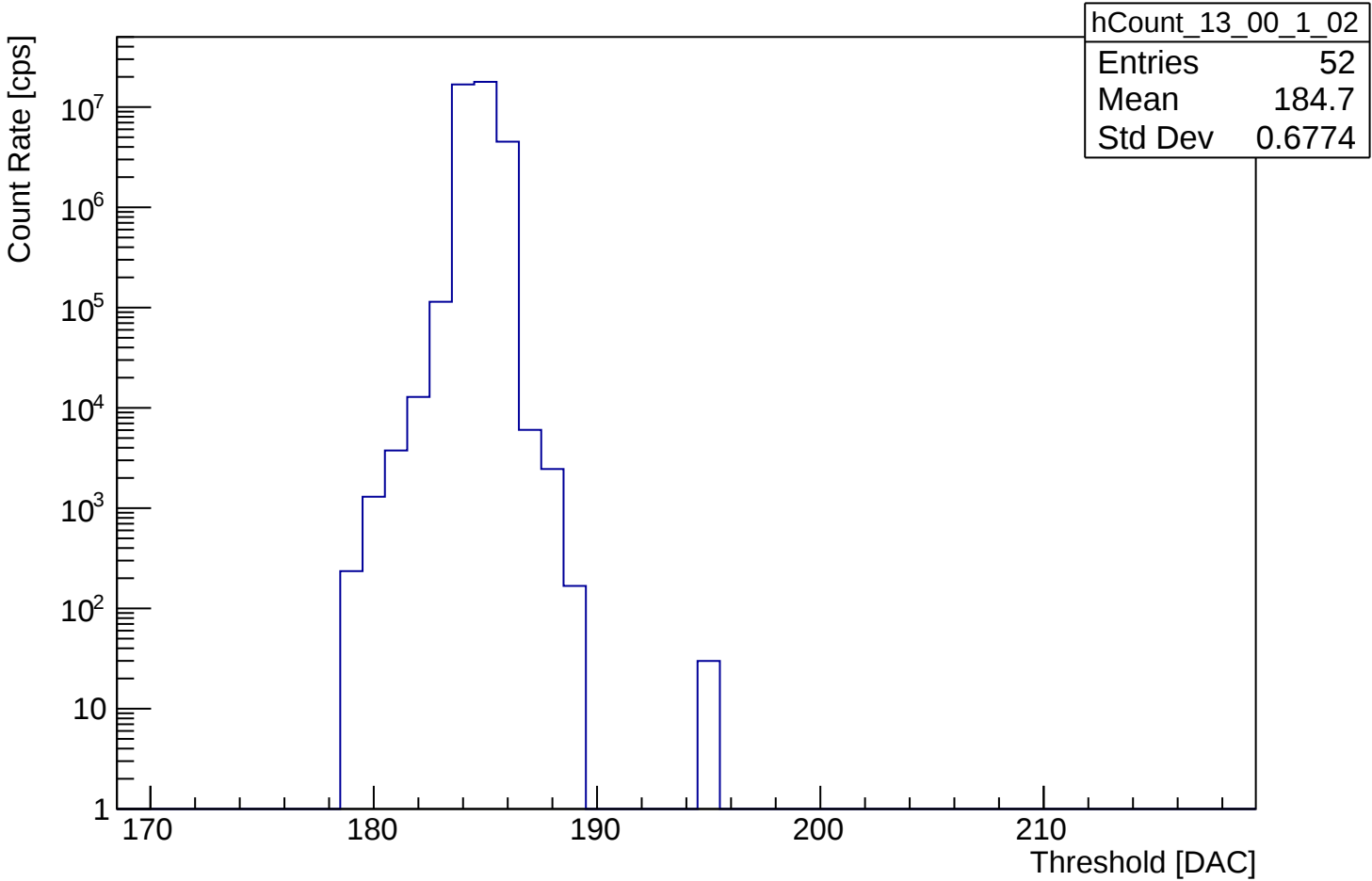
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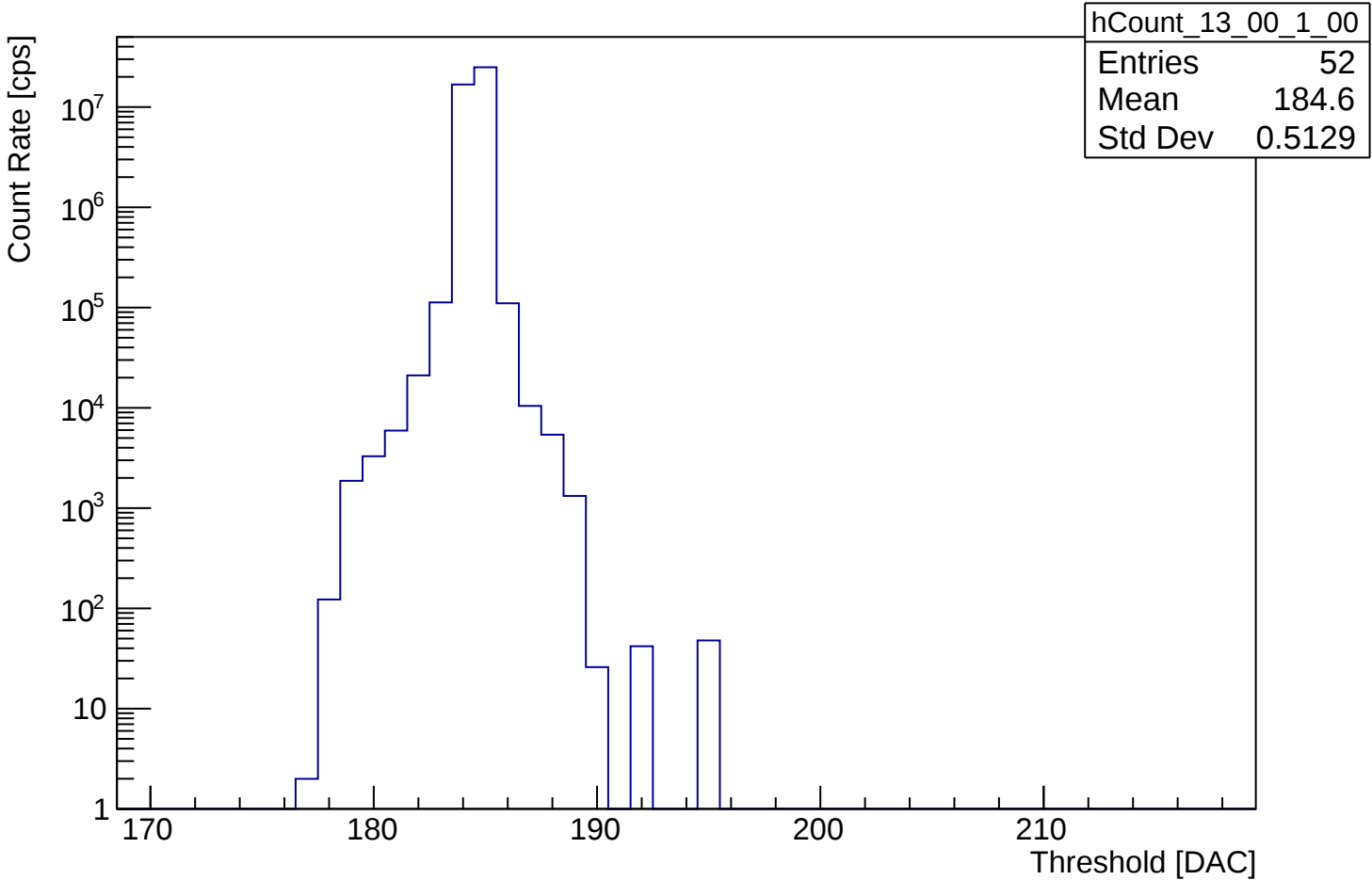




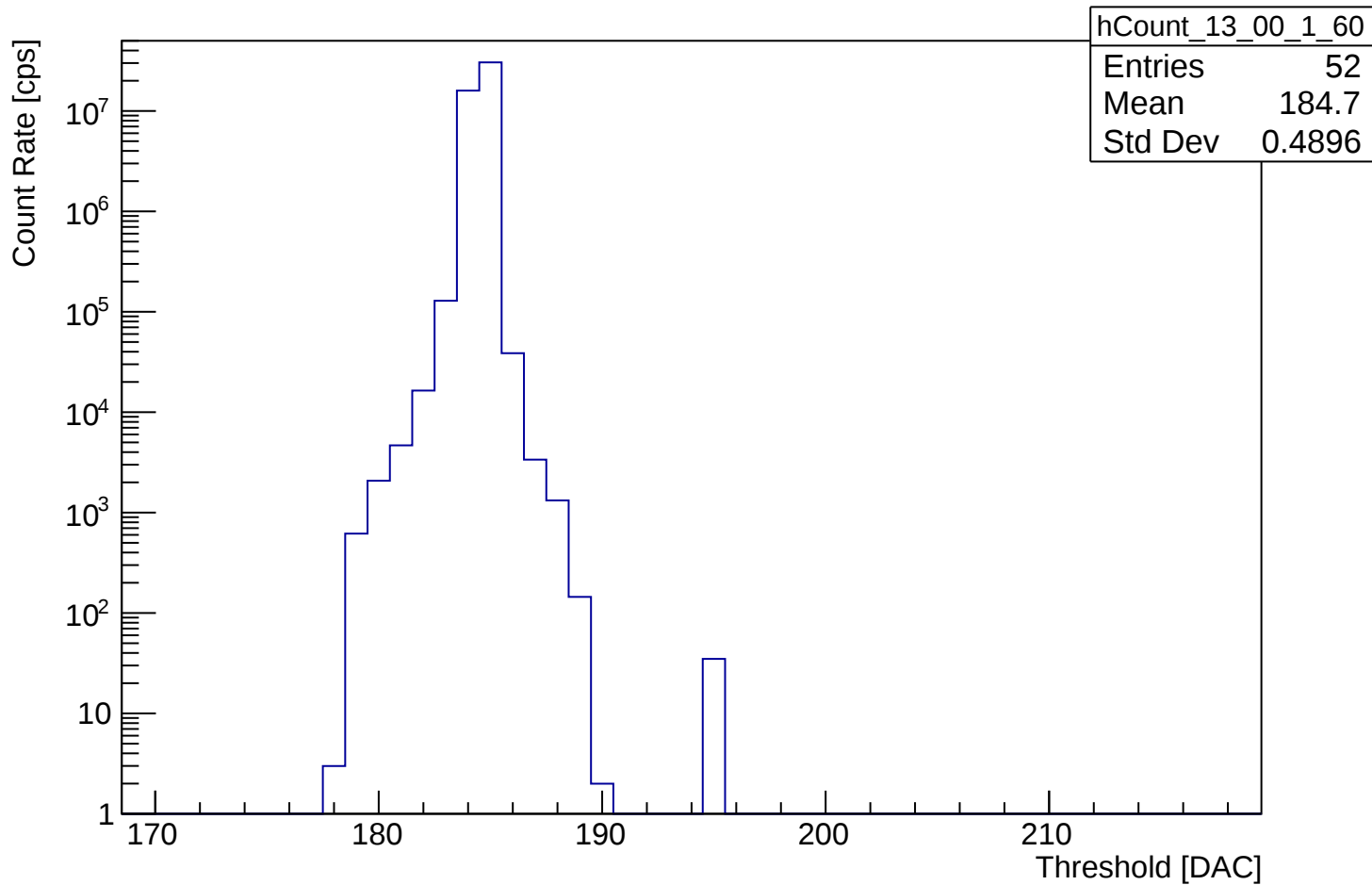
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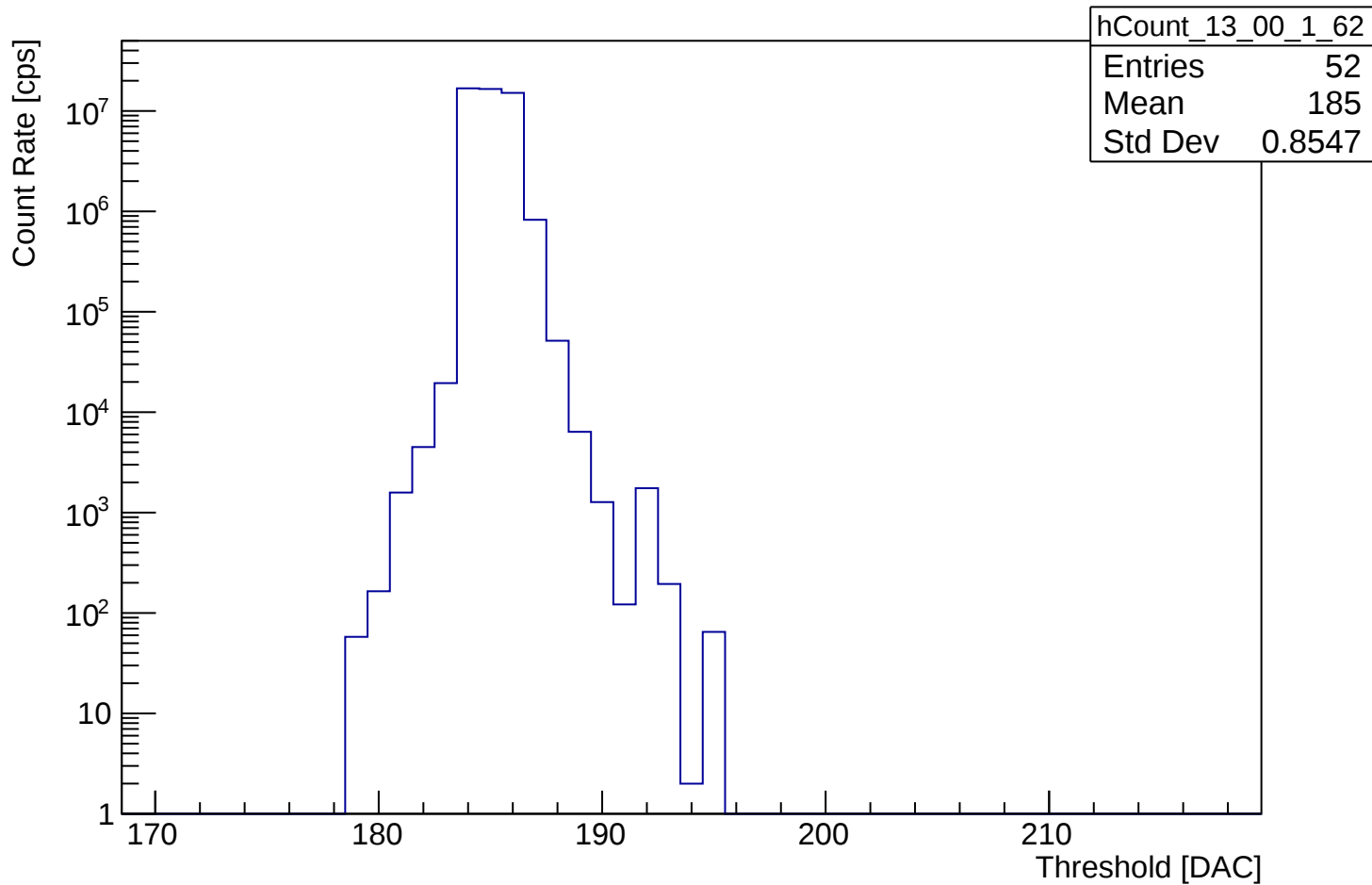




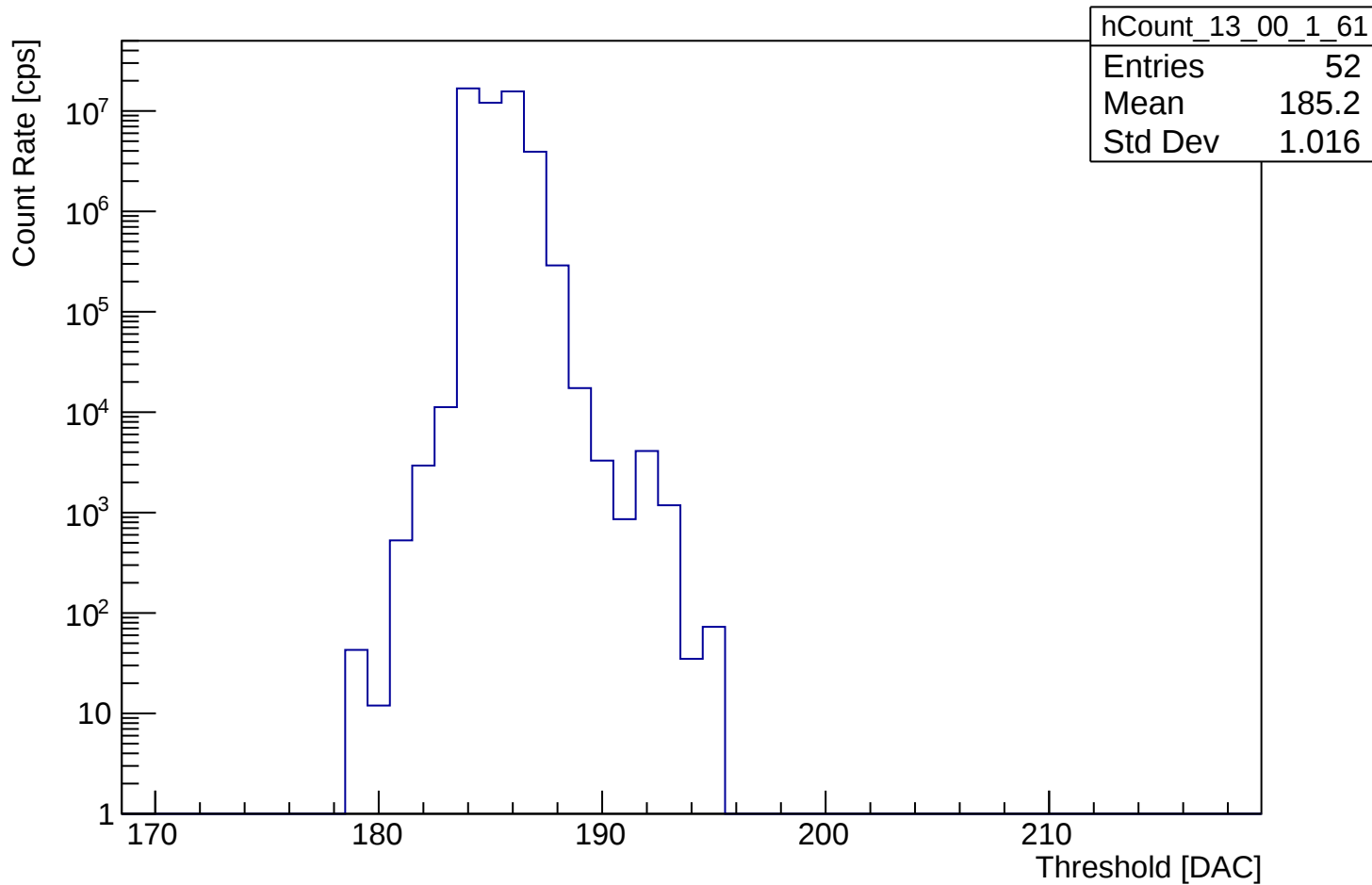
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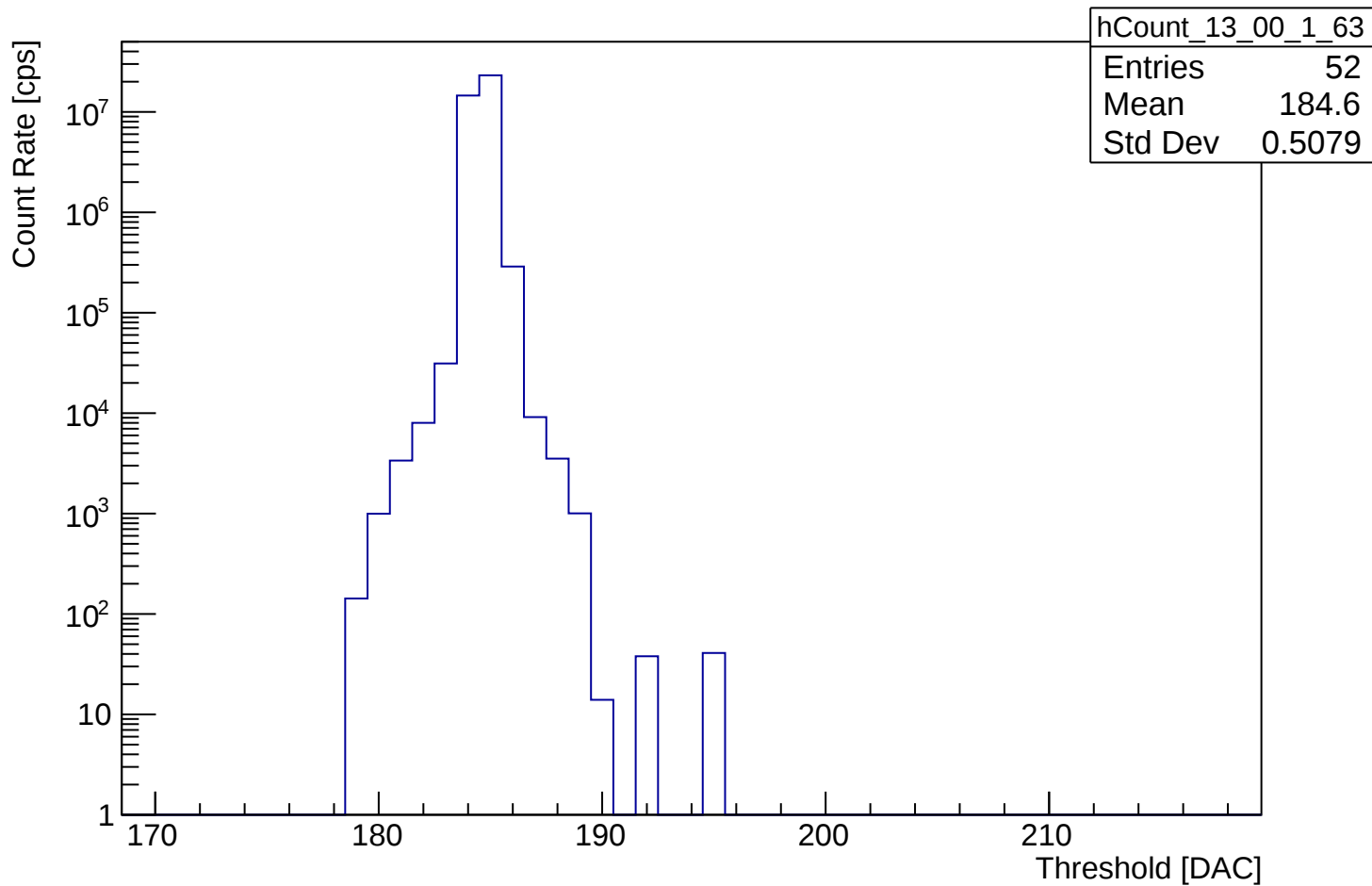
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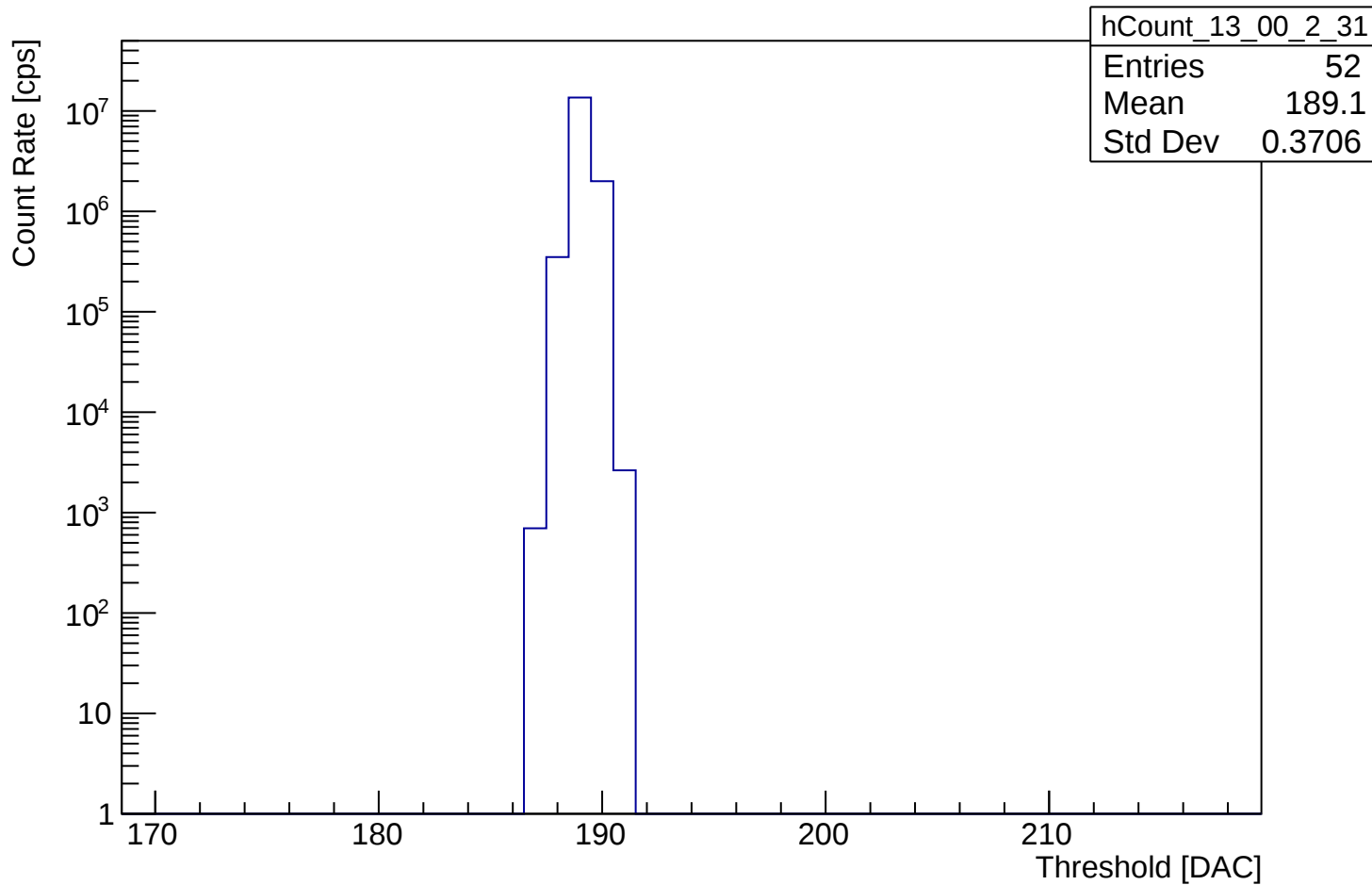
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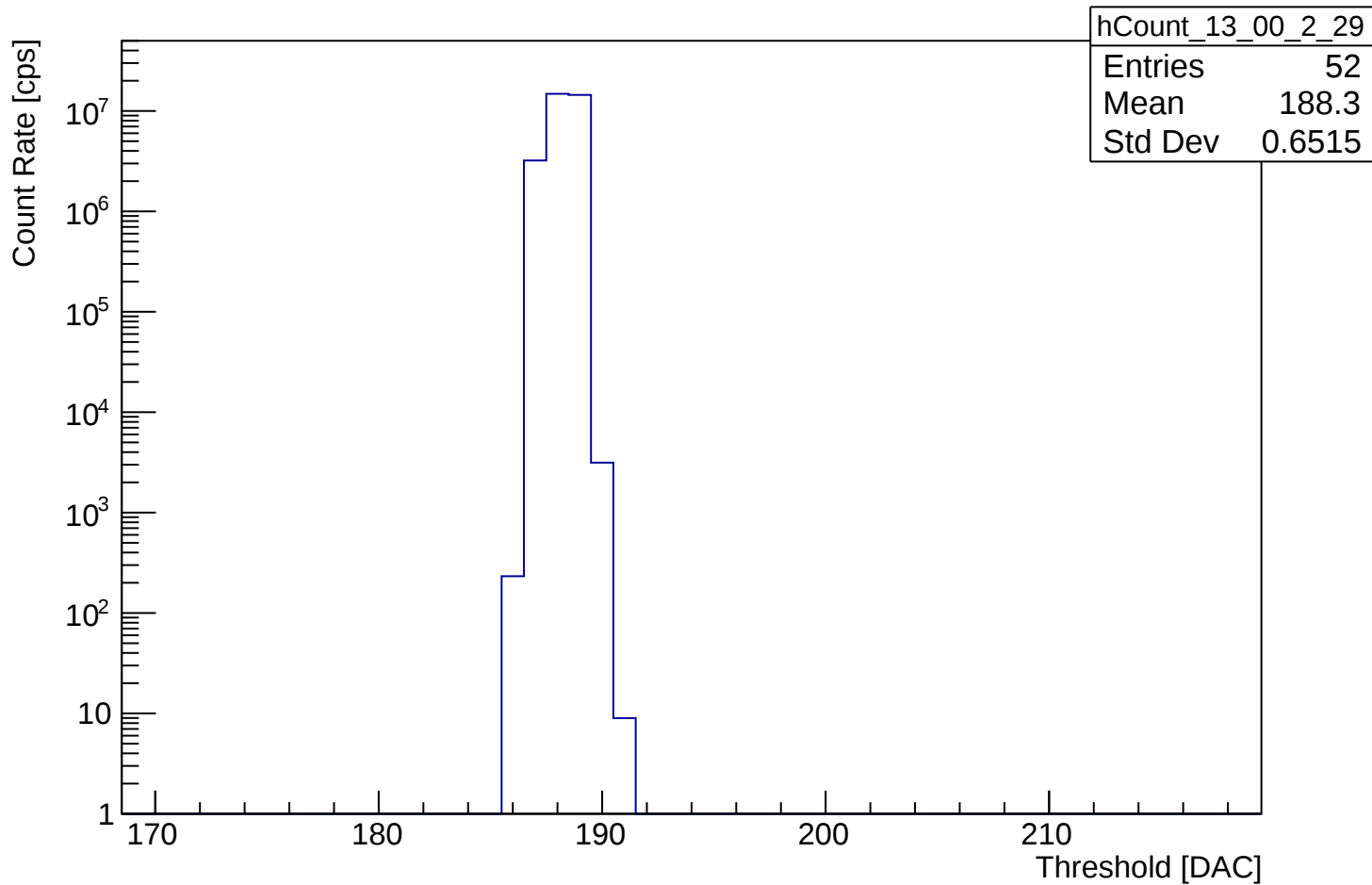
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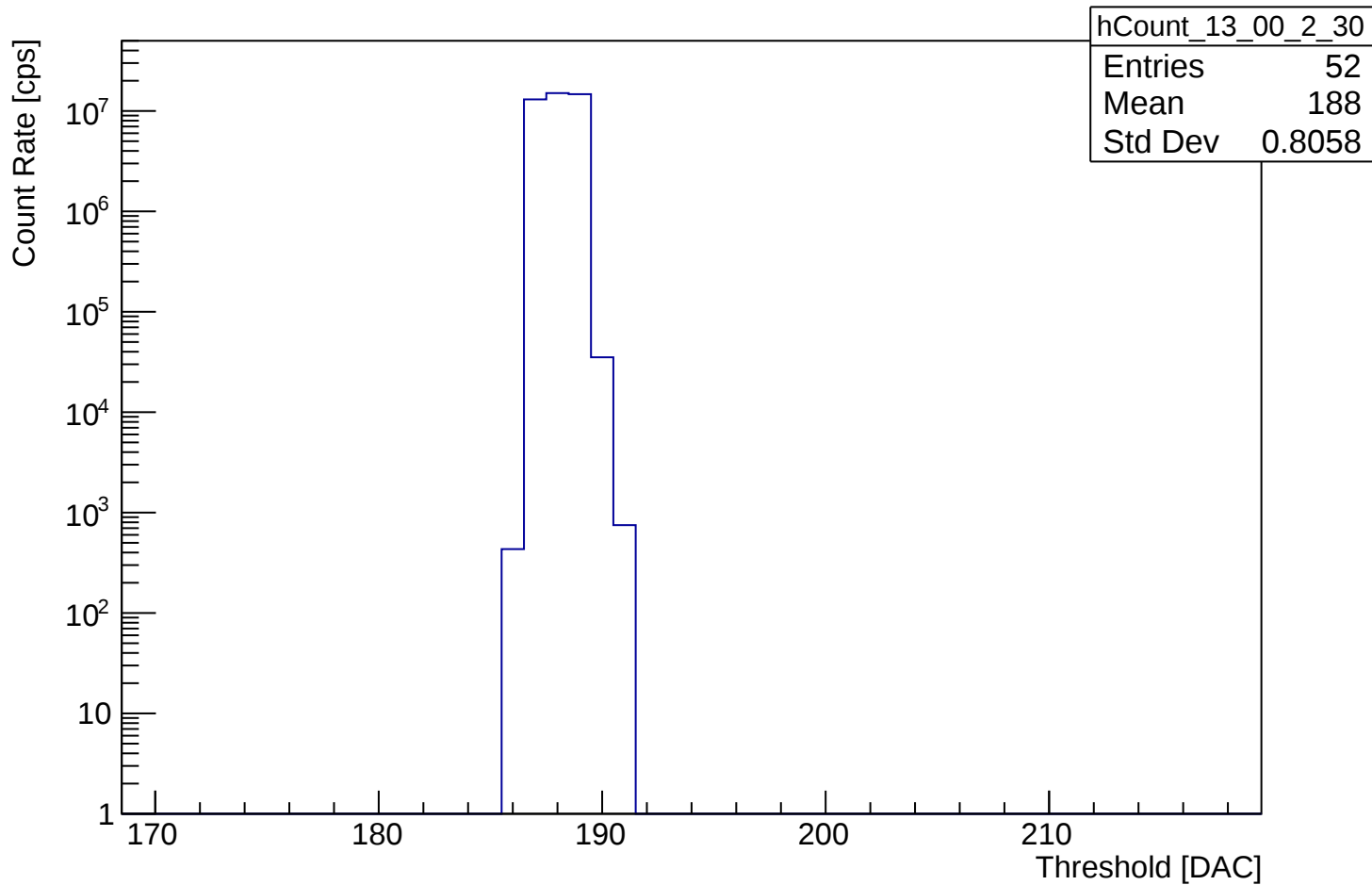
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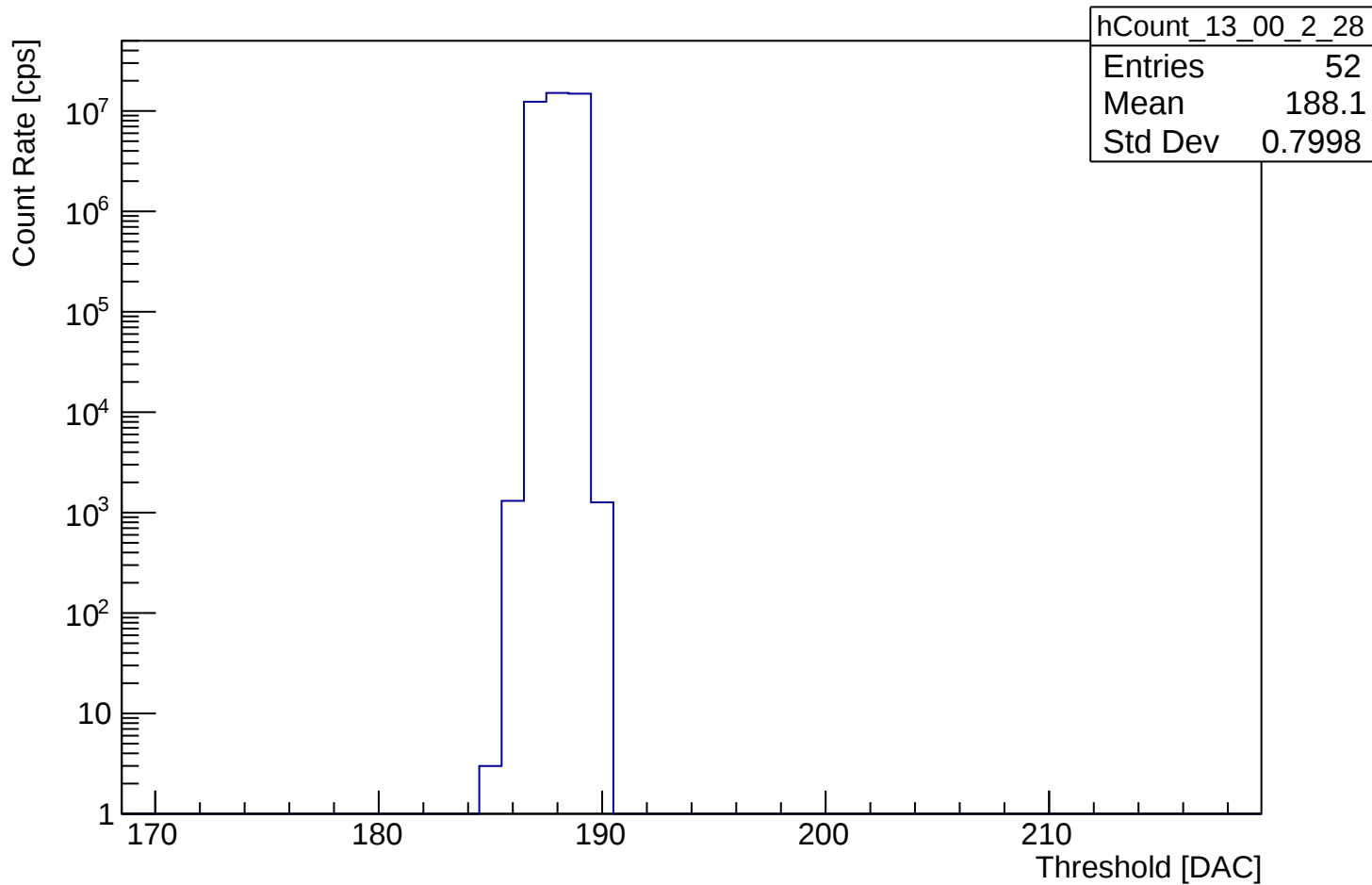
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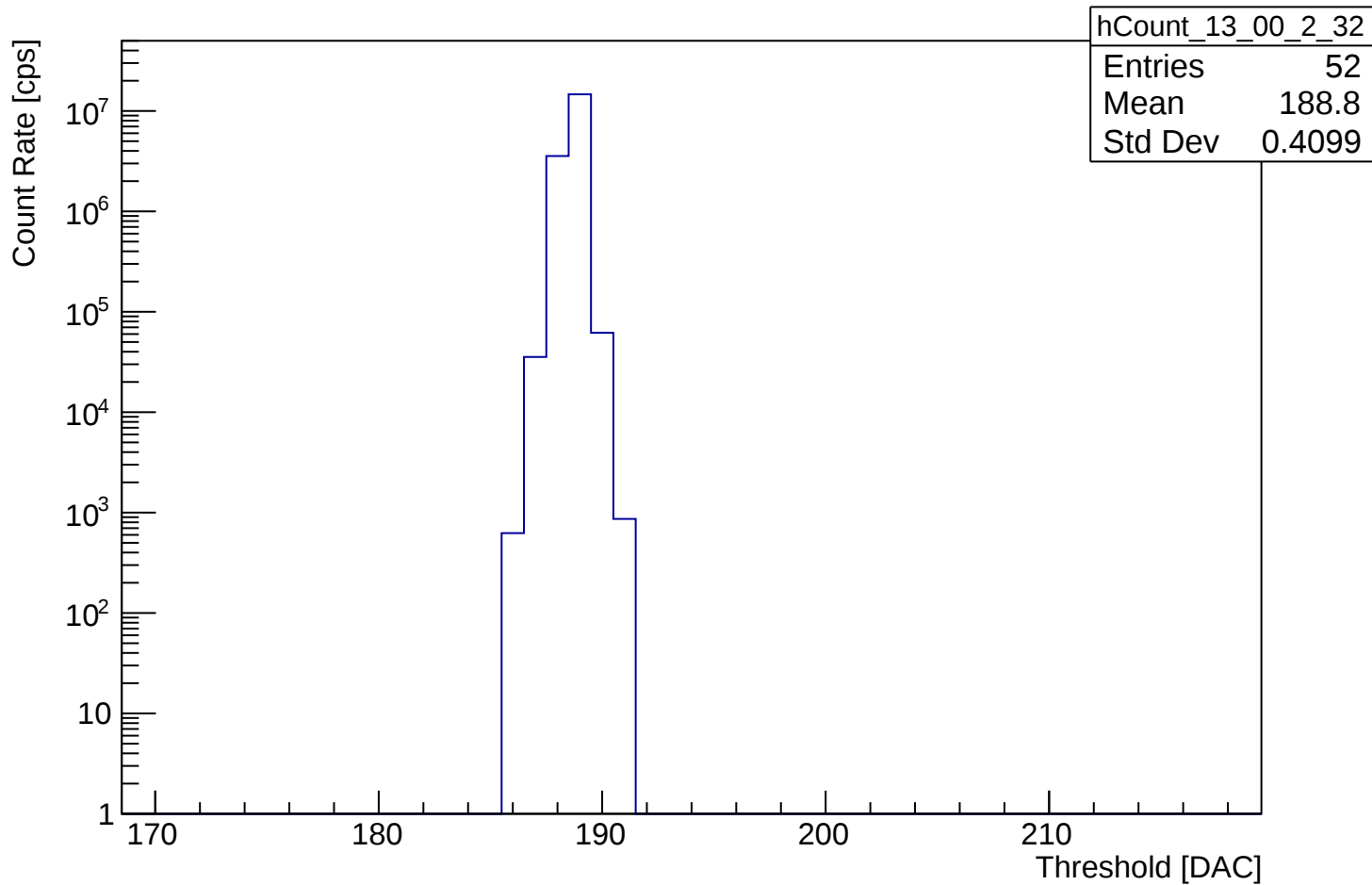
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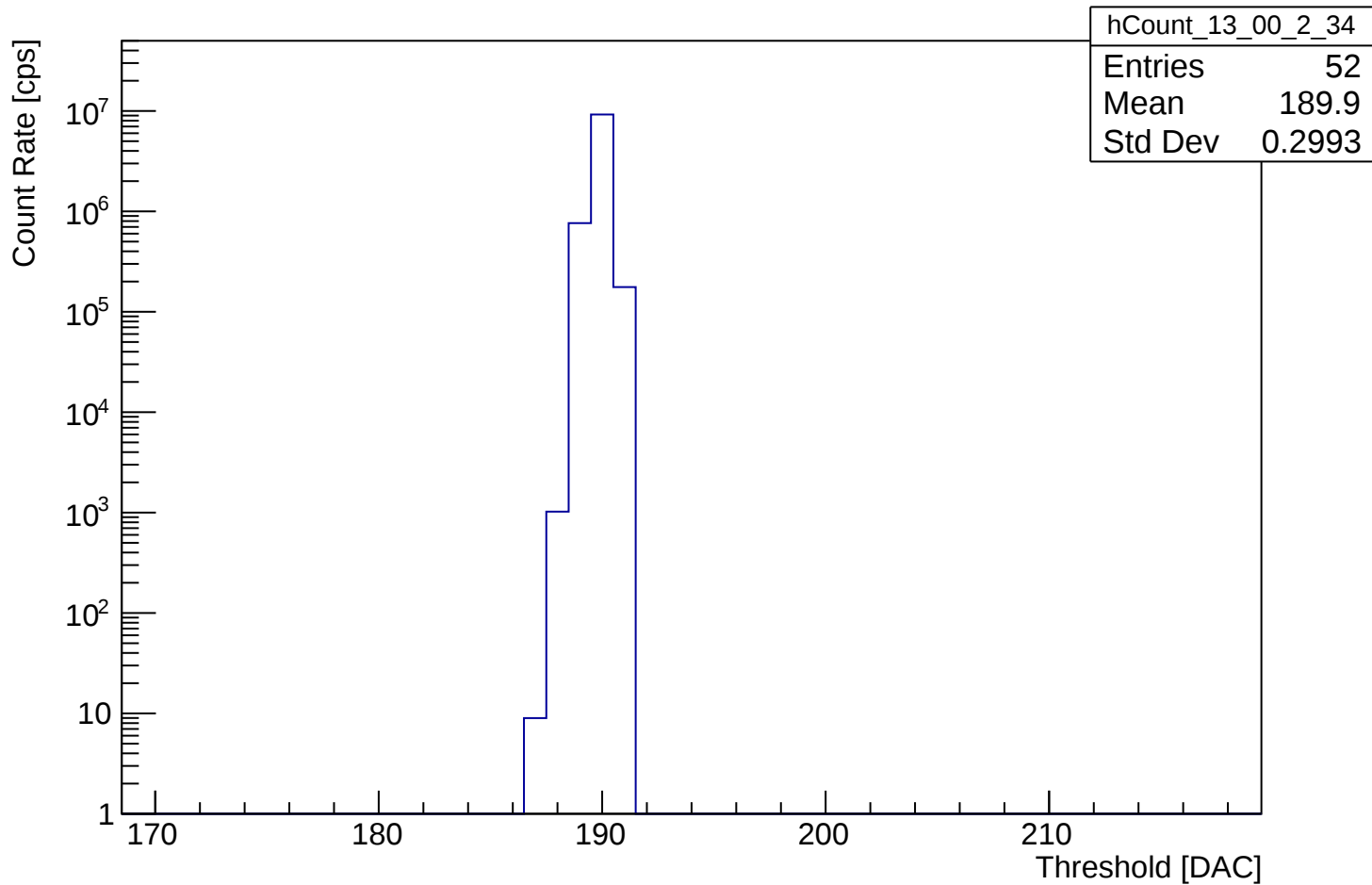
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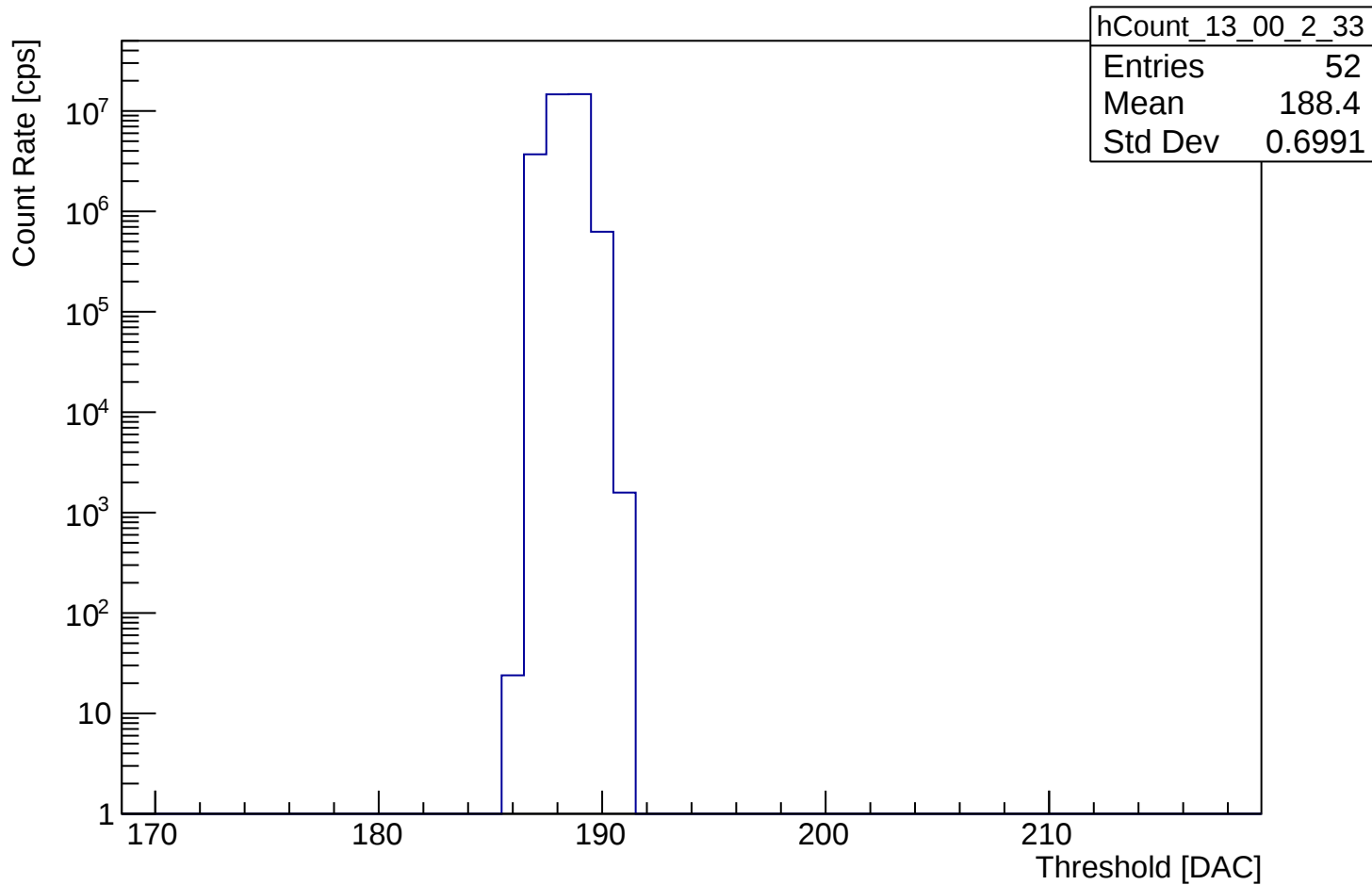
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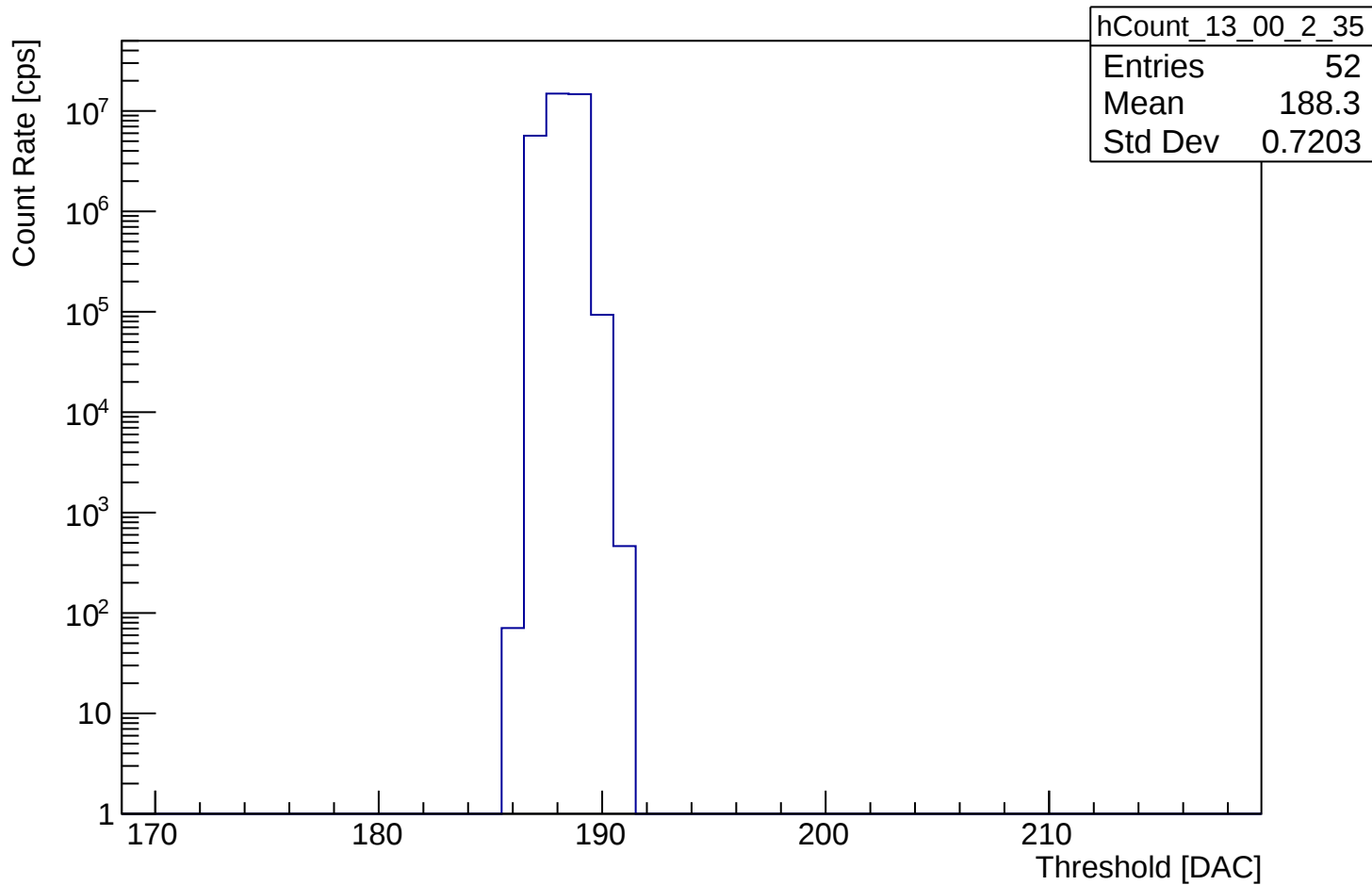
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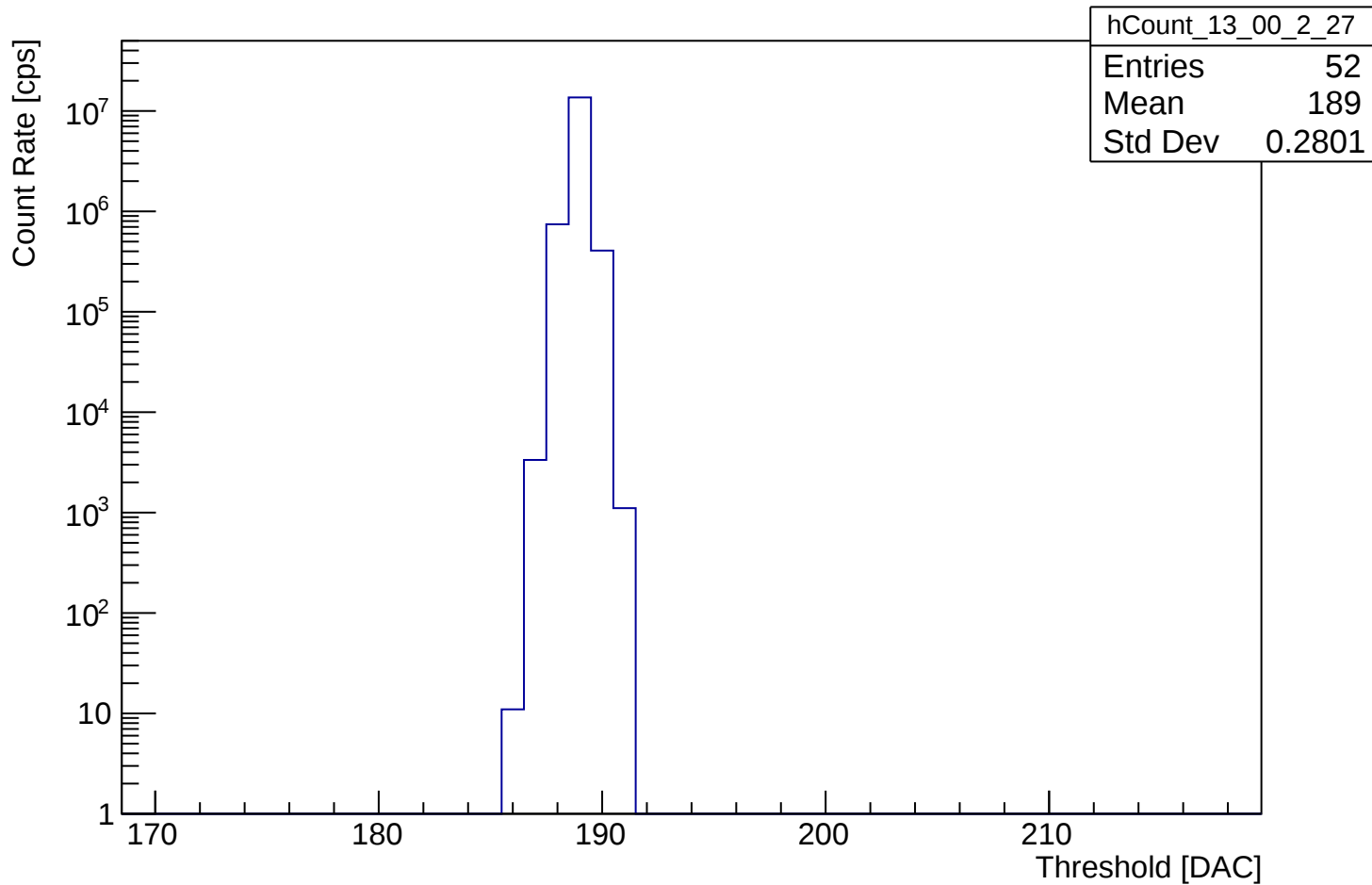
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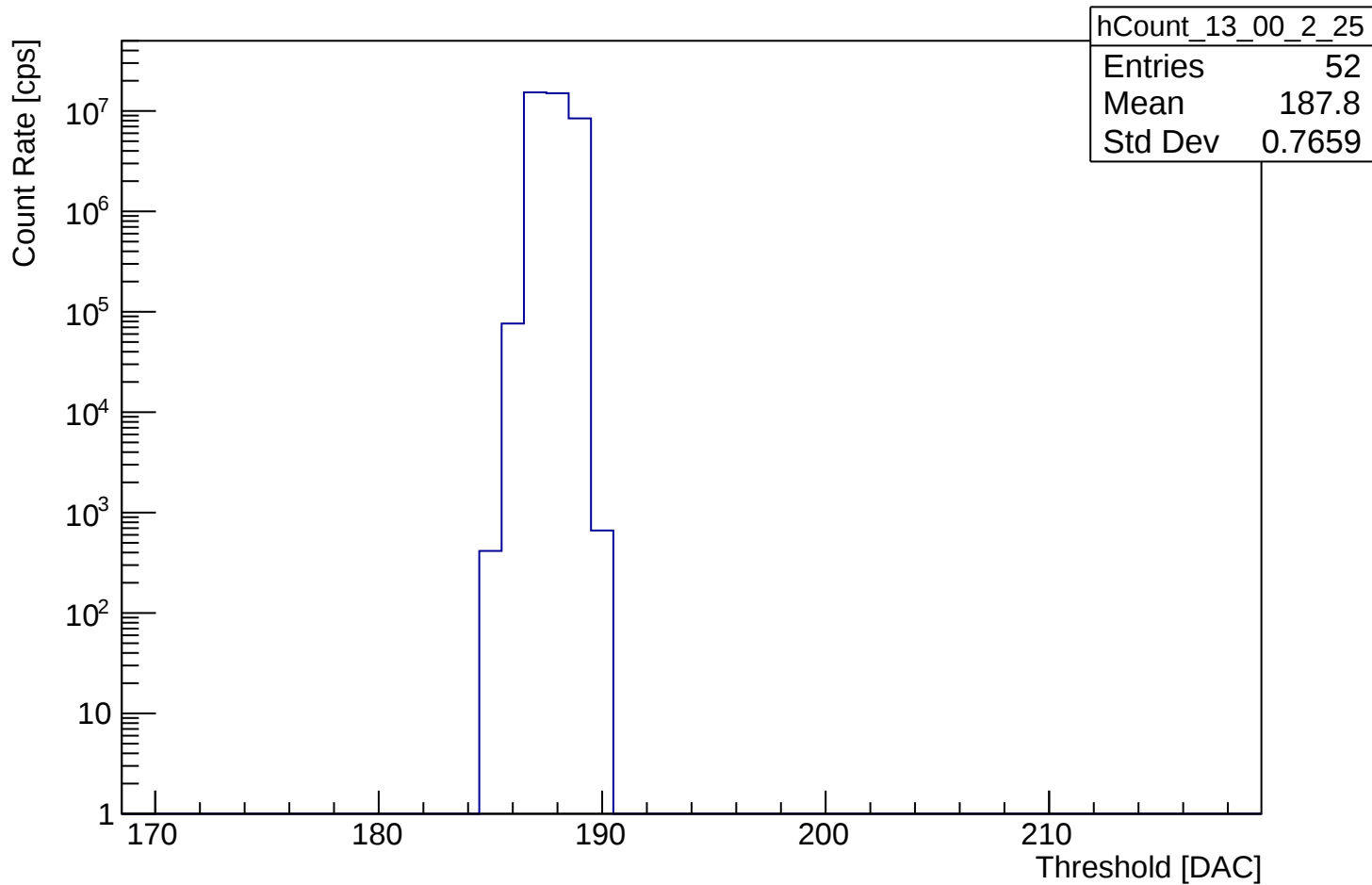
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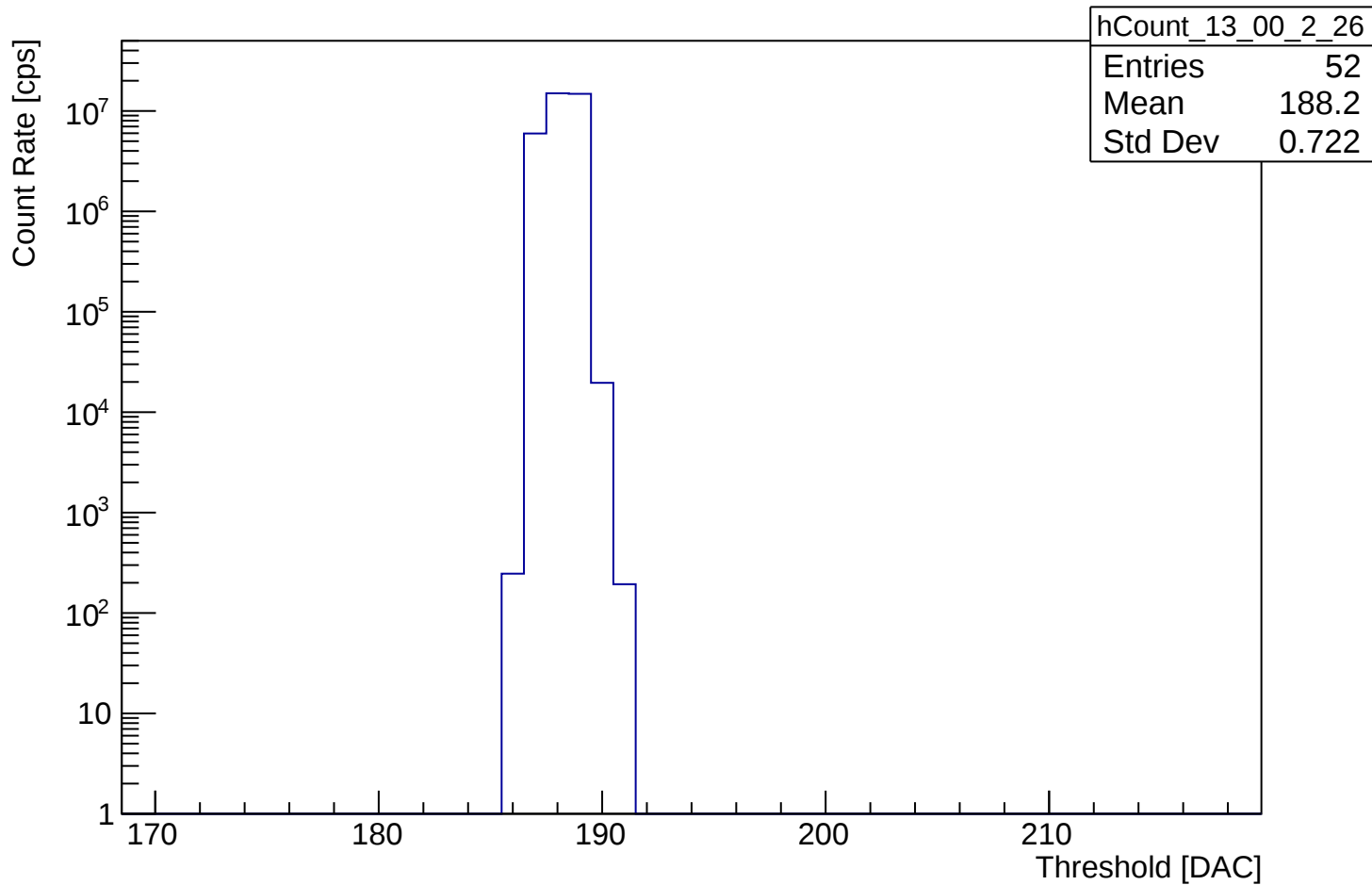
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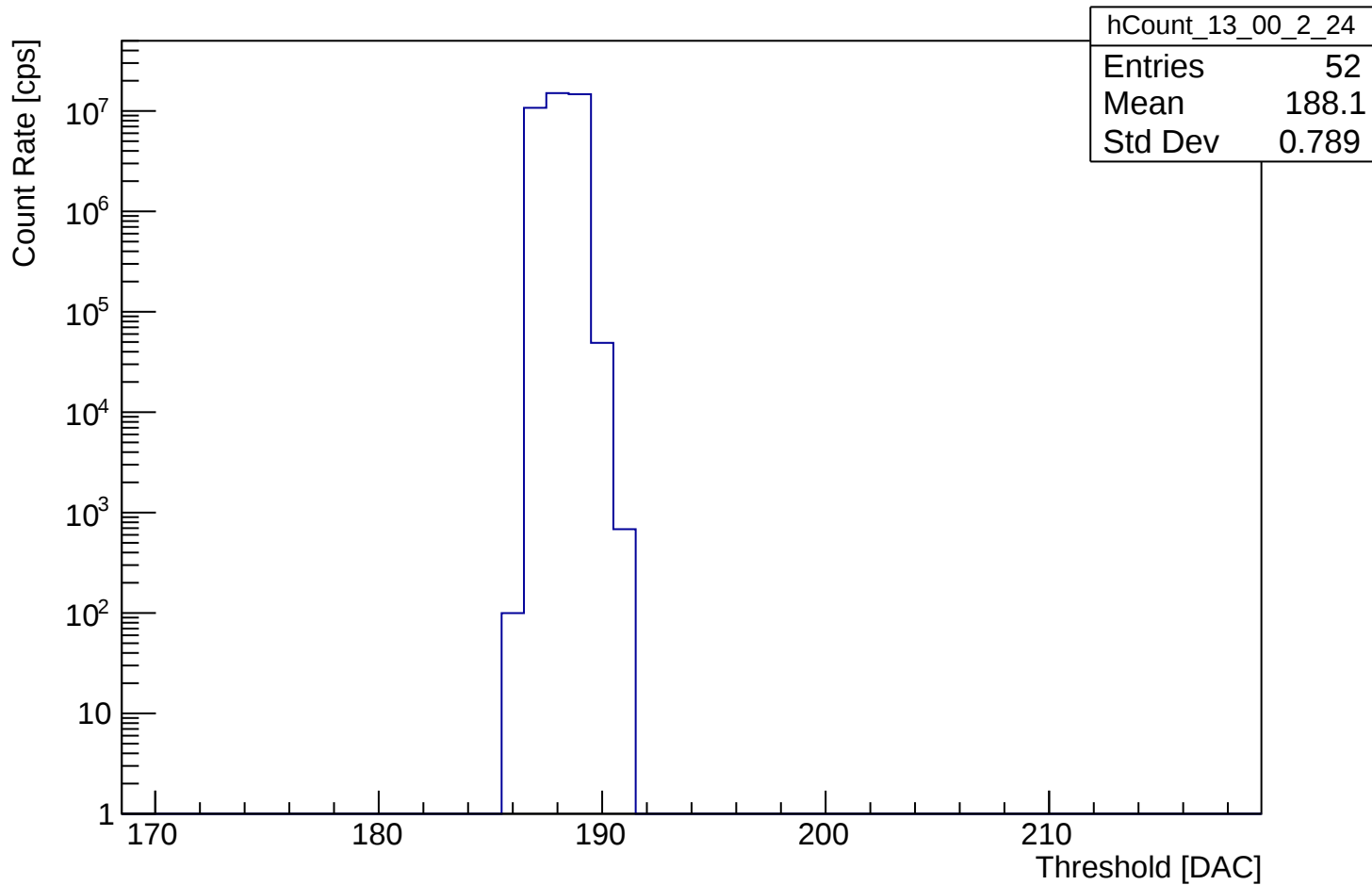
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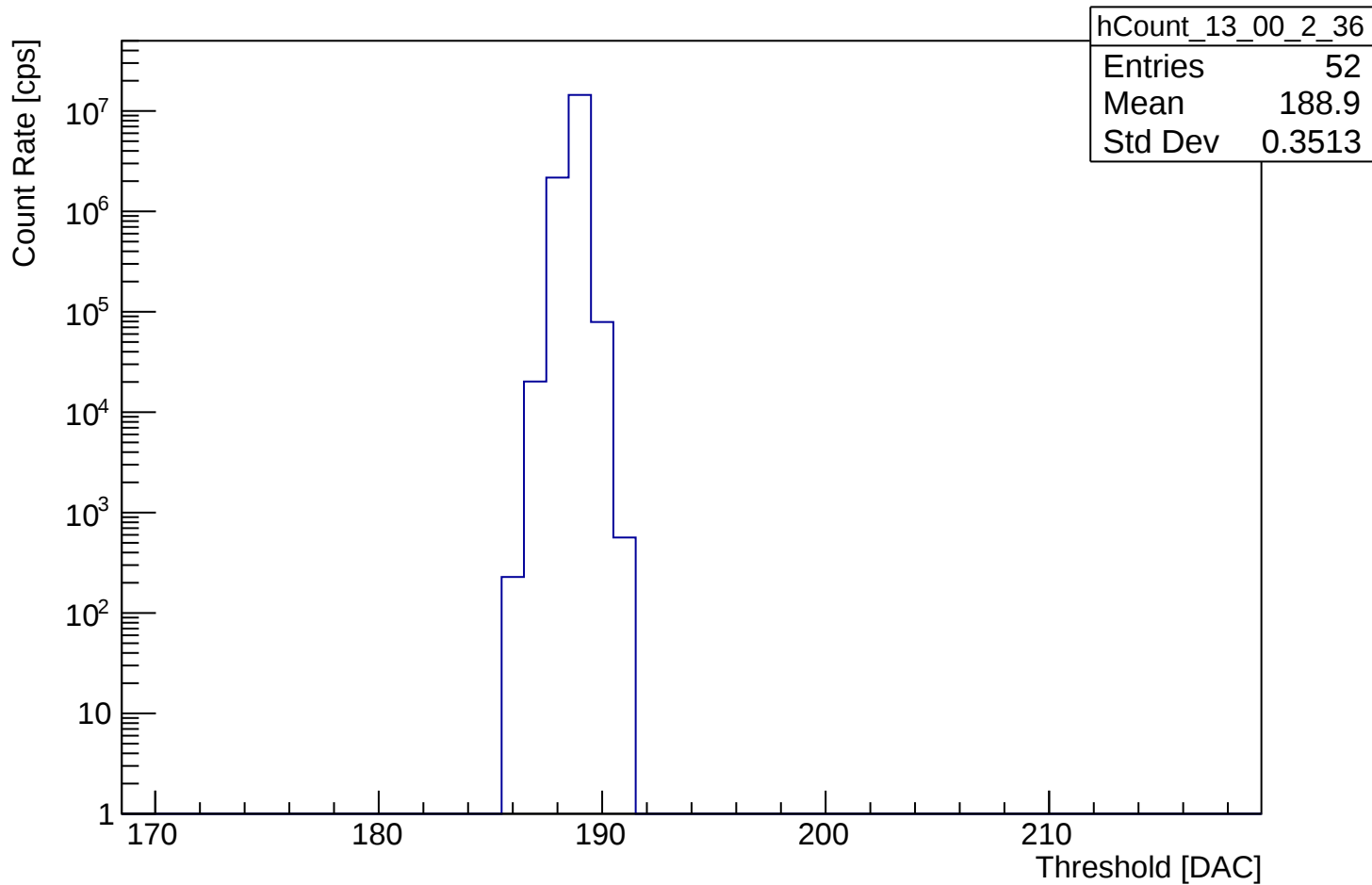
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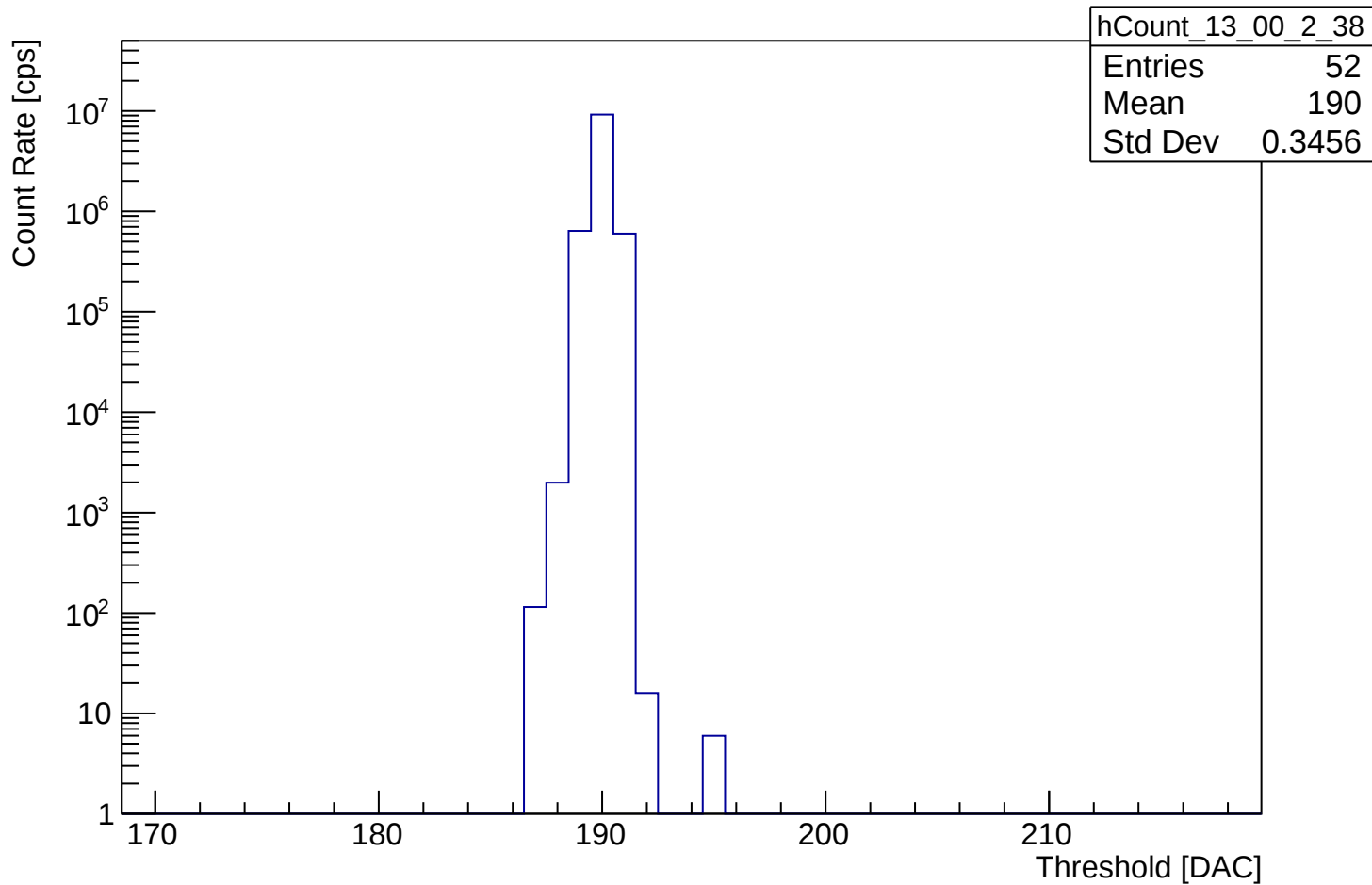
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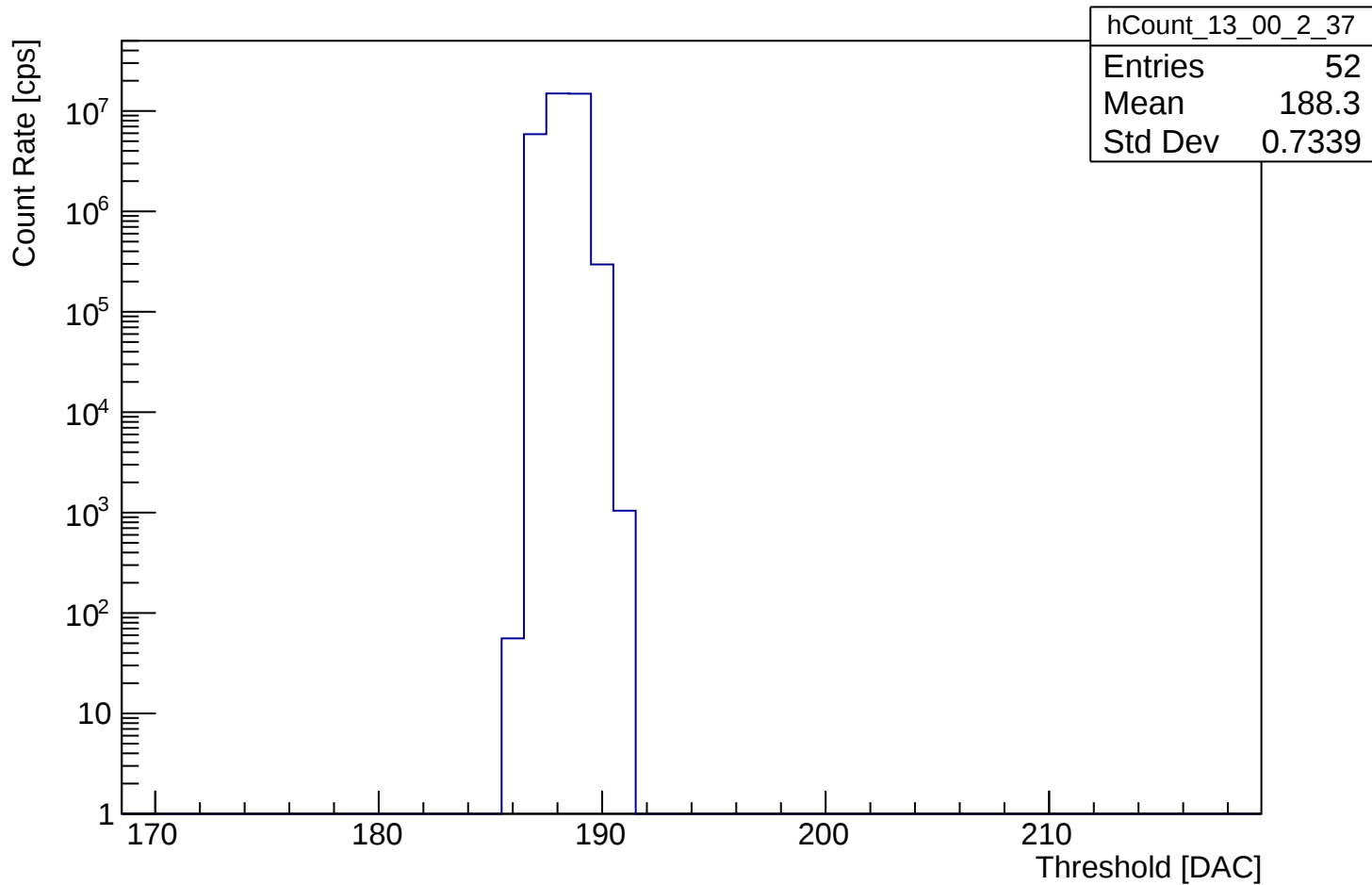
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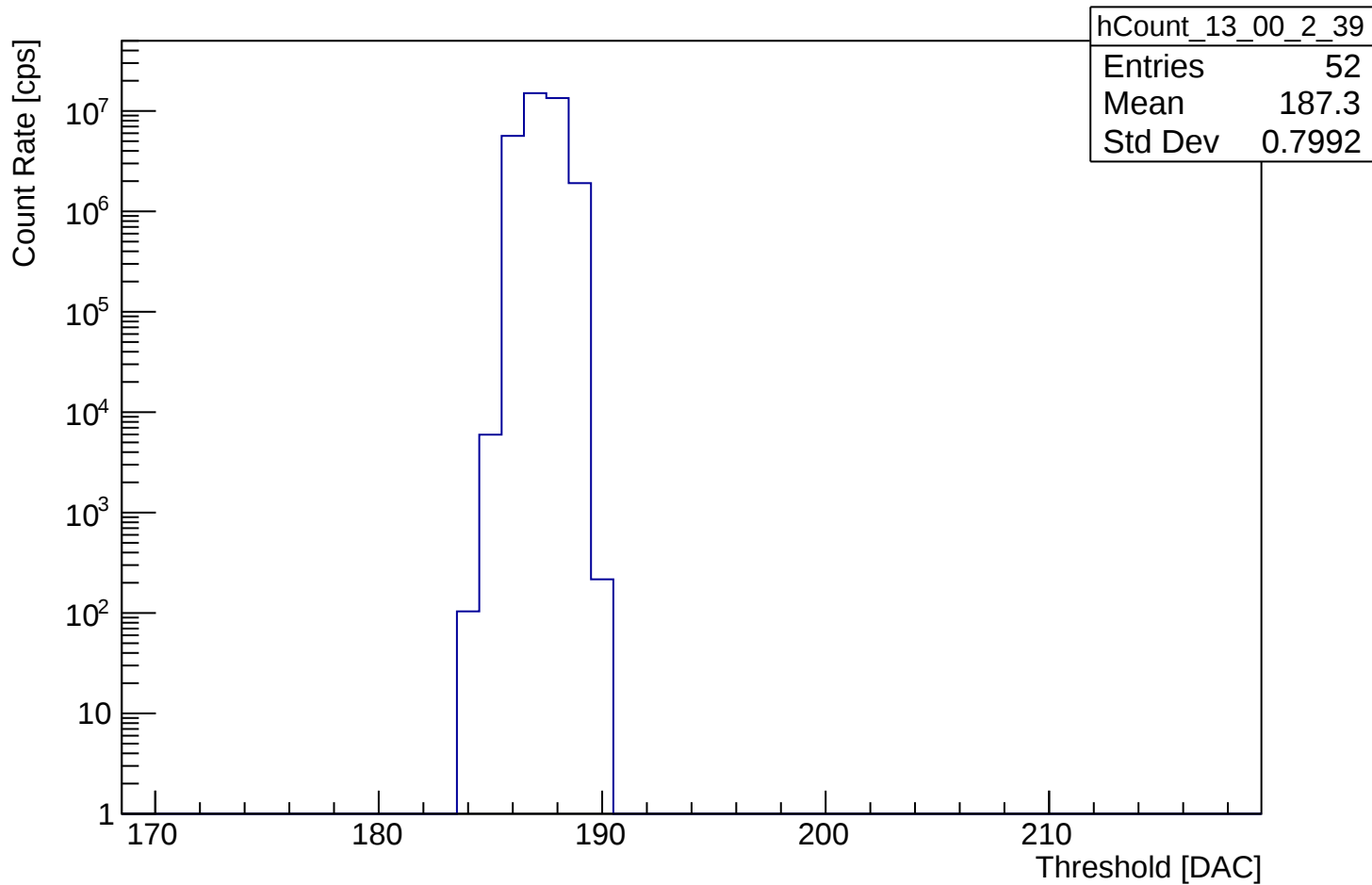
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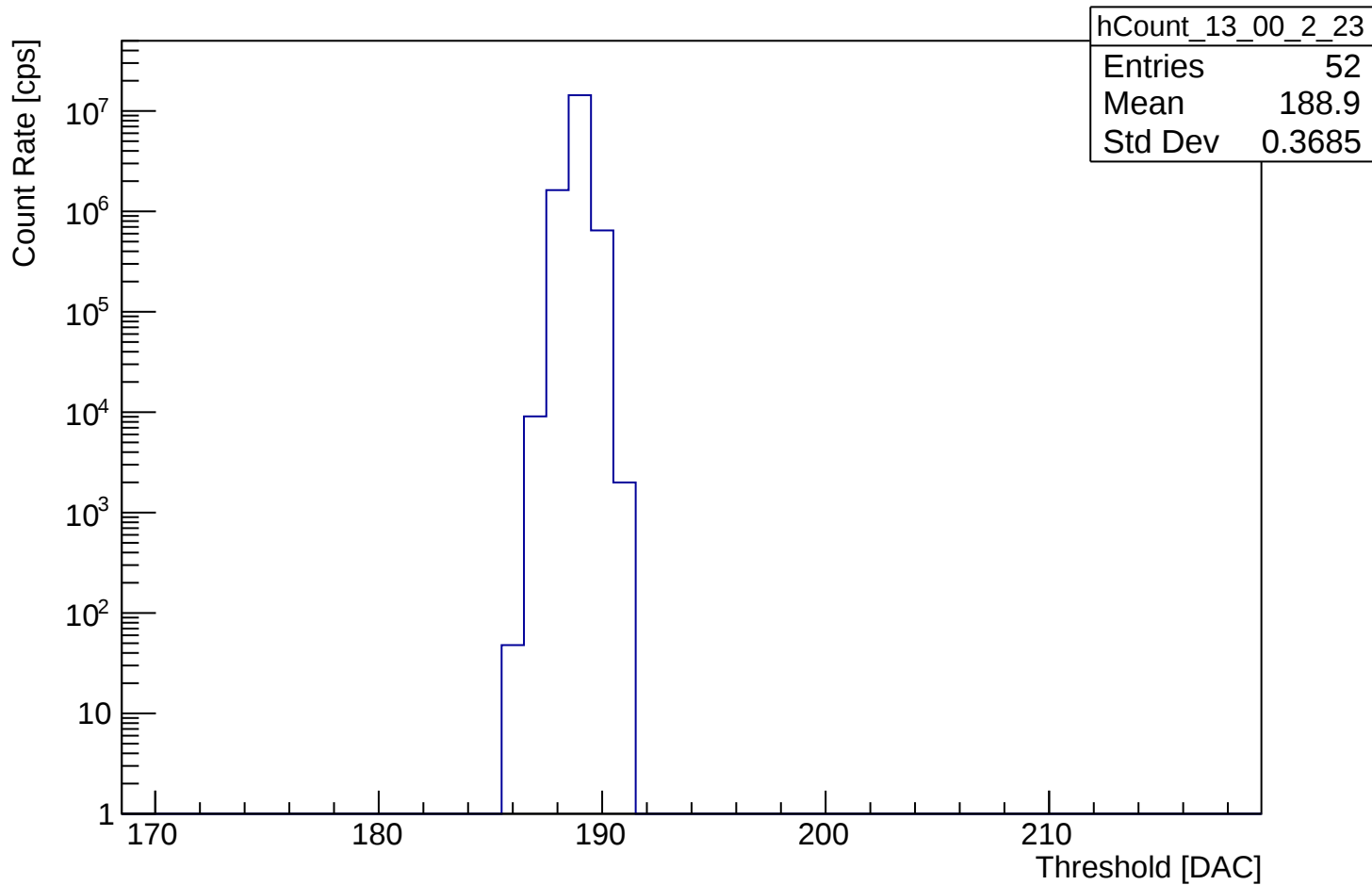
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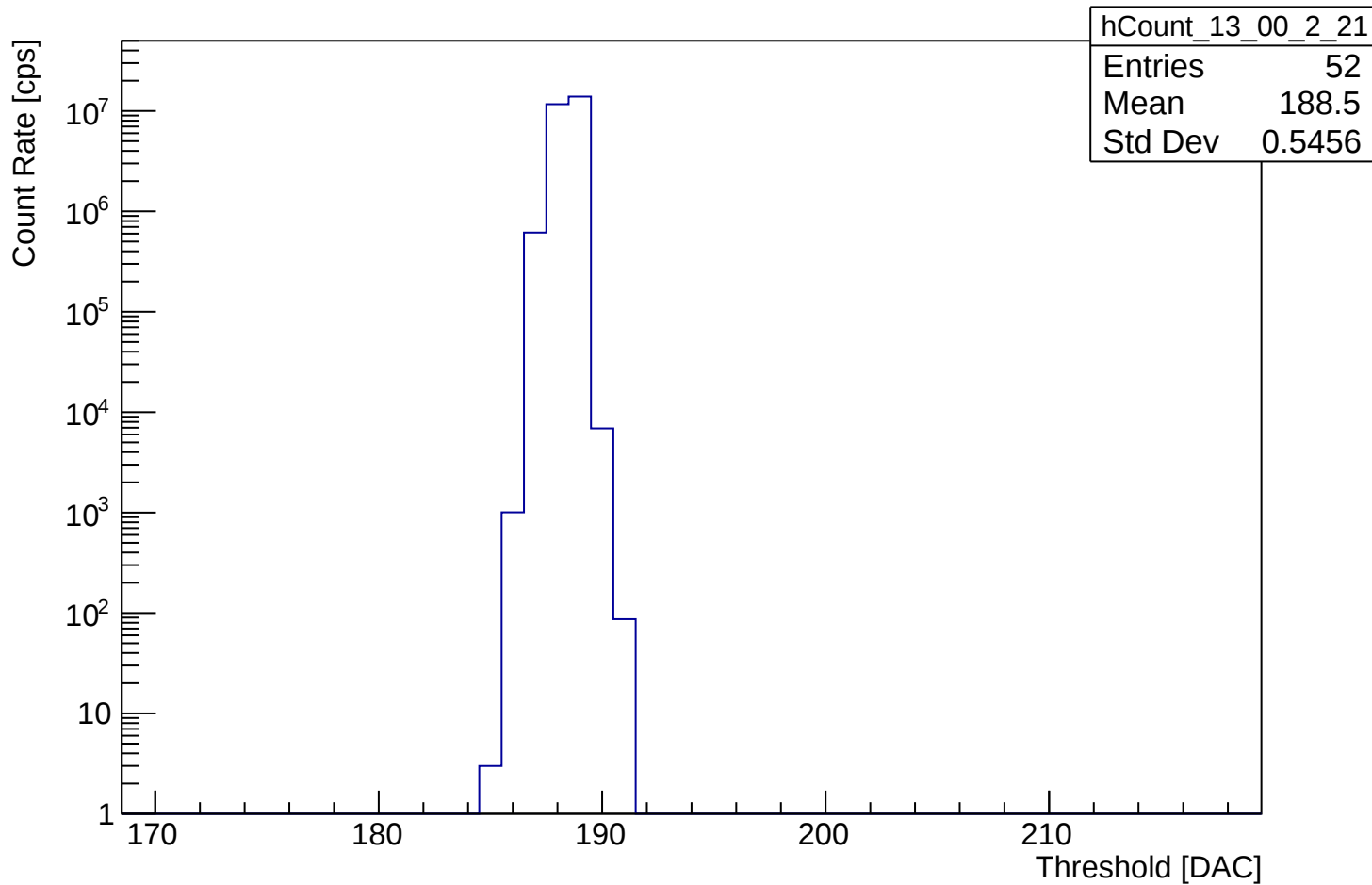
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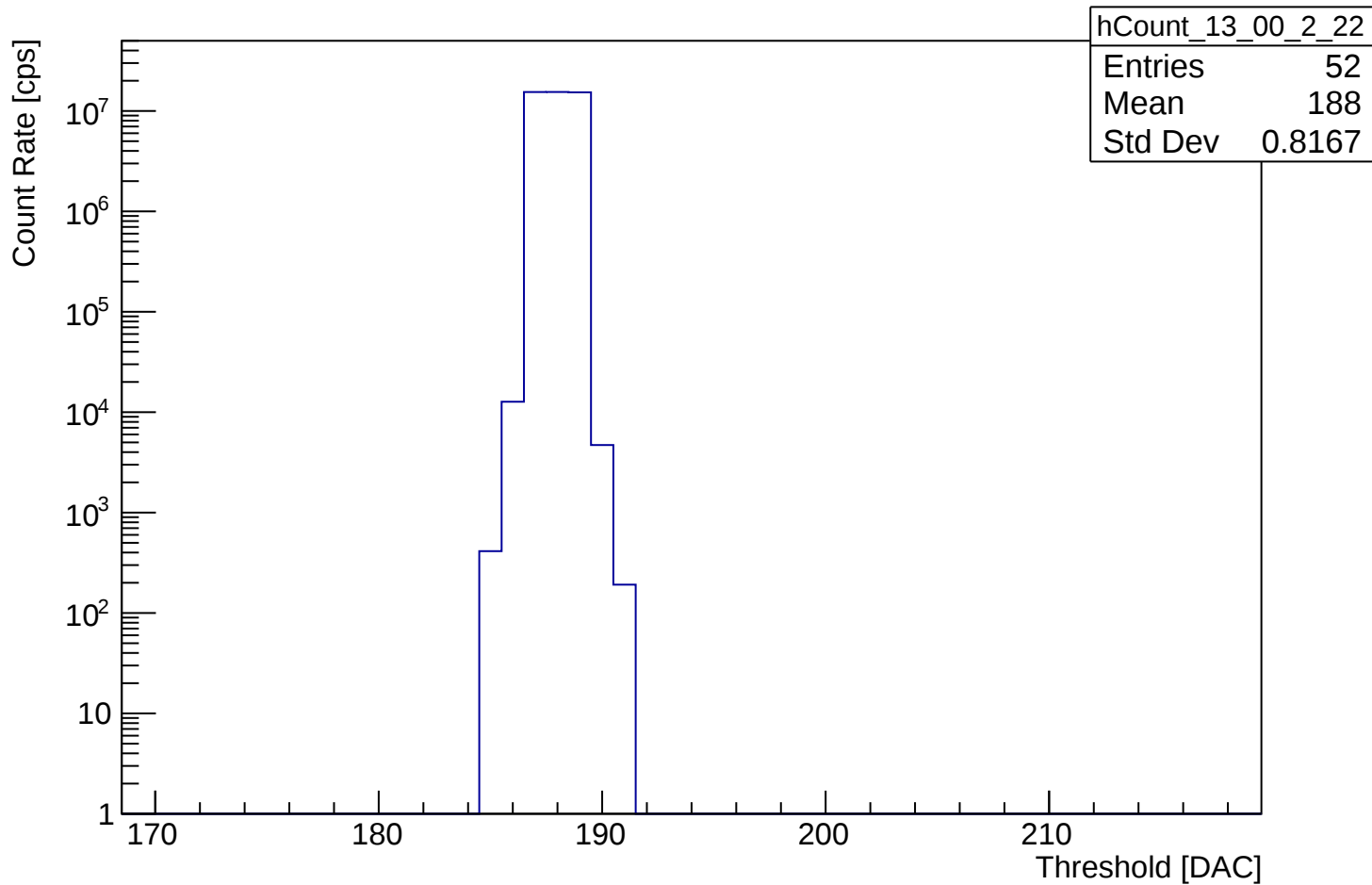
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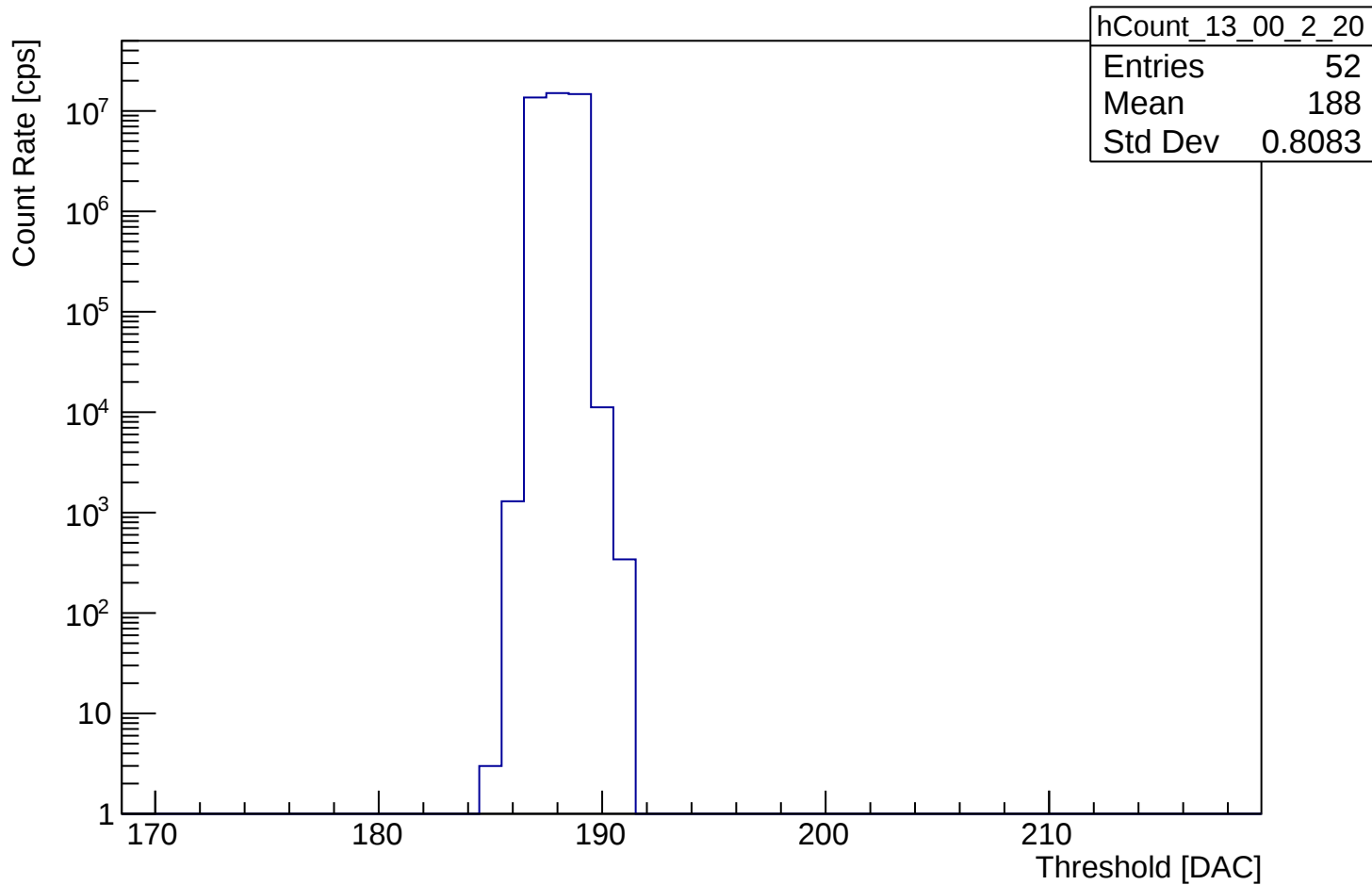
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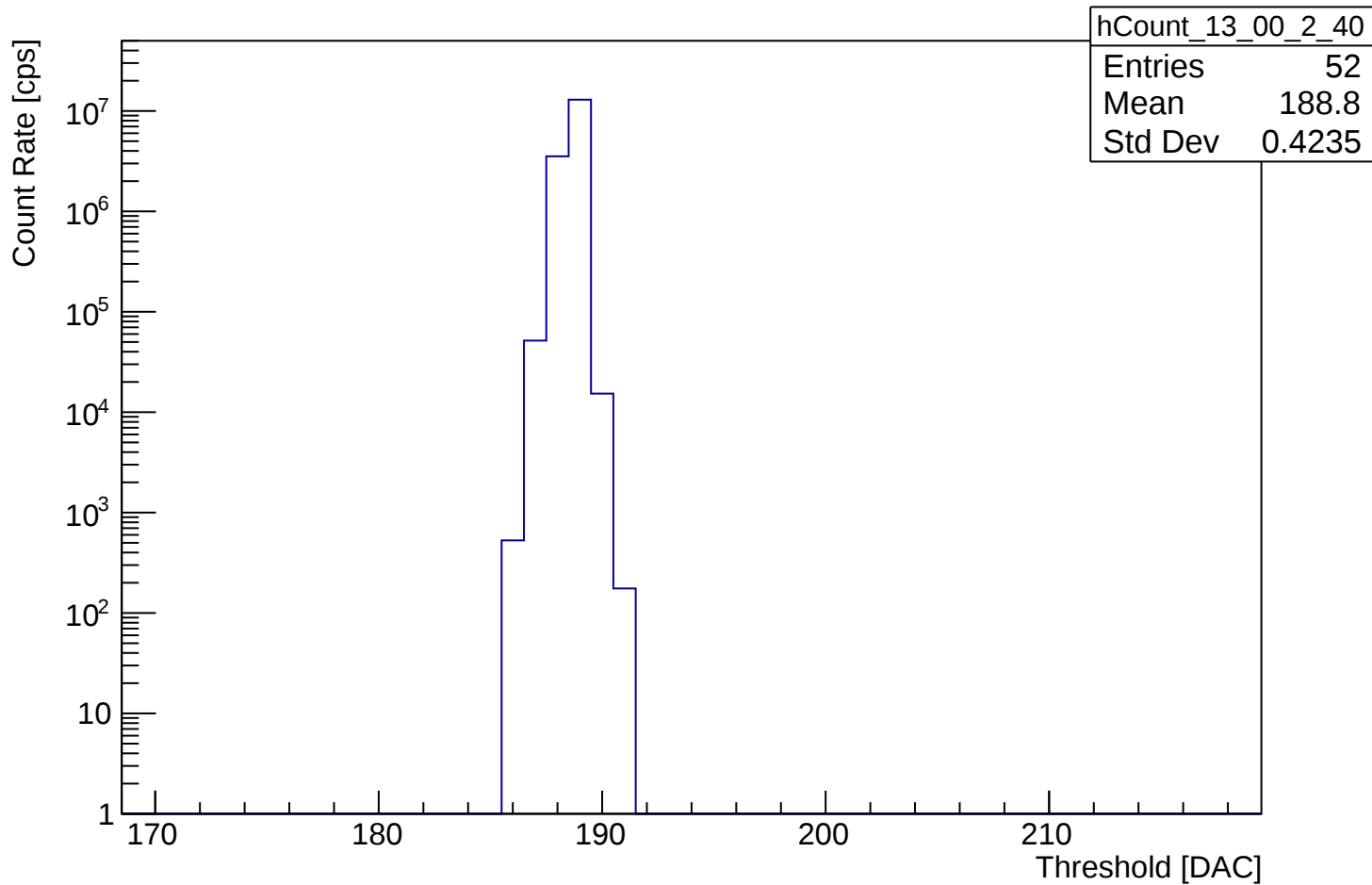
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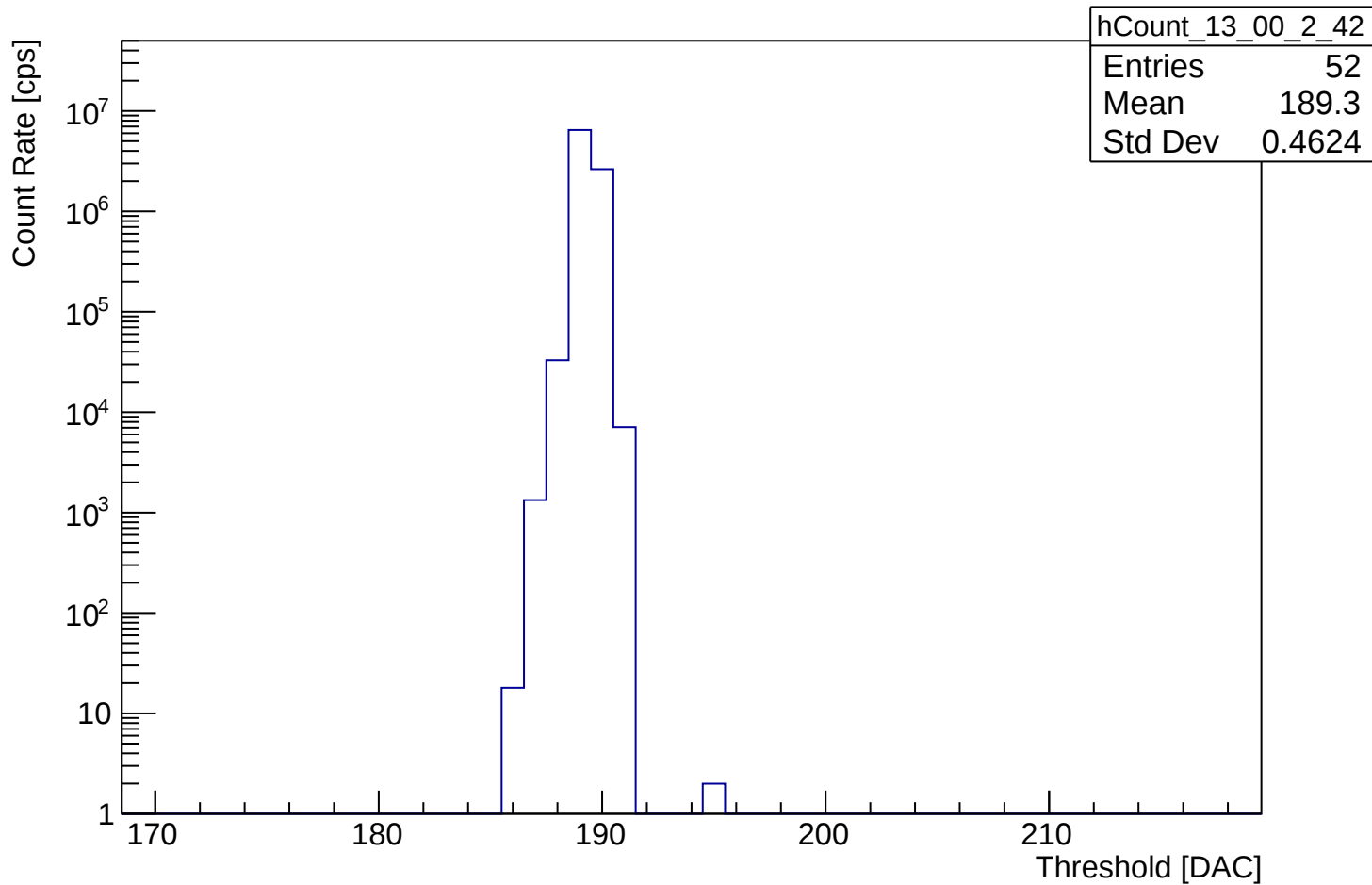
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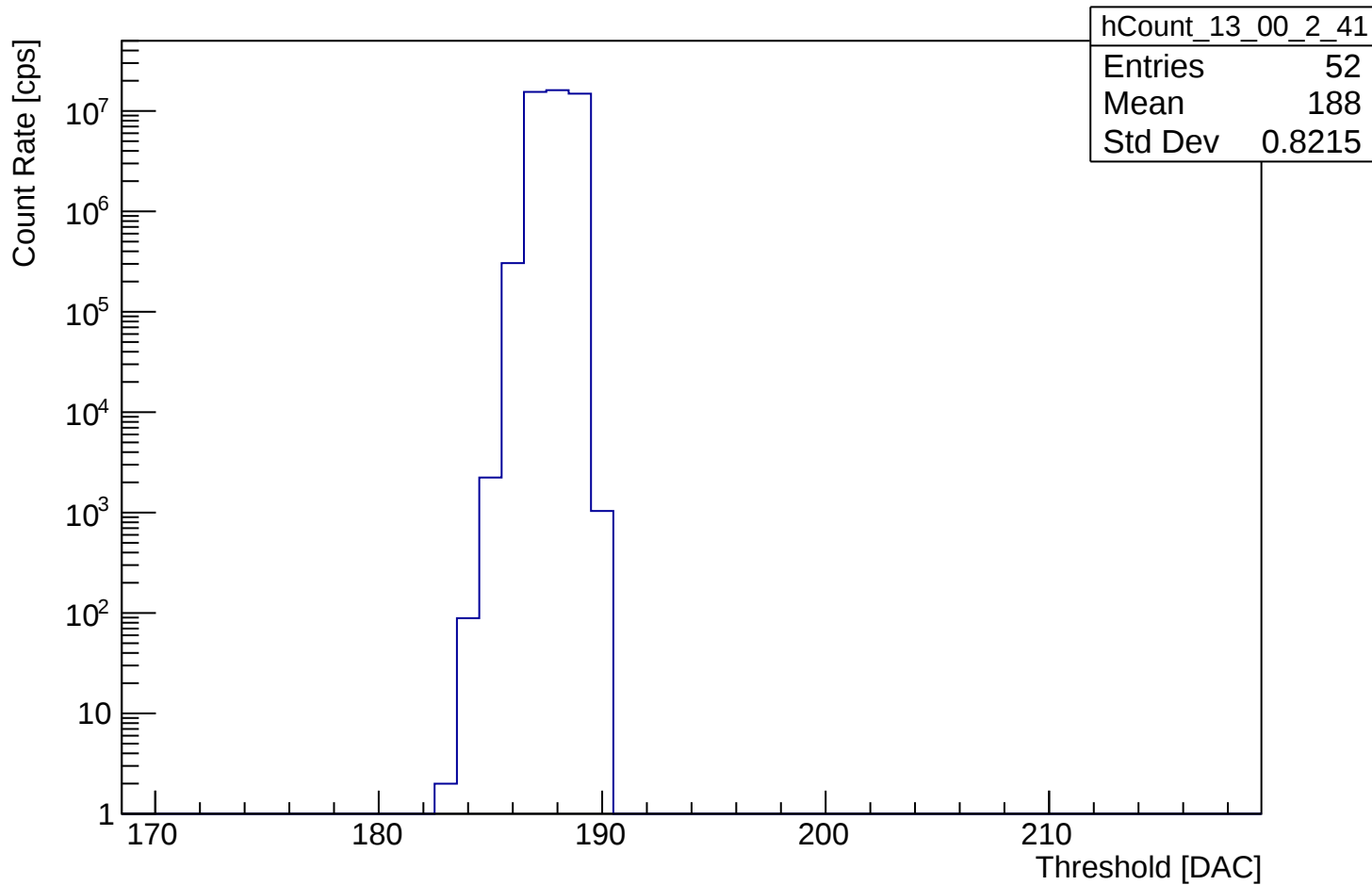
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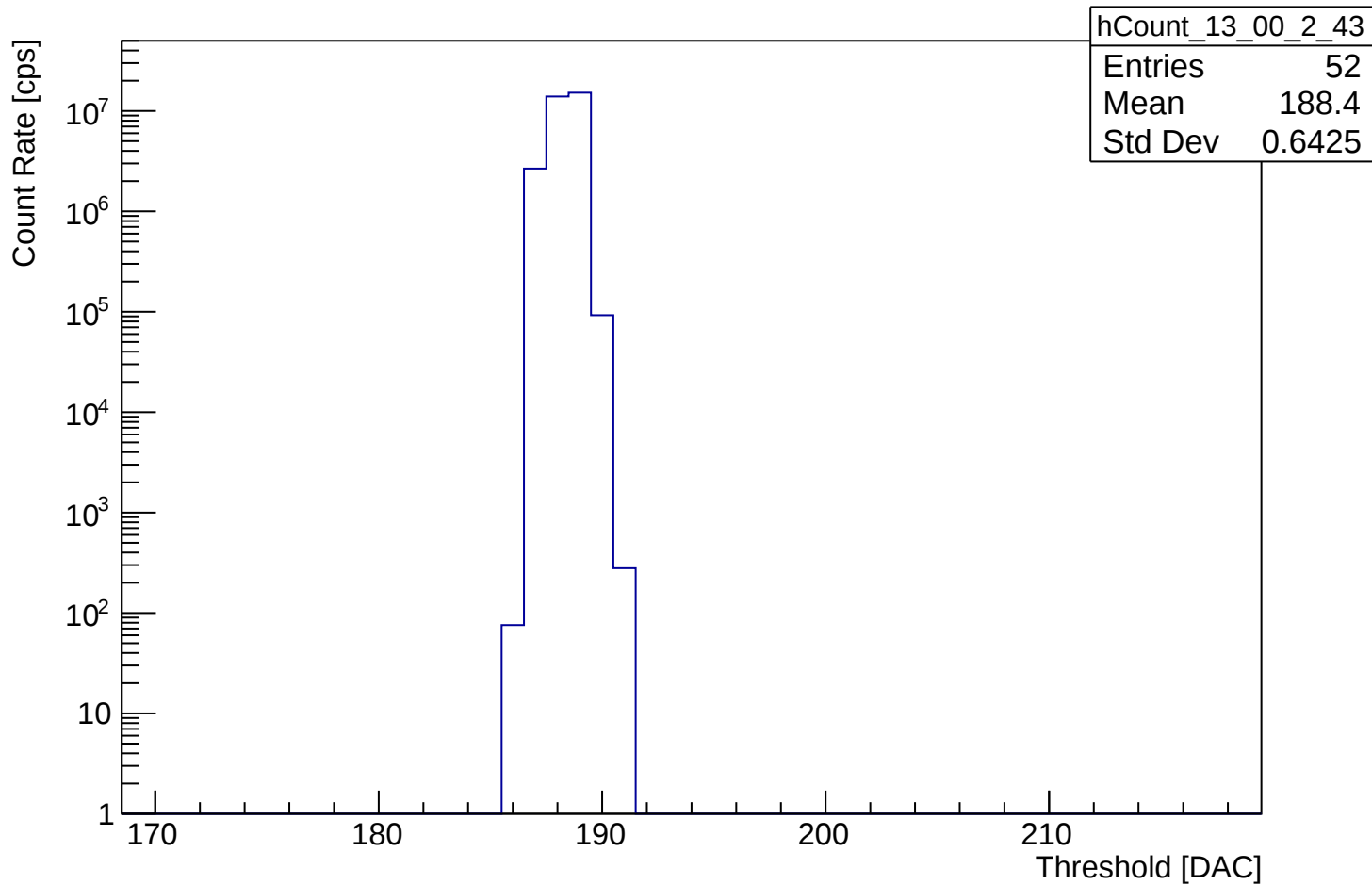
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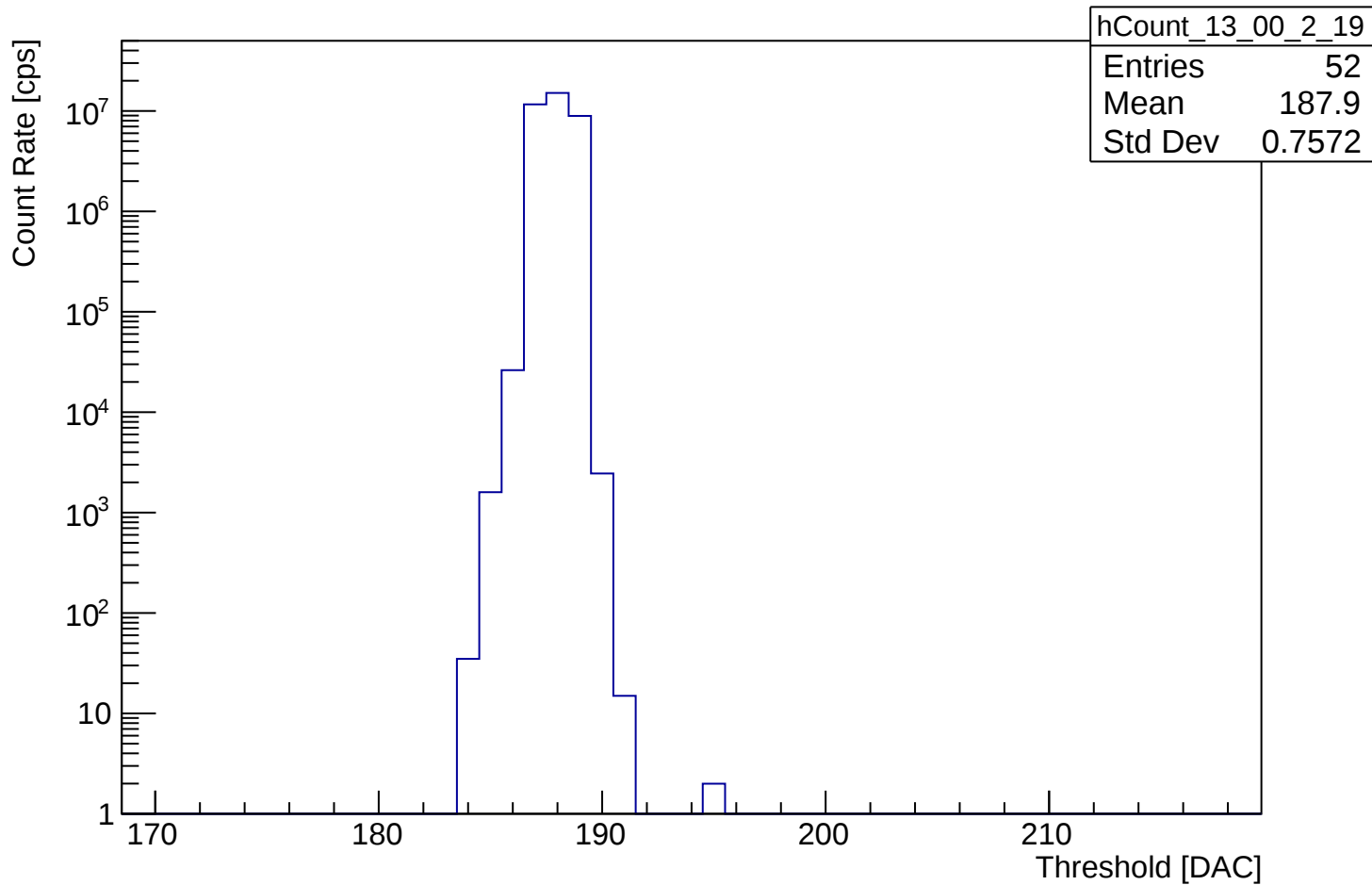
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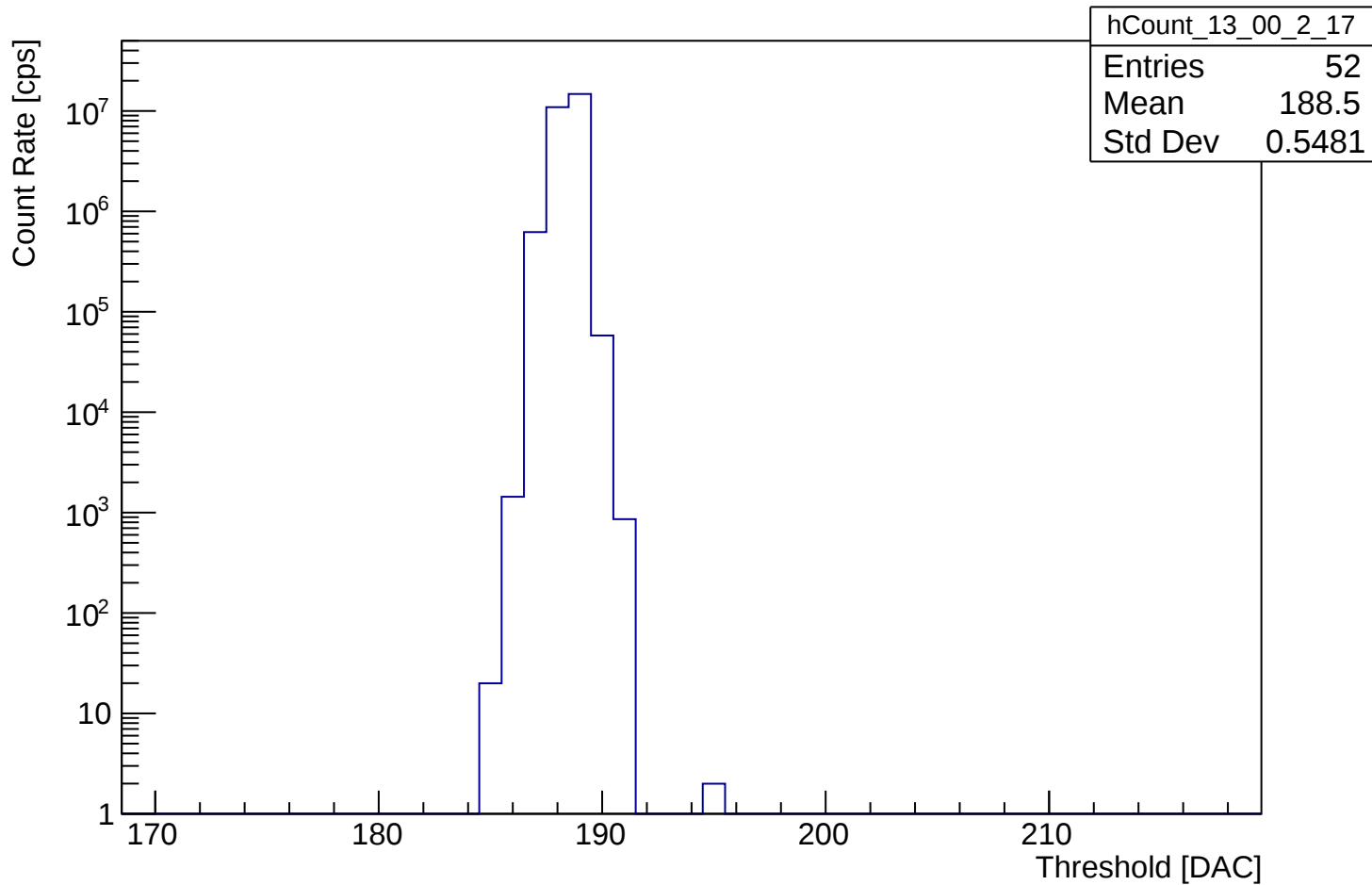
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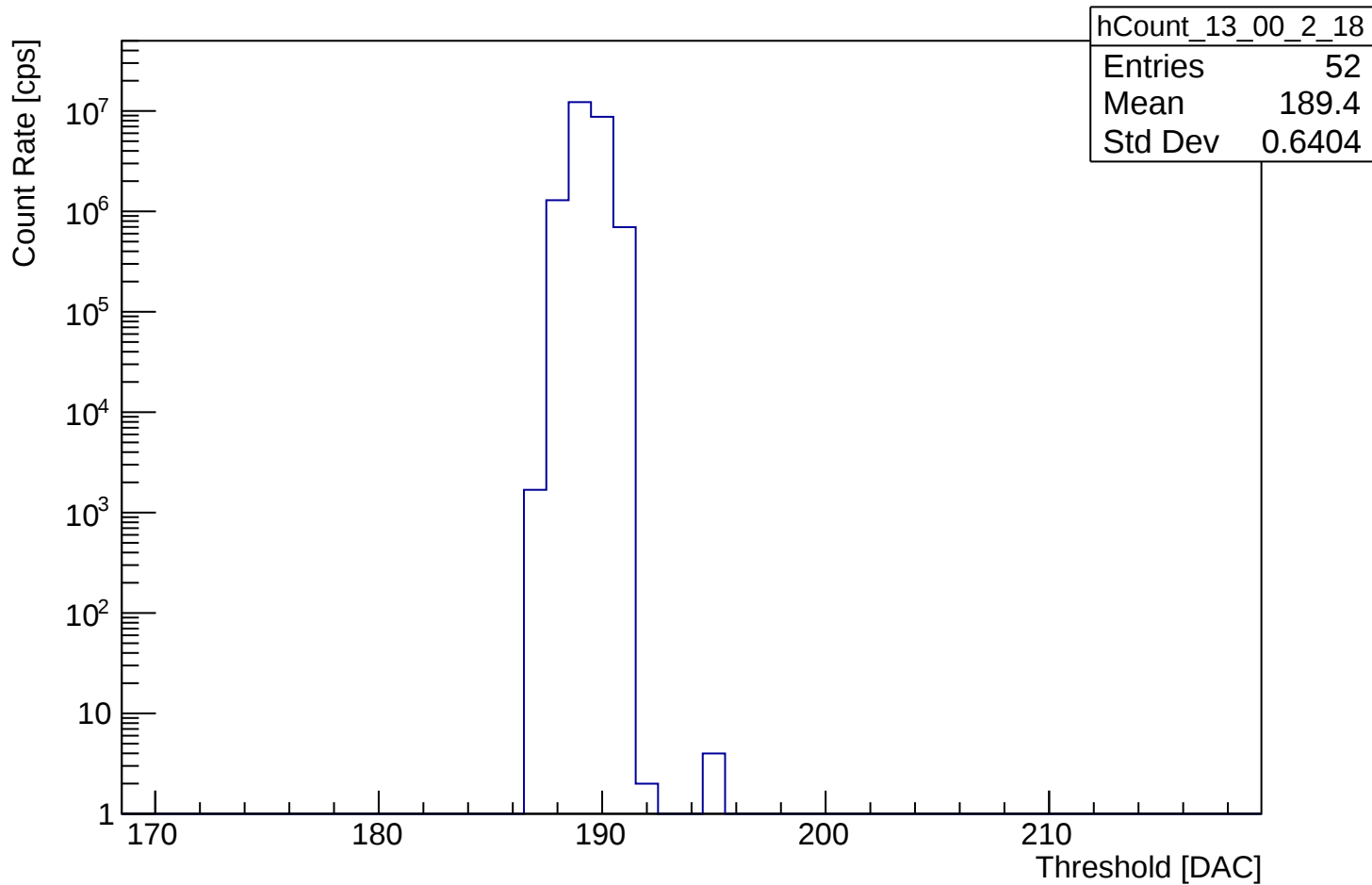
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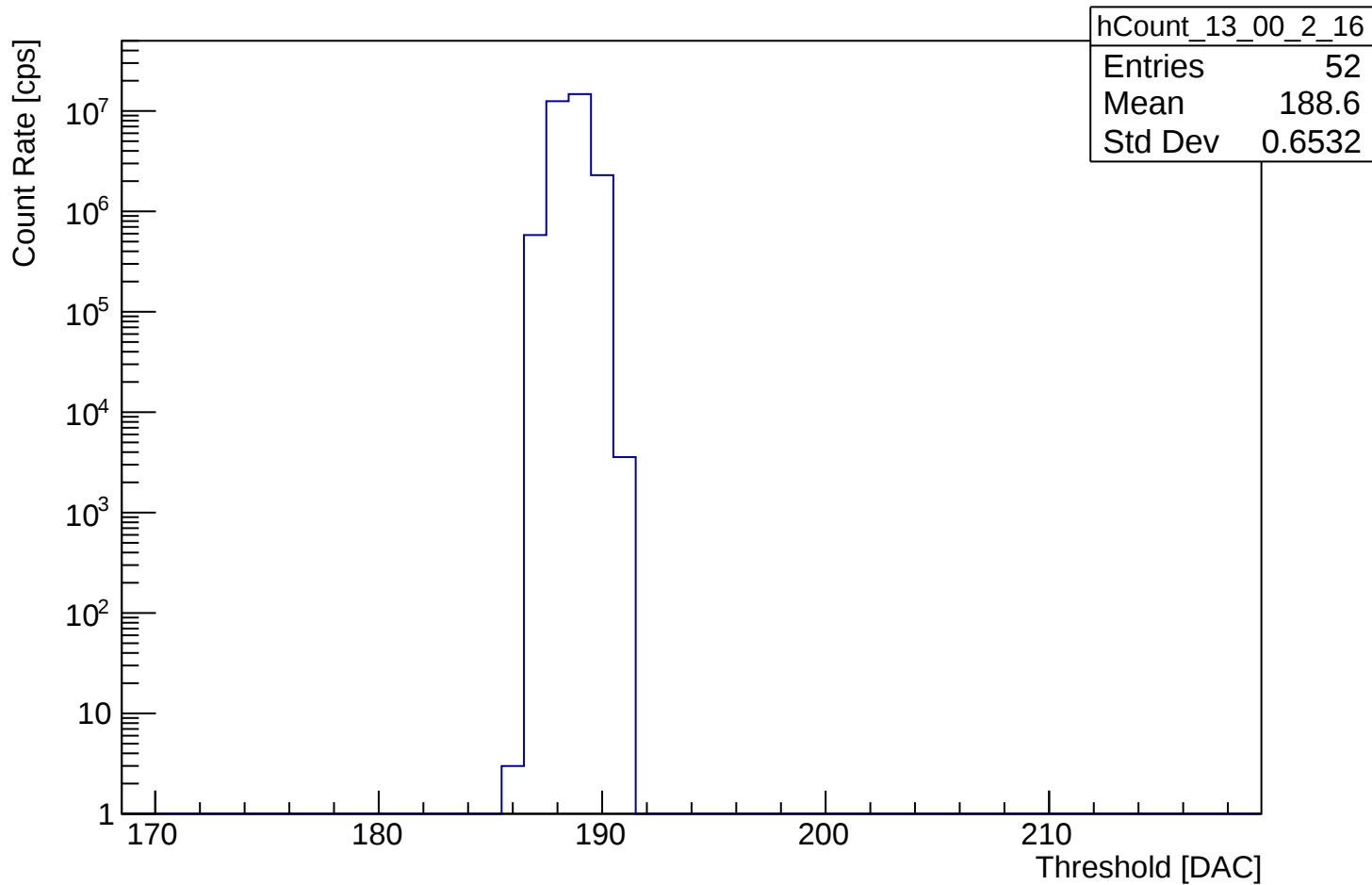
Row 1 Tile 1 Pmt 3 Pixel 26 Slot 13 Fiber 0 Asic 2 Channel 17



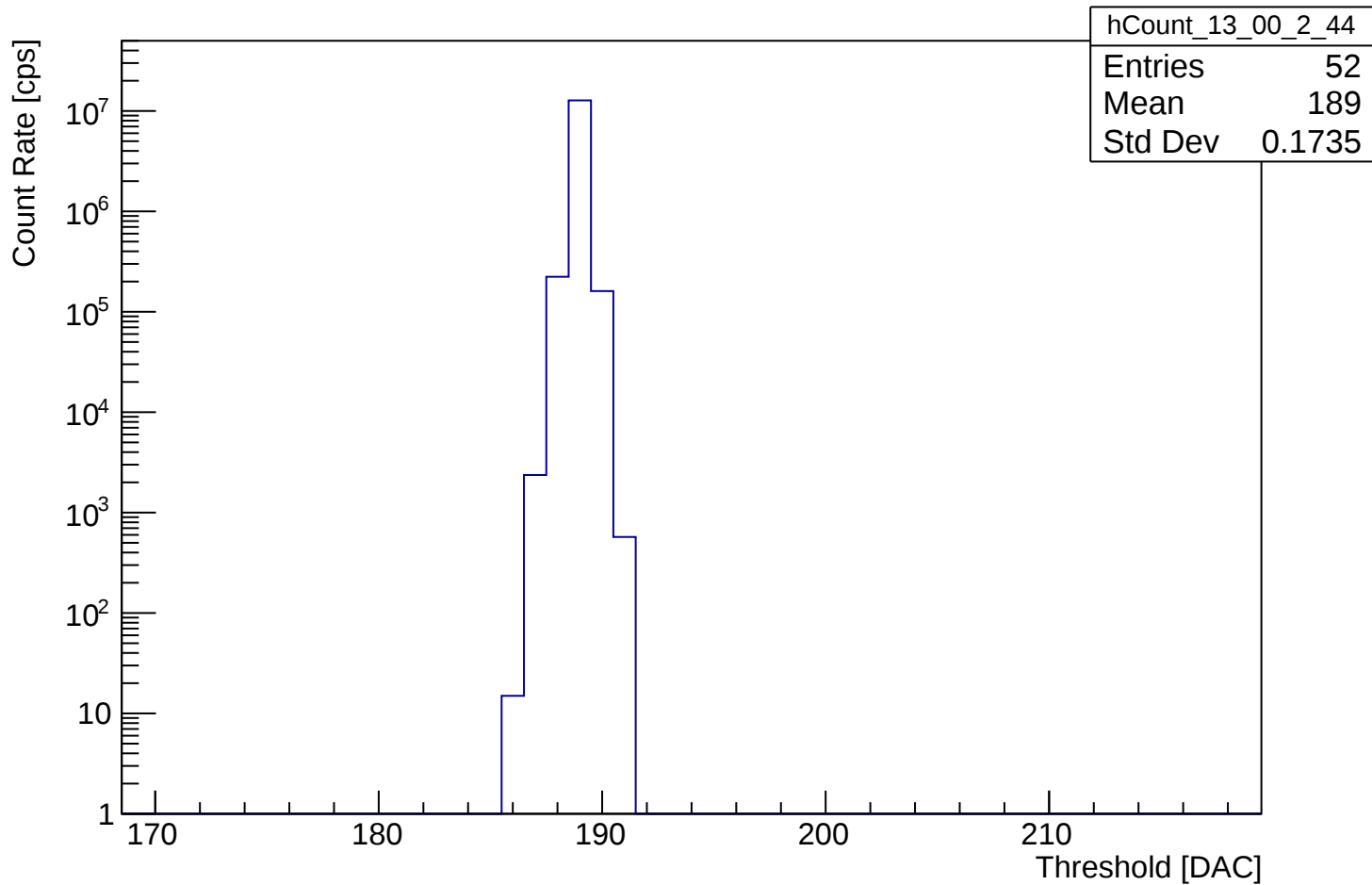
Row 1 Tile 1 Pmt 3 Pixel 27 Slot 13 Fiber 0 Asic 2 Channel 18



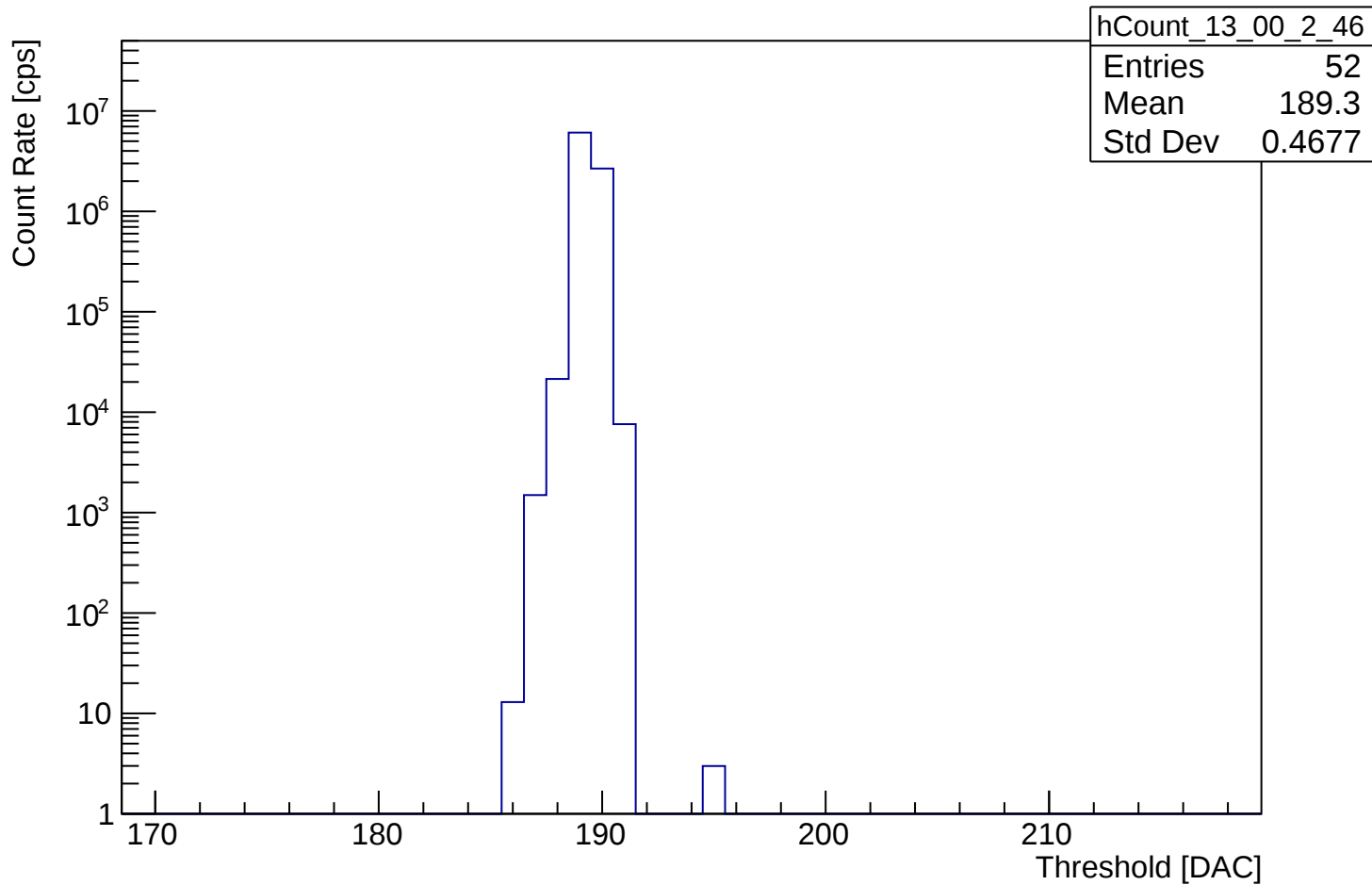
Row 1 Tile 1 Pmt 3 Pixel 28 Slot 13 Fiber 0 Asic 2 Channel 16



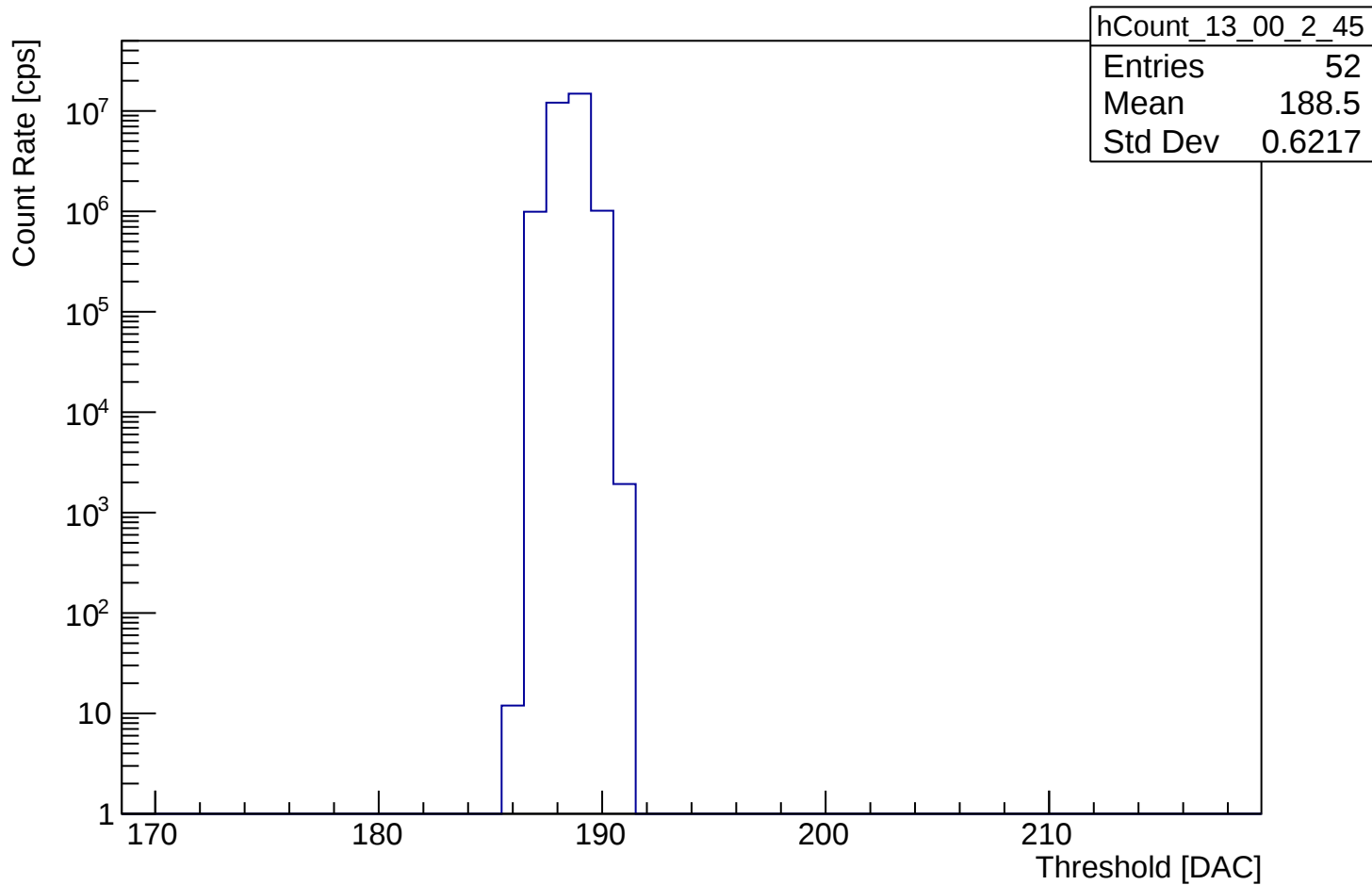
Row 1 Tile 1 Pmt 3 Pixel 29 Slot 13 Fiber 0 Asic 2 Channel 44



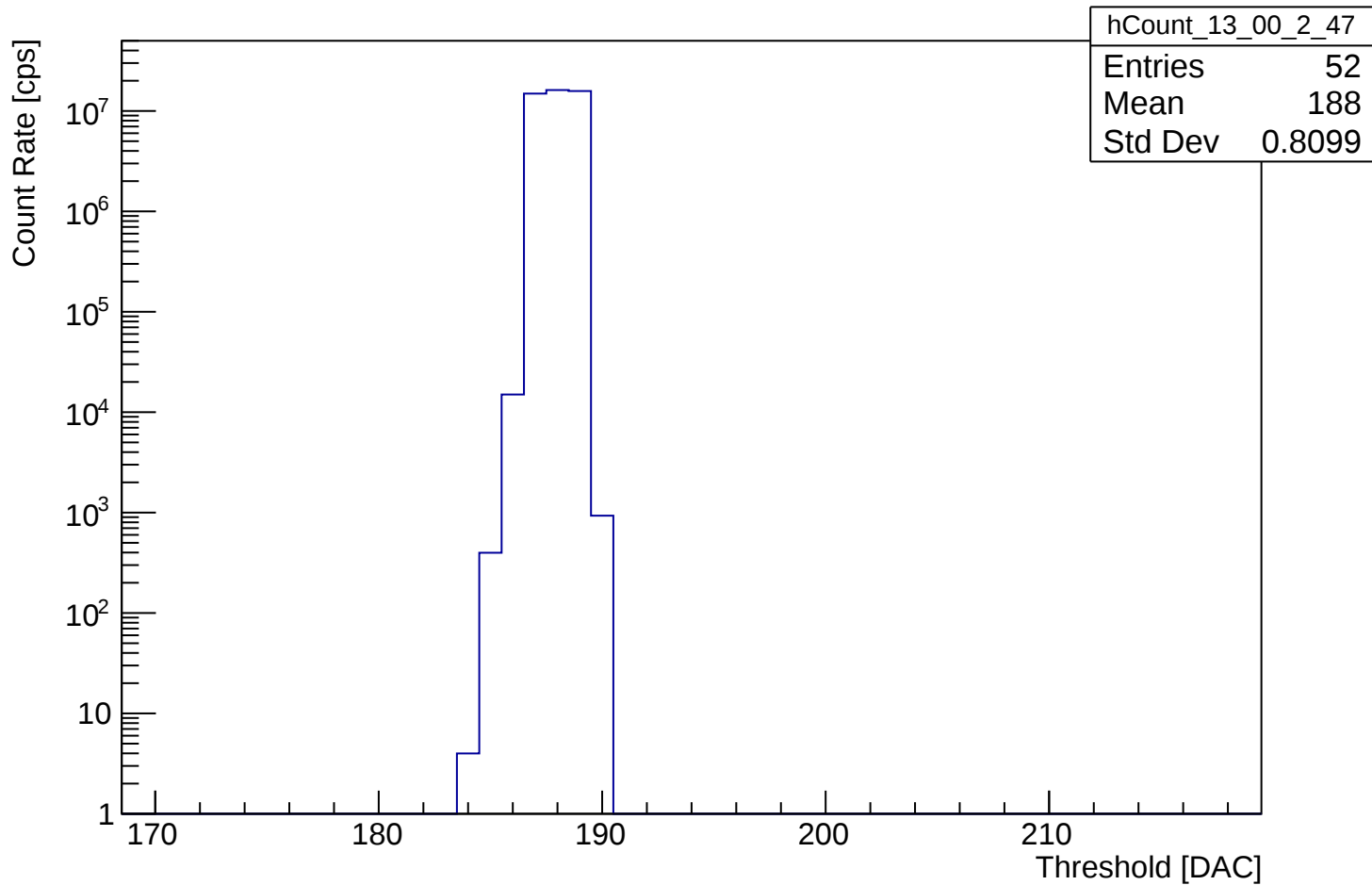
Row 1 Tile 1 Pmt 3 Pixel 30 Slot 13 Fiber 0 Asic 2 Channel 46



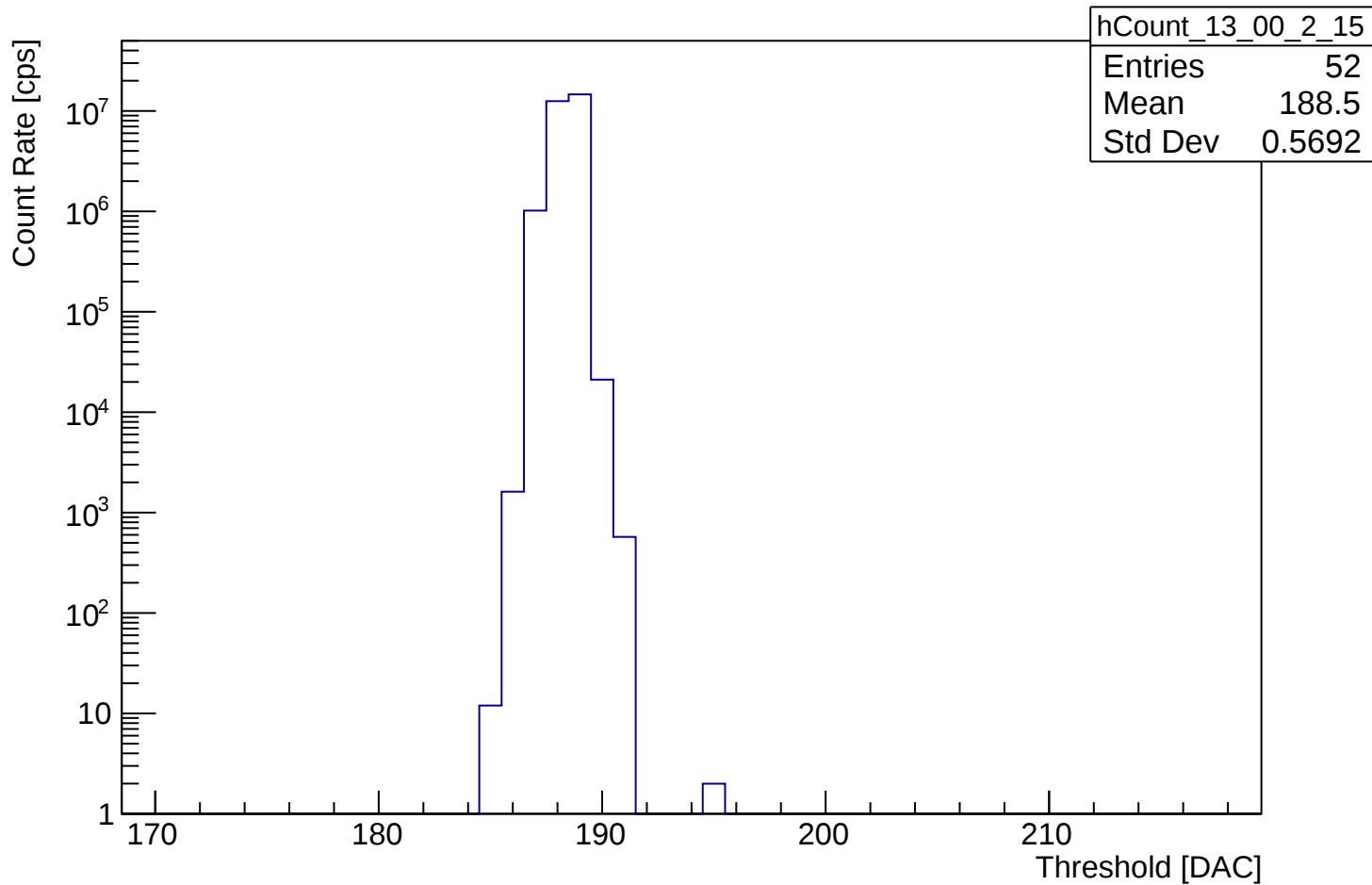
Row 1 Tile 1 Pmt 3 Pixel 31 Slot 13 Fiber 0 Asic 2 Channel 45



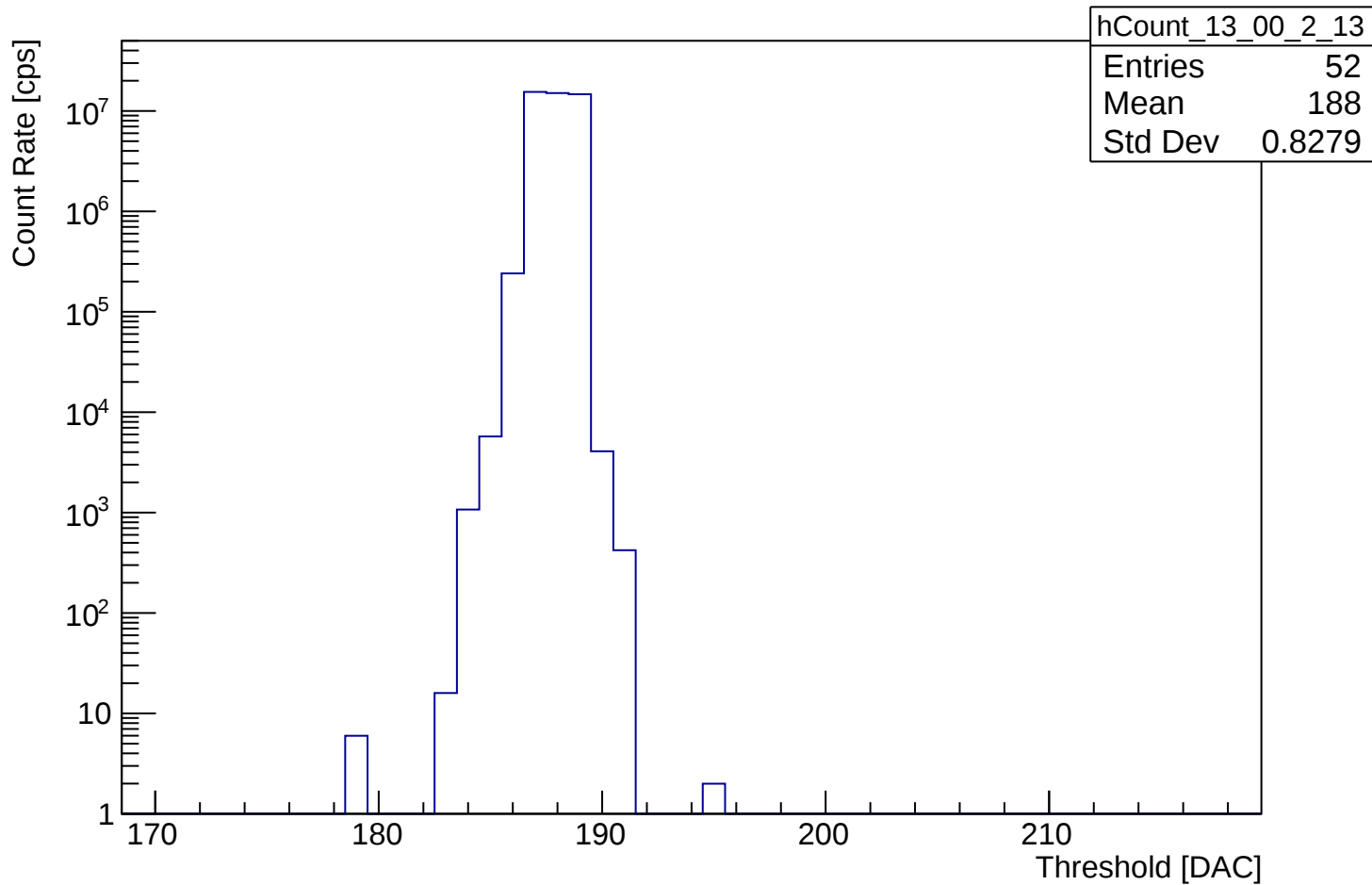
Row 1 Tile 1 Pmt 3 Pixel 32 Slot 13 Fiber 0 Asic 2 Channel 47



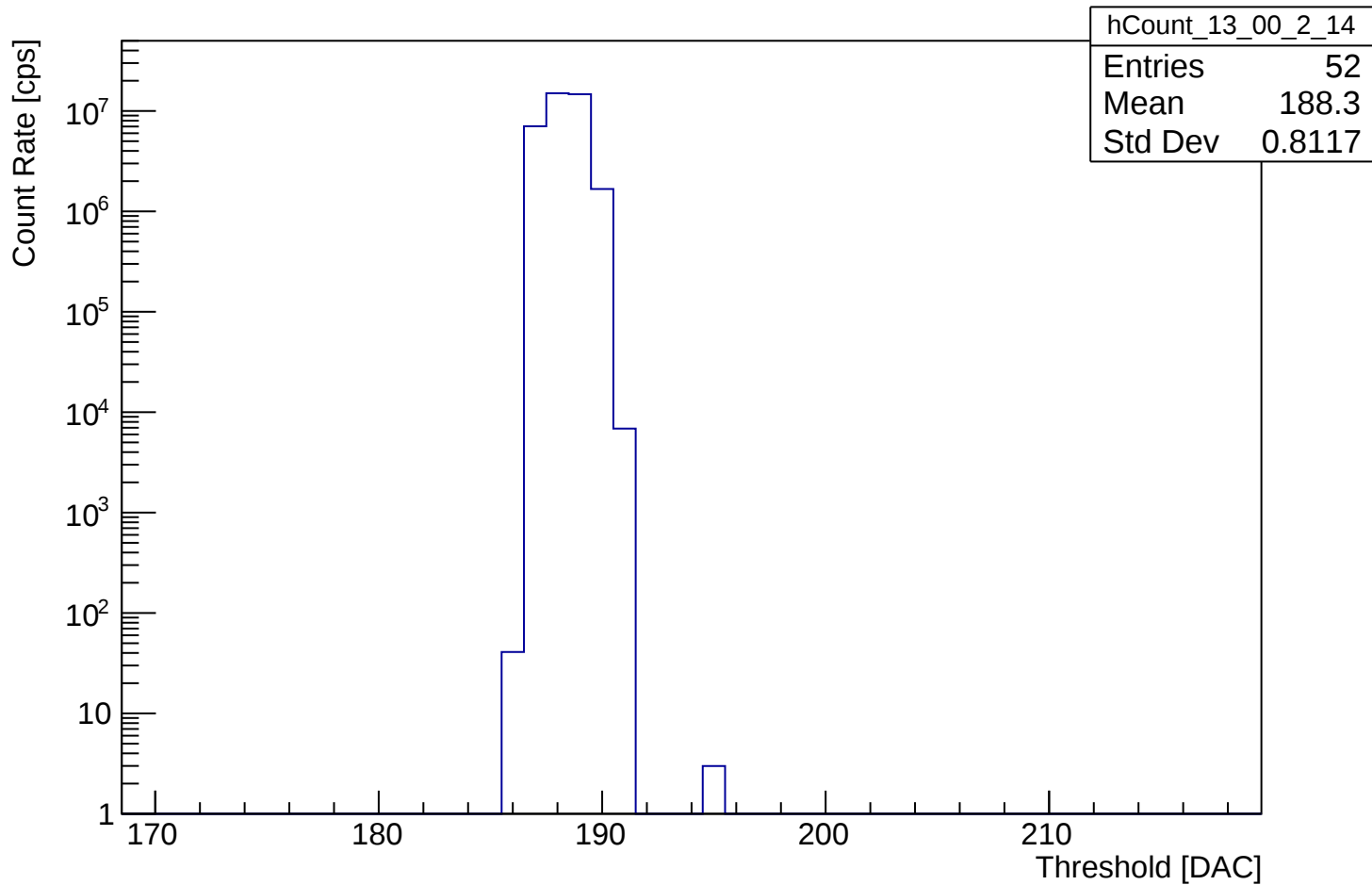
Row 1 Tile 1 Pmt 3 Pixel 33 Slot 13 Fiber 0 Asic 2 Channel 15



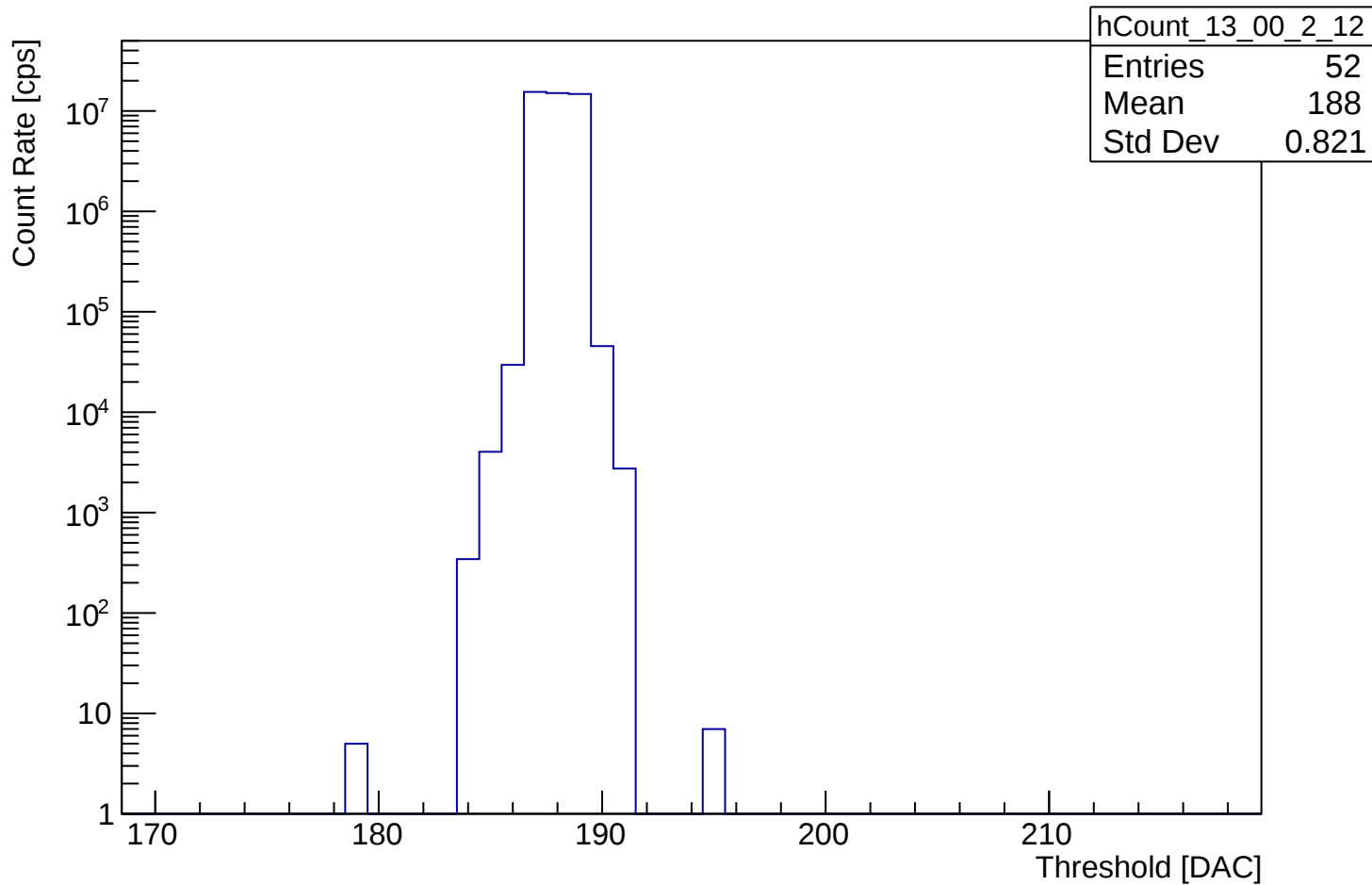
Row 1 Tile 1 Pmt 3 Pixel 34 Slot 13 Fiber 0 Asic 2 Channel 13



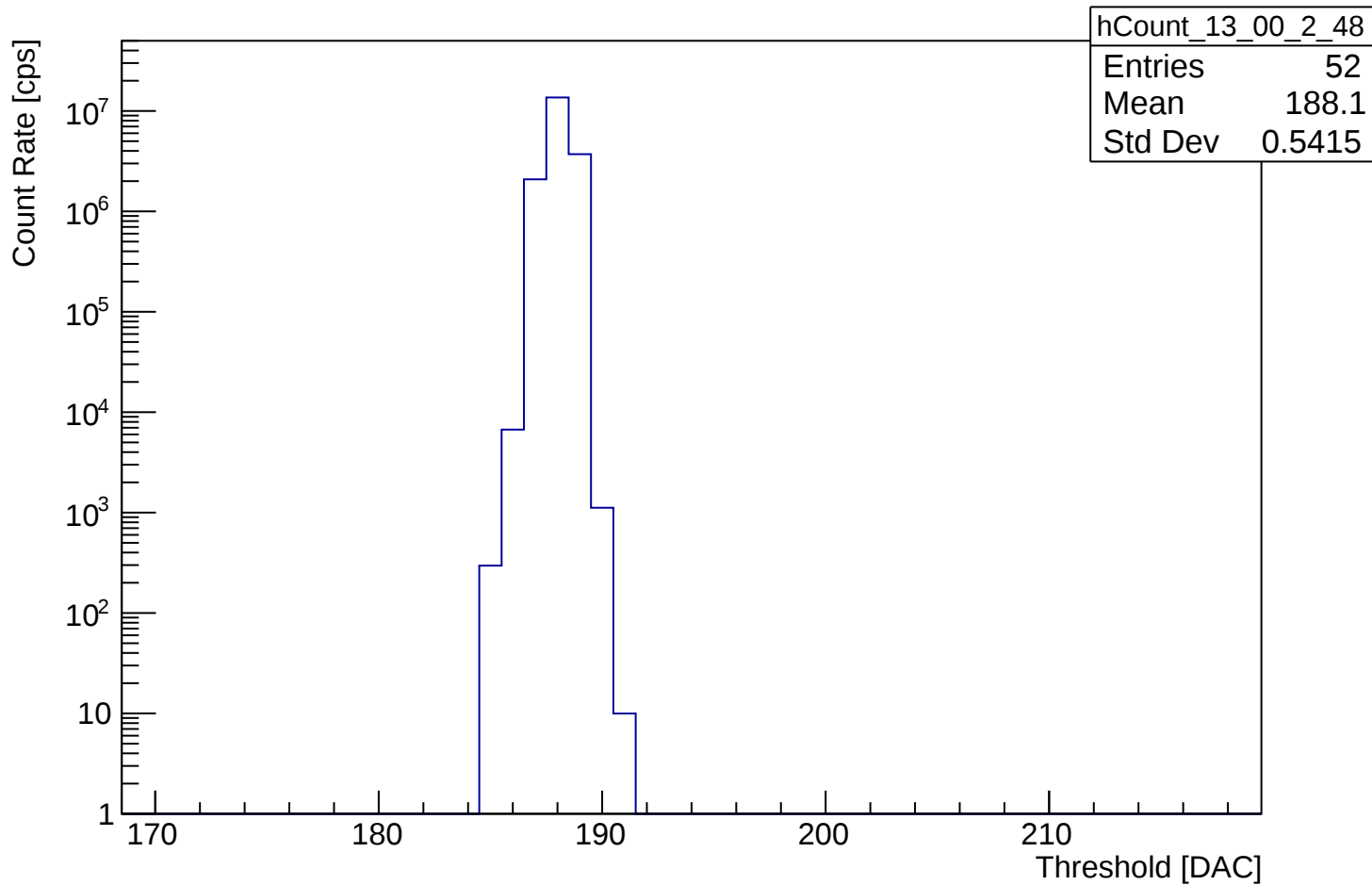
Row 1 Tile 1 Pmt 3 Pixel 35 Slot 13 Fiber 0 Asic 2 Channel 14



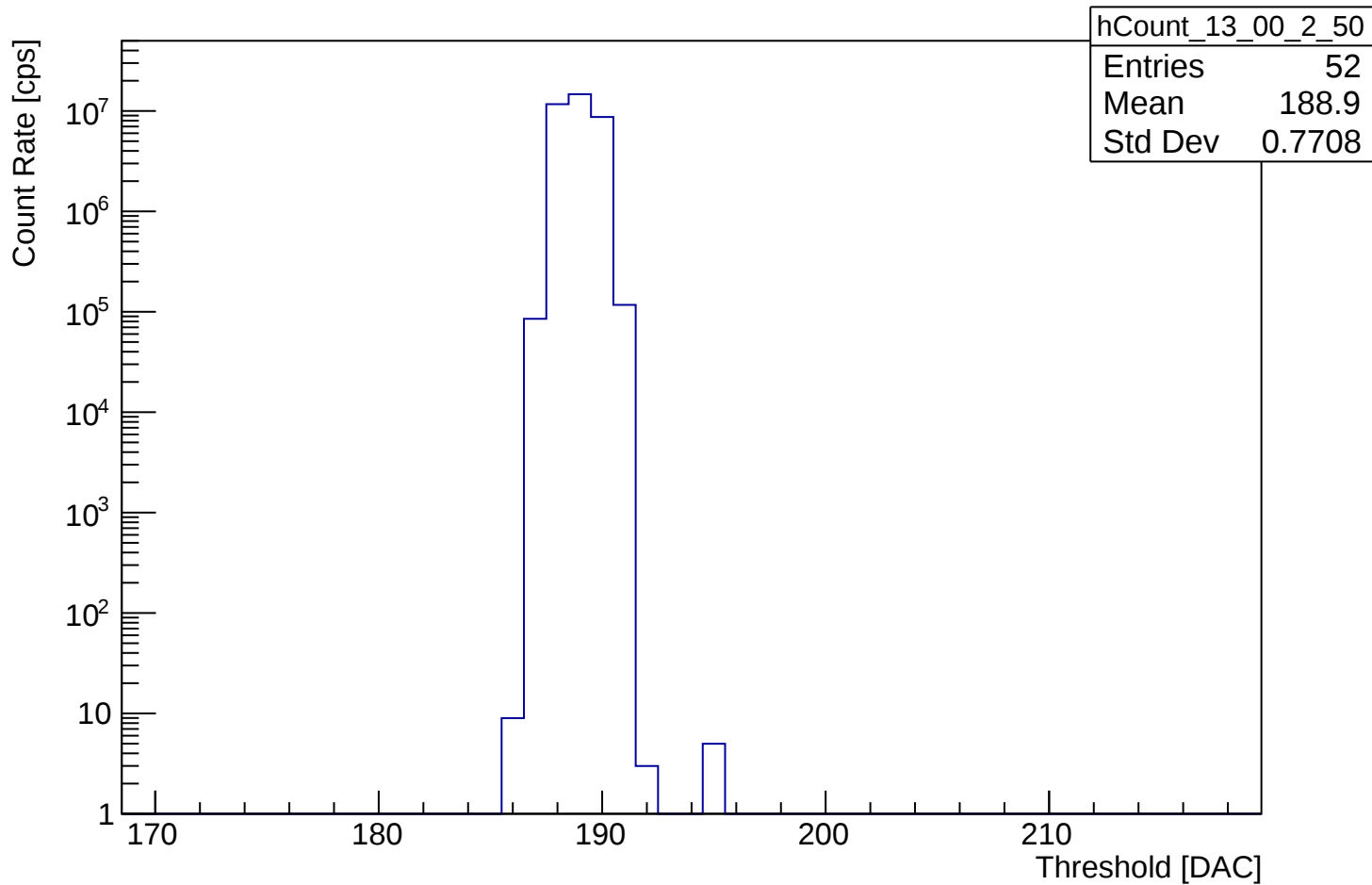
Row 1 Tile 1 Pmt 3 Pixel 36 Slot 13 Fiber 0 Asic 2 Channel 12



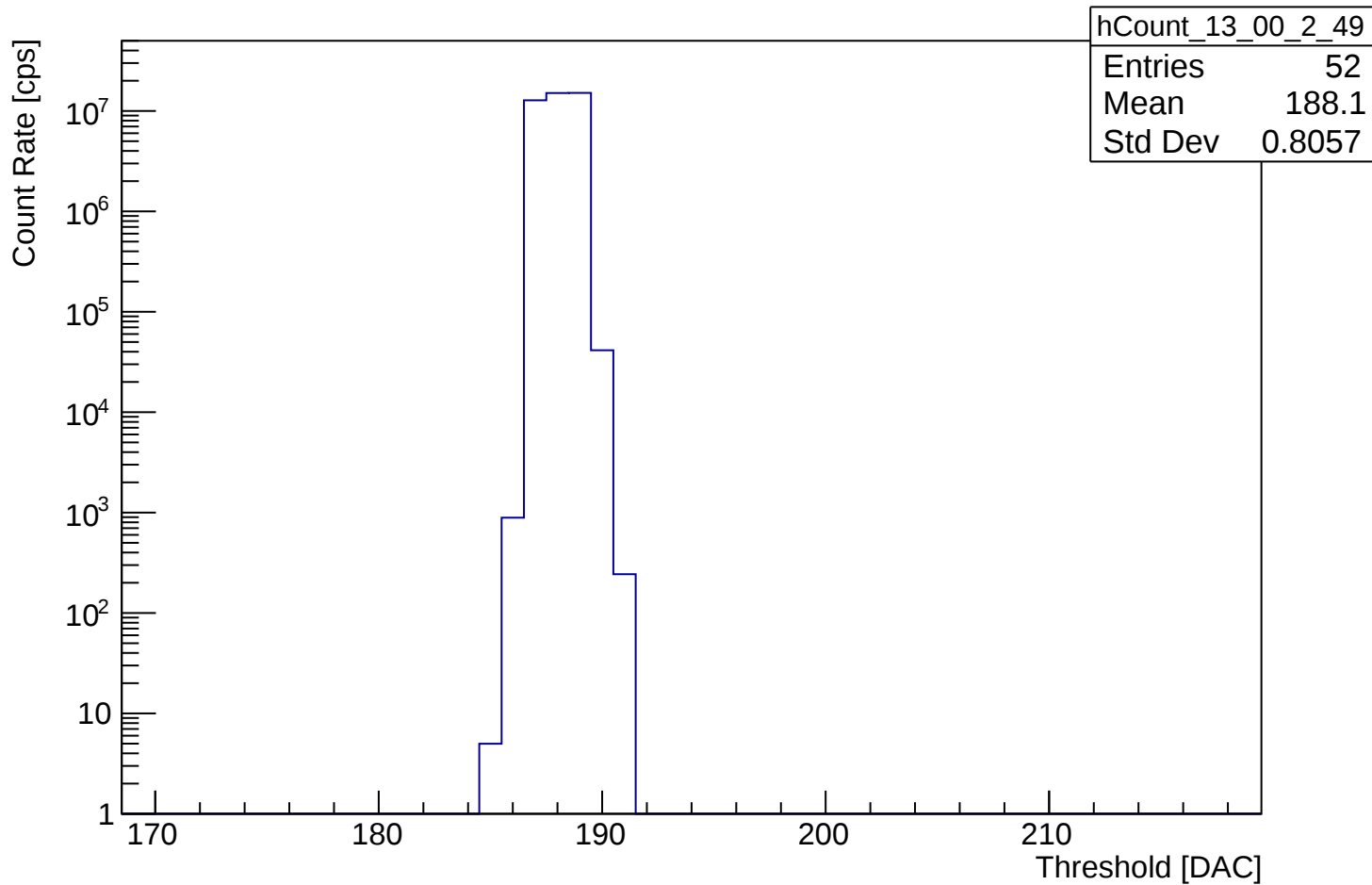
Row 1 Tile 1 Pmt 3 Pixel 37 Slot 13 Fiber 0 Asic 2 Channel 48



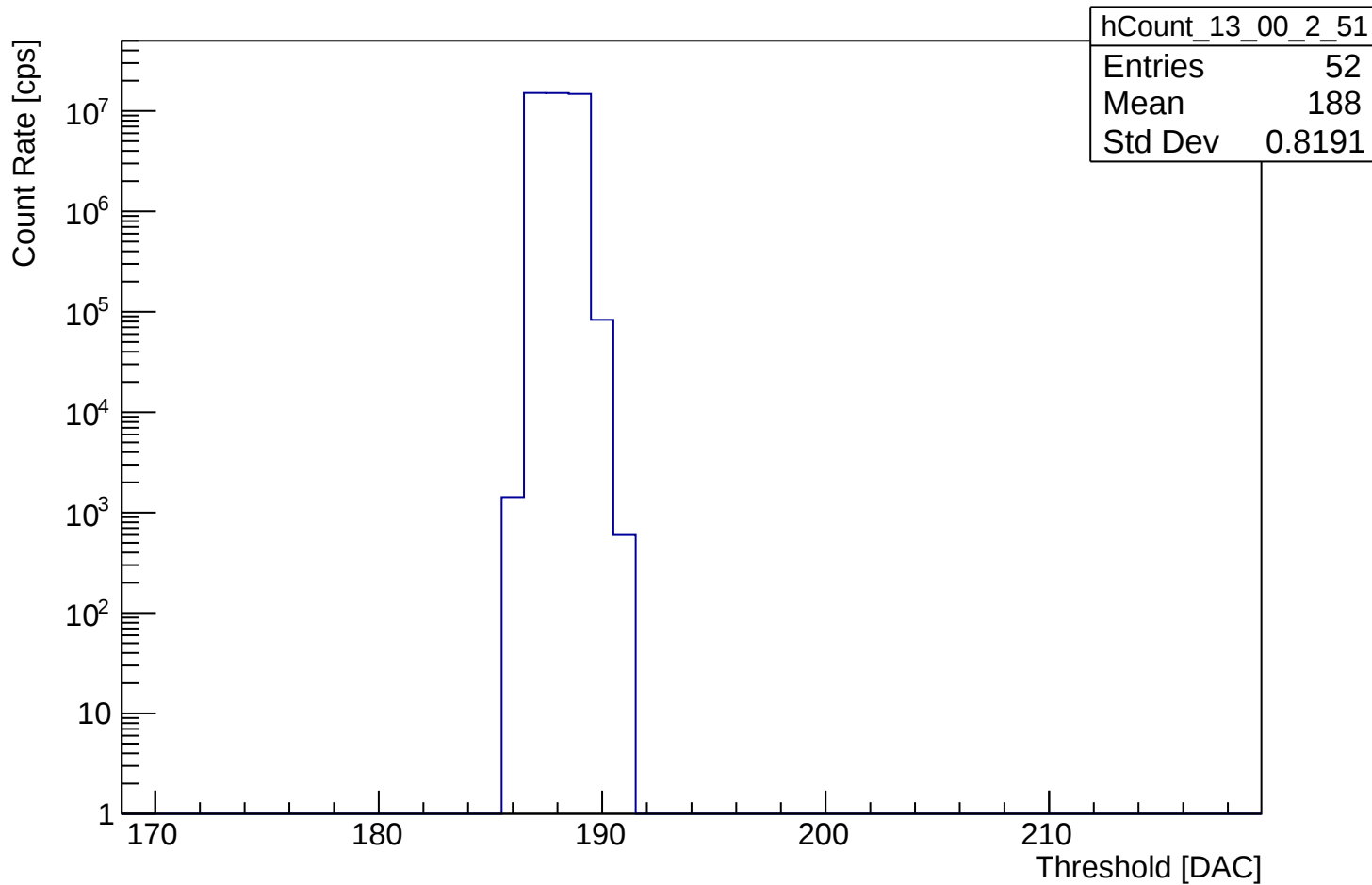
Row 1 Tile 1 Pmt 3 Pixel 38 Slot 13 Fiber 0 Asic 2 Channel 50



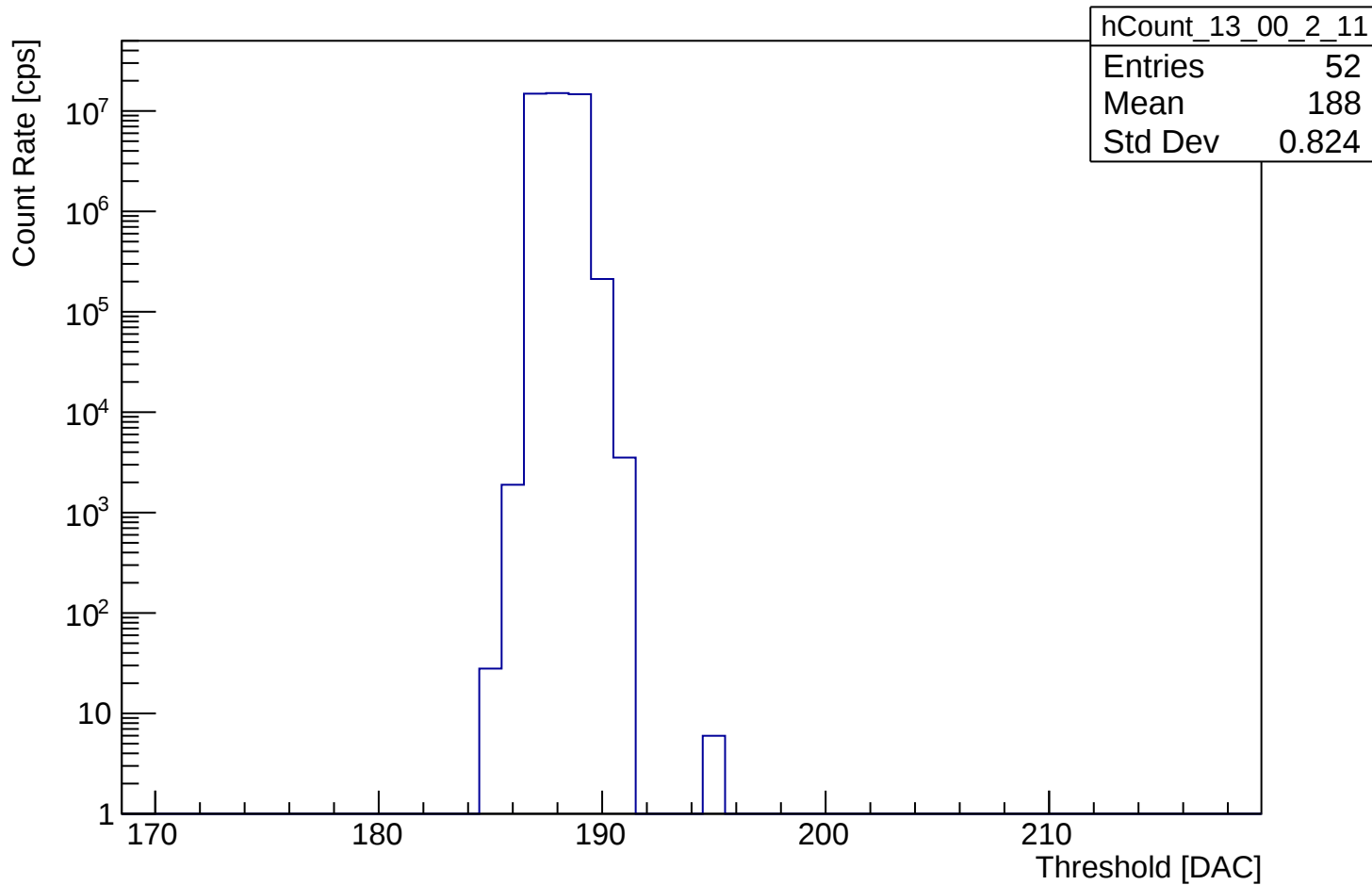
Row 1 Tile 1 Pmt 3 Pixel 39 Slot 13 Fiber 0 Asic 2 Channel 49

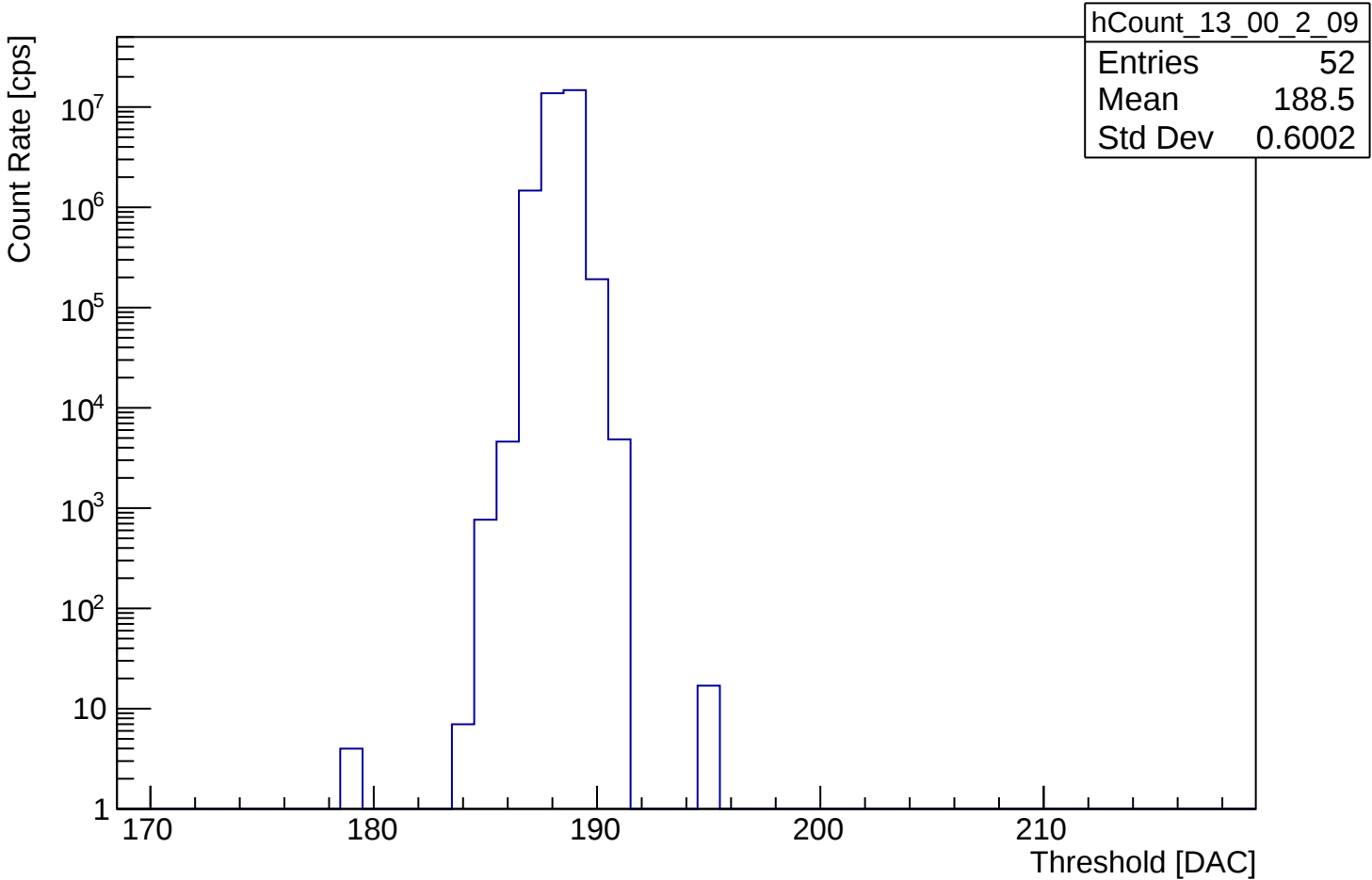


Row 1 Tile 1 Pmt 3 Pixel 40 Slot 13 Fiber 0 Asic 2 Channel 51

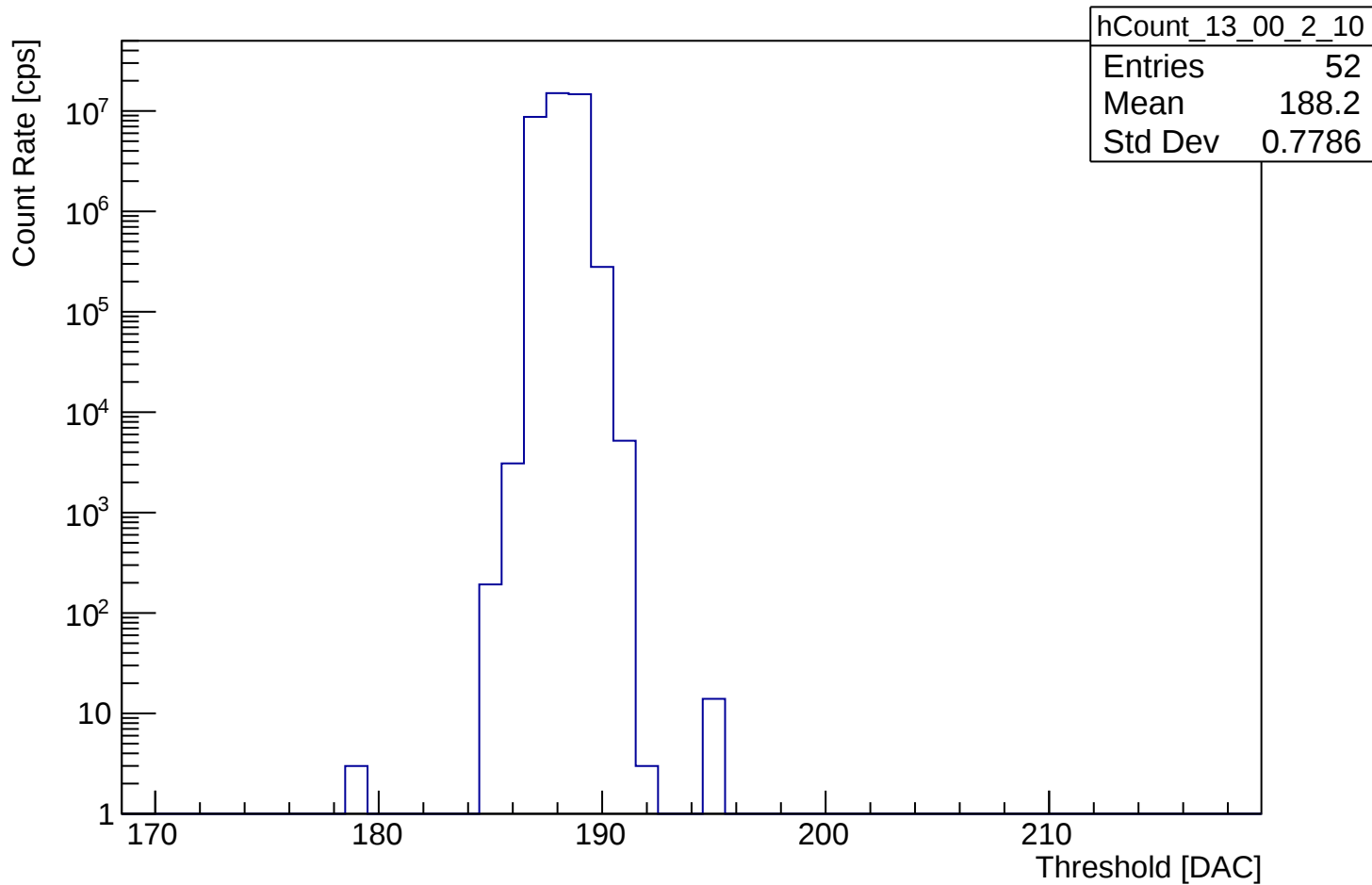


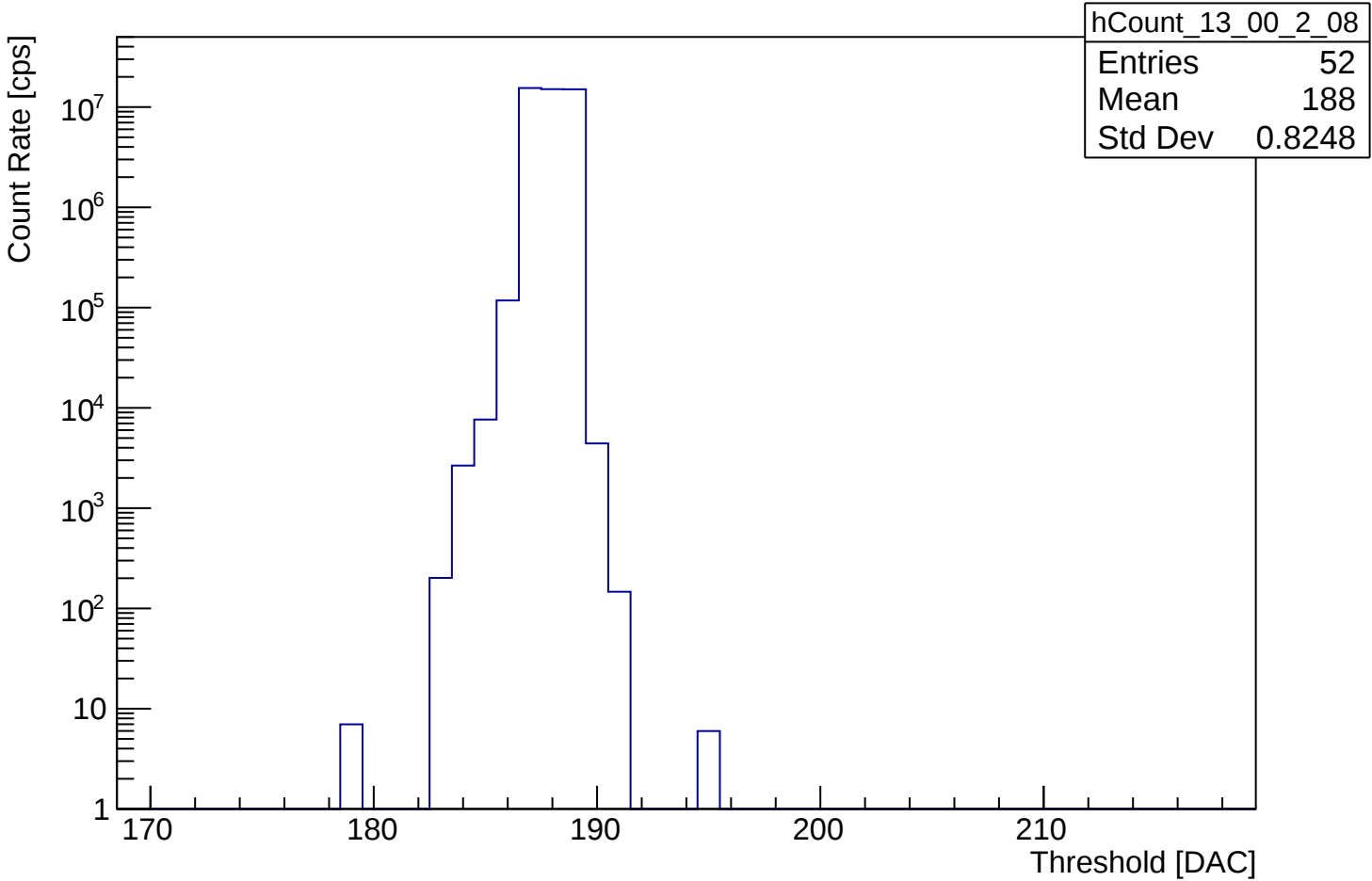
Row 1 Tile 1 Pmt 3 Pixel 41 Slot 13 Fiber 0 Asic 2 Channel 11



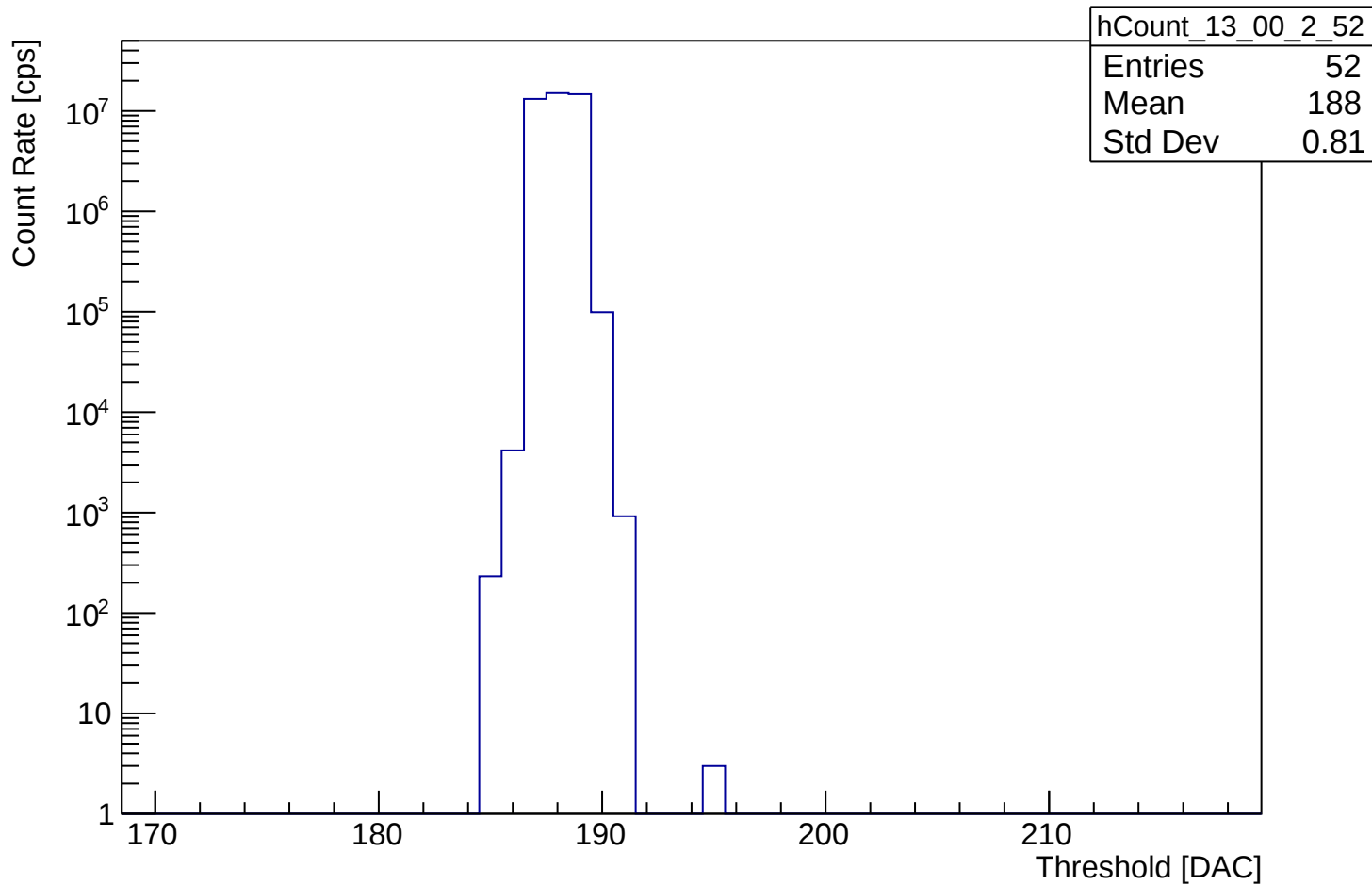


Row 1 Tile 1 Pmt 3 Pixel 43 Slot 13 Fiber 0 Asic 2 Channel 10

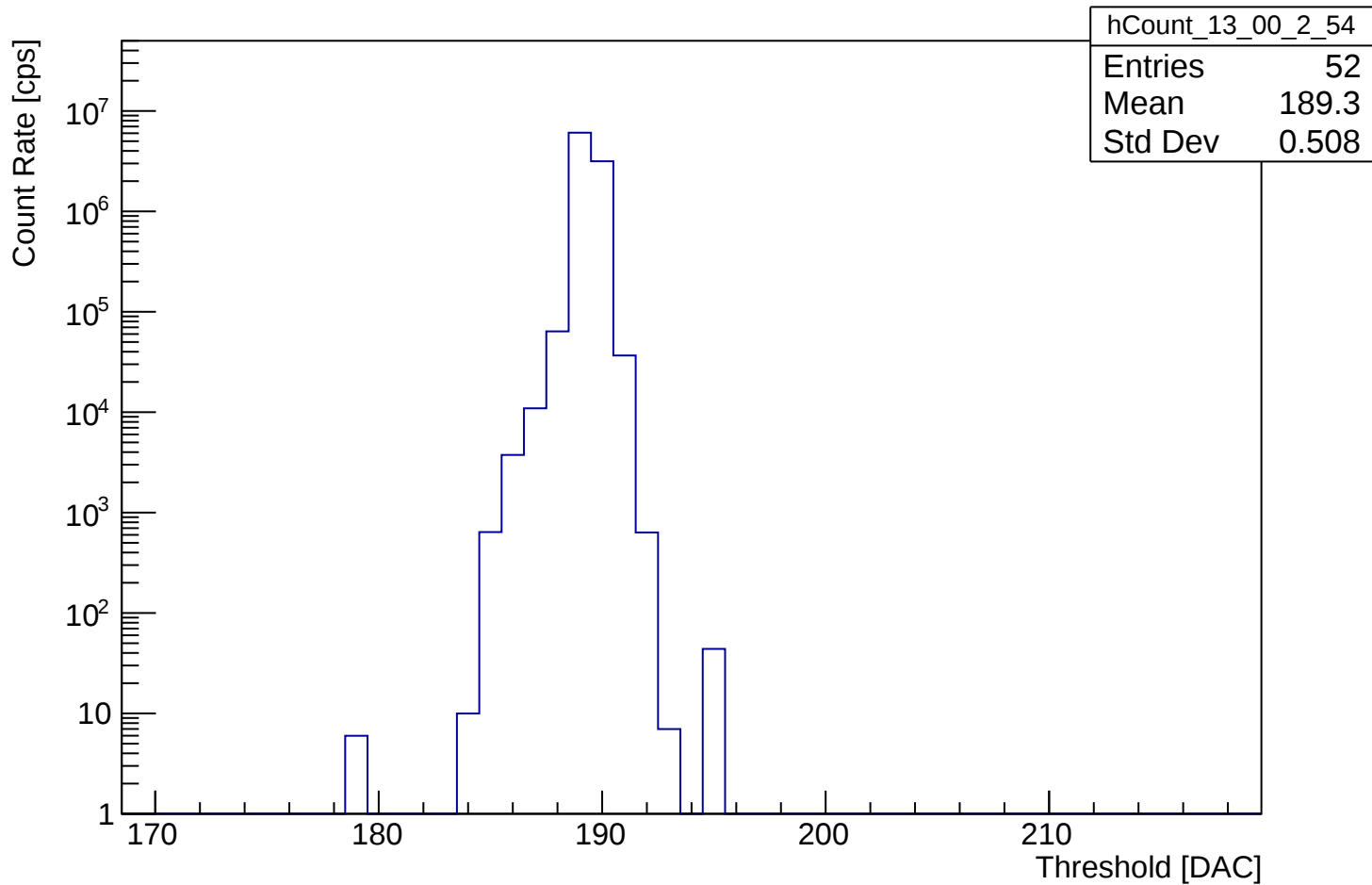




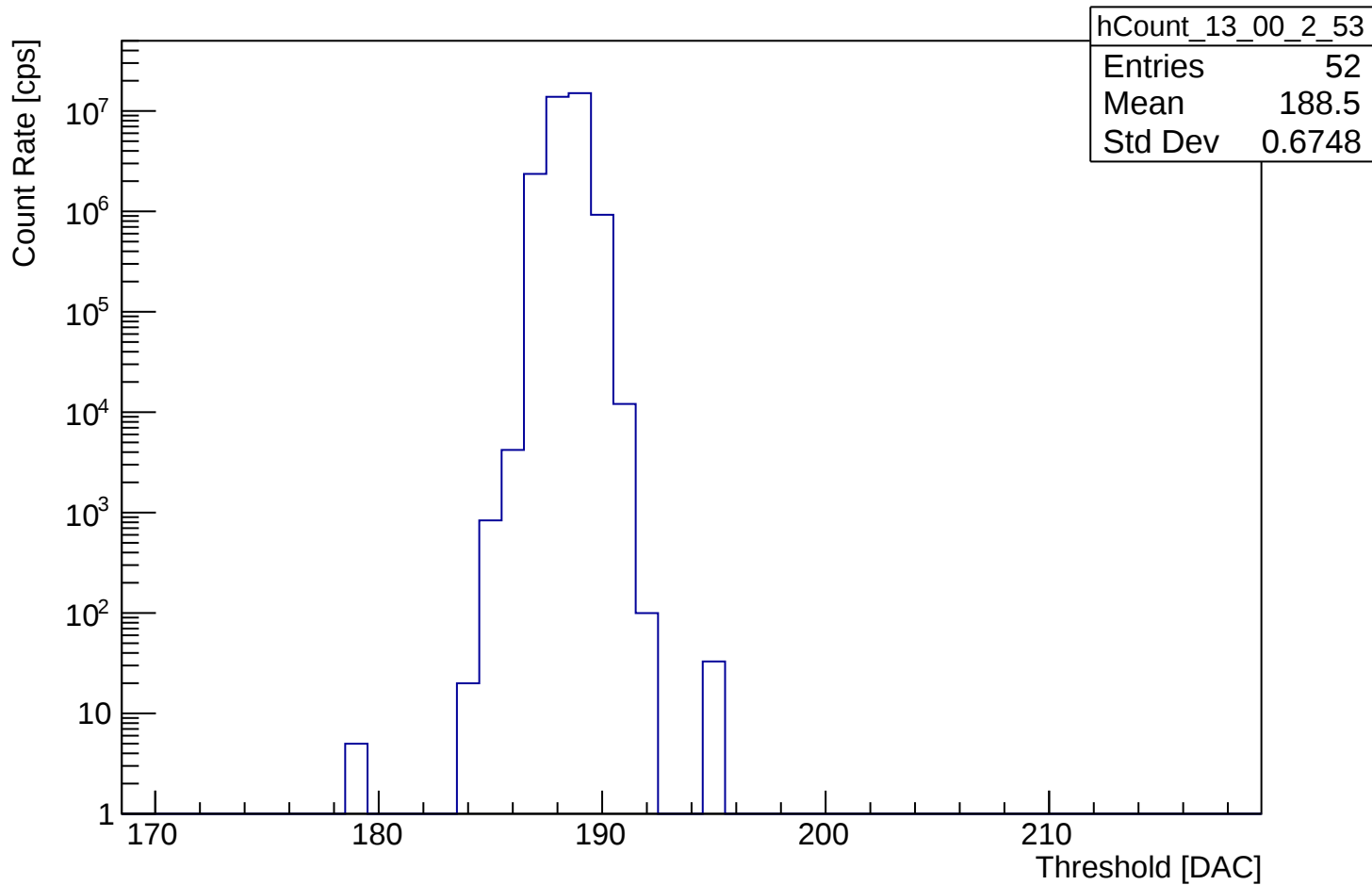
Row 1 Tile 1 Pmt 3 Pixel 45 Slot 13 Fiber 0 Asic 2 Channel 52



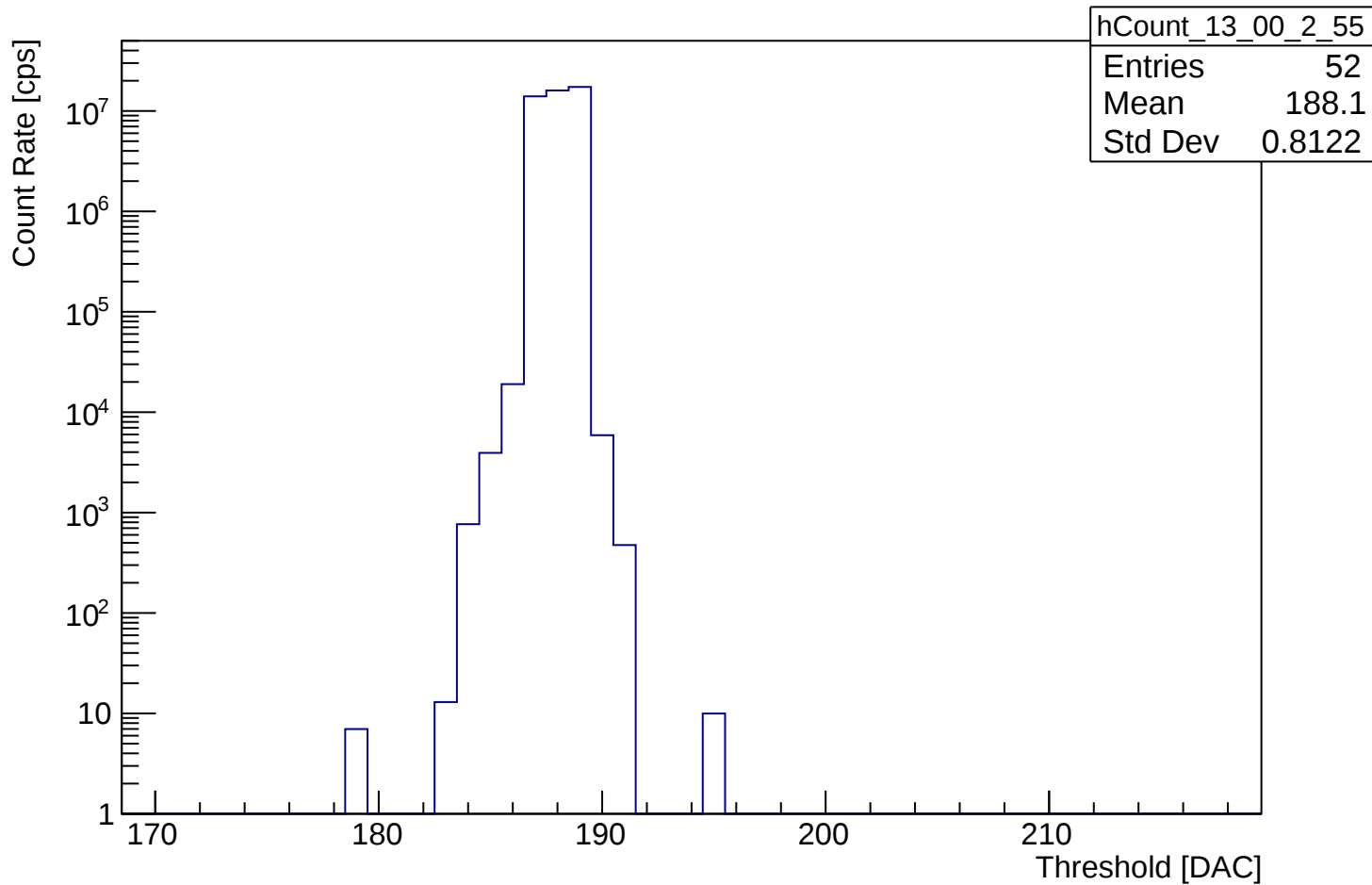
Row 1 Tile 1 Pmt 3 Pixel 46 Slot 13 Fiber 0 Asic 2 Channel 54

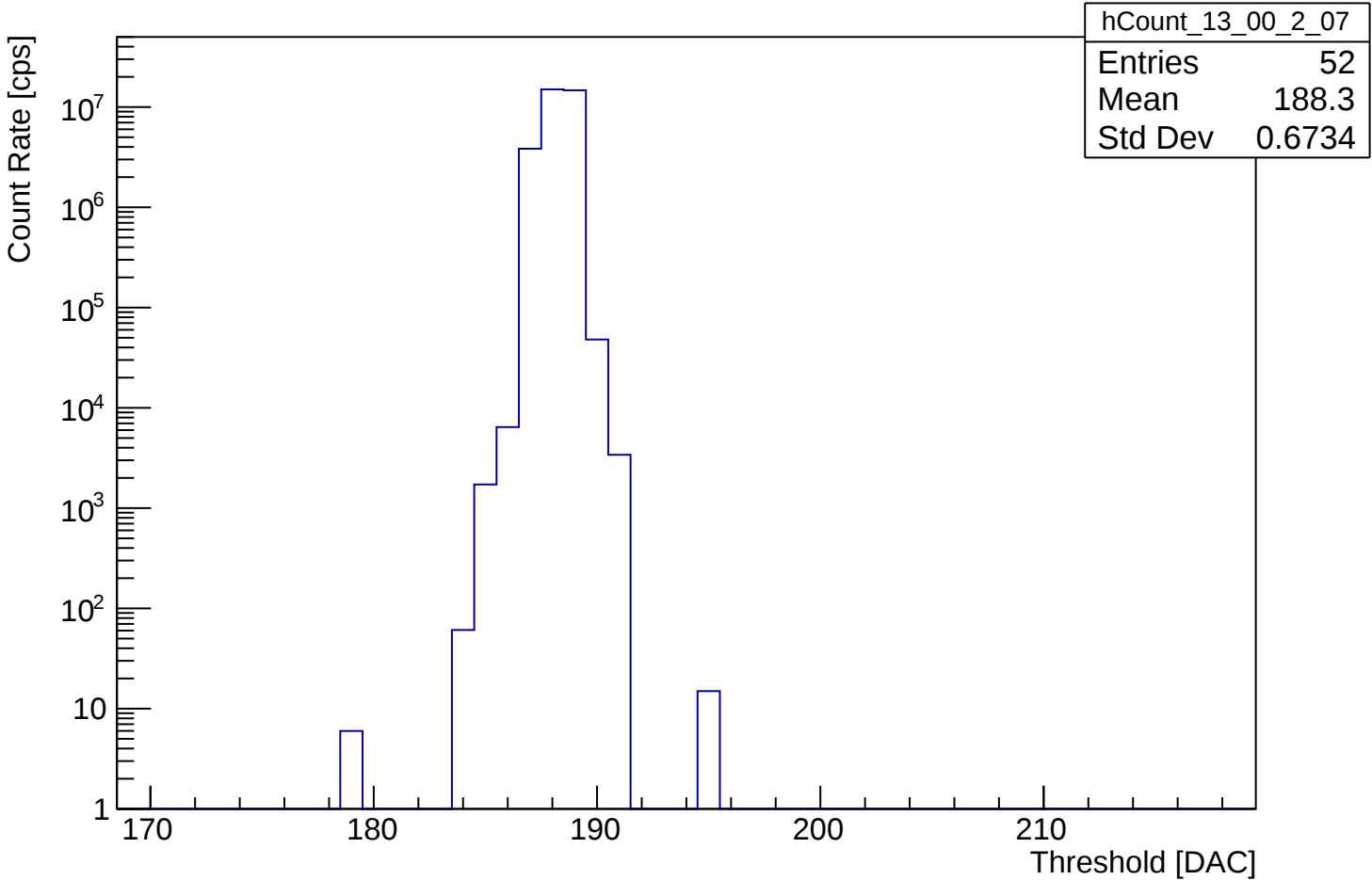


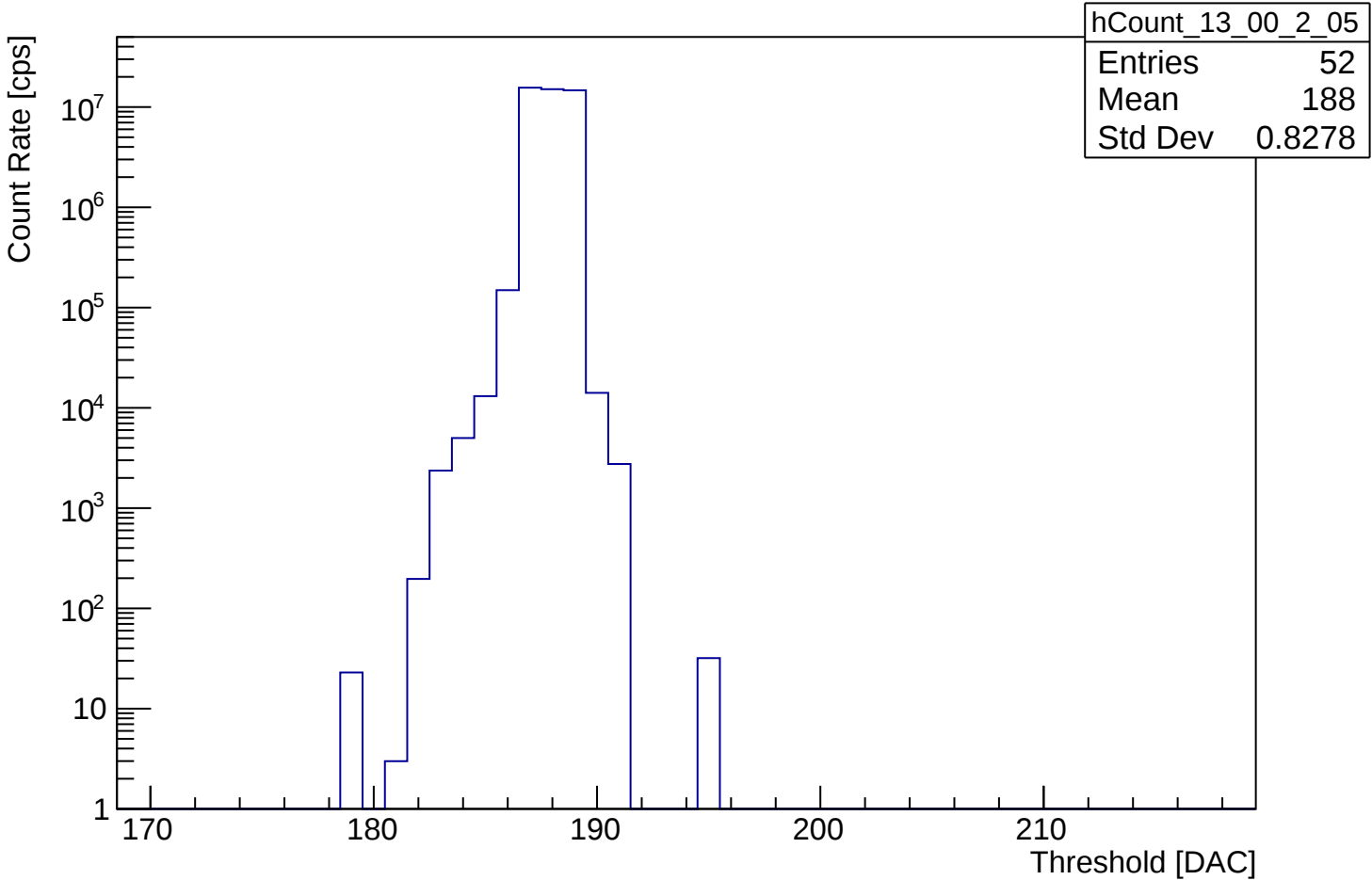
Row 1 Tile 1 Pmt 3 Pixel 47 Slot 13 Fiber 0 Asic 2 Channel 53

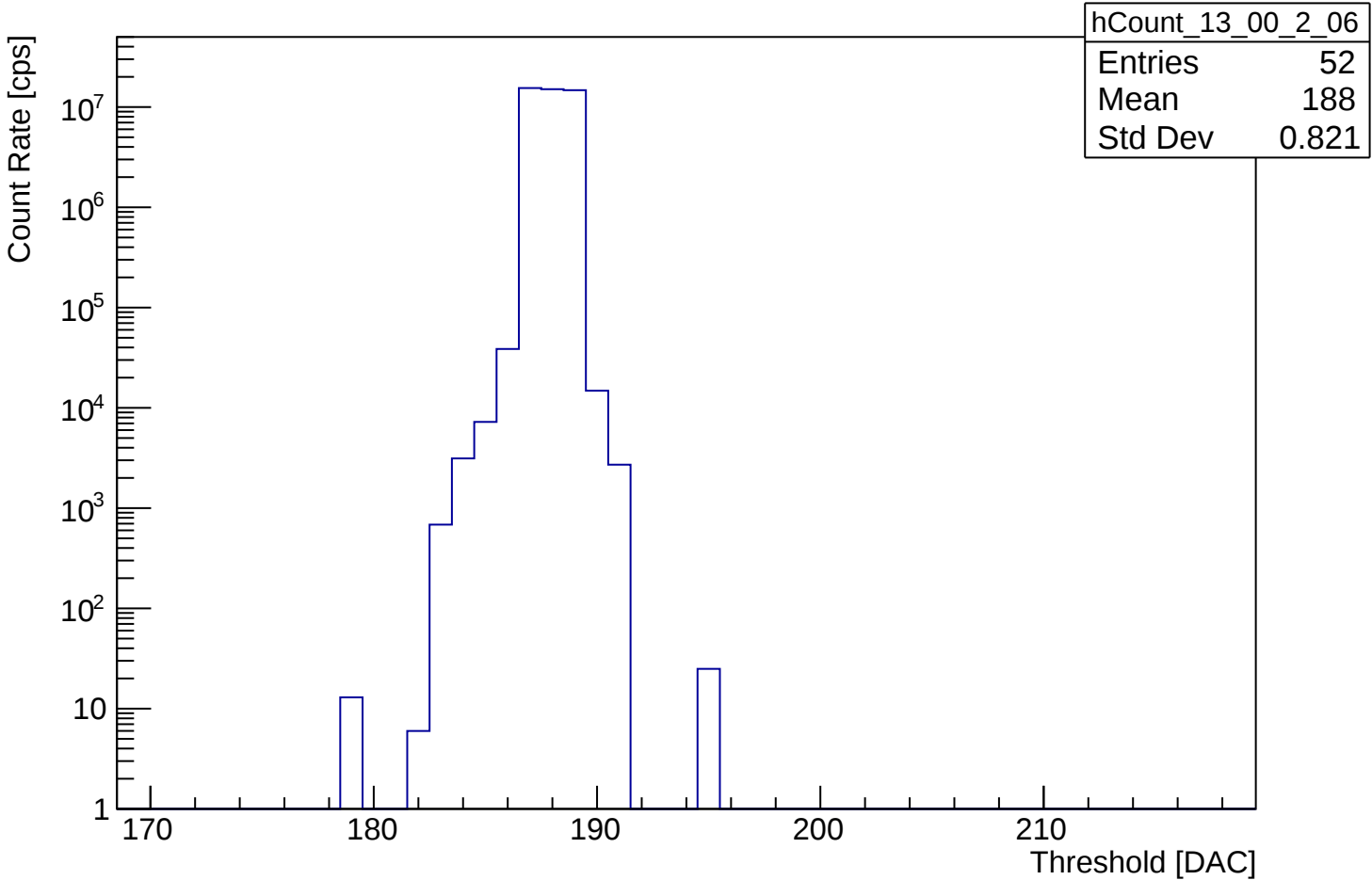


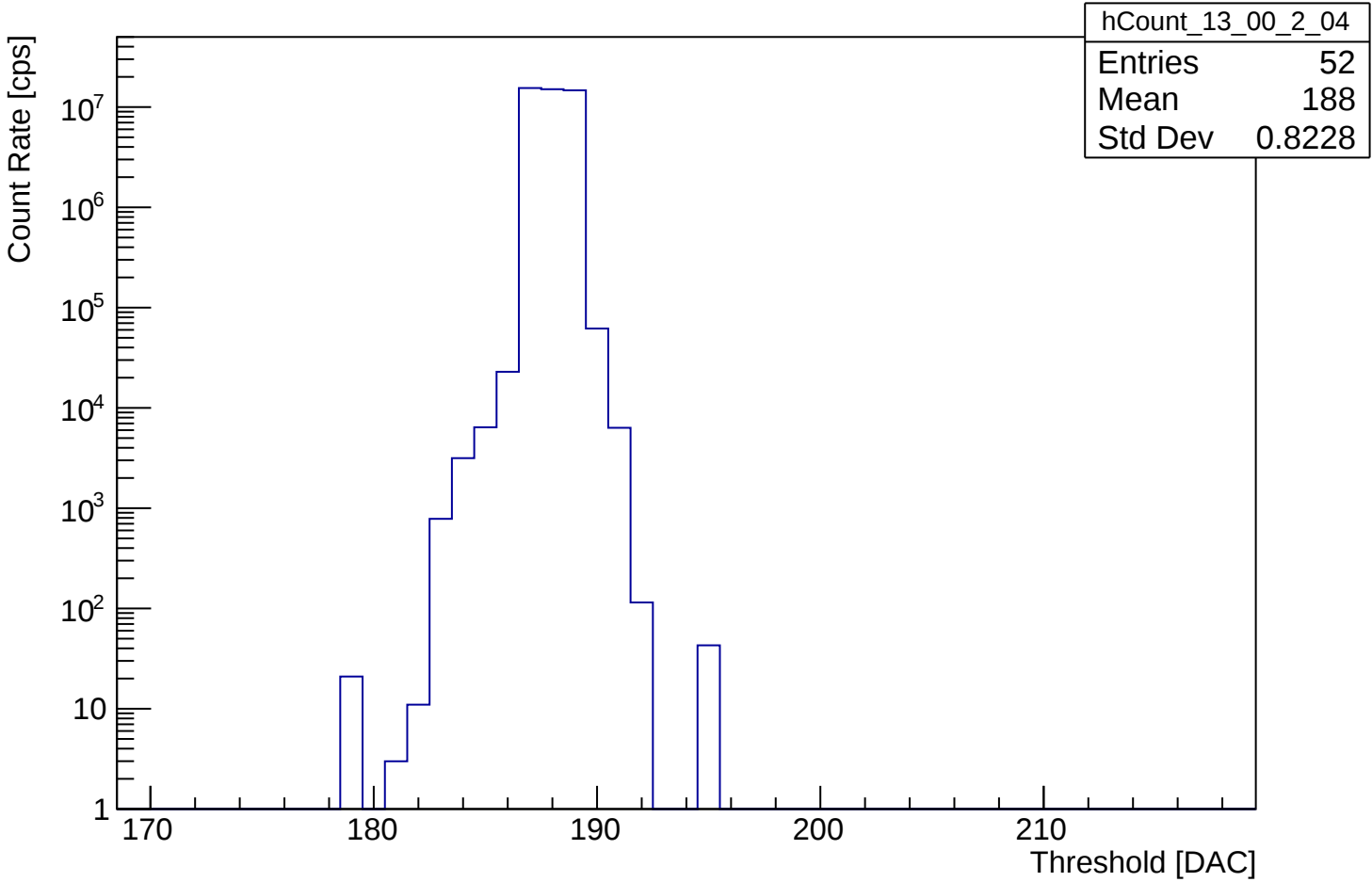
Row 1 Tile 1 Pmt 3 Pixel 48 Slot 13 Fiber 0 Asic 2 Channel 55



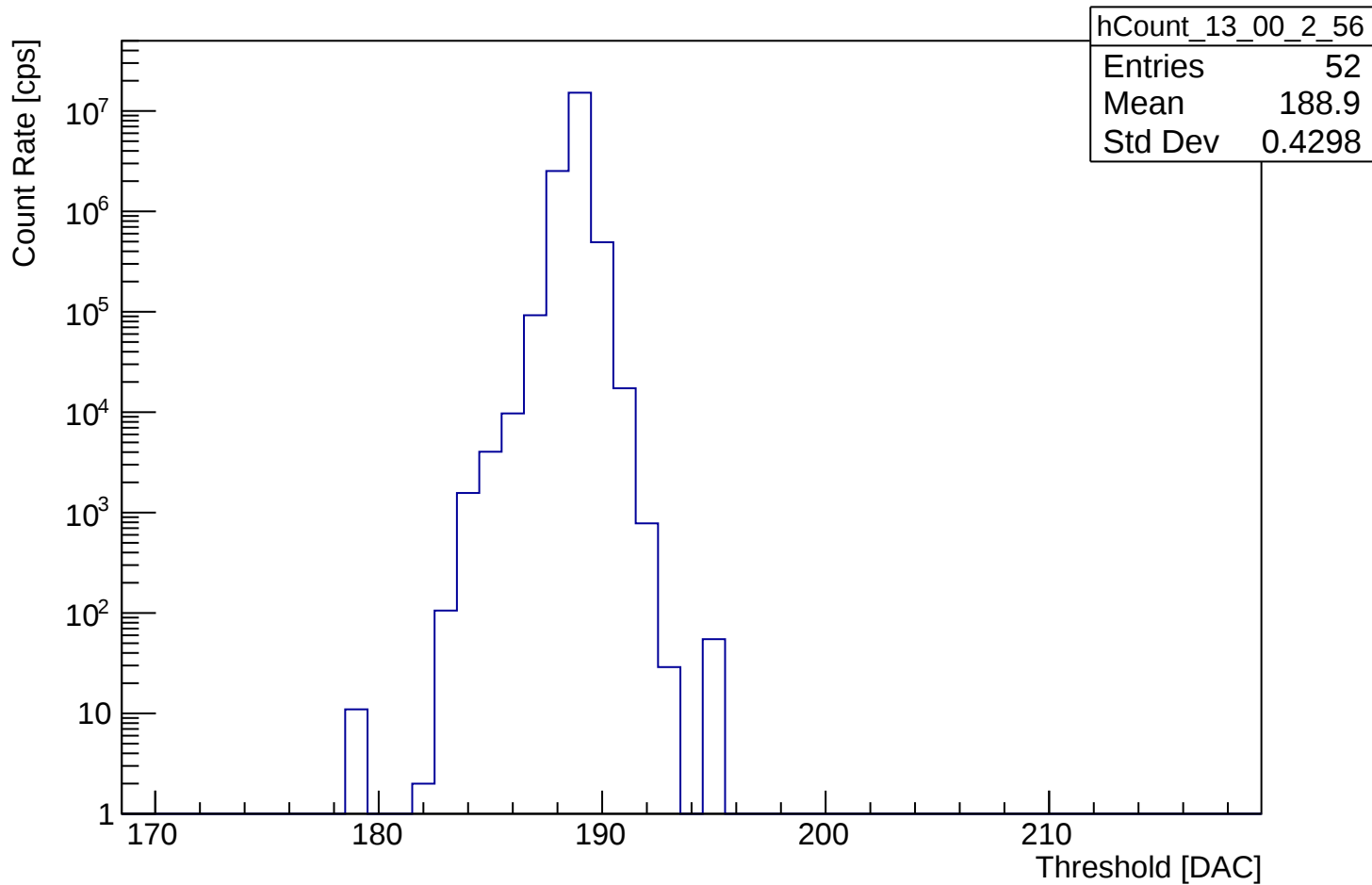




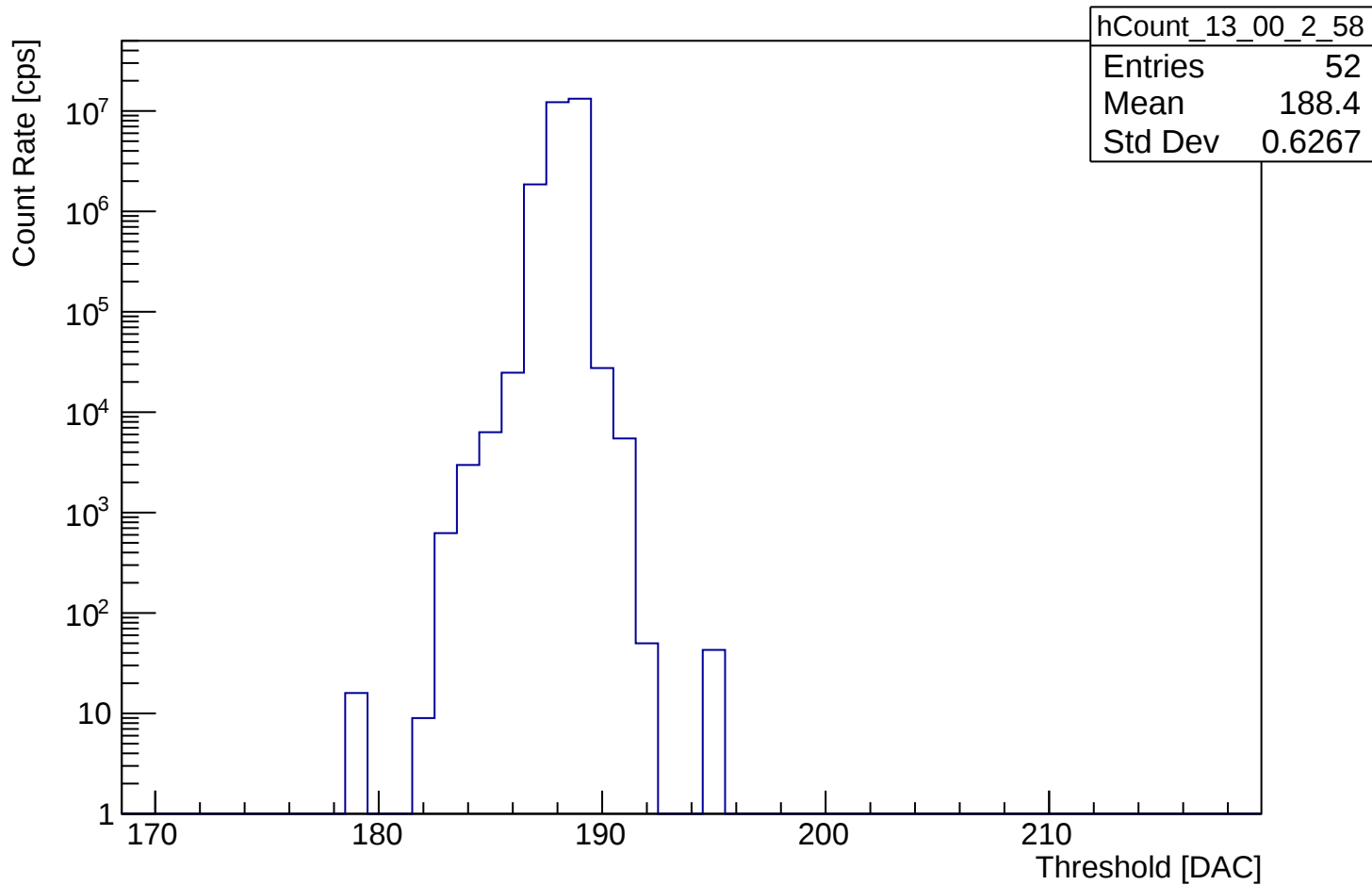




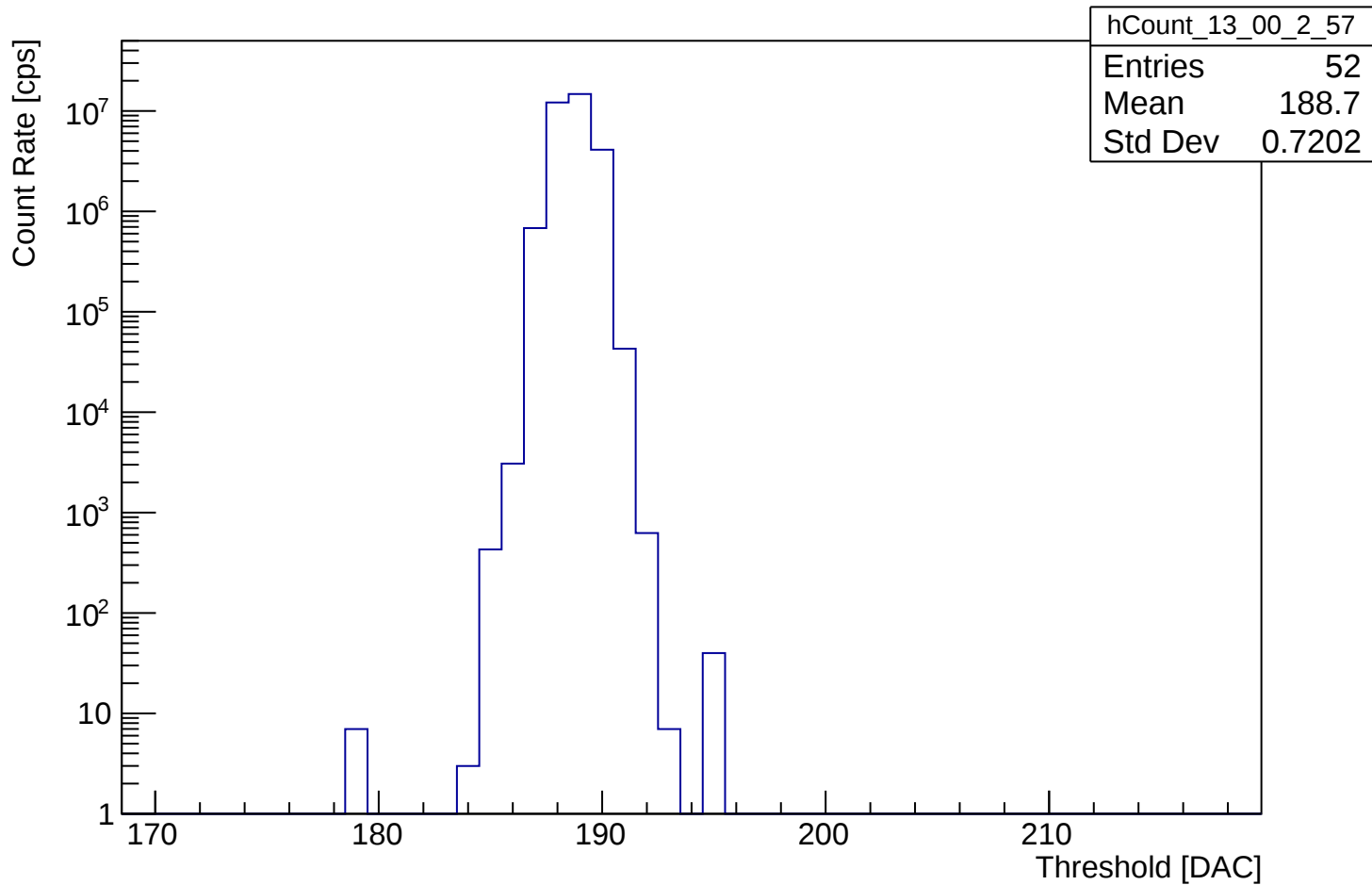
Row 1 Tile 1 Pmt 3 Pixel 53 Slot 13 Fiber 0 Asic 2 Channel 56



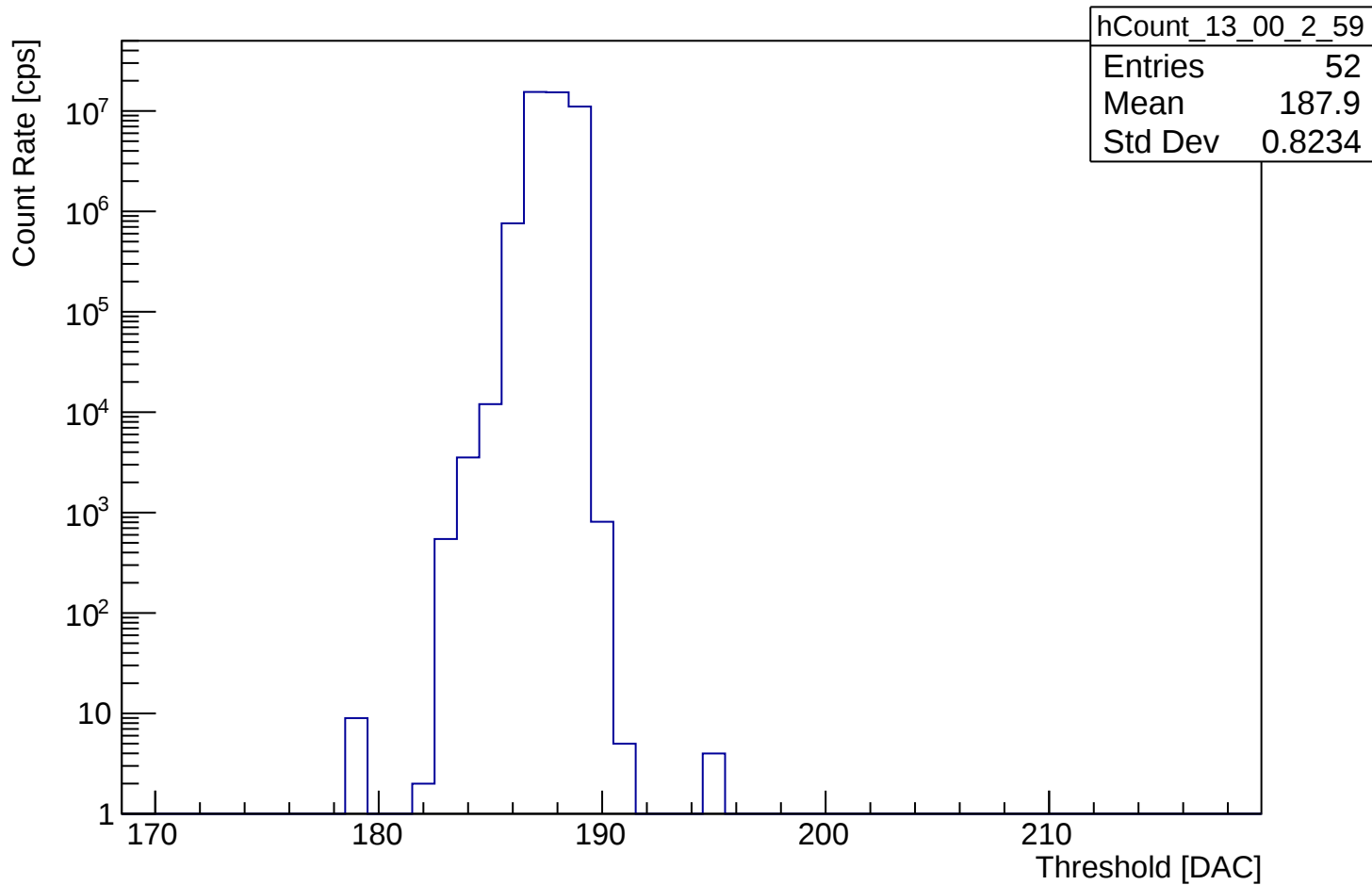
Row 1 Tile 1 Pmt 3 Pixel 54 Slot 13 Fiber 0 Asic 2 Channel 58

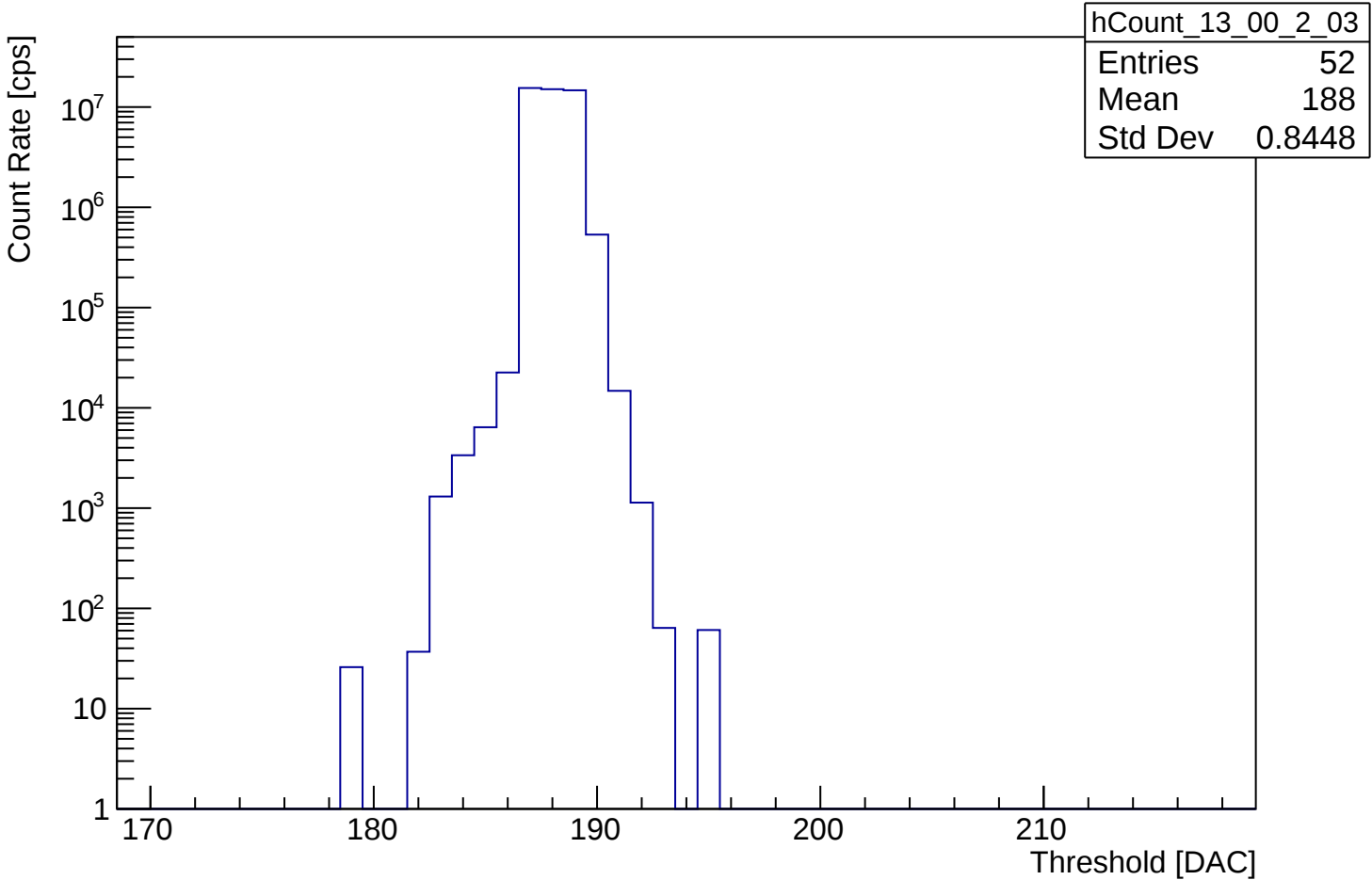


Row 1 Tile 1 Pmt 3 Pixel 55 Slot 13 Fiber 0 Asic 2 Channel 57

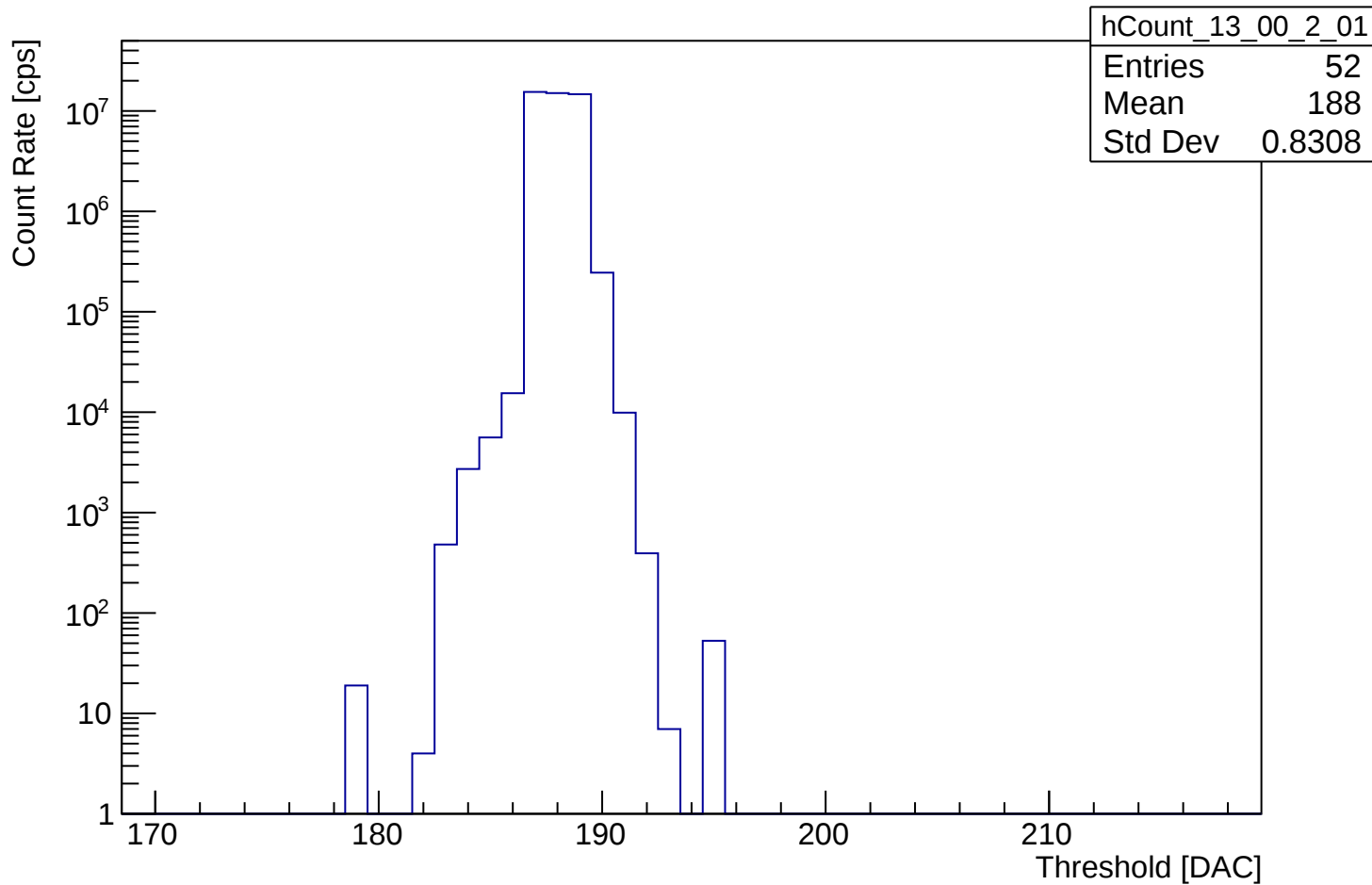


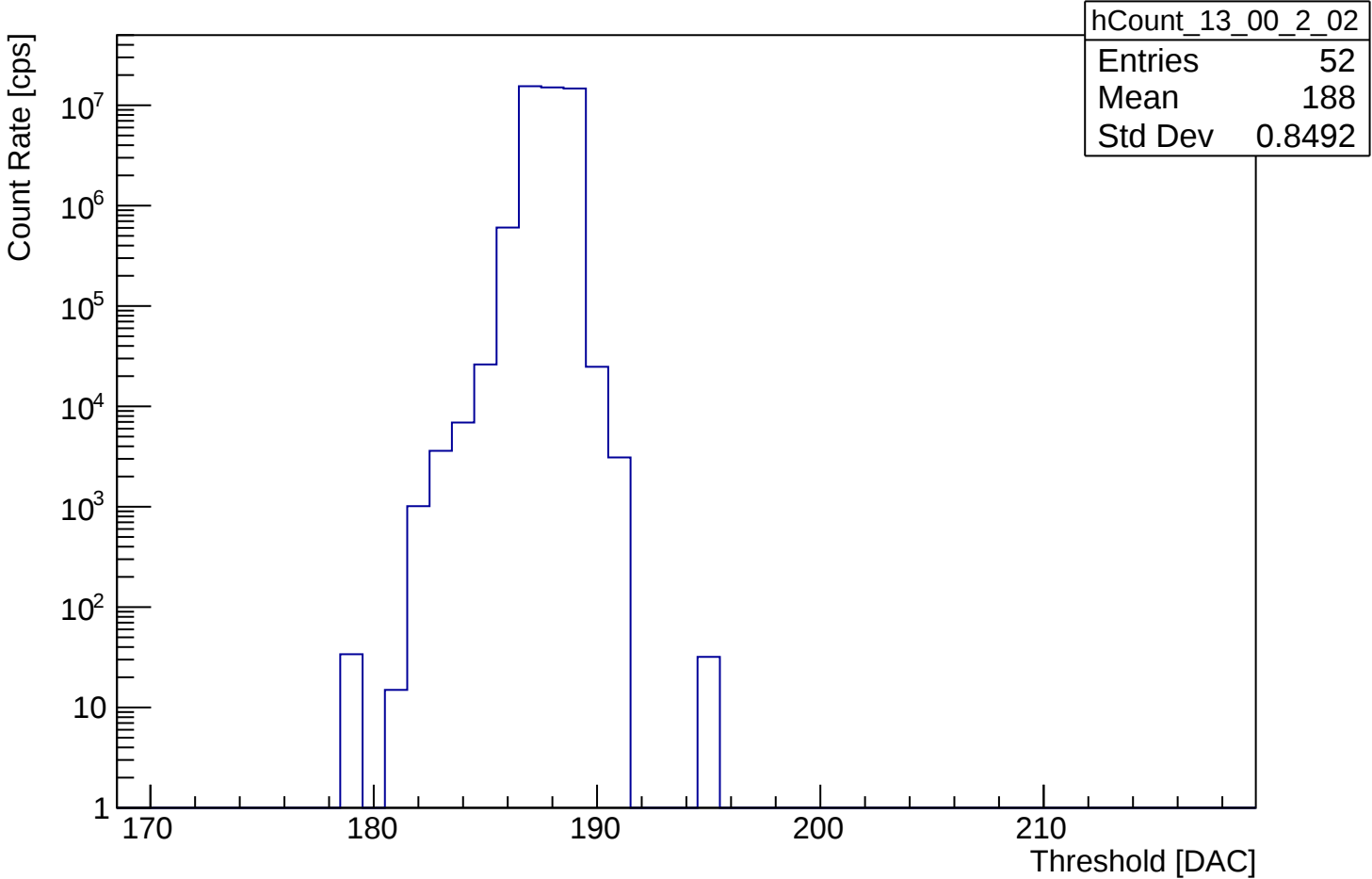
Row 1 Tile 1 Pmt 3 Pixel 56 Slot 13 Fiber 0 Asic 2 Channel 59

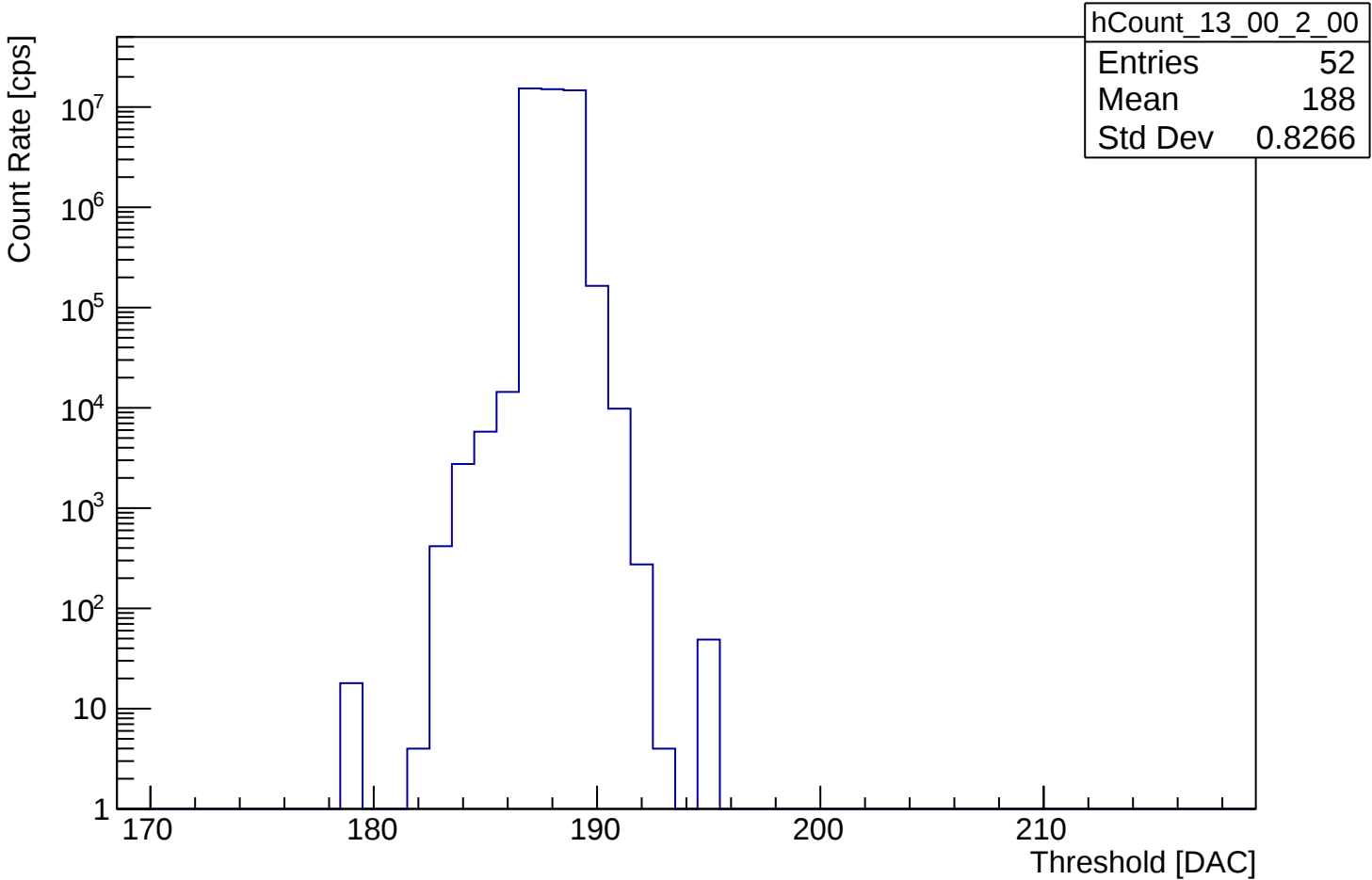




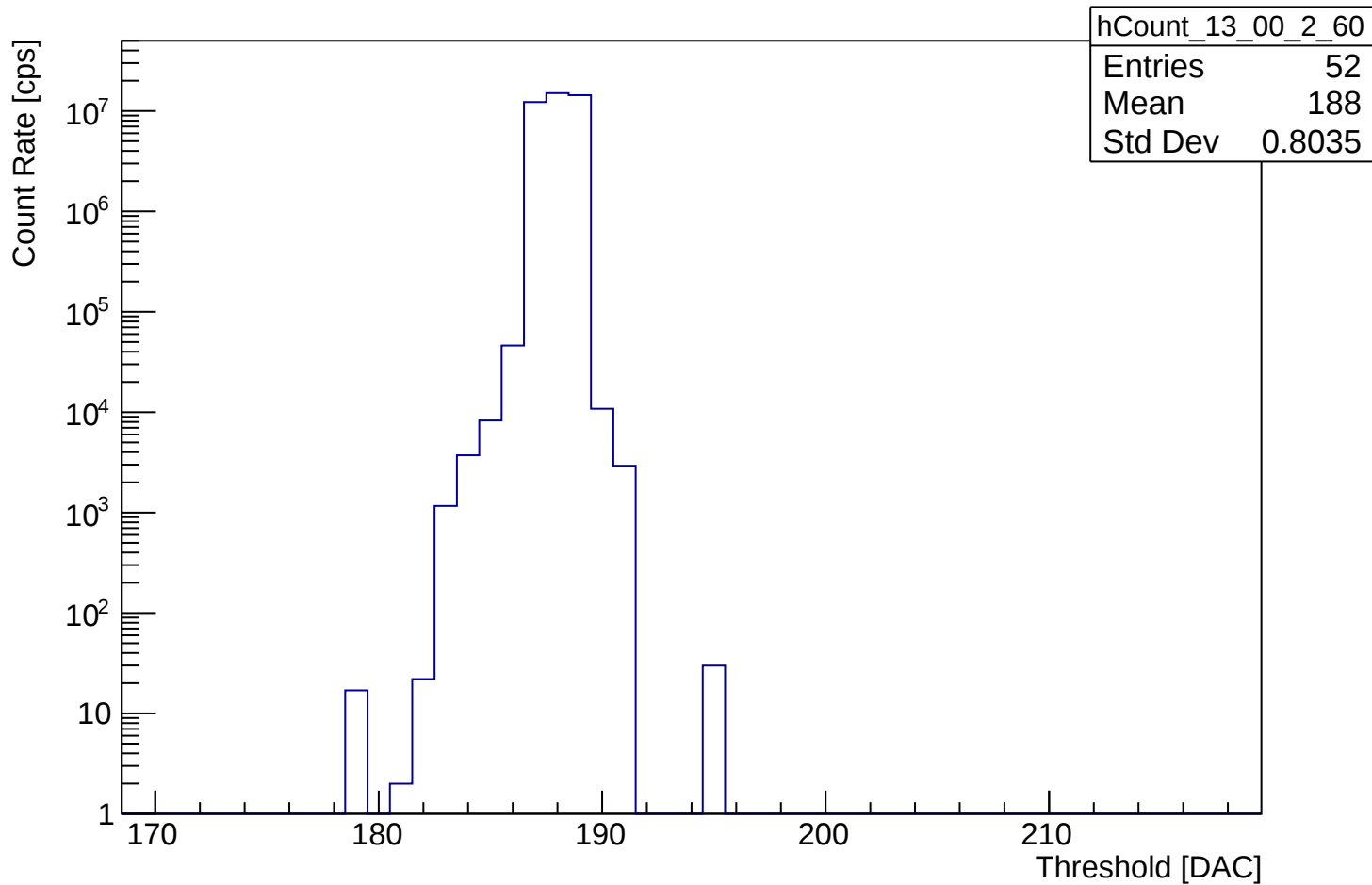
Row 1 Tile 1 Pmt 3 Pixel 58 Slot 13 Fiber 0 Asic 2 Channel 1



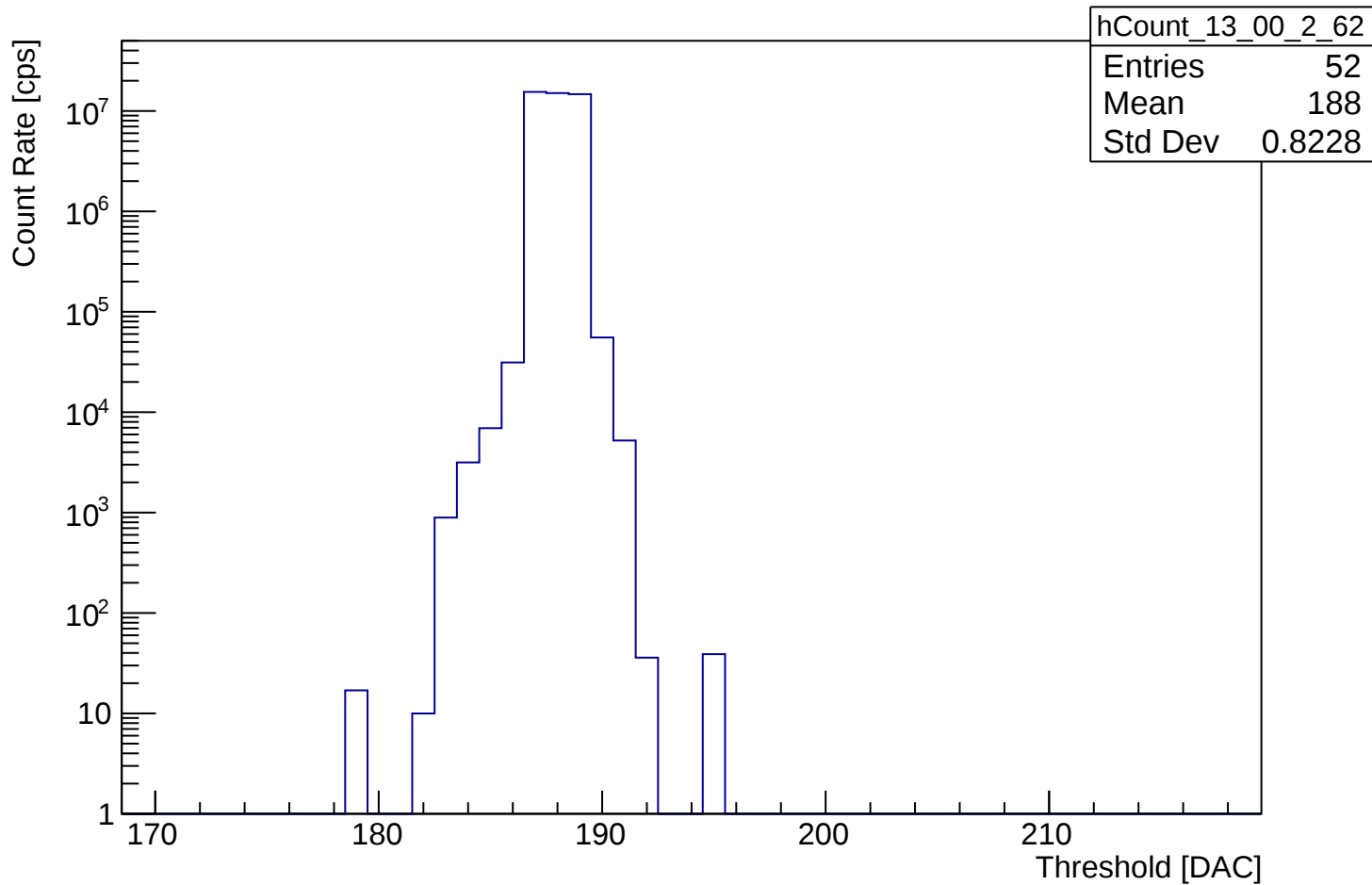




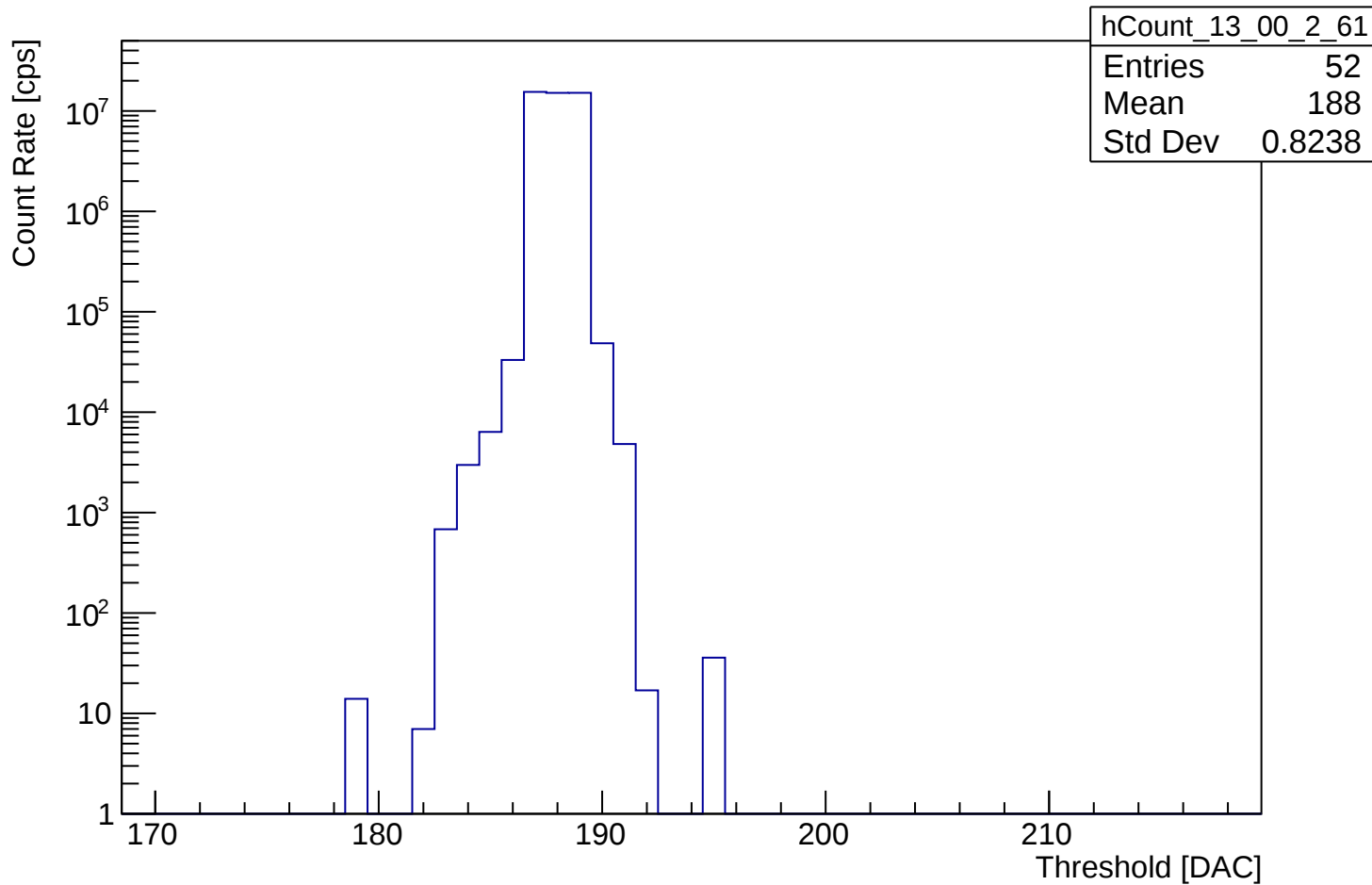
Row 1 Tile 1 Pmt 3 Pixel 61 Slot 13 Fiber 0 Asic 2 Channel 60



Row 1 Tile 1 Pmt 3 Pixel 62 Slot 13 Fiber 0 Asic 2 Channel 62



Row 1 Tile 1 Pmt 3 Pixel 63 Slot 13 Fiber 0 Asic 2 Channel 61



Row 1 Tile 1 Pmt 3 Pixel 64 Slot 13 Fiber 0 Asic 2 Channel 63

