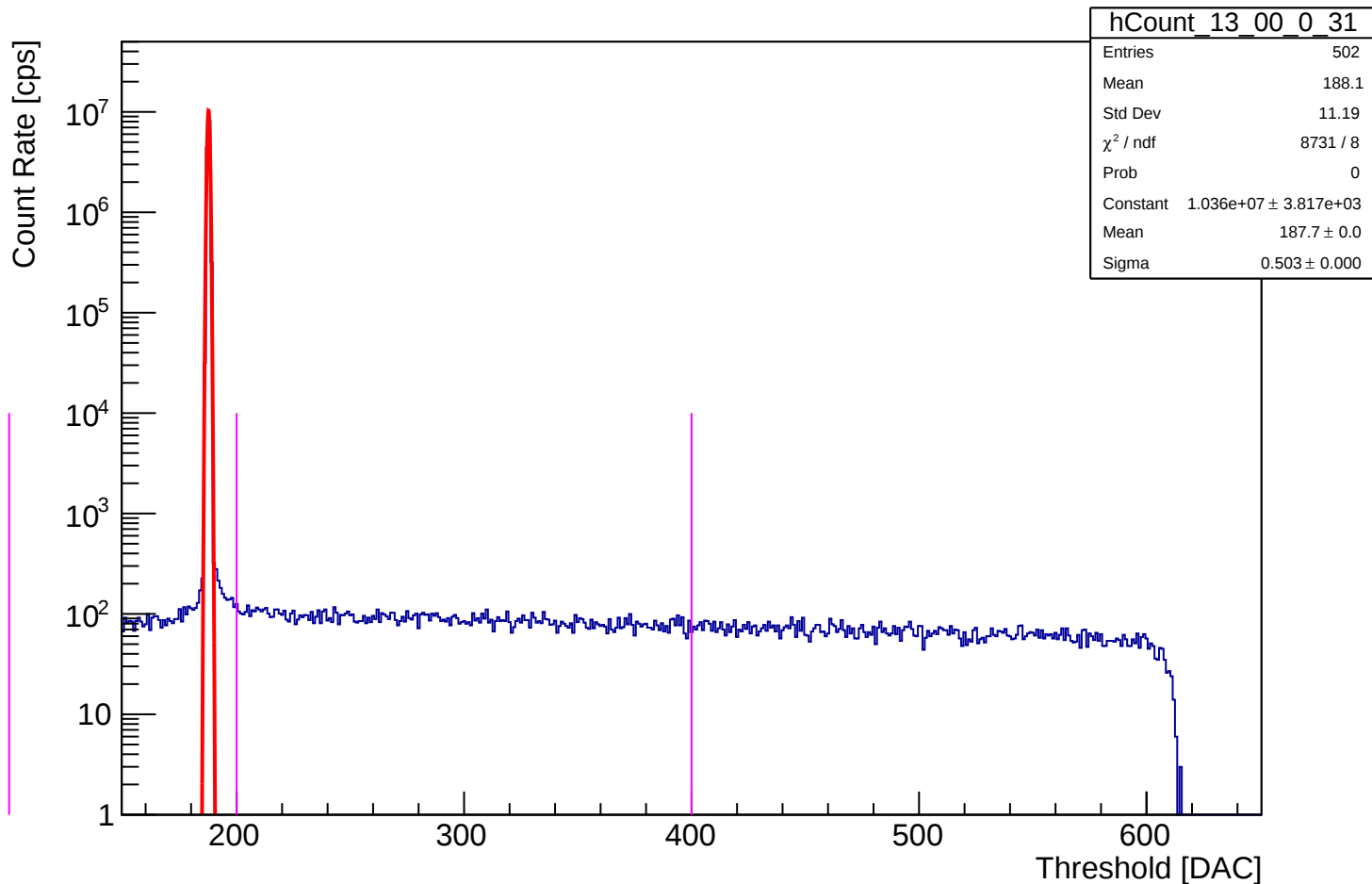
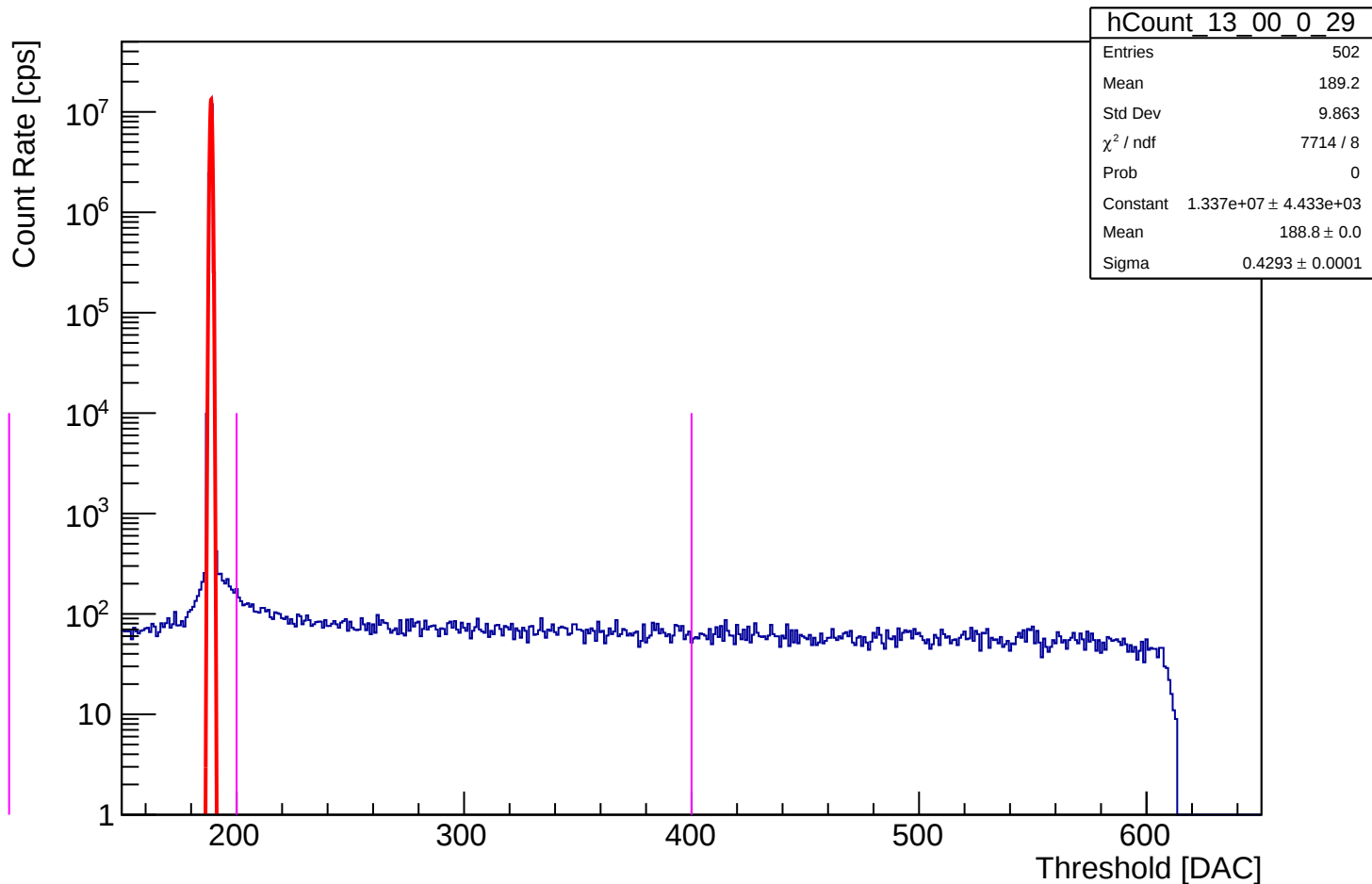


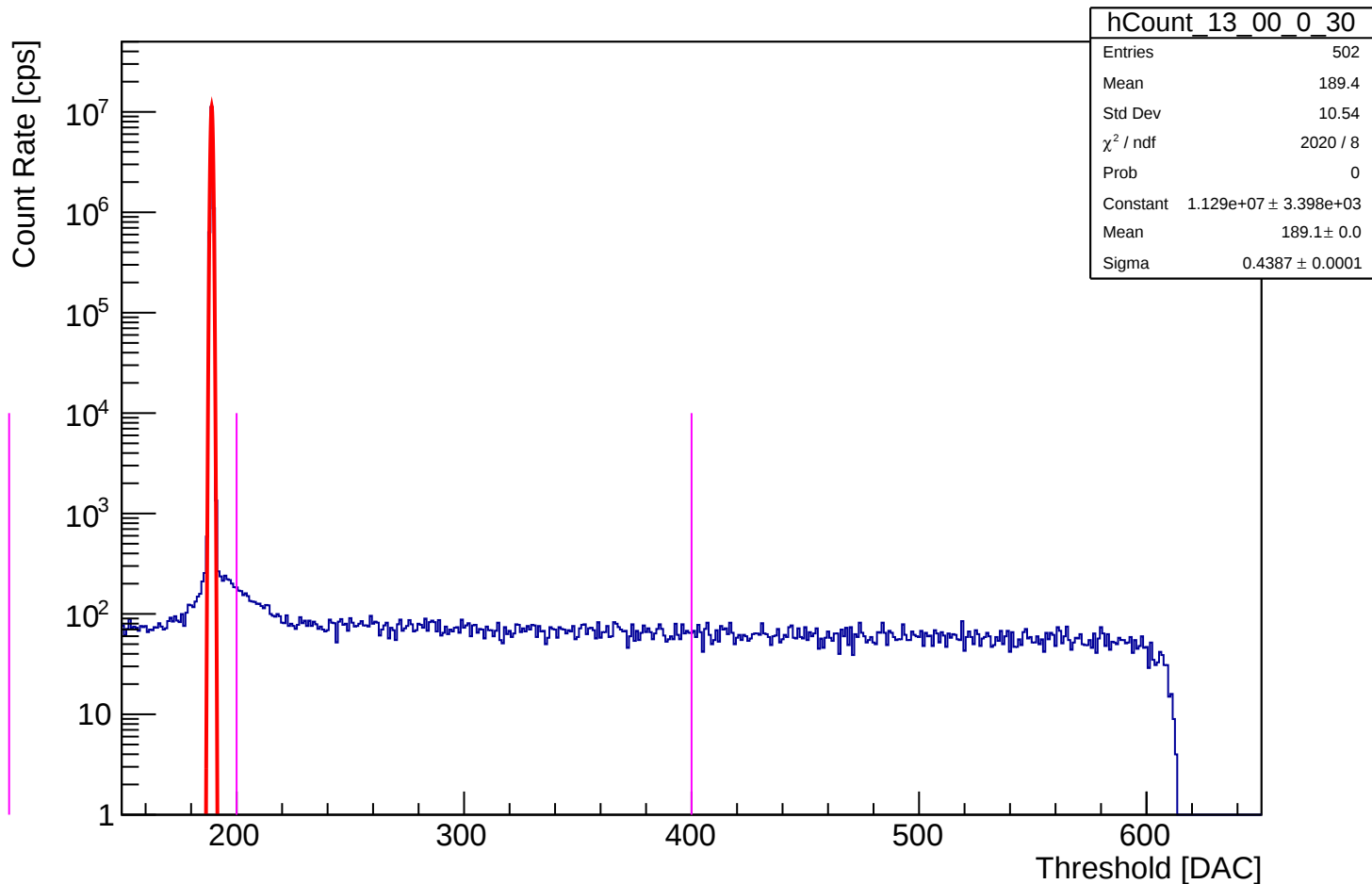
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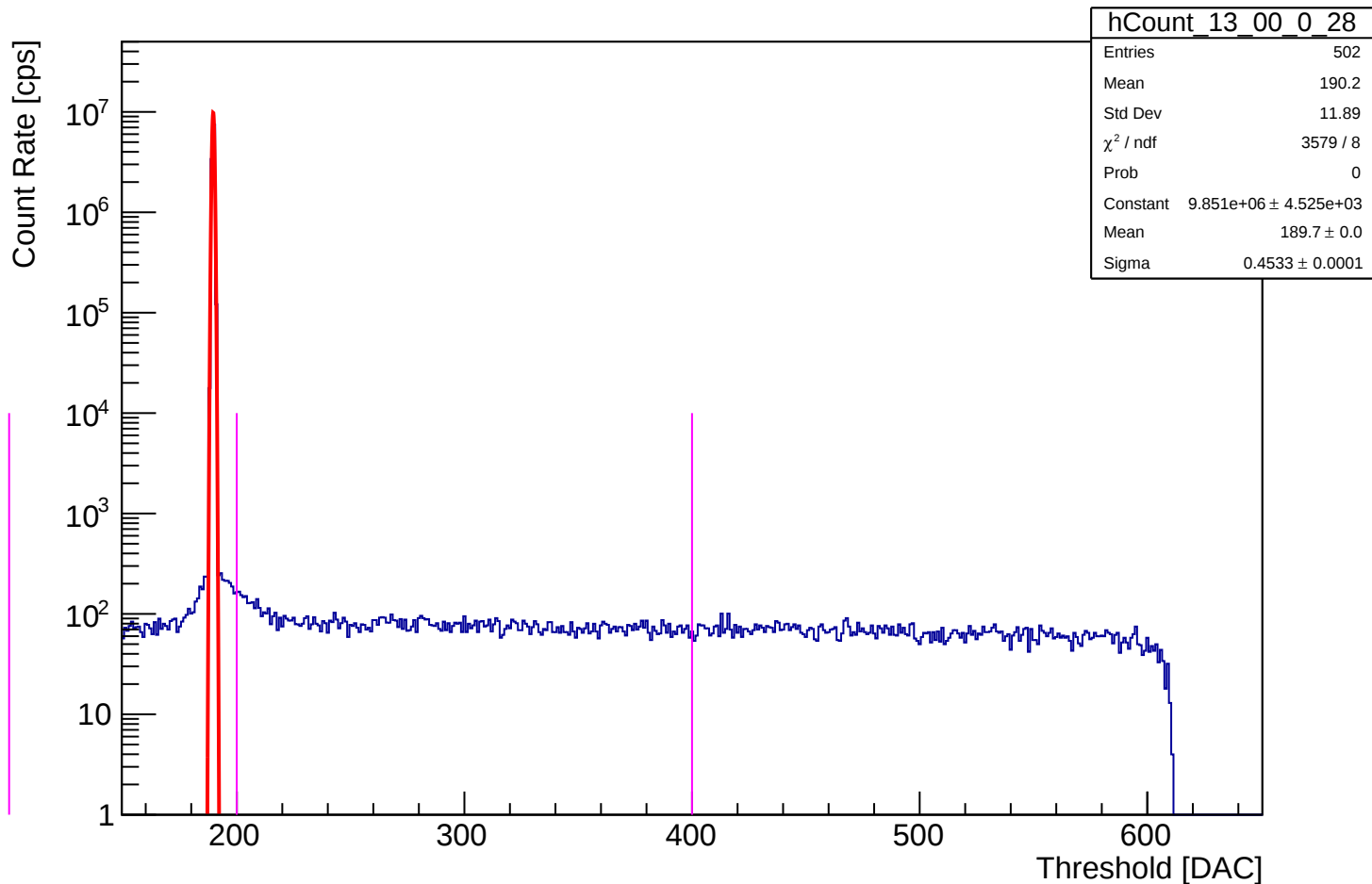
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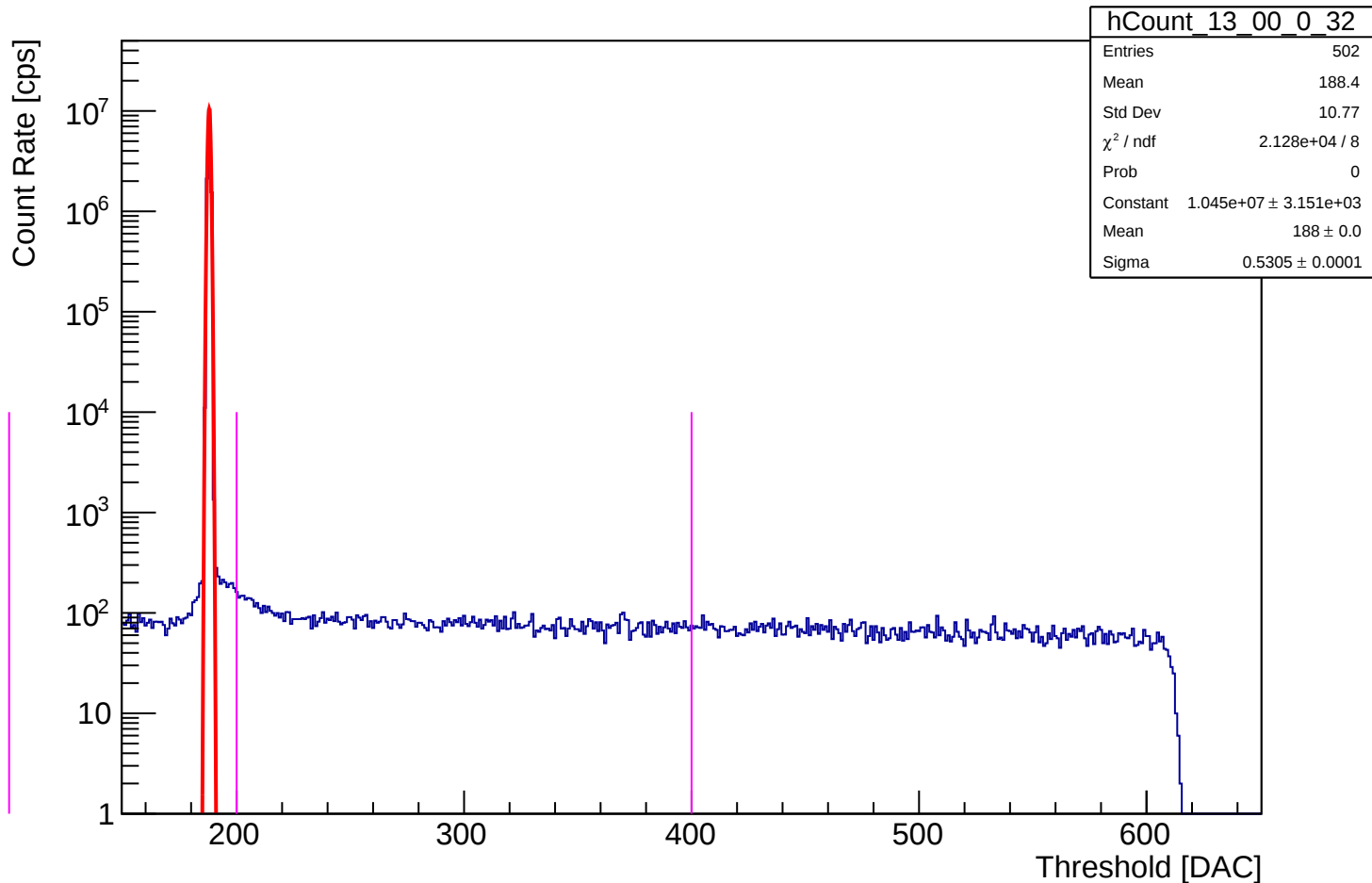
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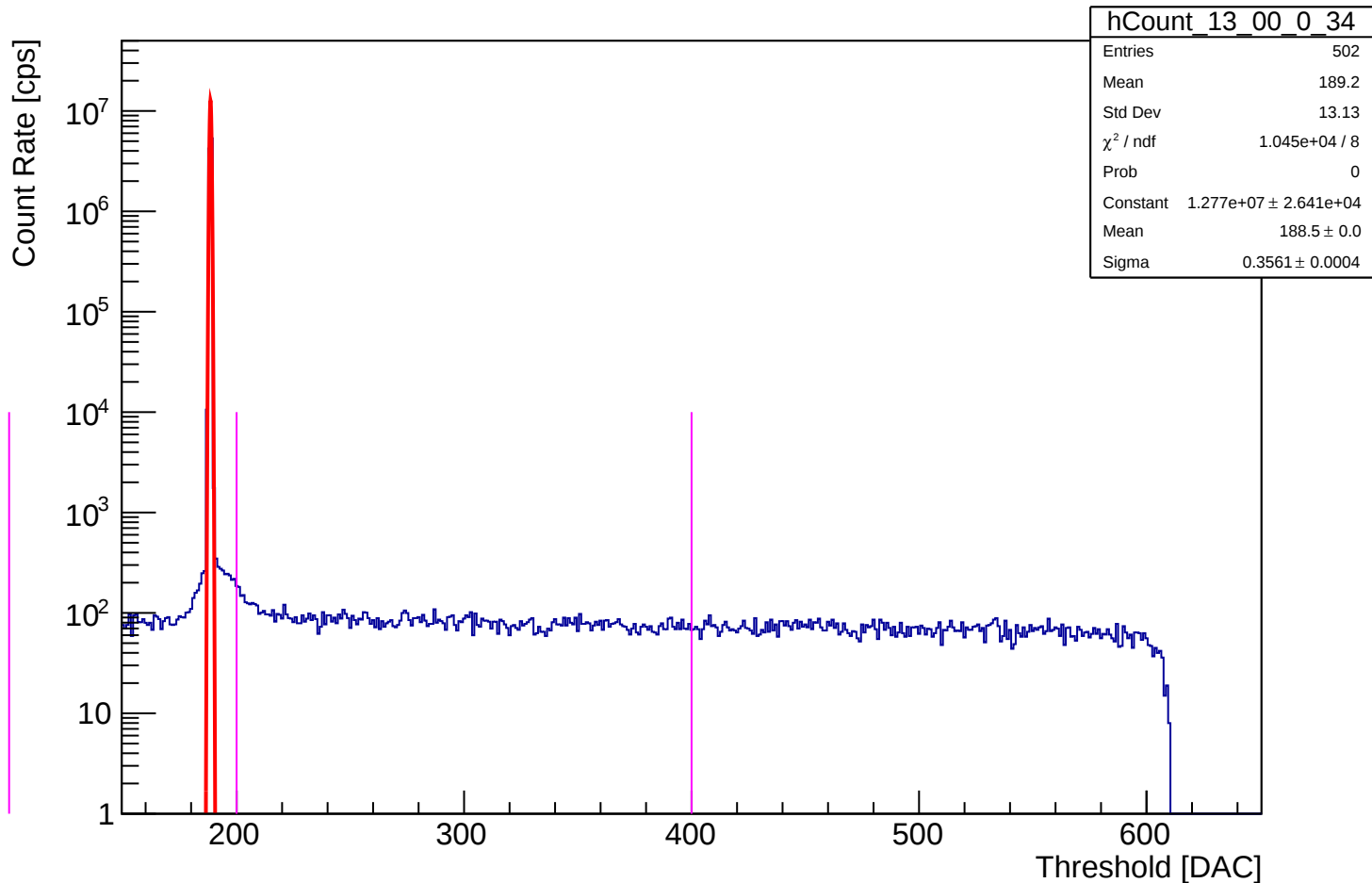
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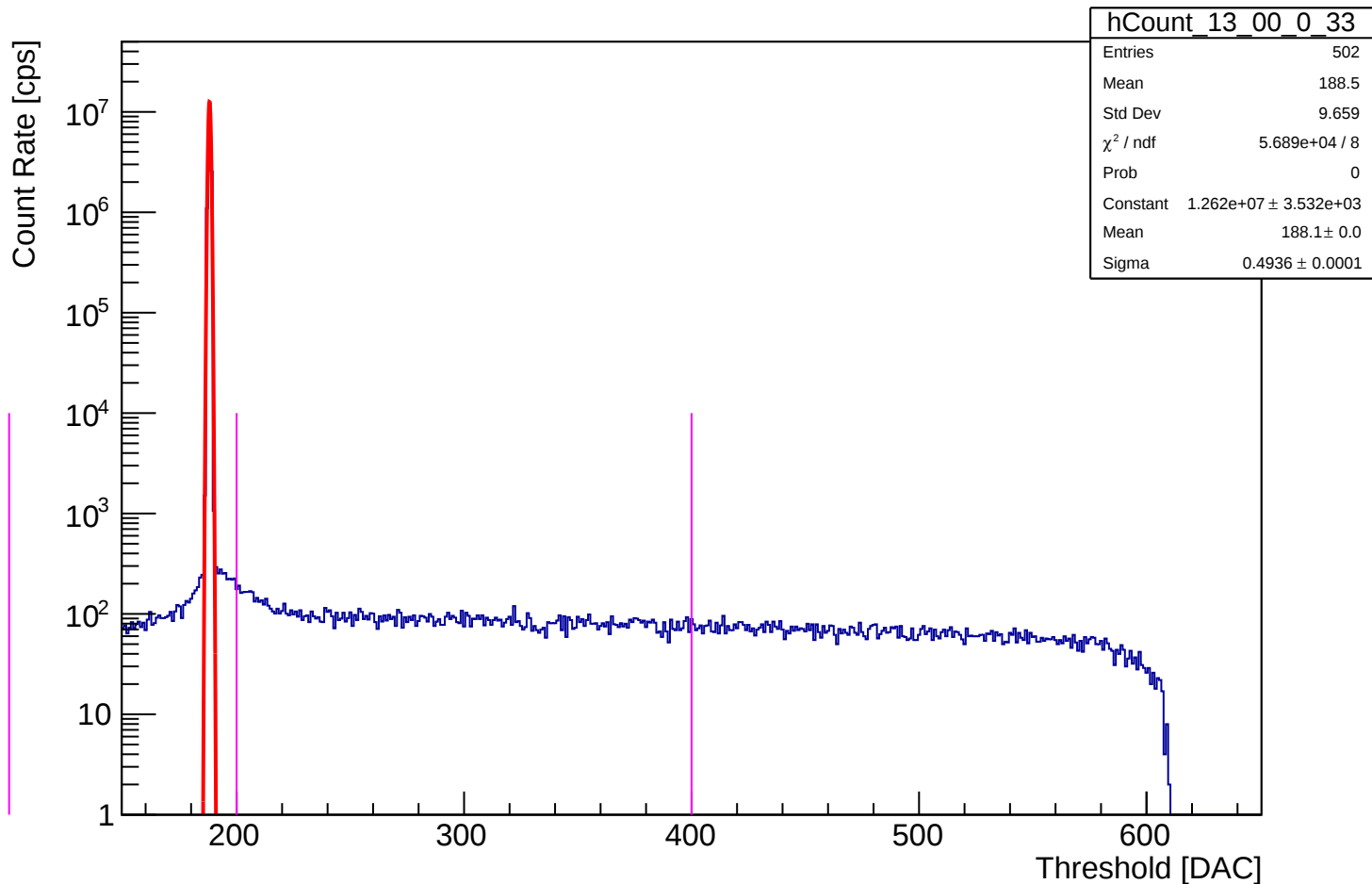
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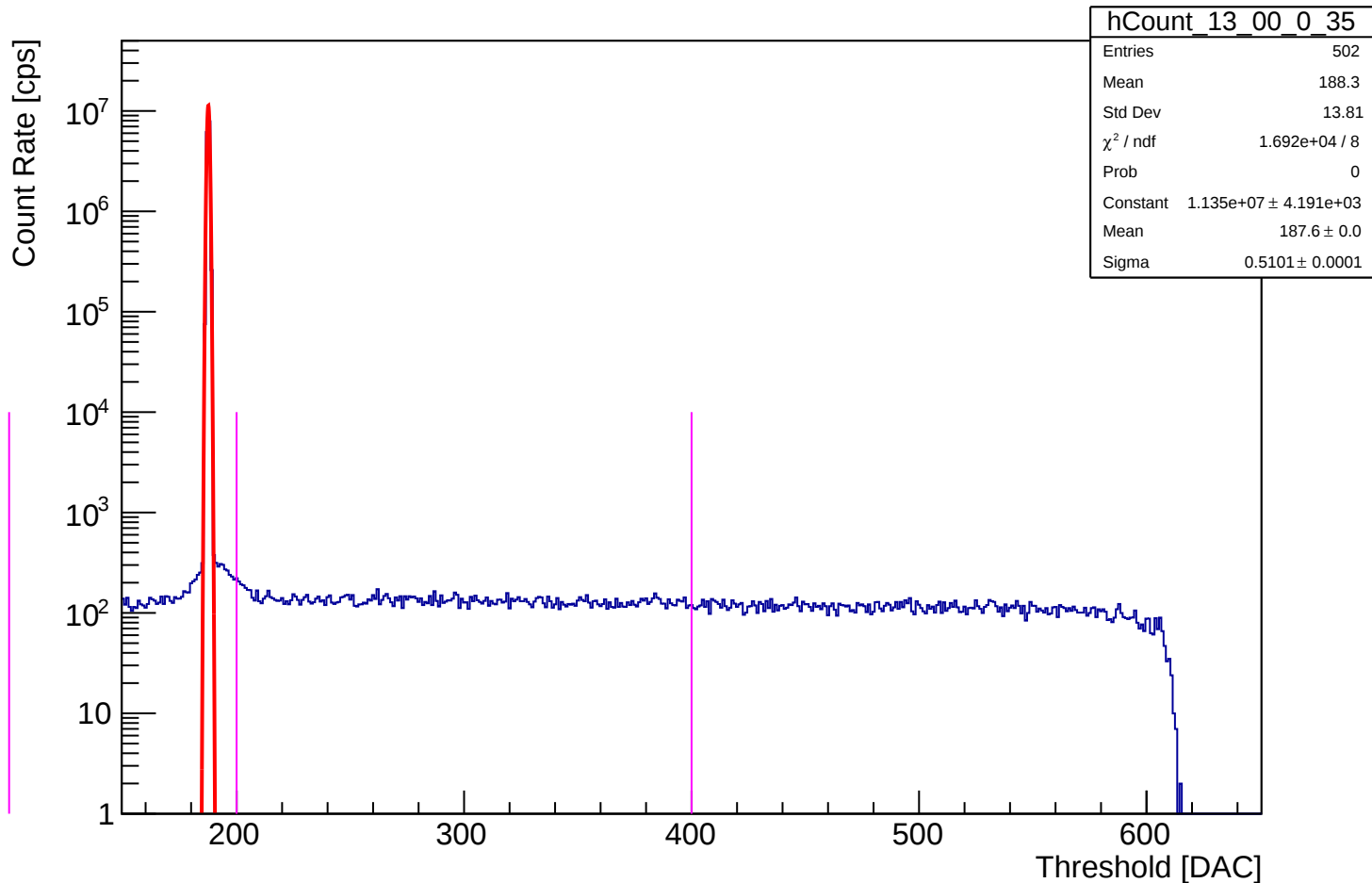
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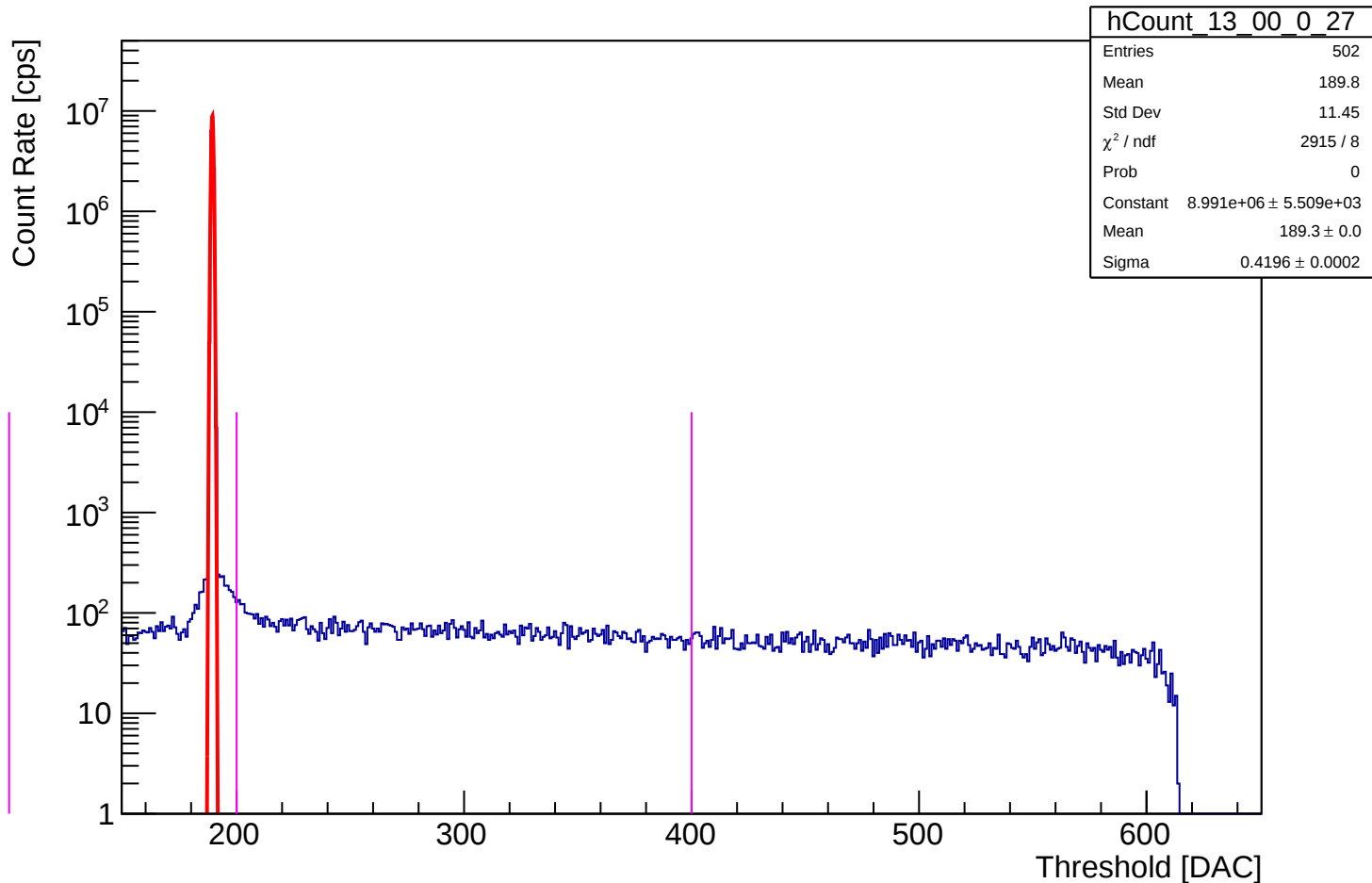
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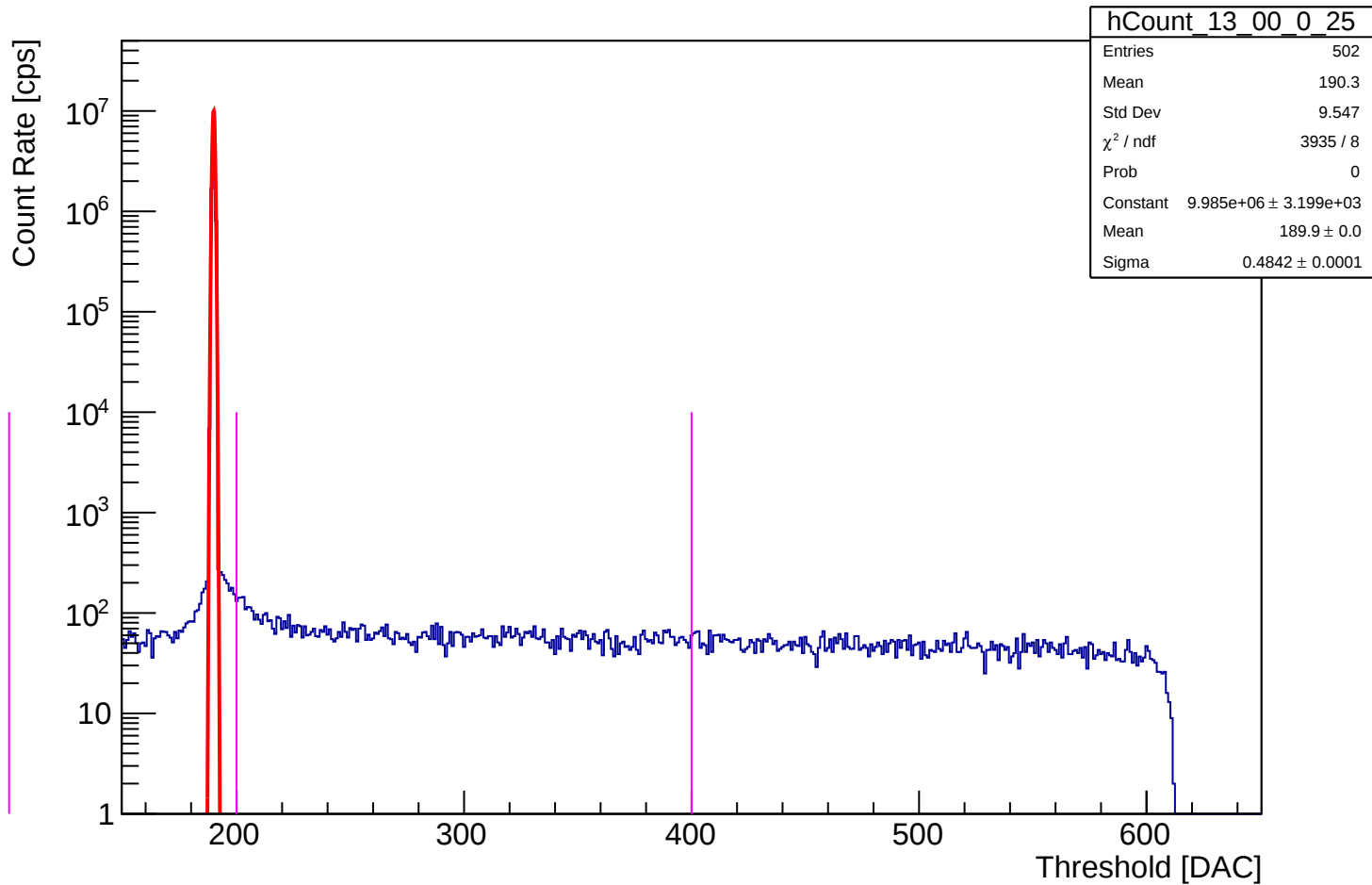
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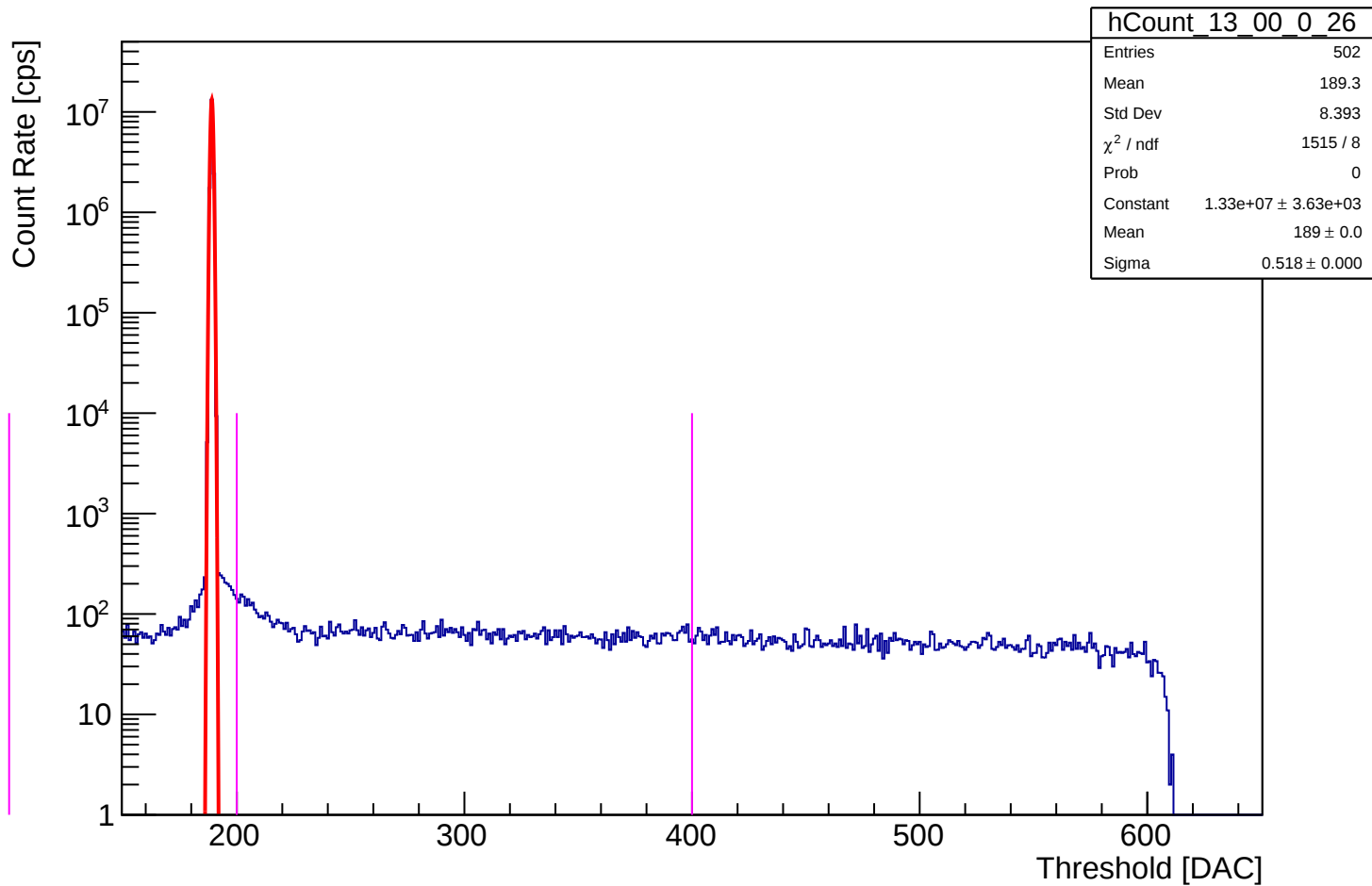
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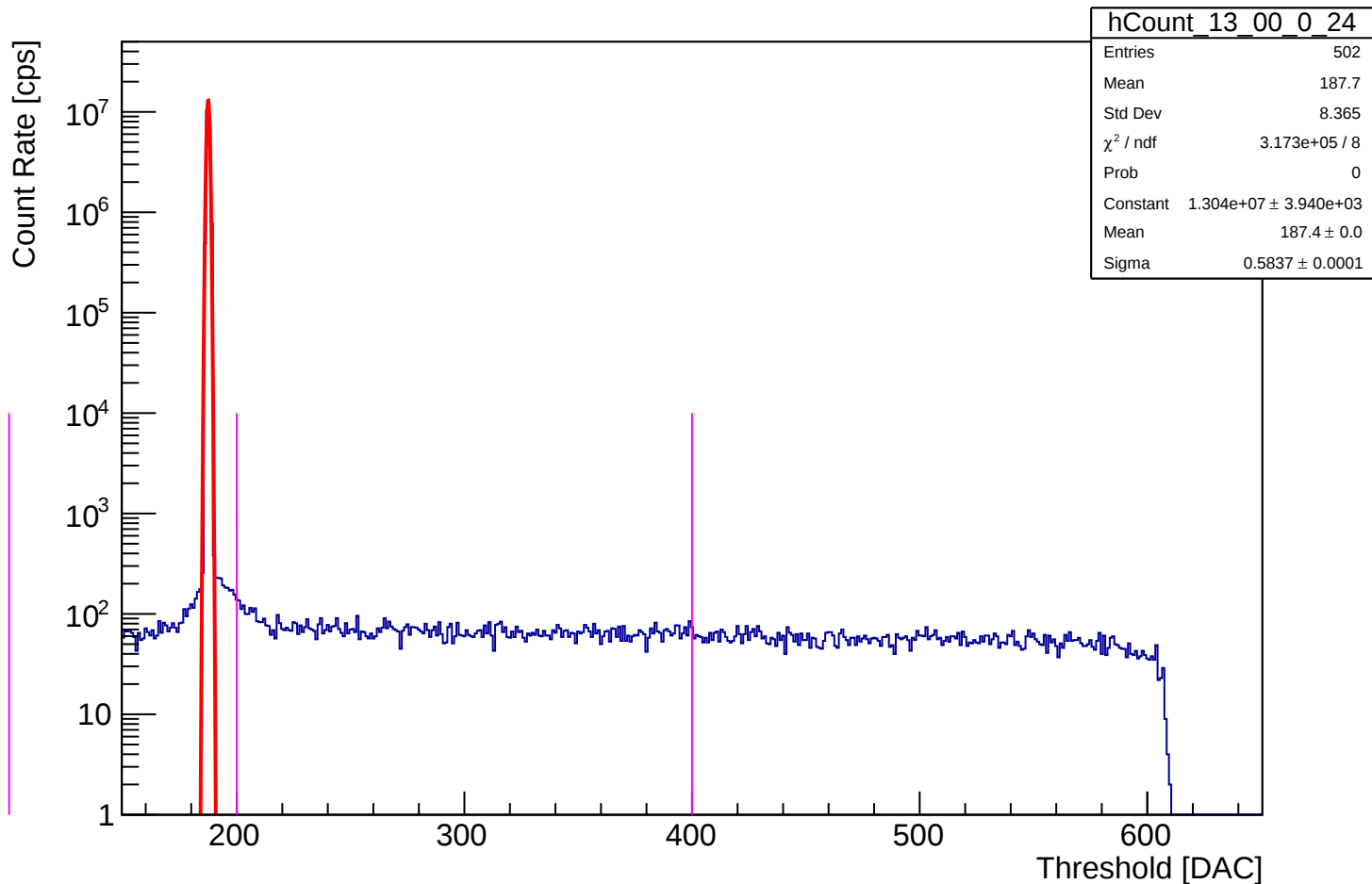
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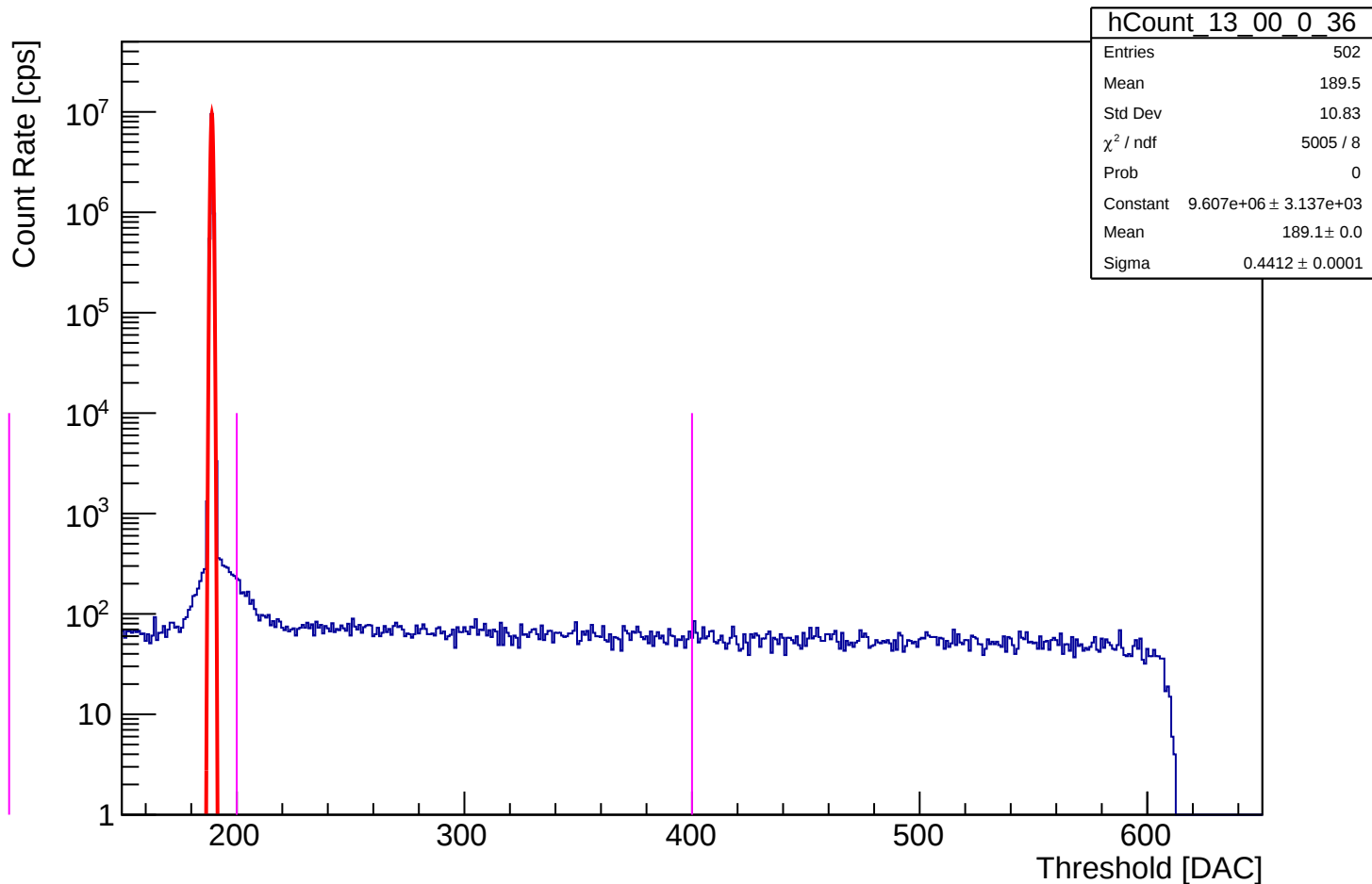
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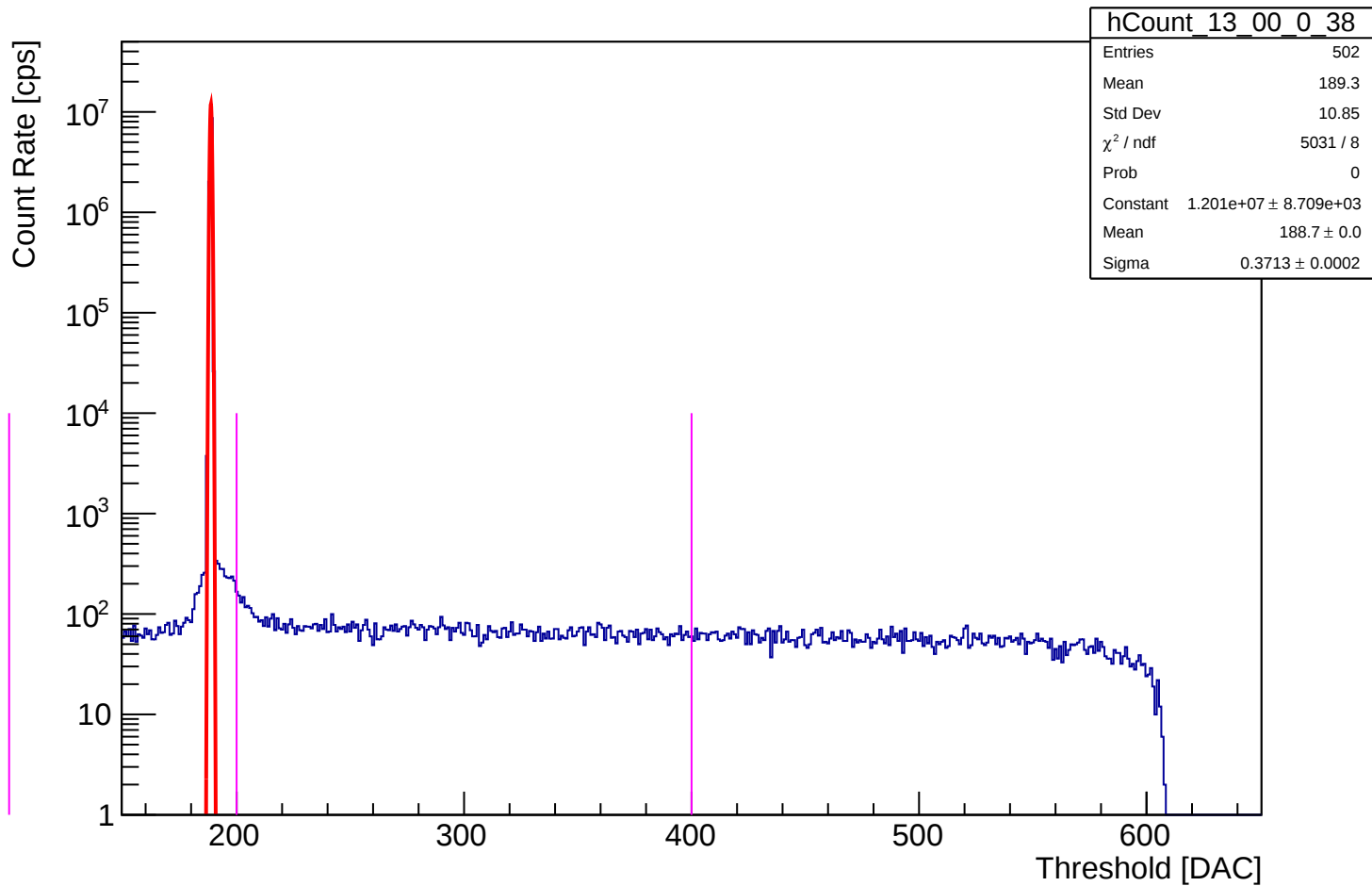
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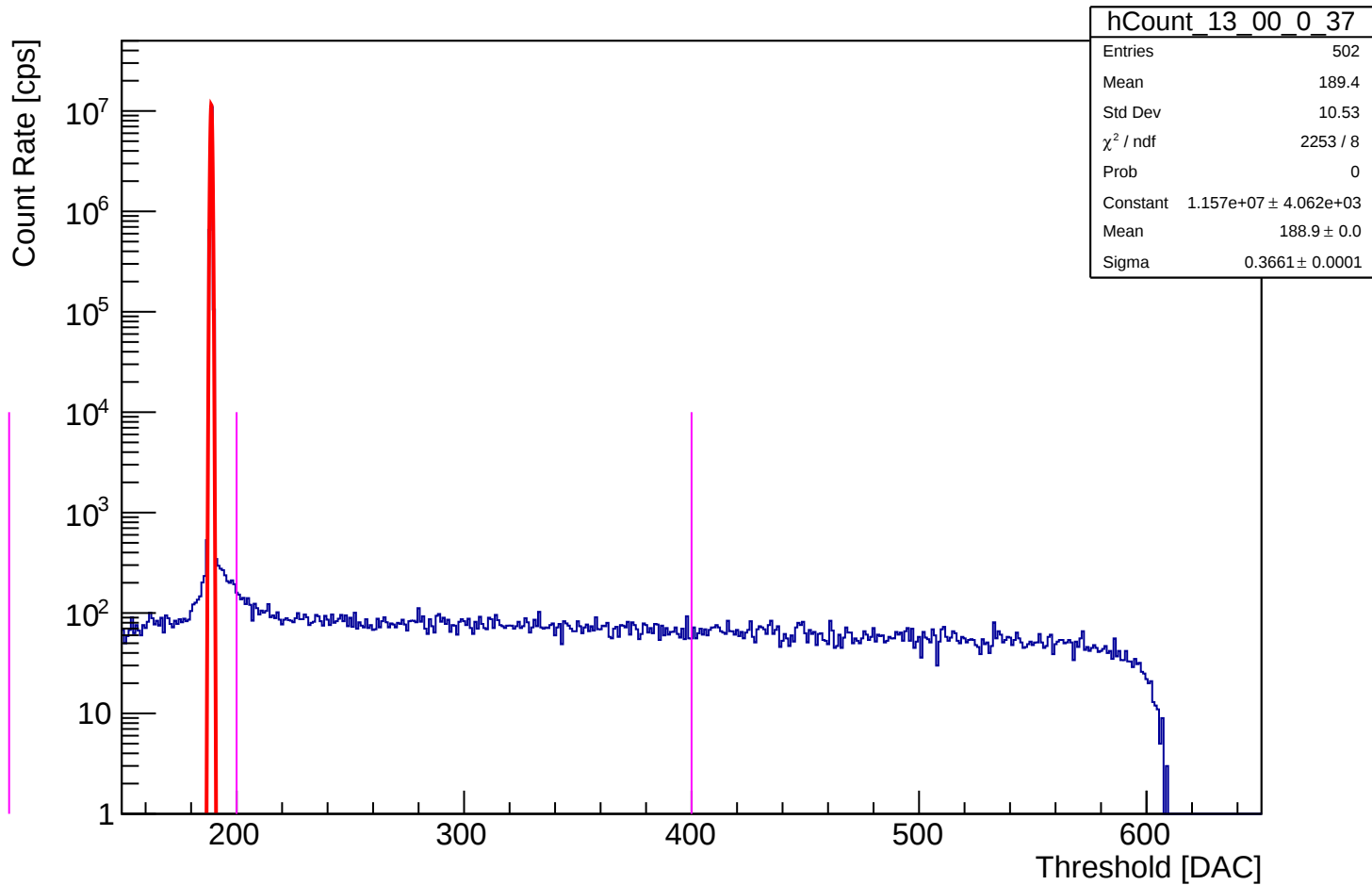
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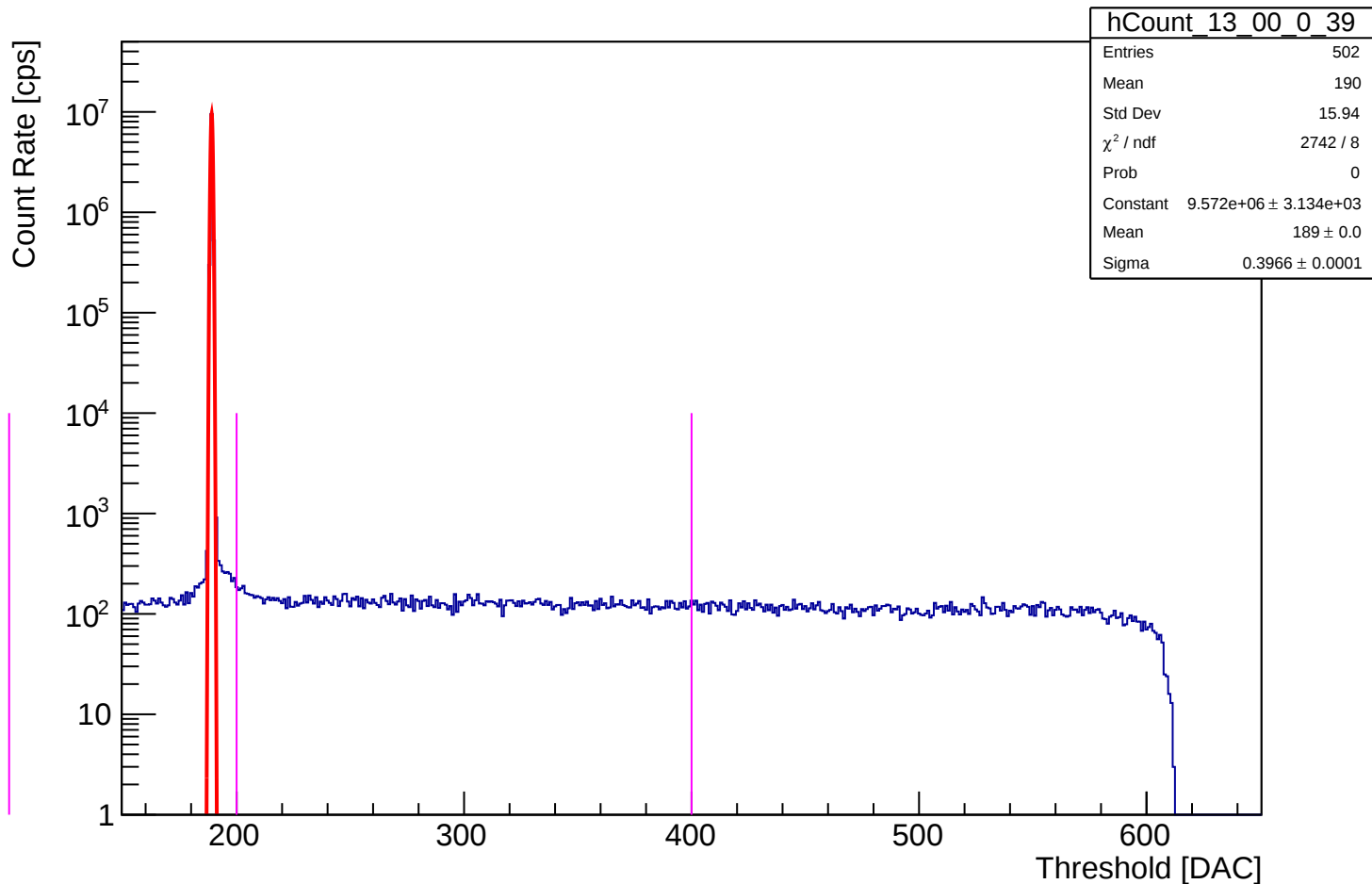
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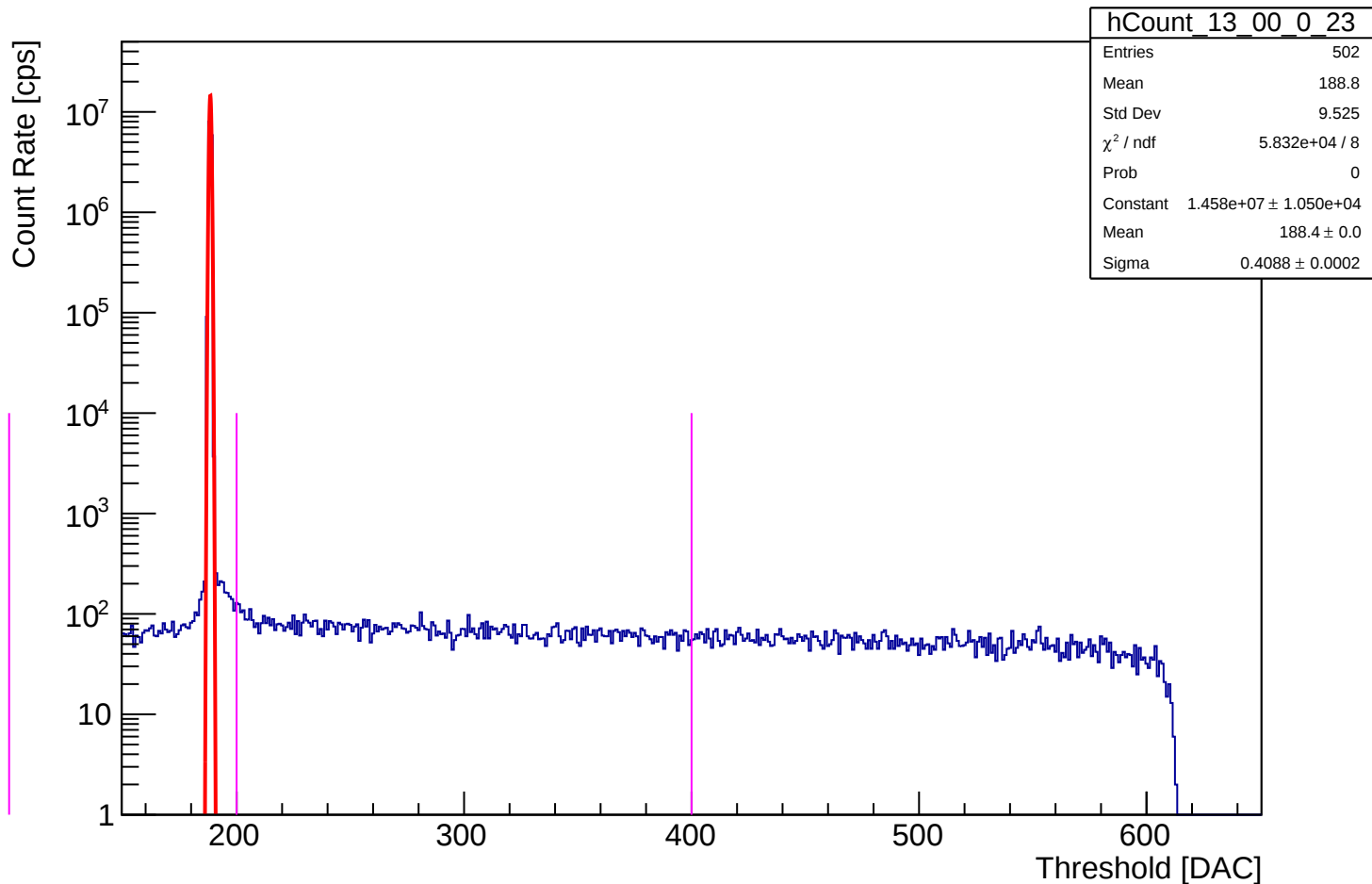
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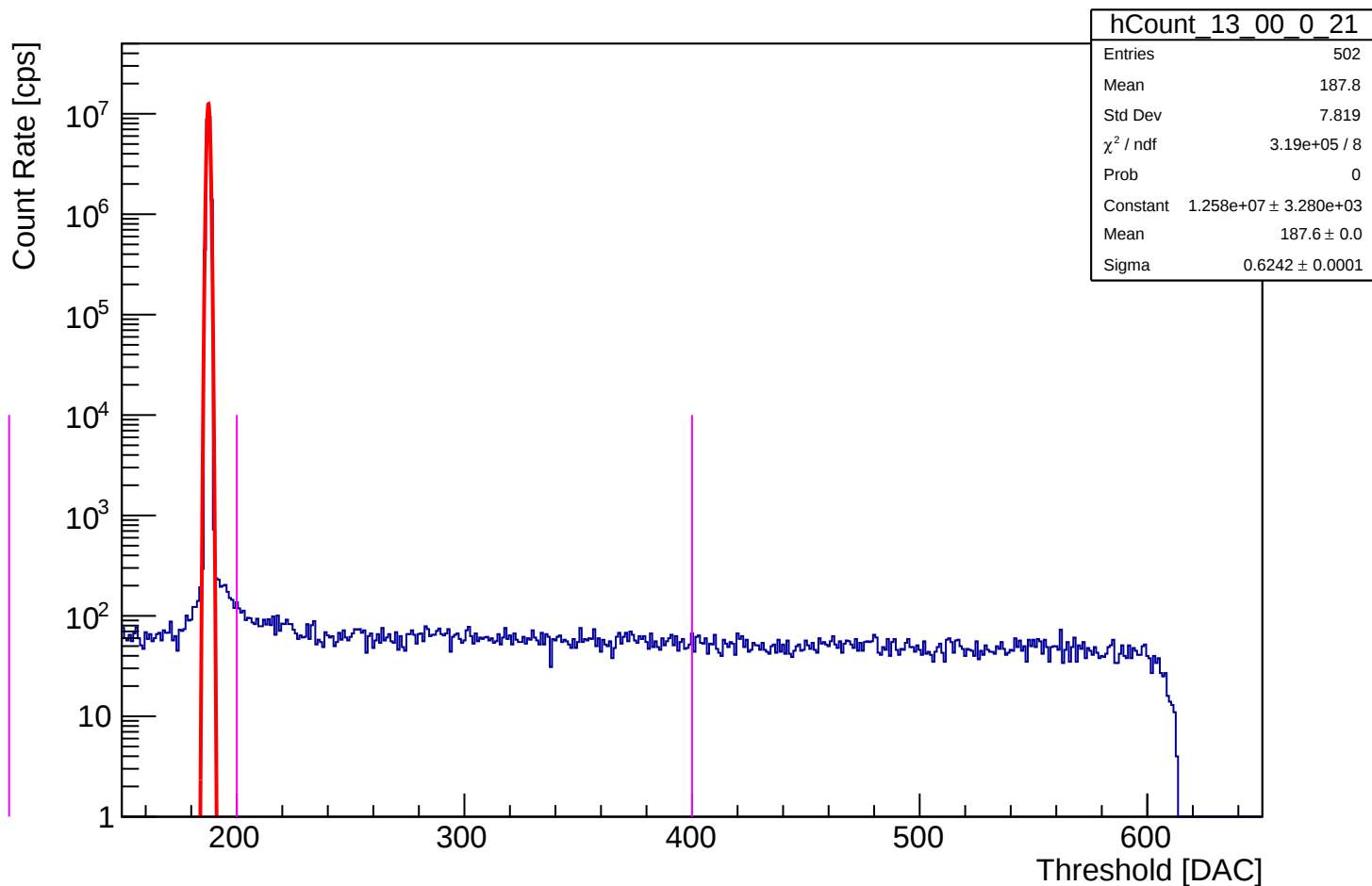
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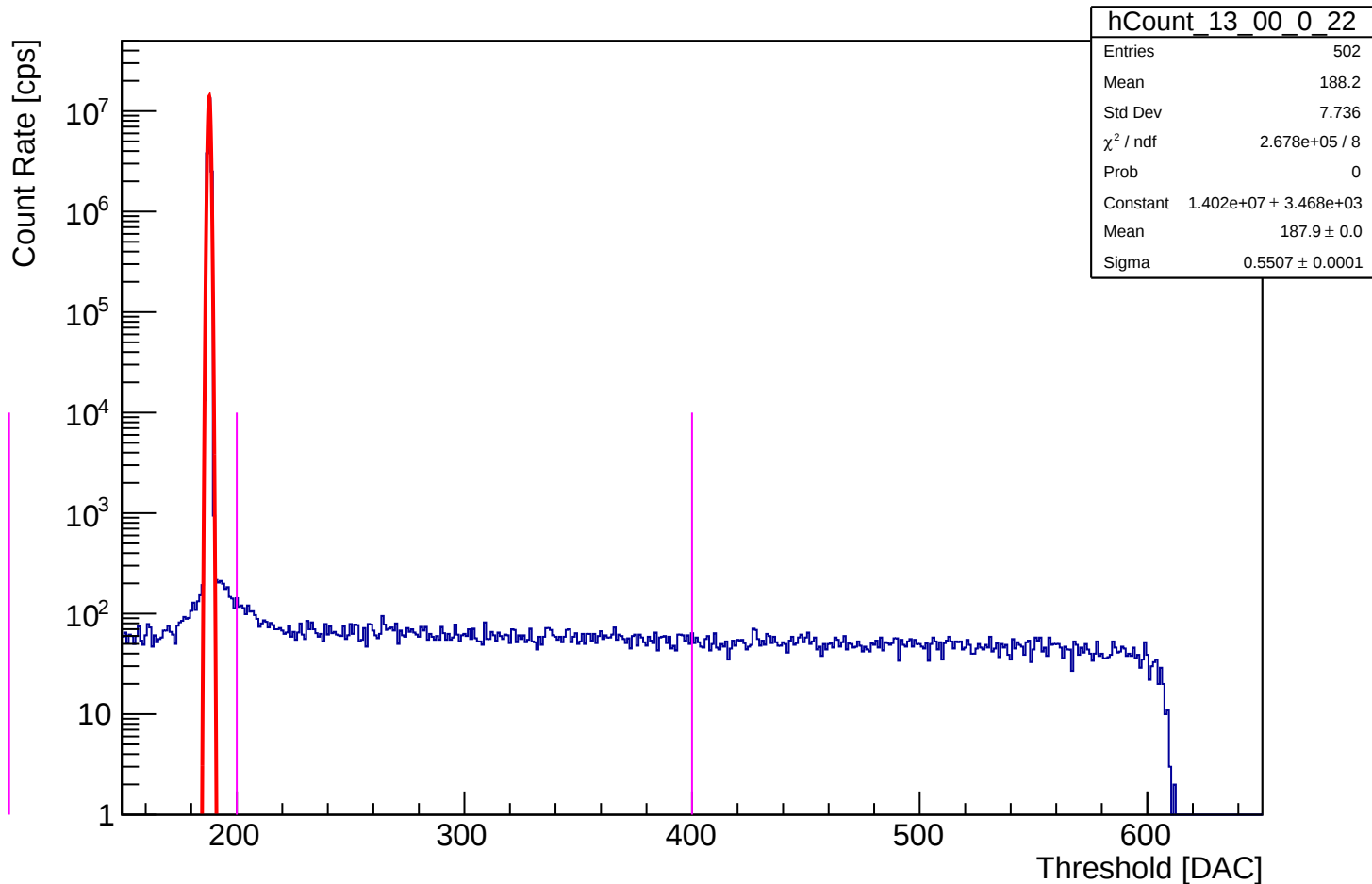
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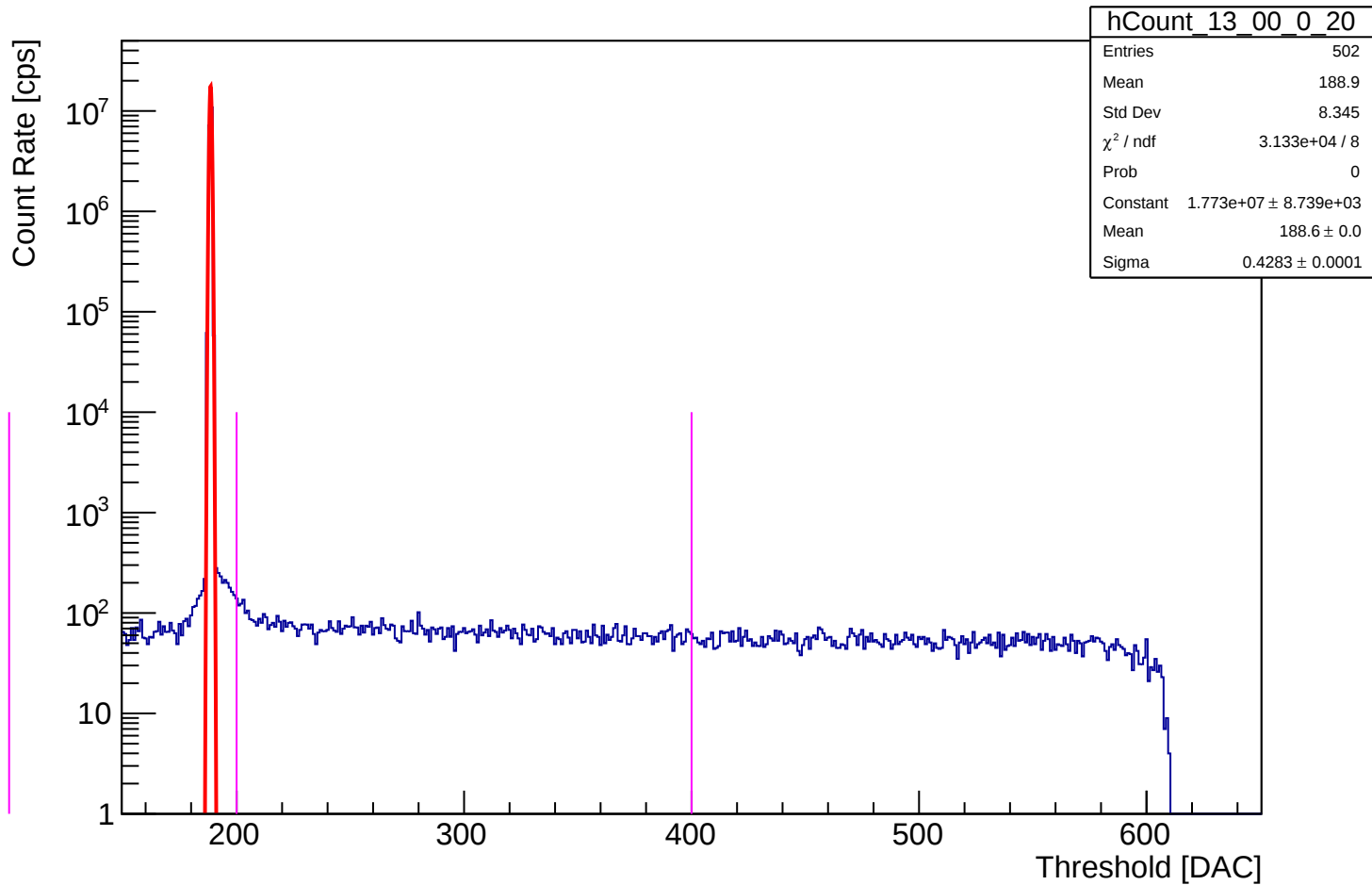
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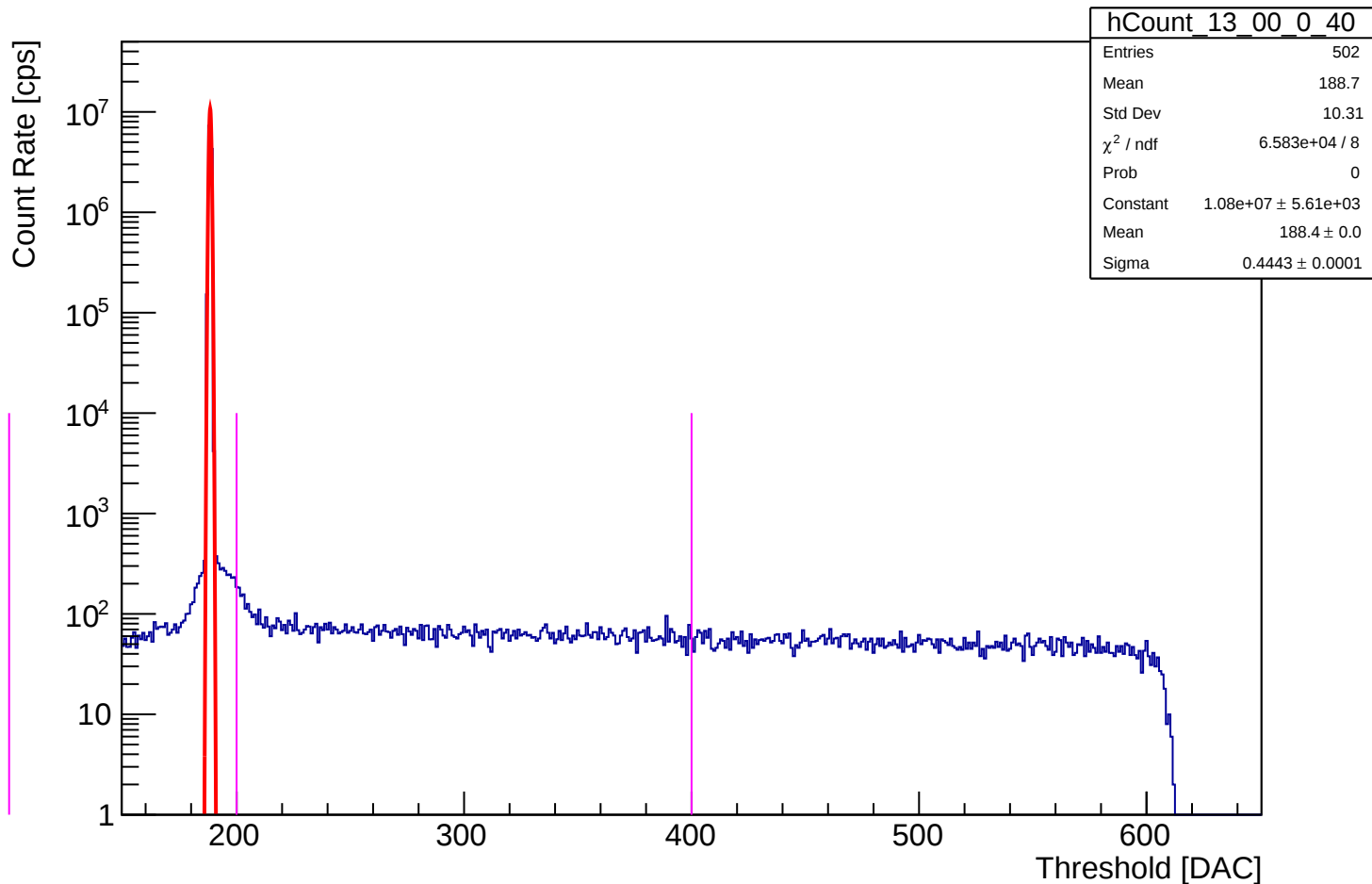
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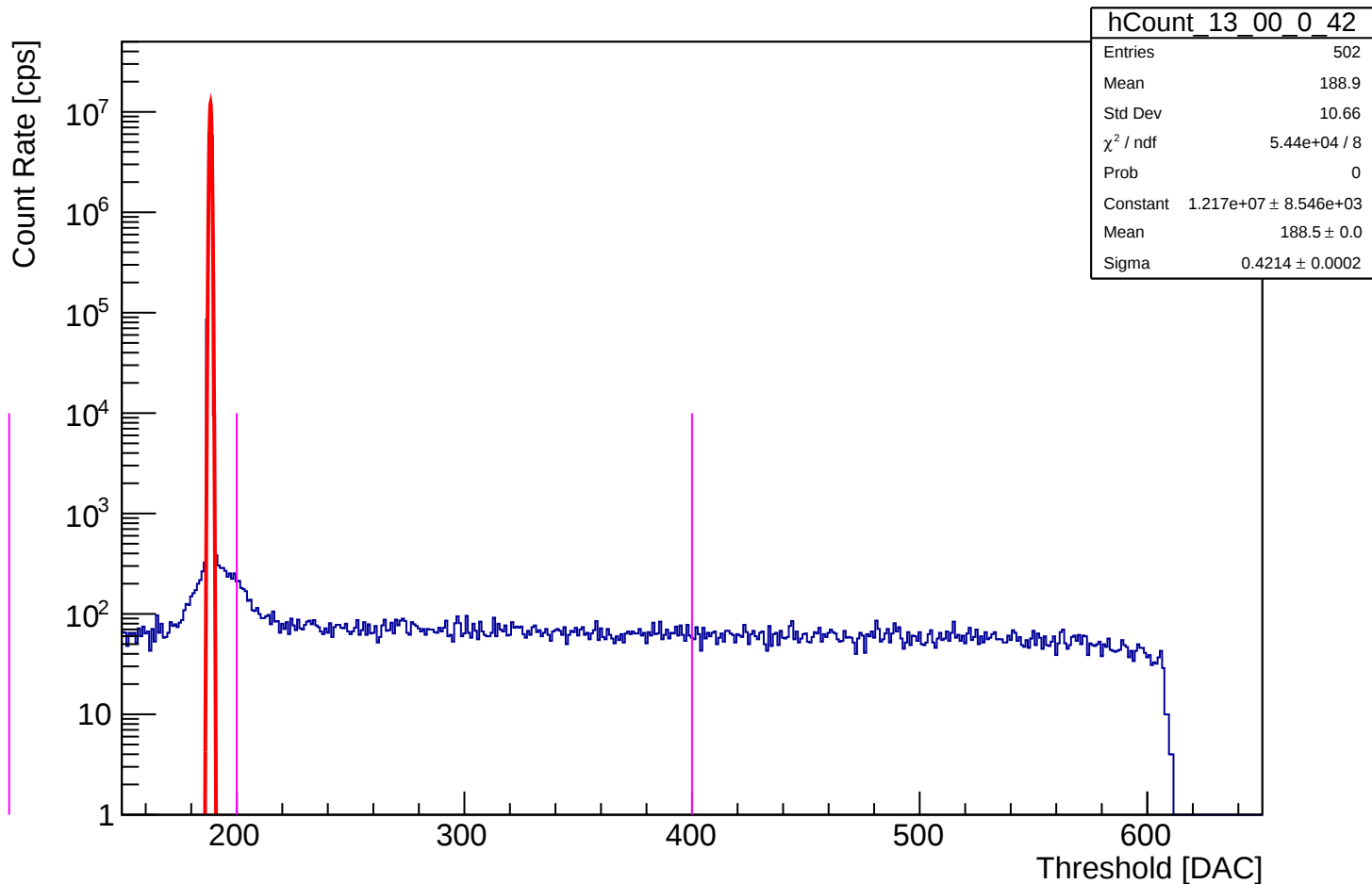
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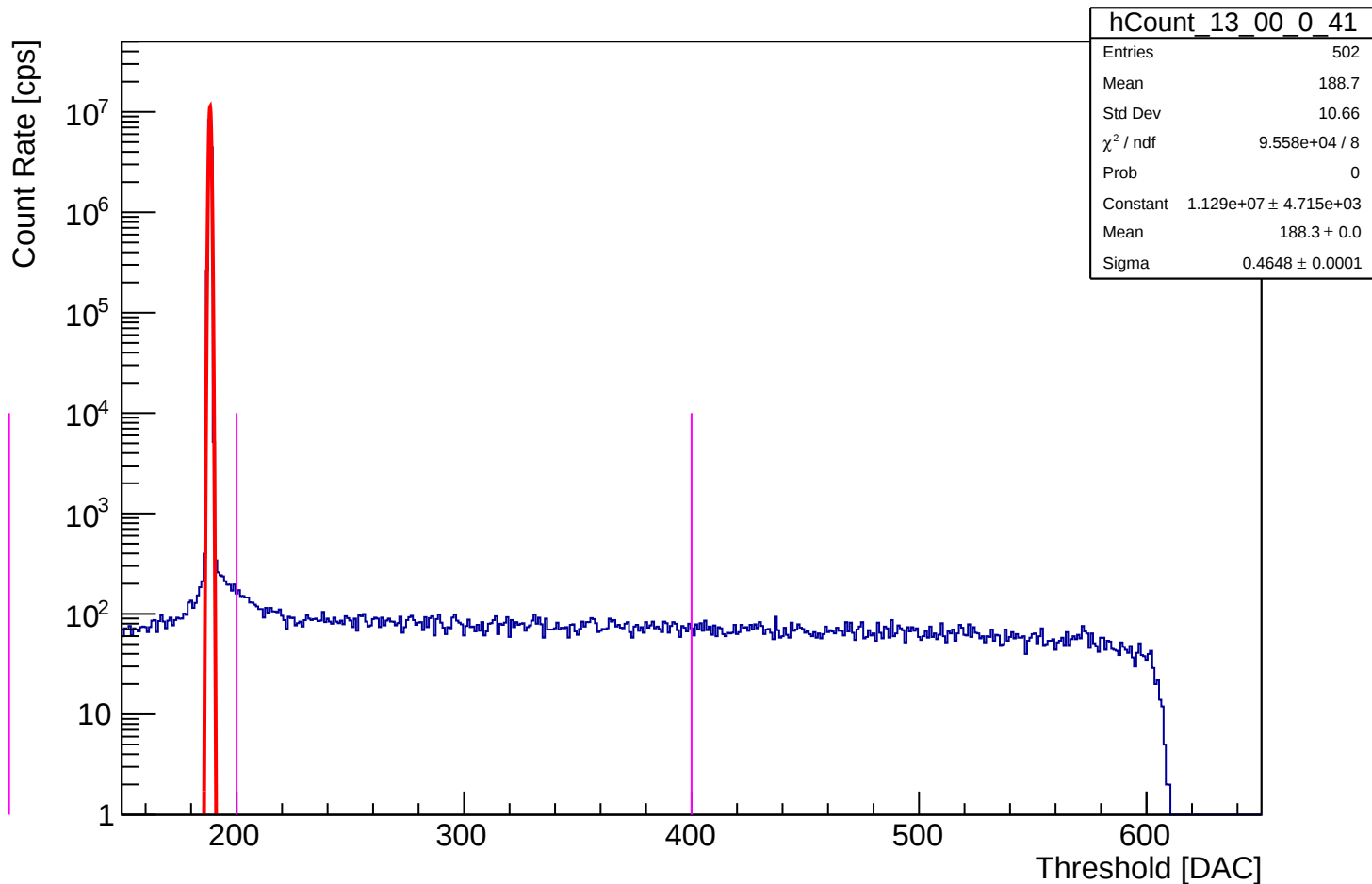
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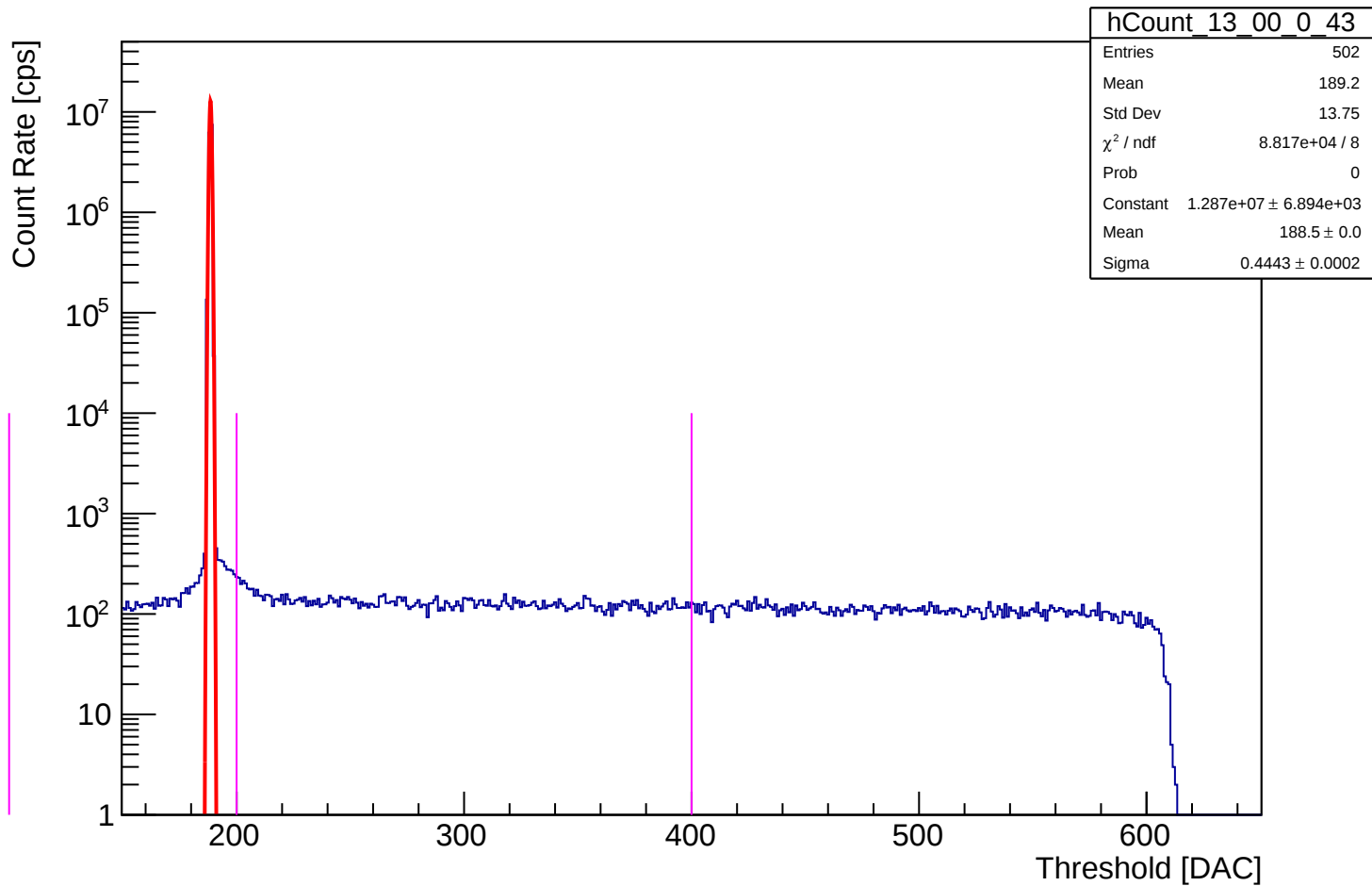
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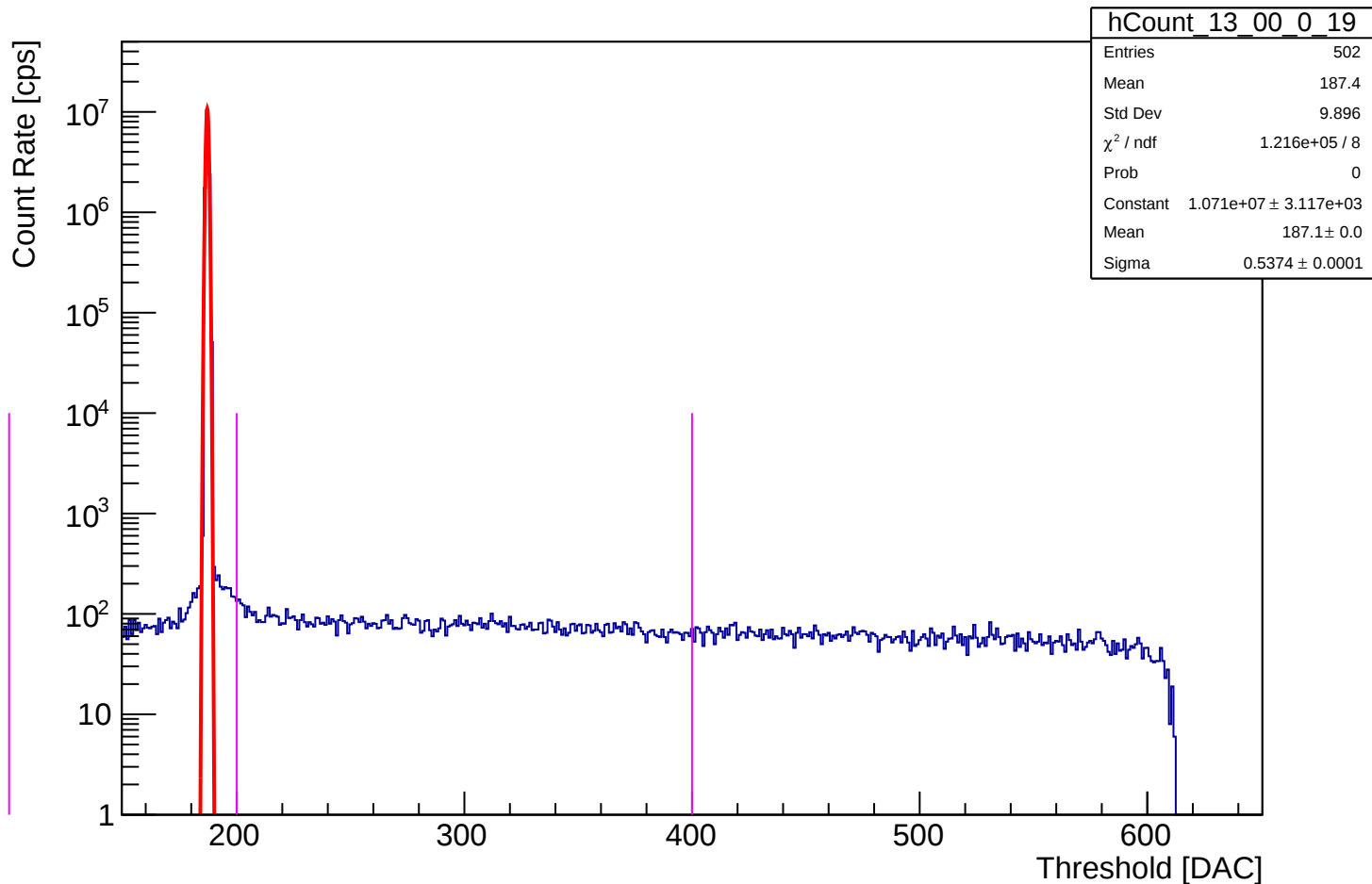
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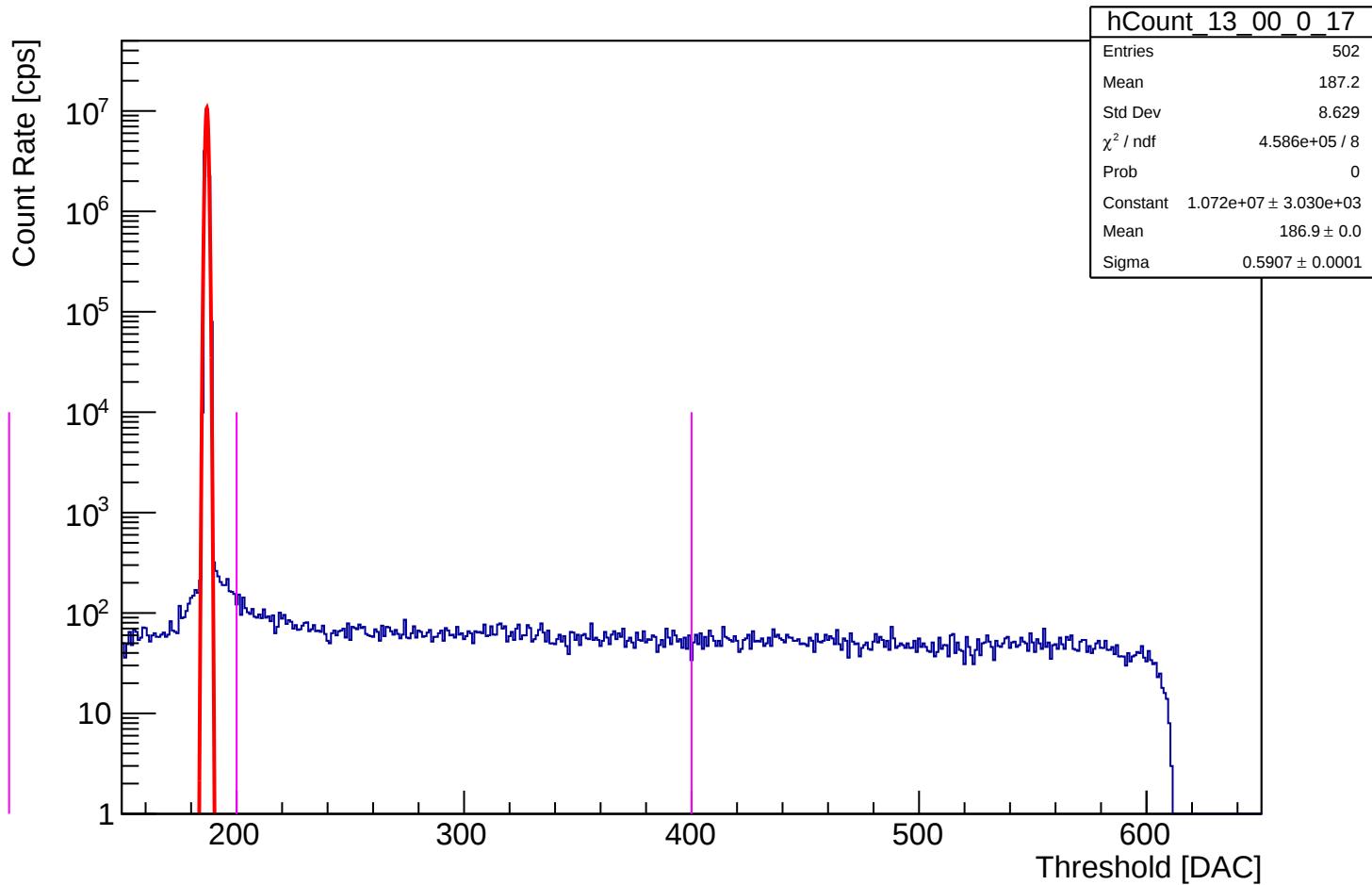
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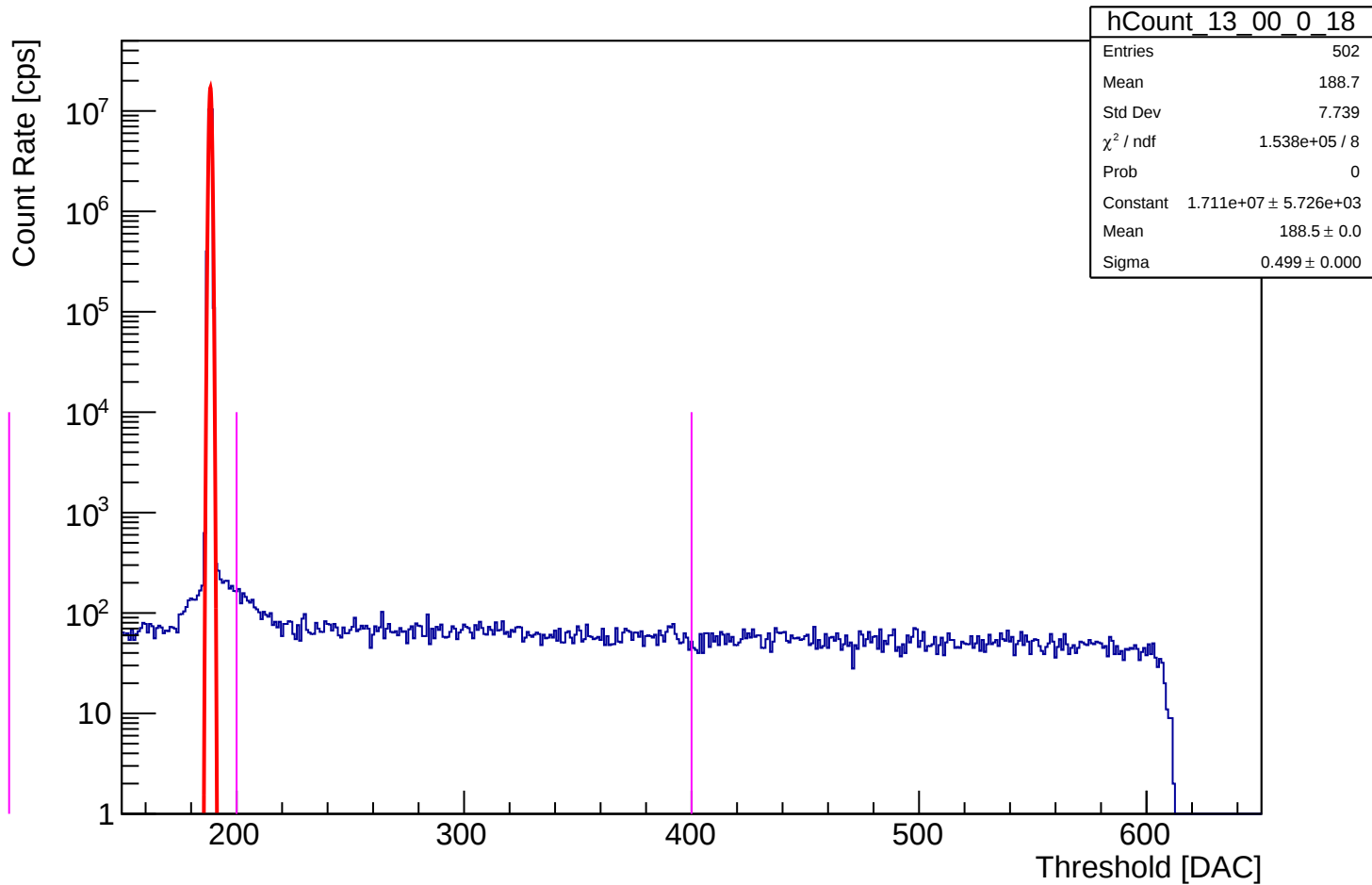
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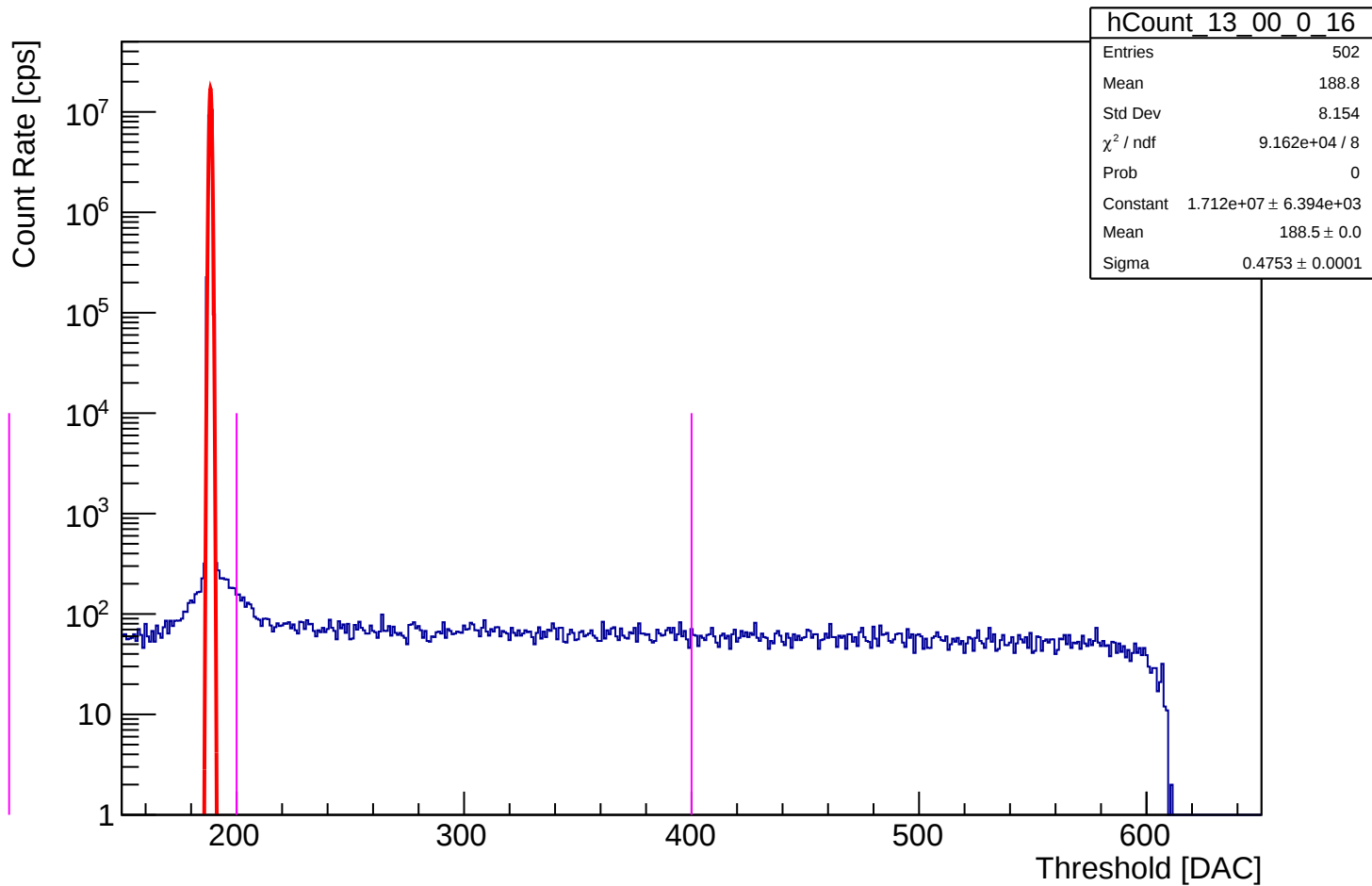
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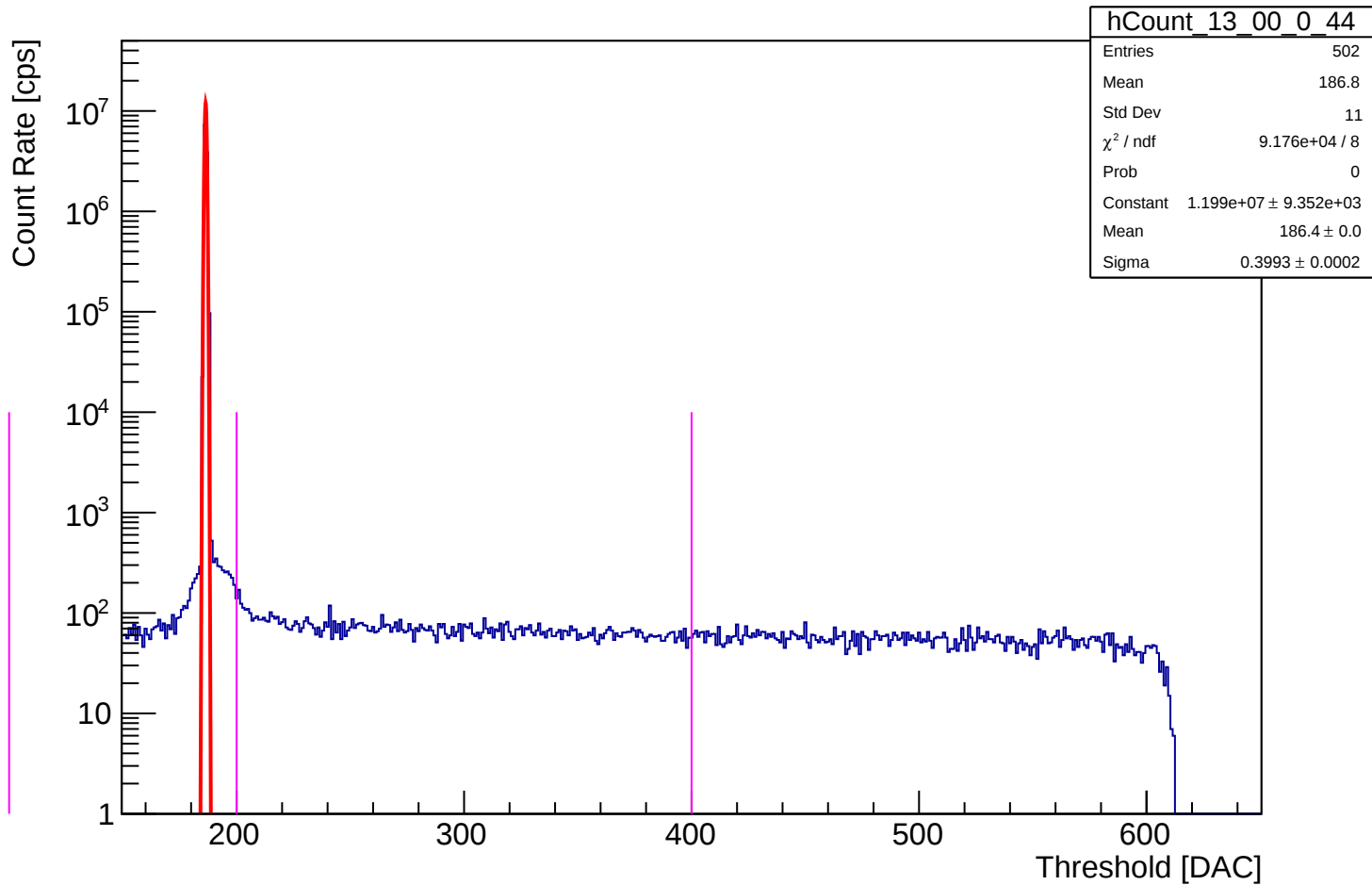
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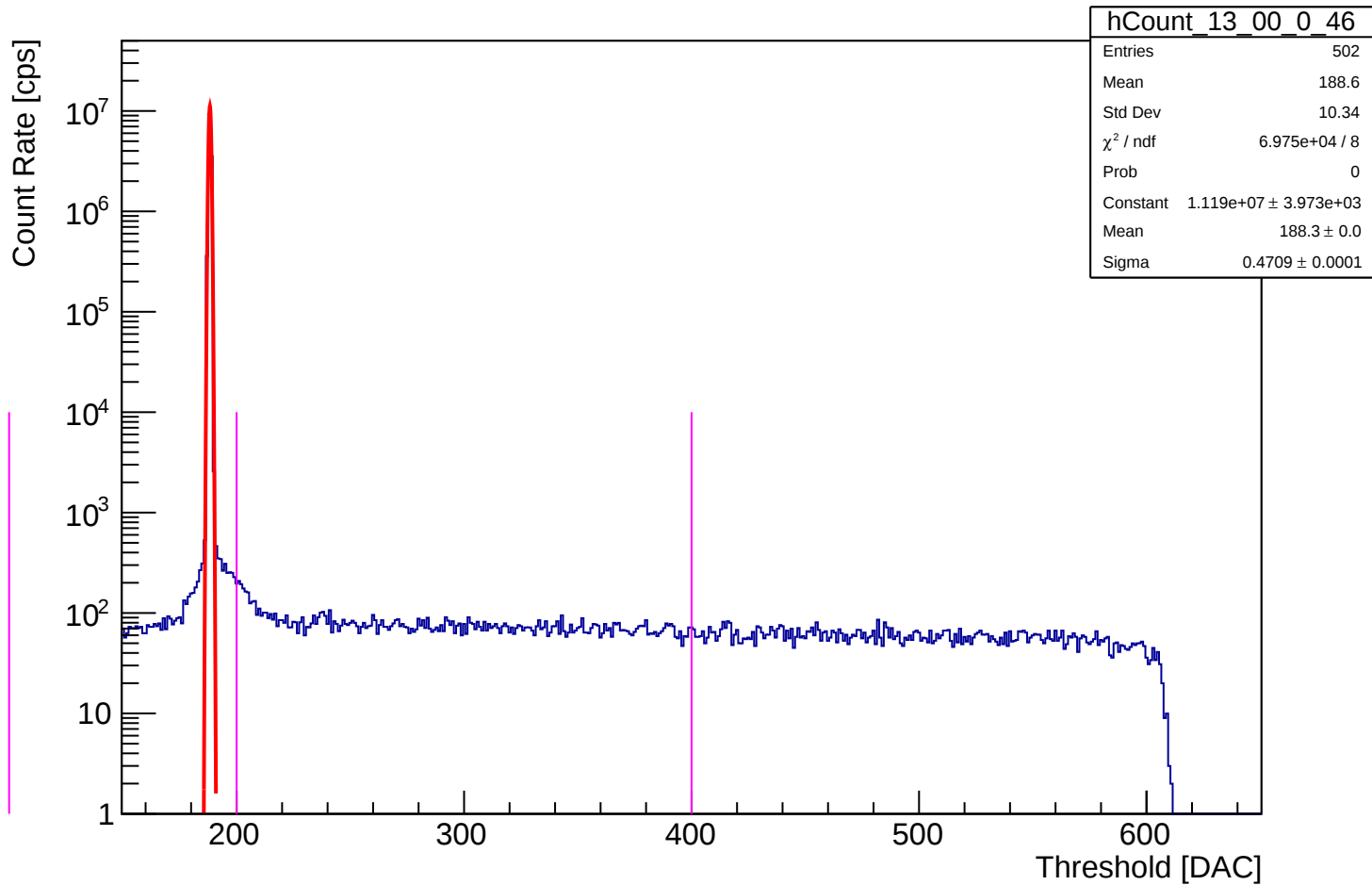
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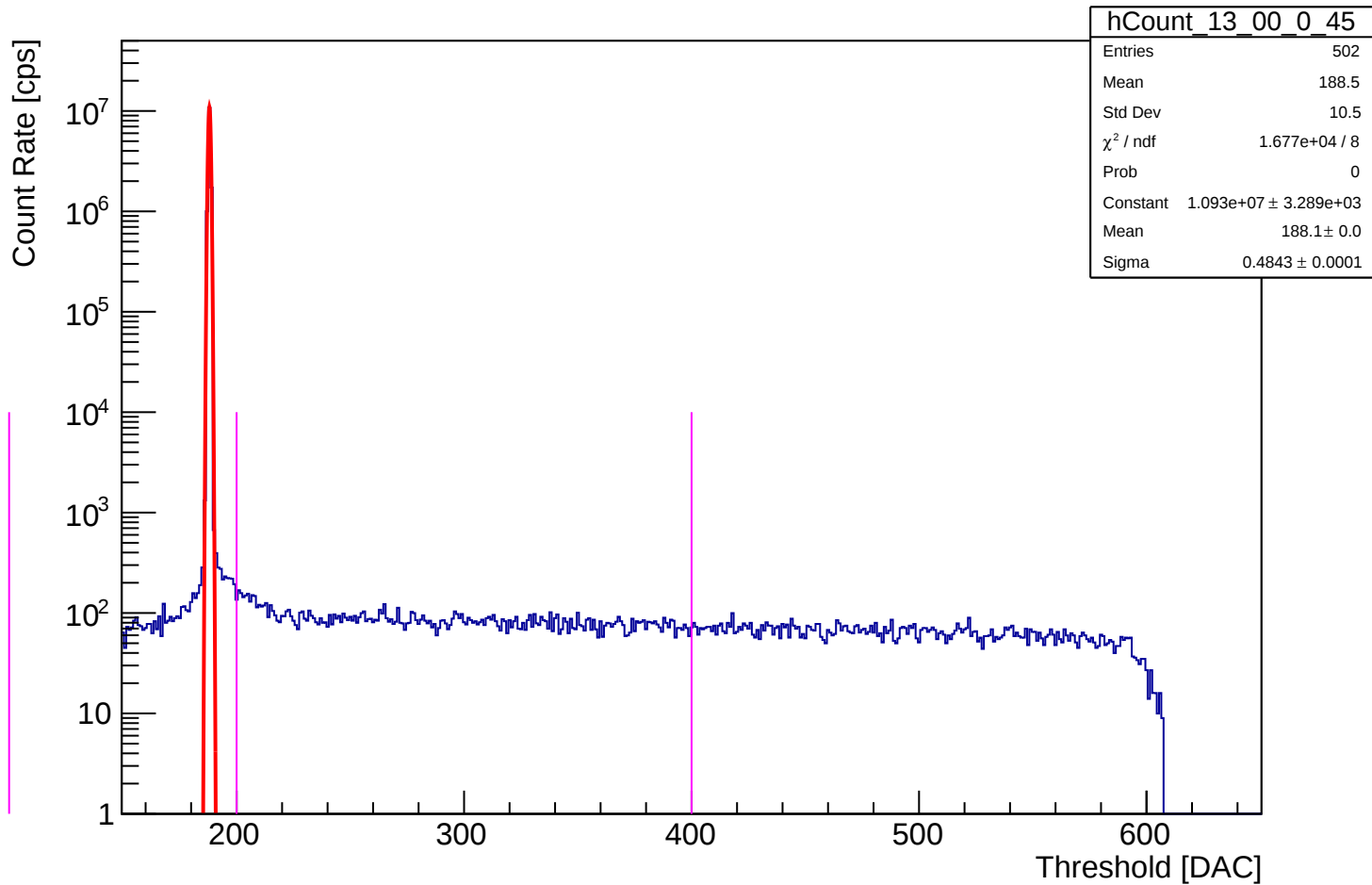
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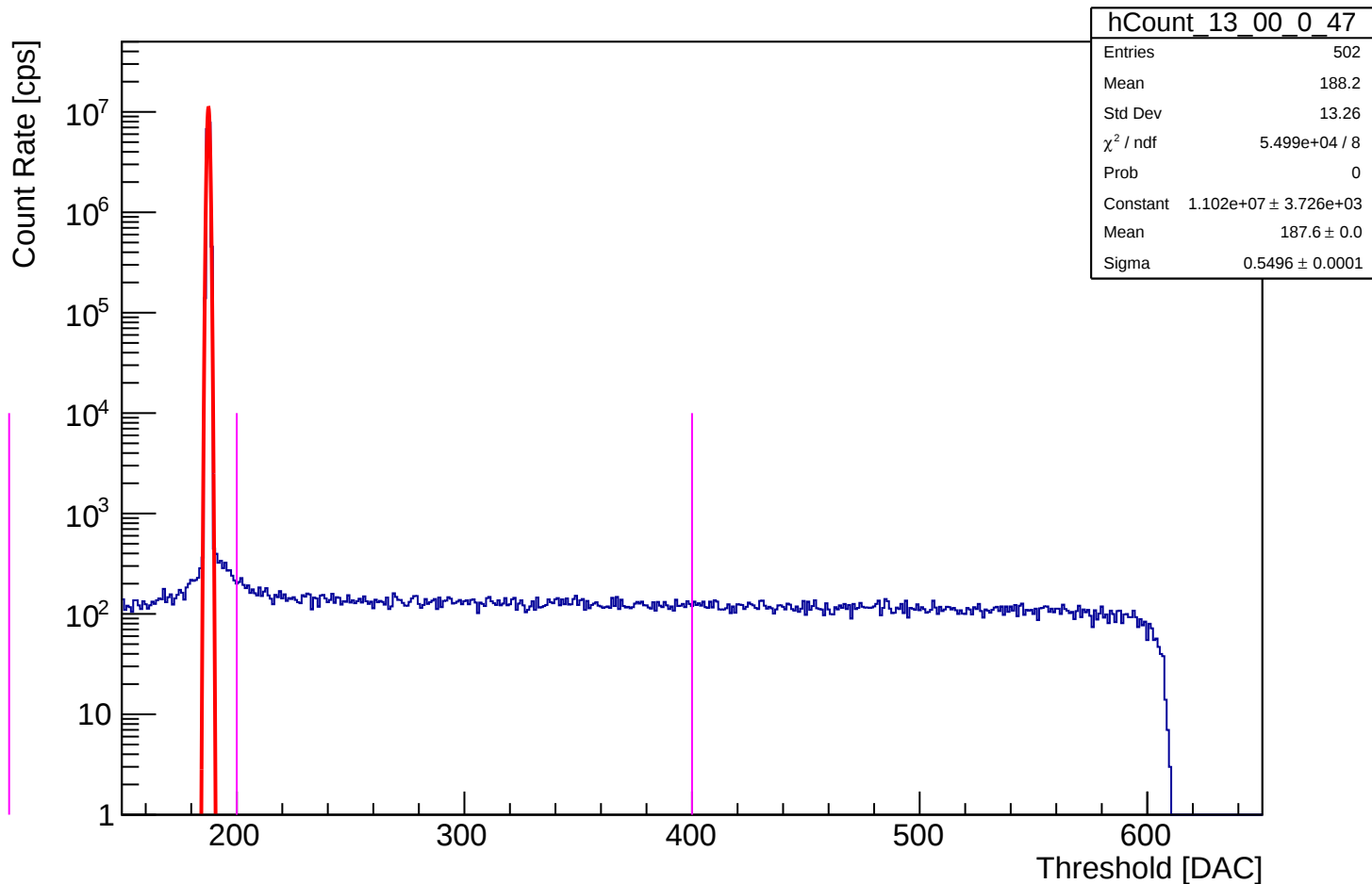
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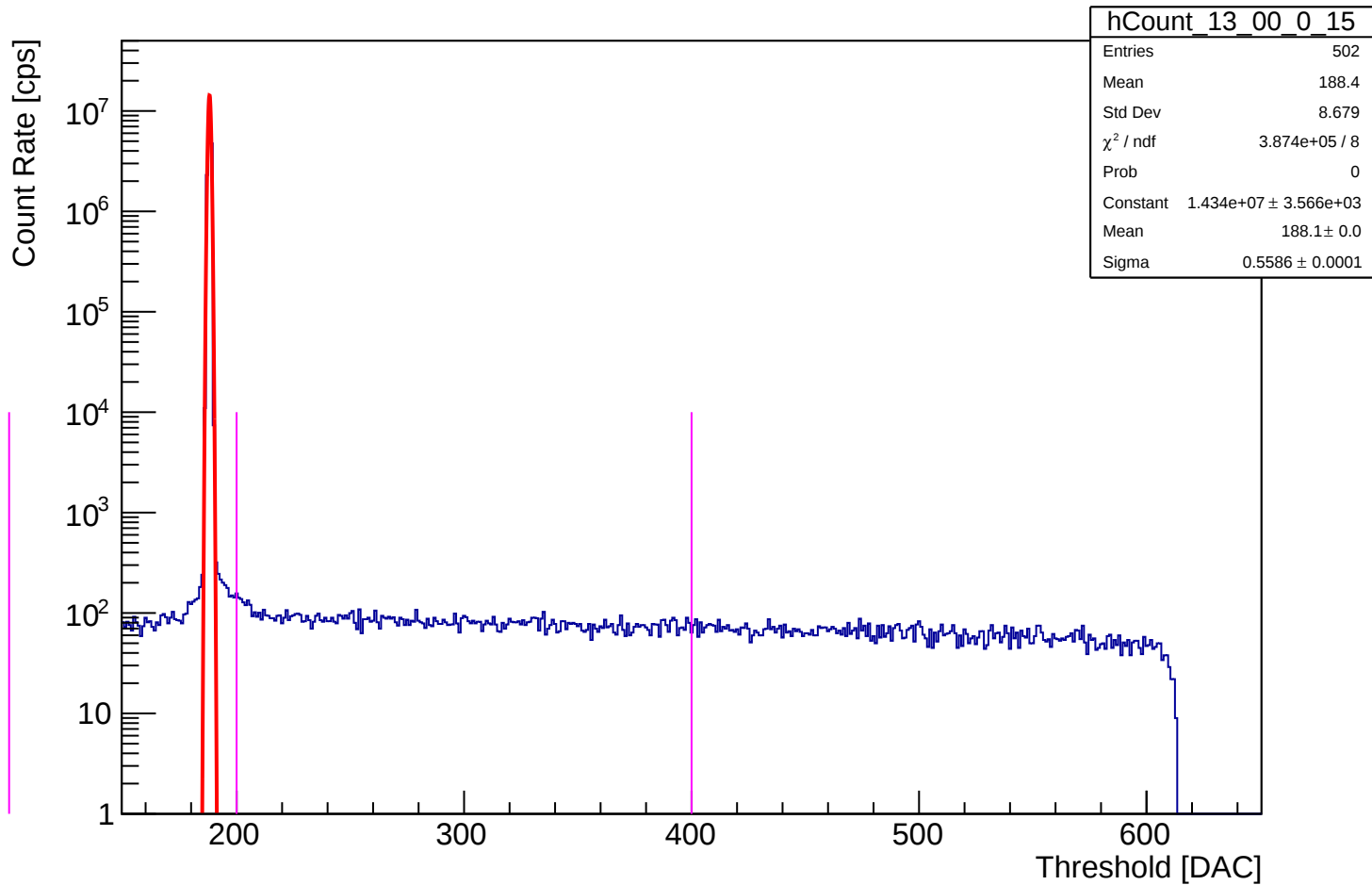
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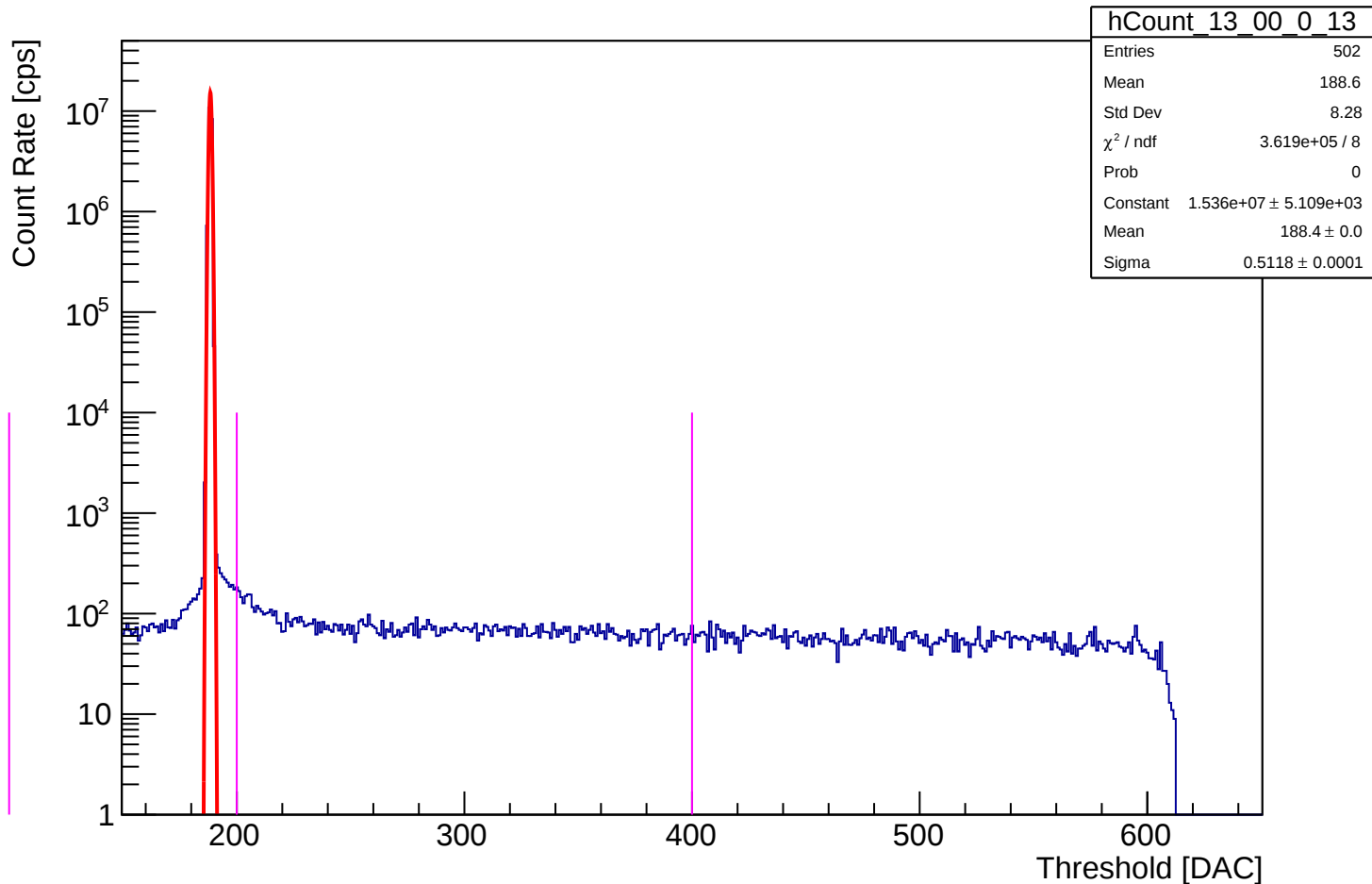
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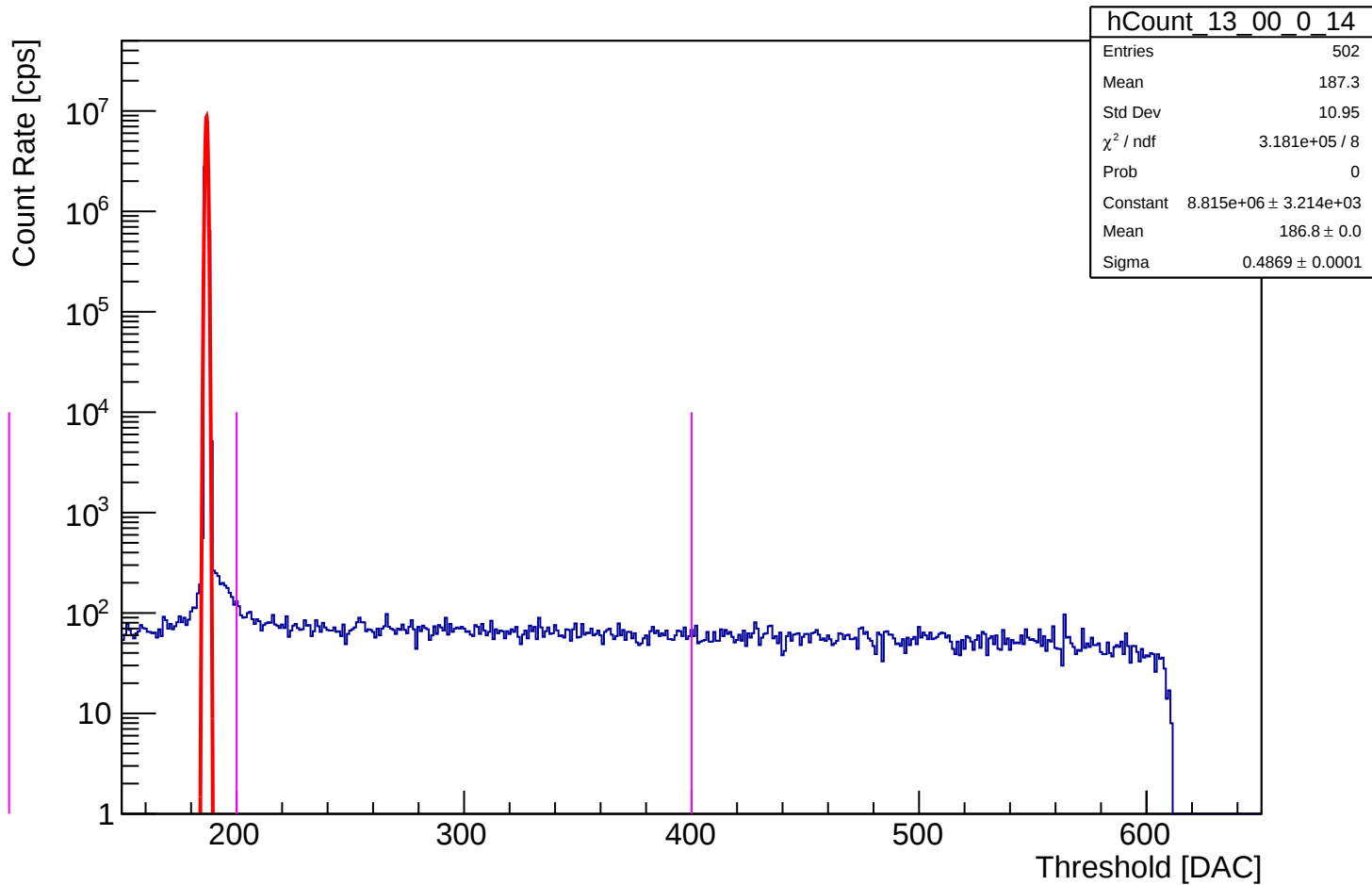
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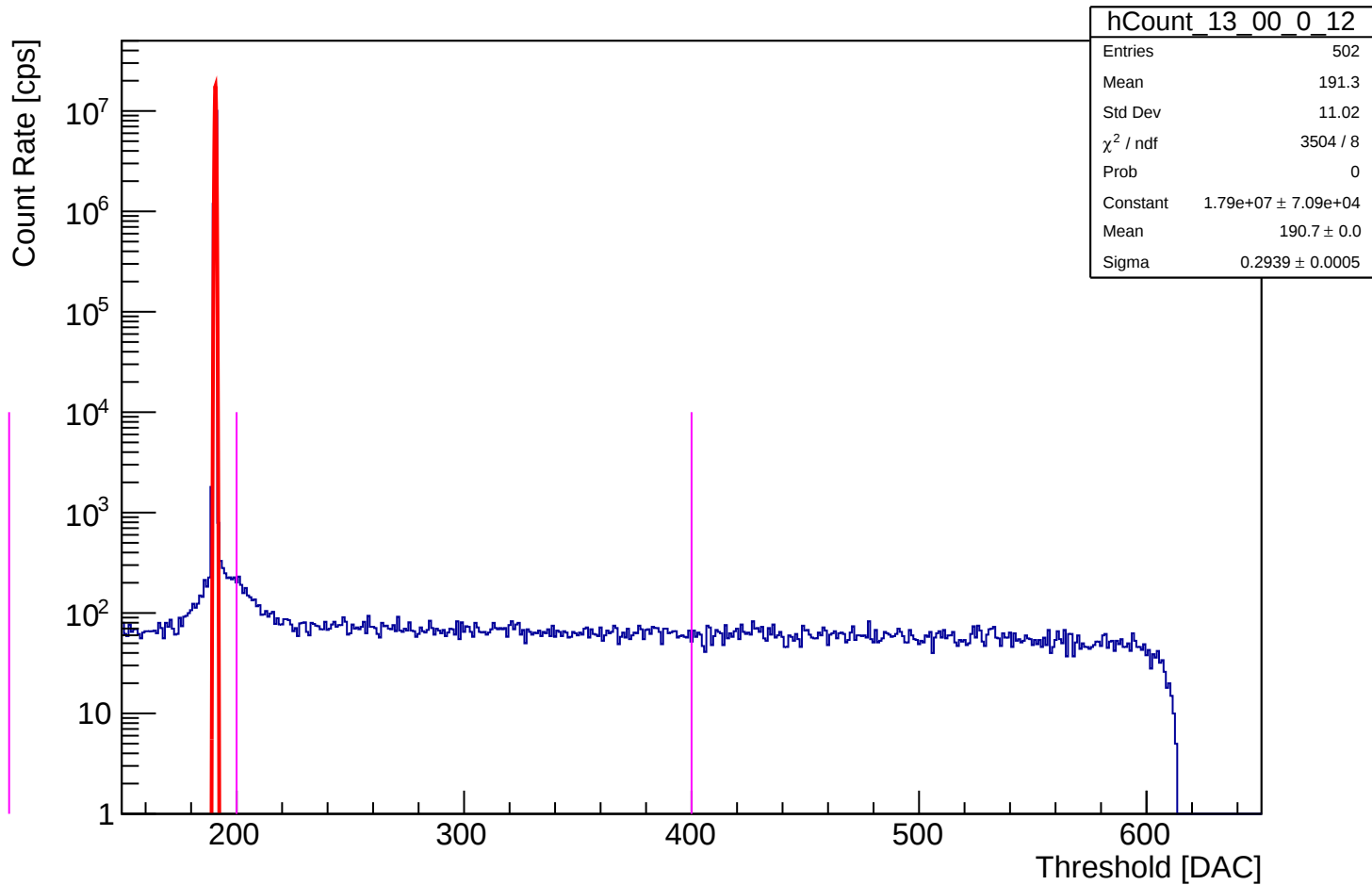
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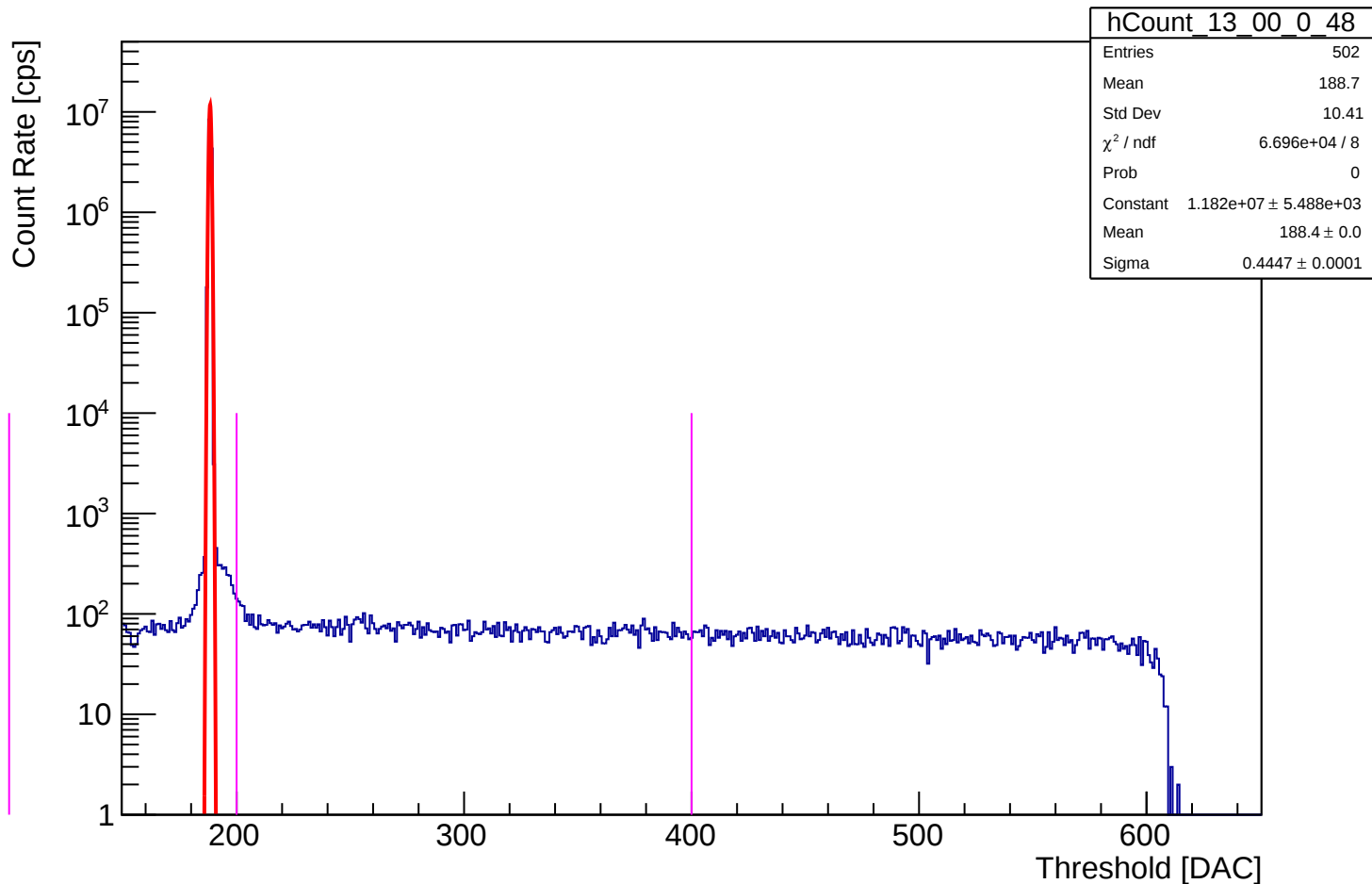
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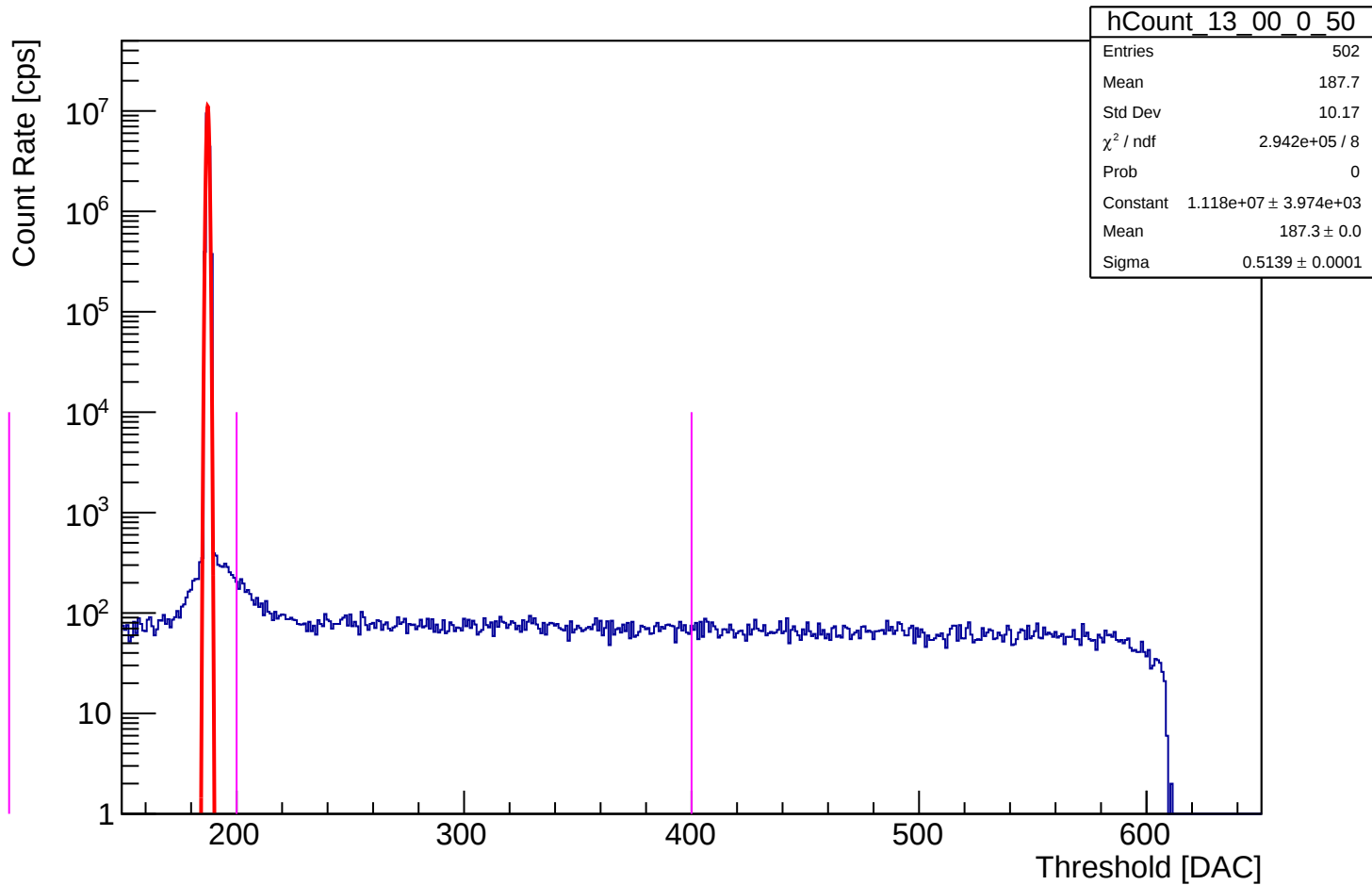
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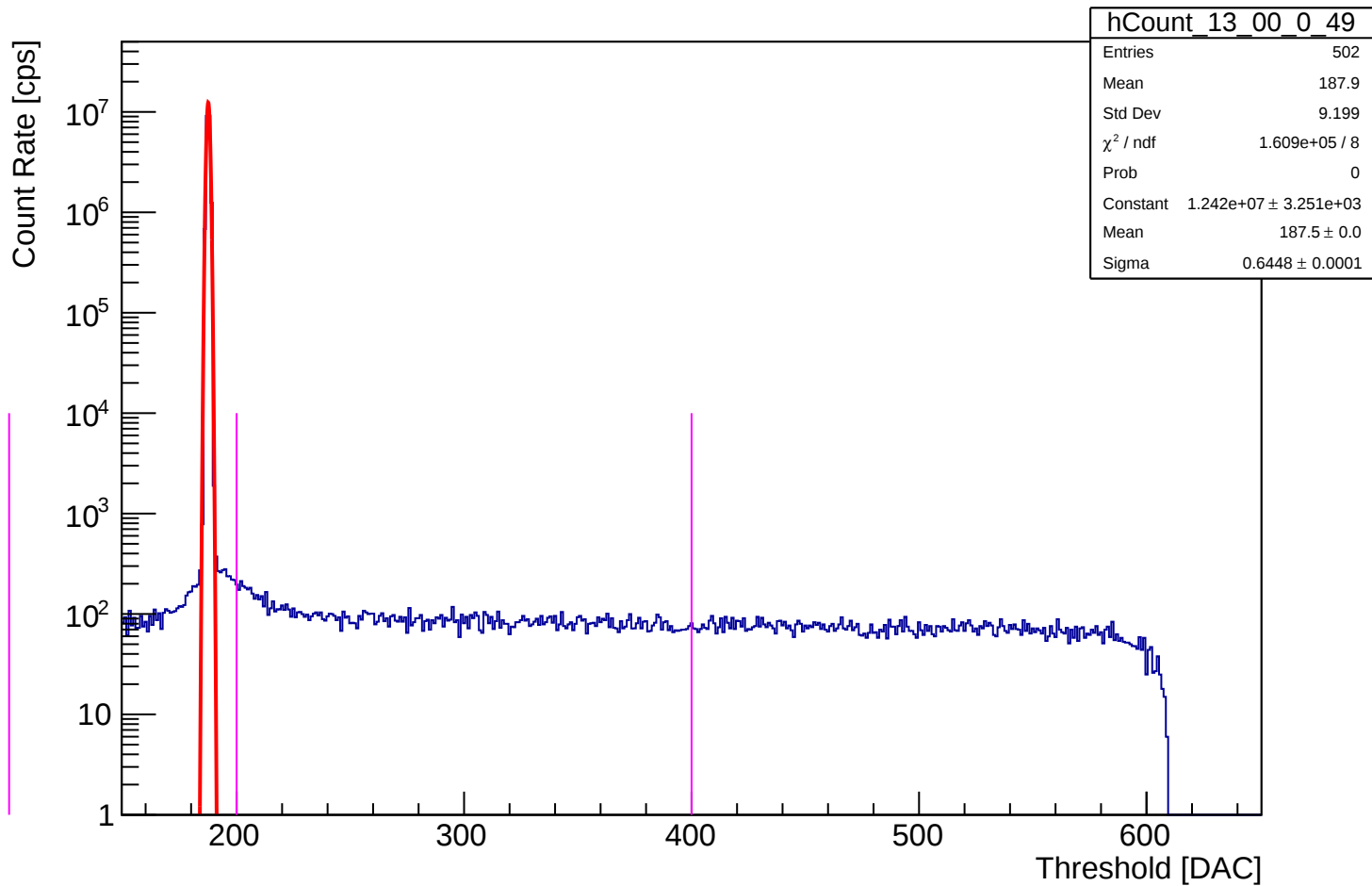
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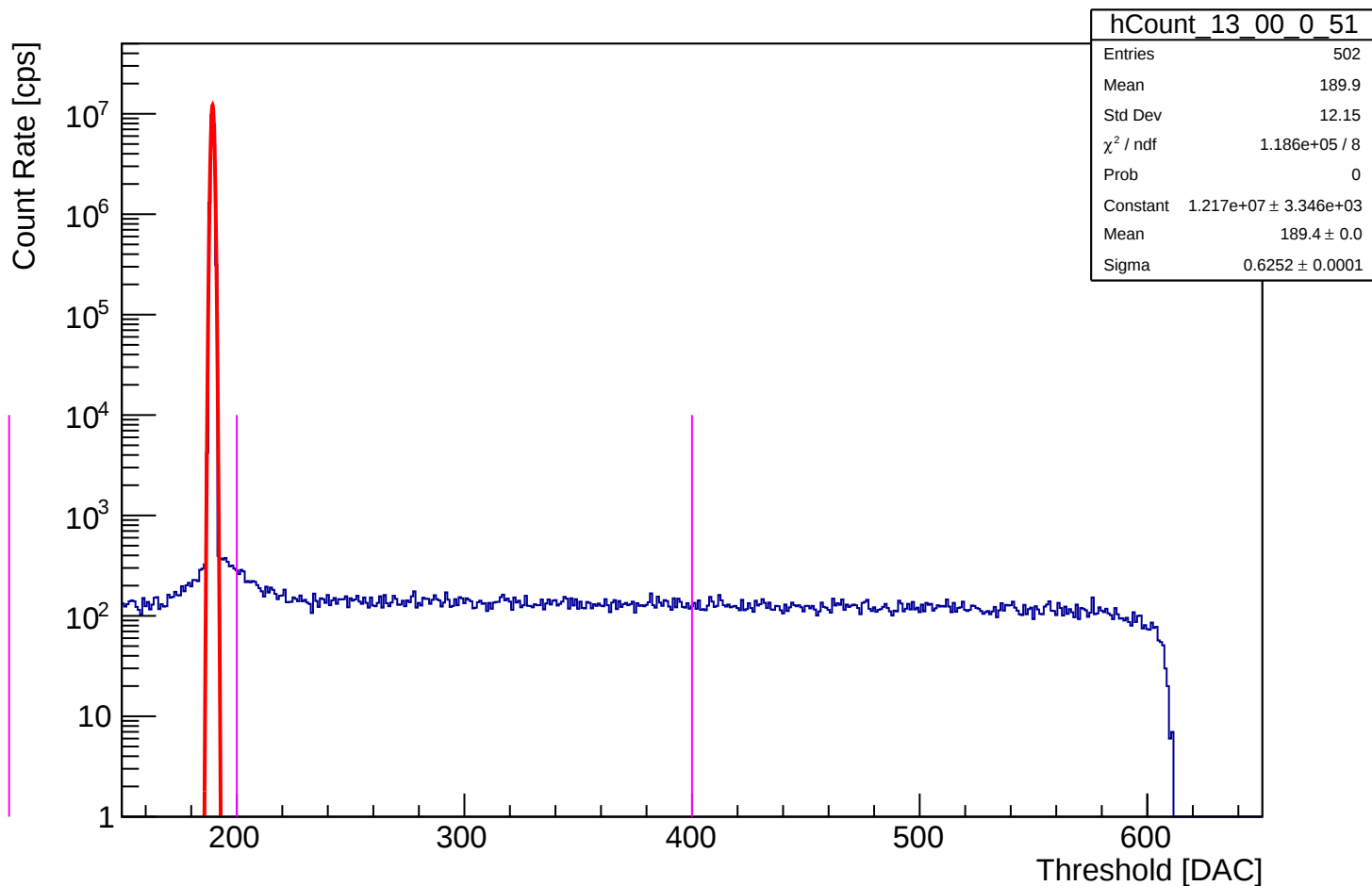
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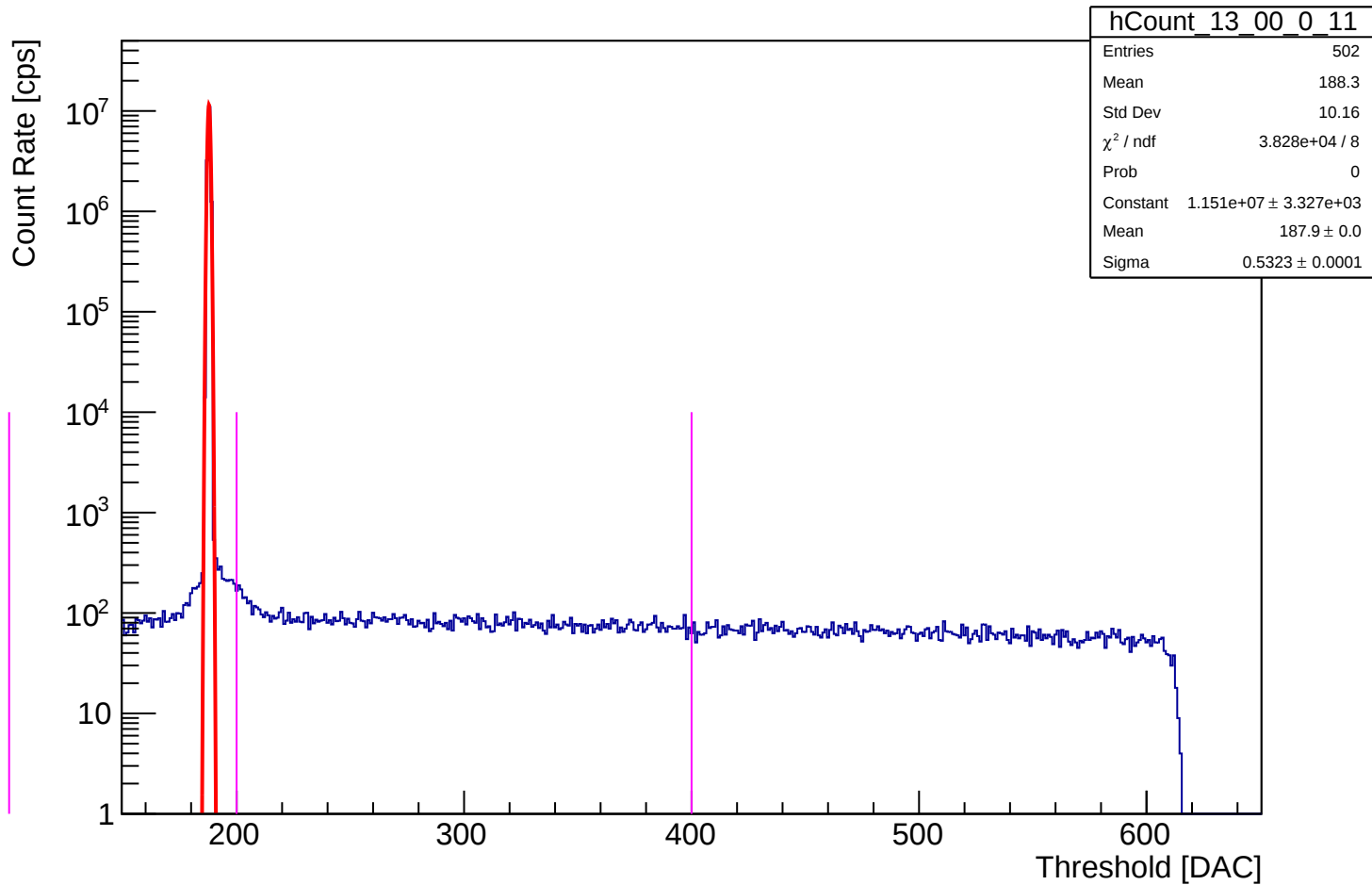
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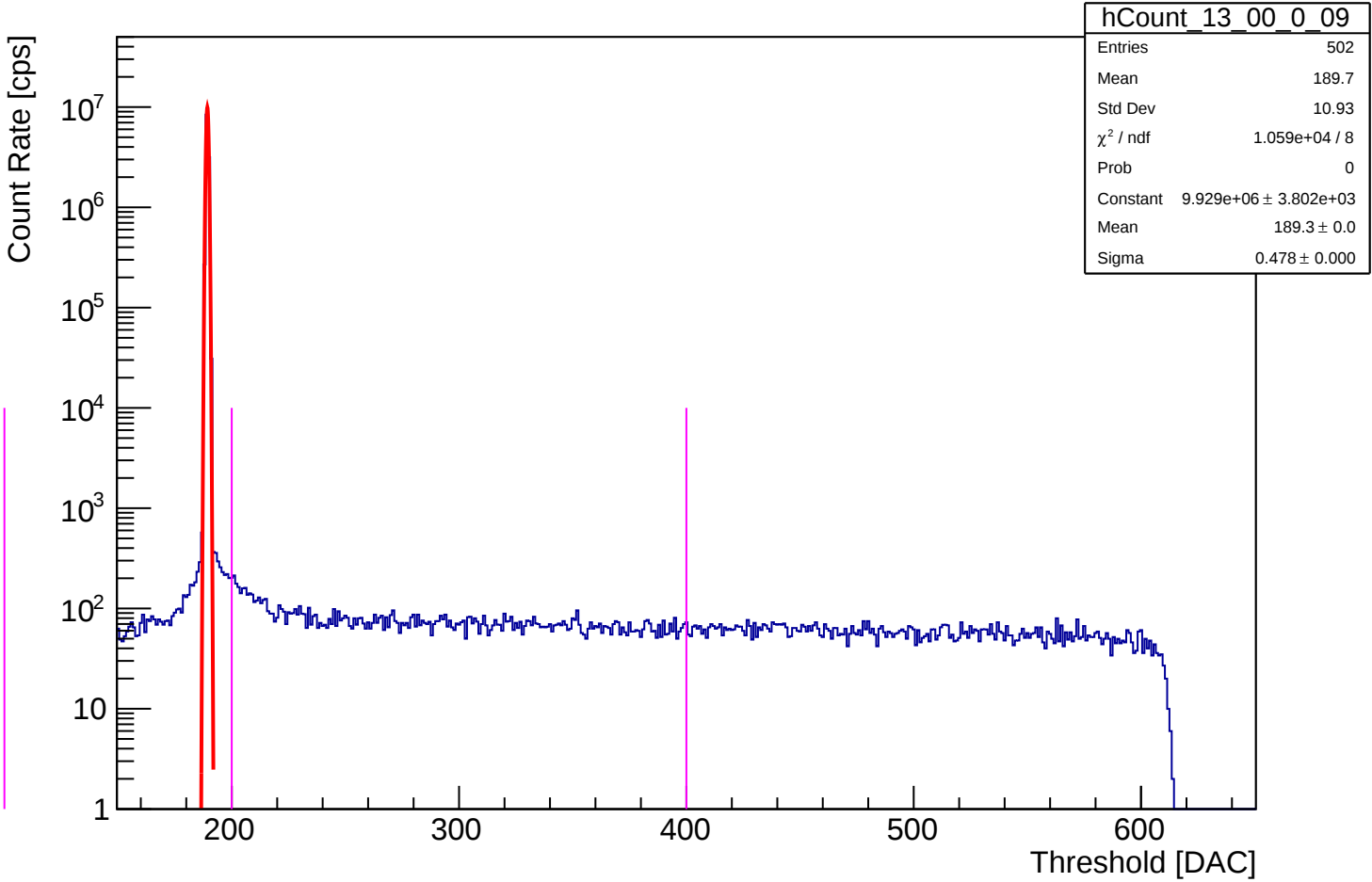


Row 1 Tile 1 Pmt 1 Pixel 40 Slot 13 Fiber 0 Asic 0 Channel 51

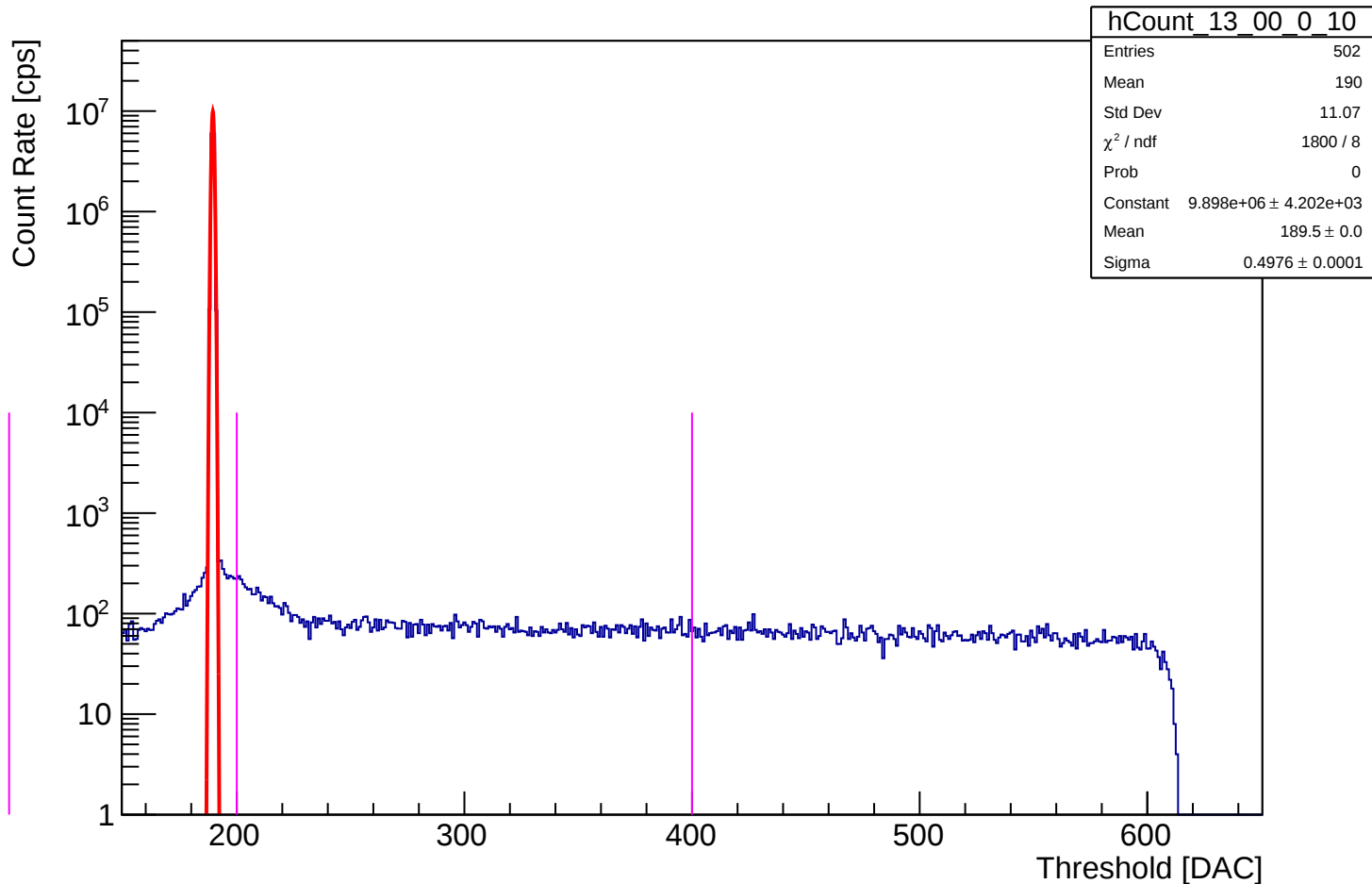


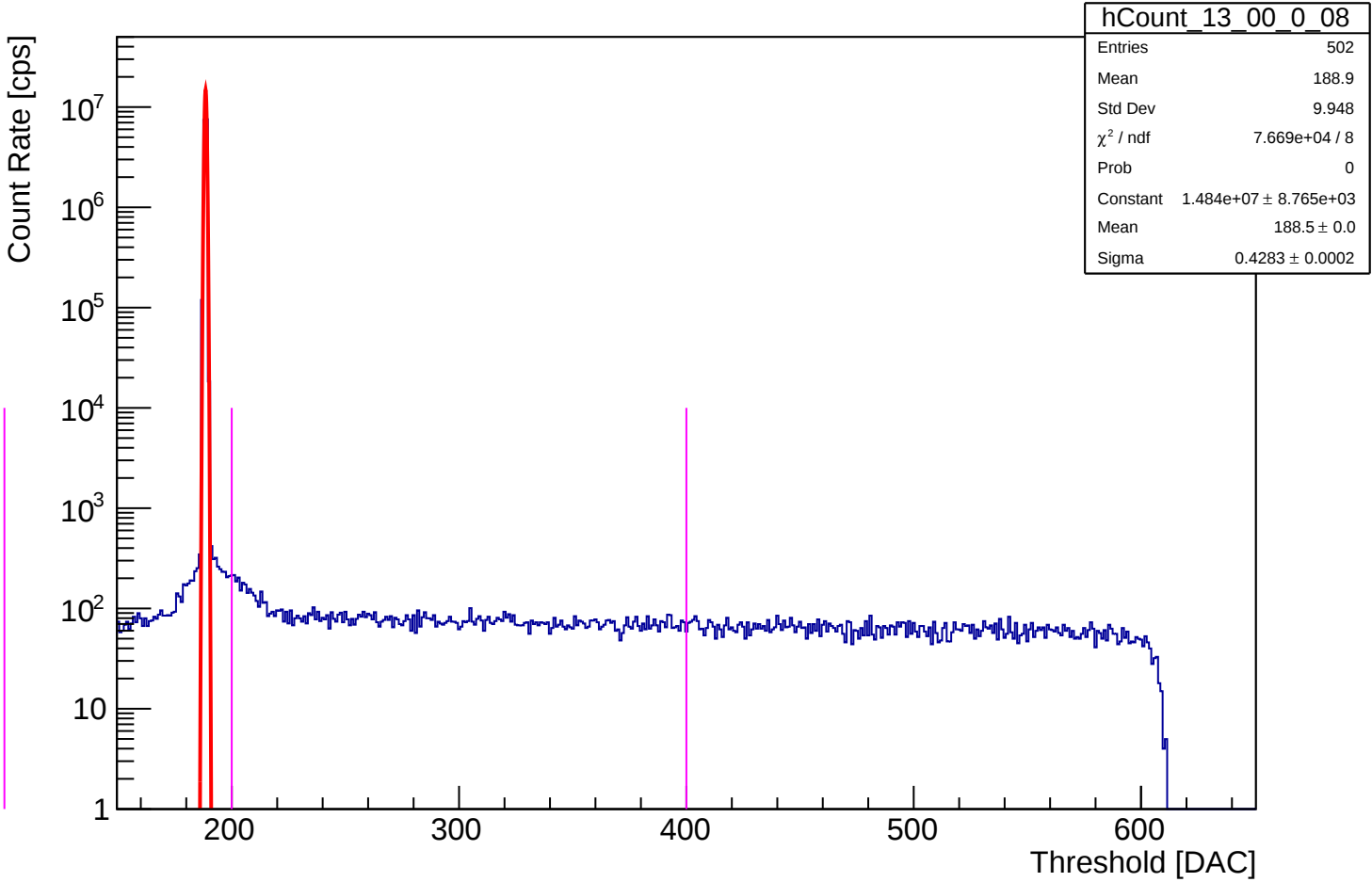
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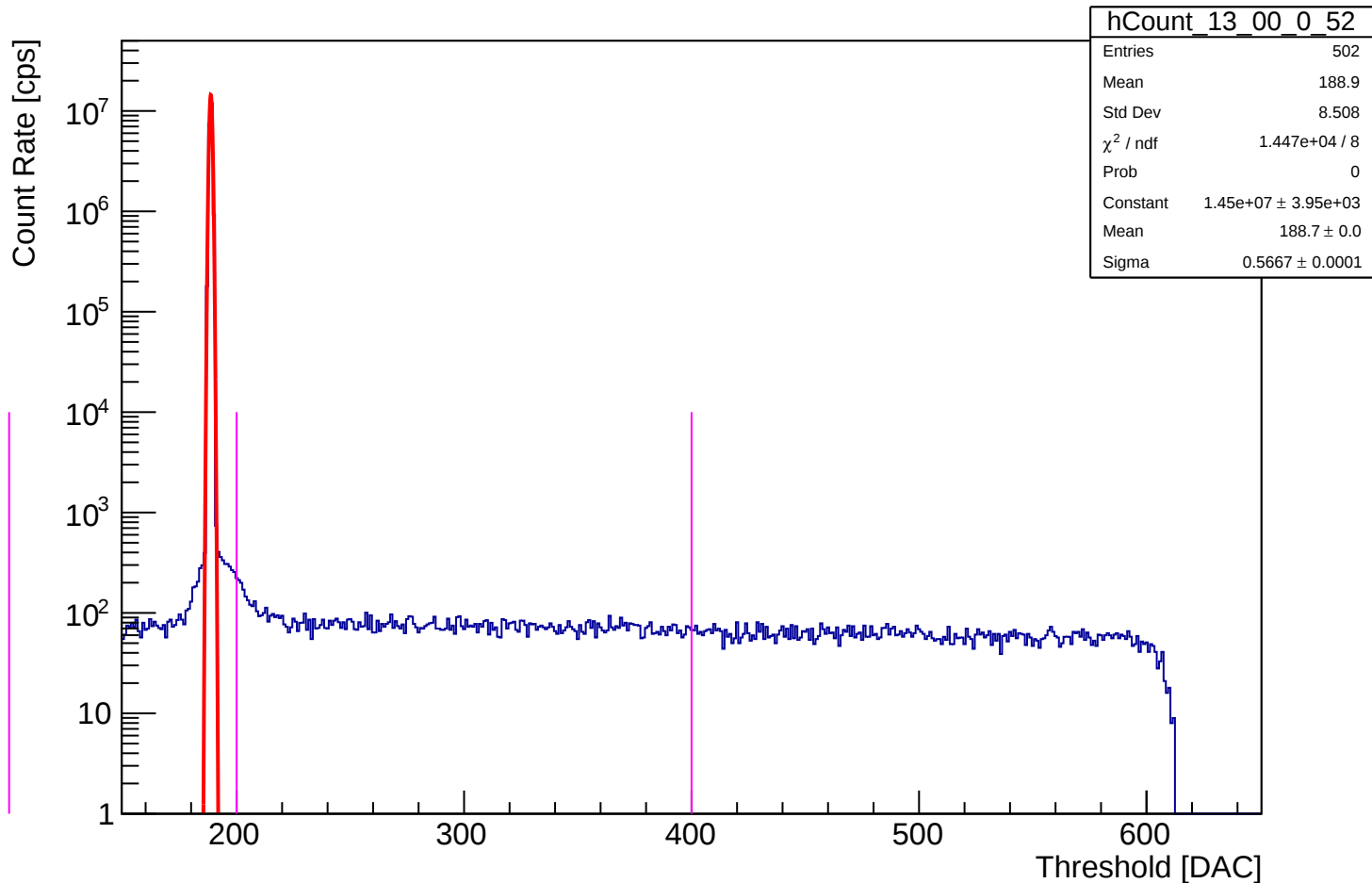


Row 1 Tile 1 Pmt 1 Pixel 43 Slot 13 Fiber 0 Asic 0 Channel 10

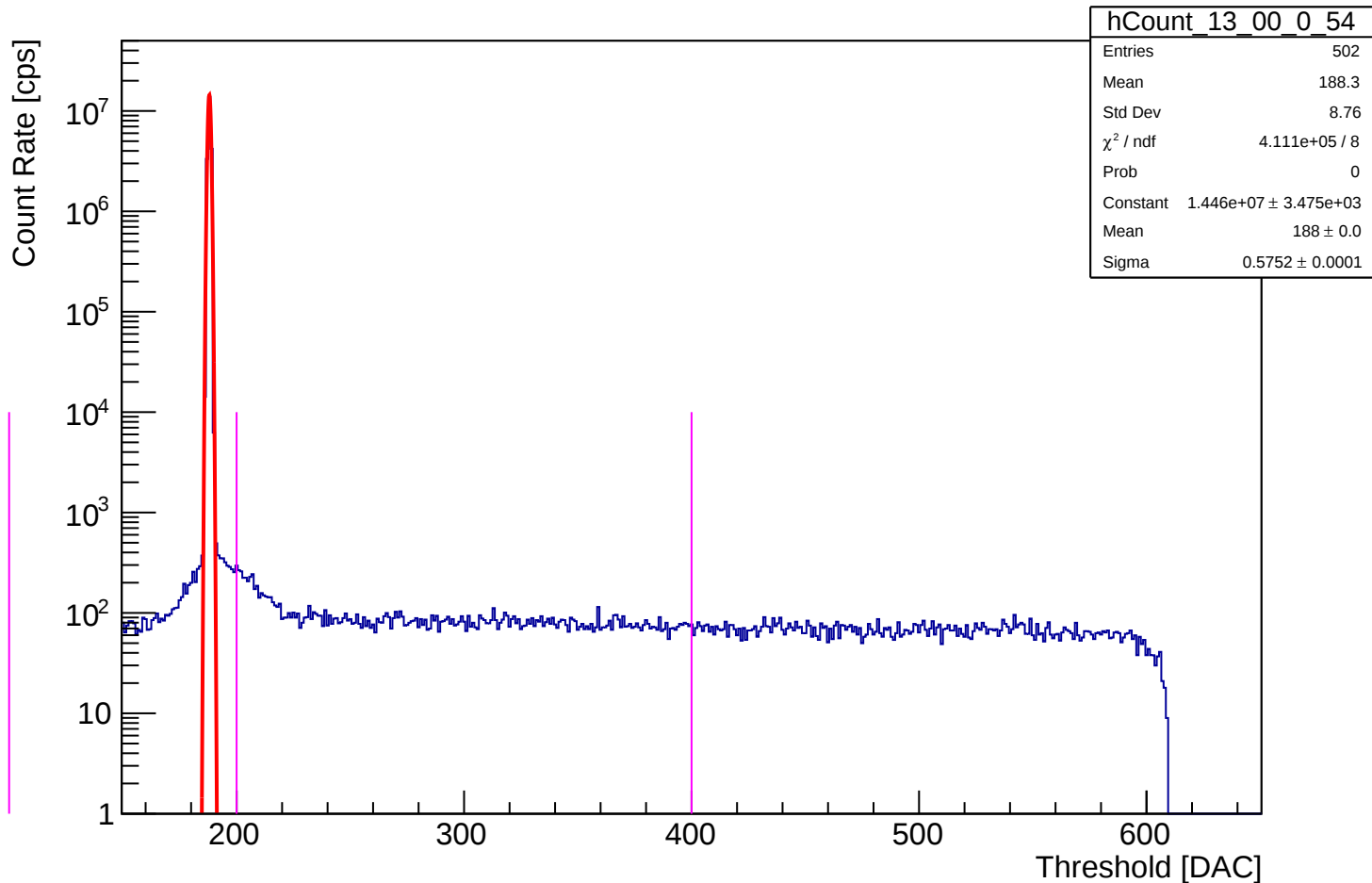




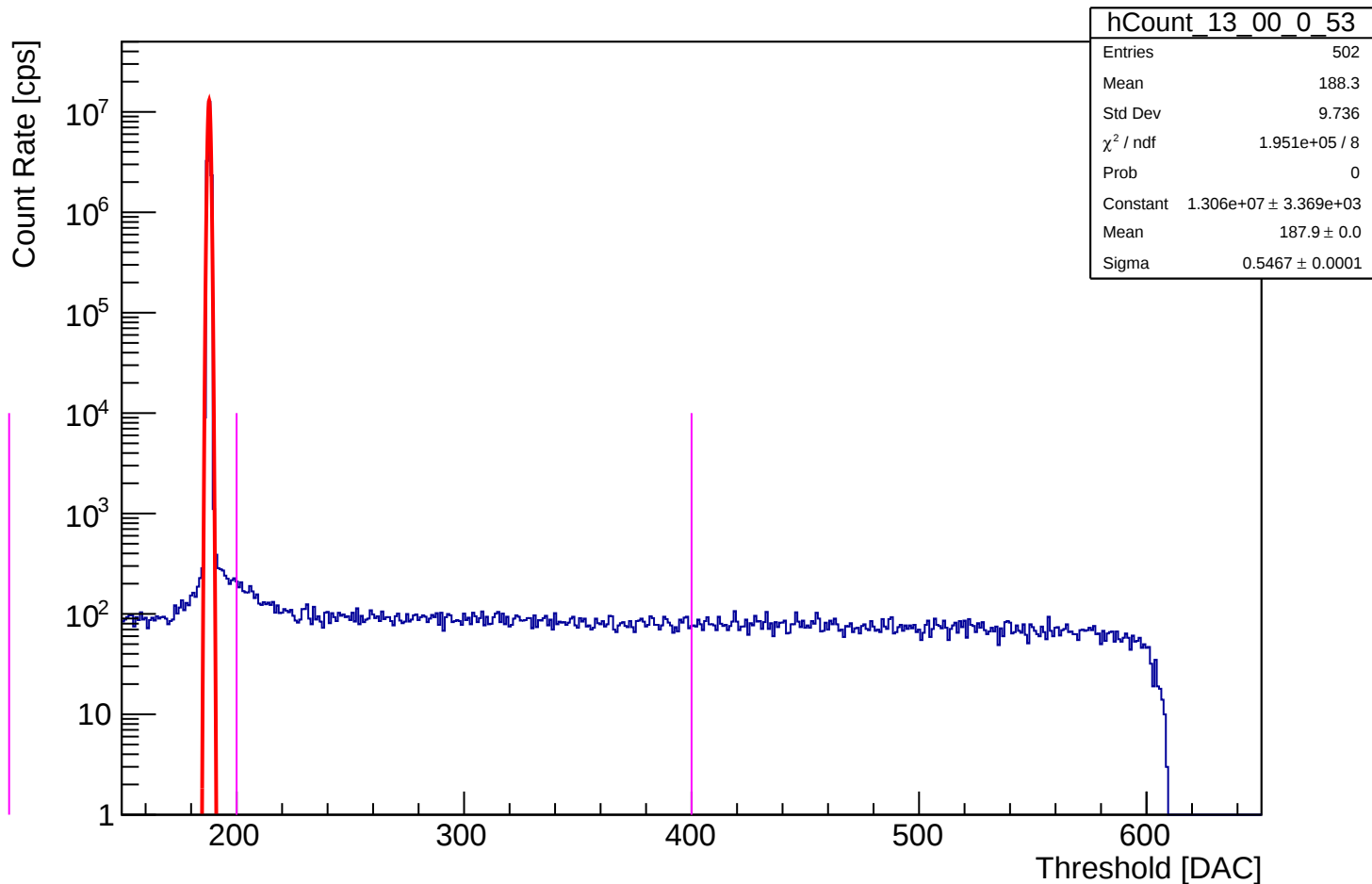
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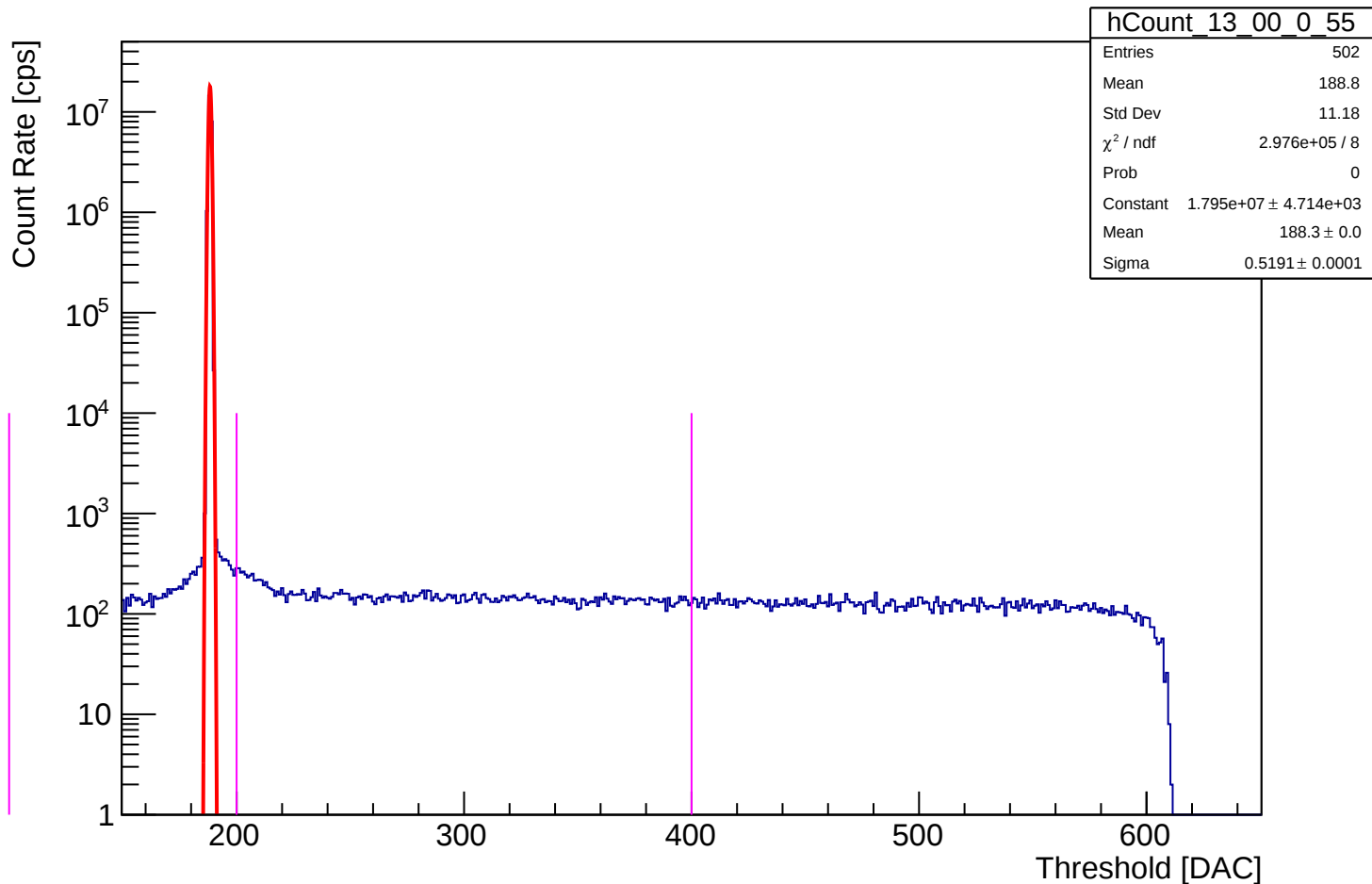
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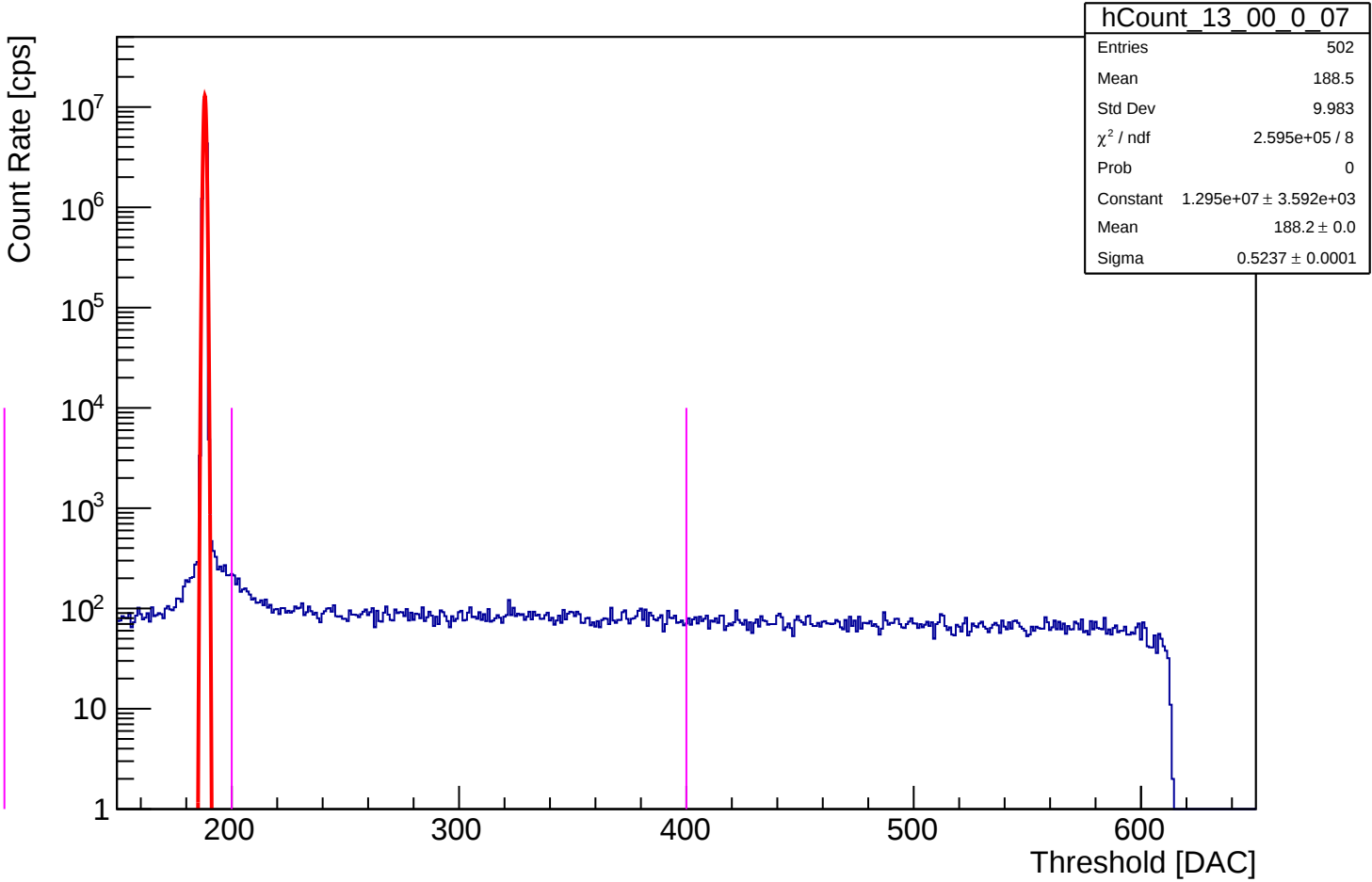


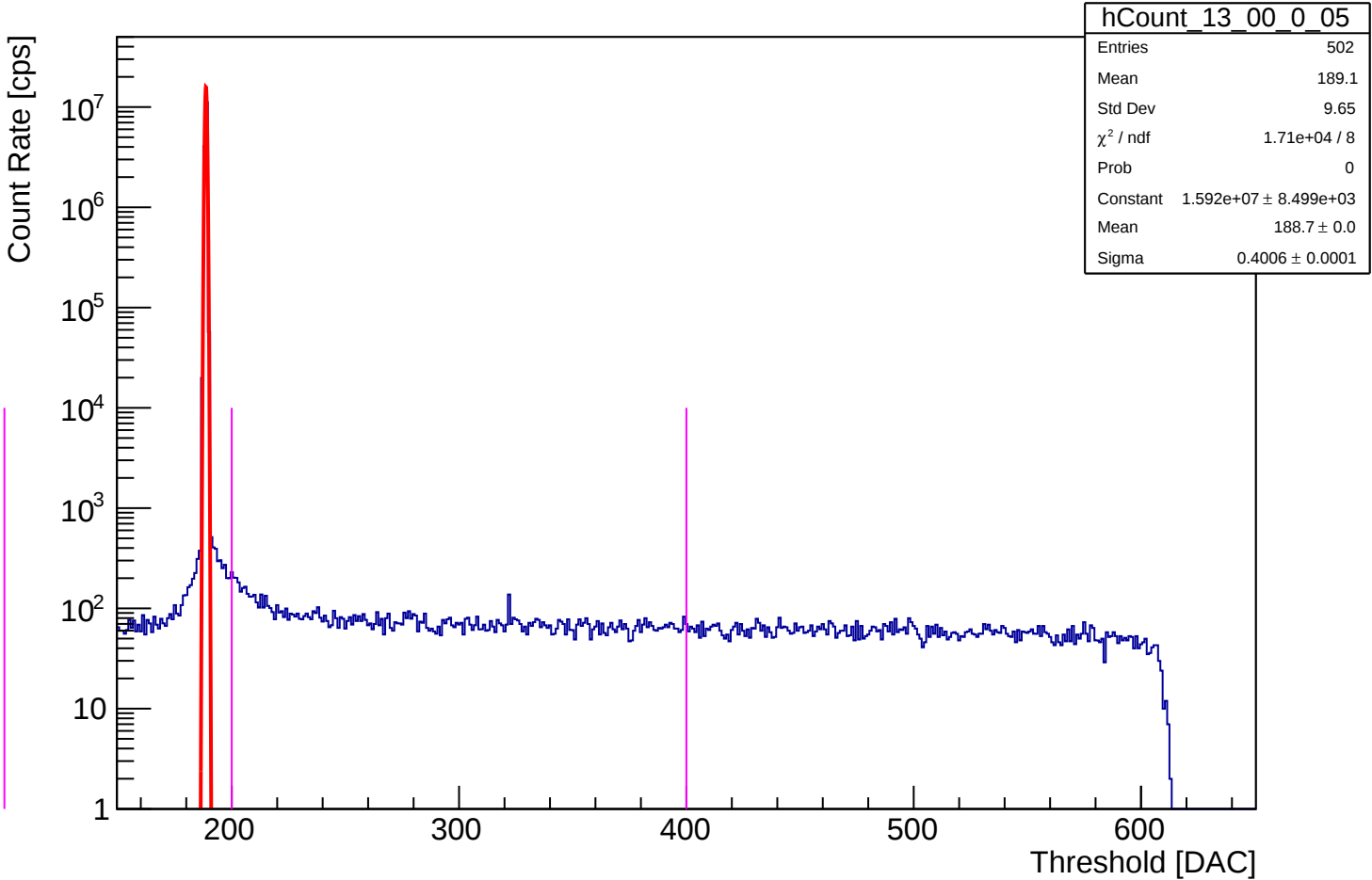
Row 1 Tile 1 Pmt 1 Pixel 47 Slot 13 Fiber 0 Asic 0 Channel 53



Row 1 Tile 1 Pmt 1 Pixel 48 Slot 13 Fiber 0 Asic 0 Channel 55







Count Rate [cps]

10⁷
10⁶
10⁵
10⁴
10³
10²
10
1

200

300

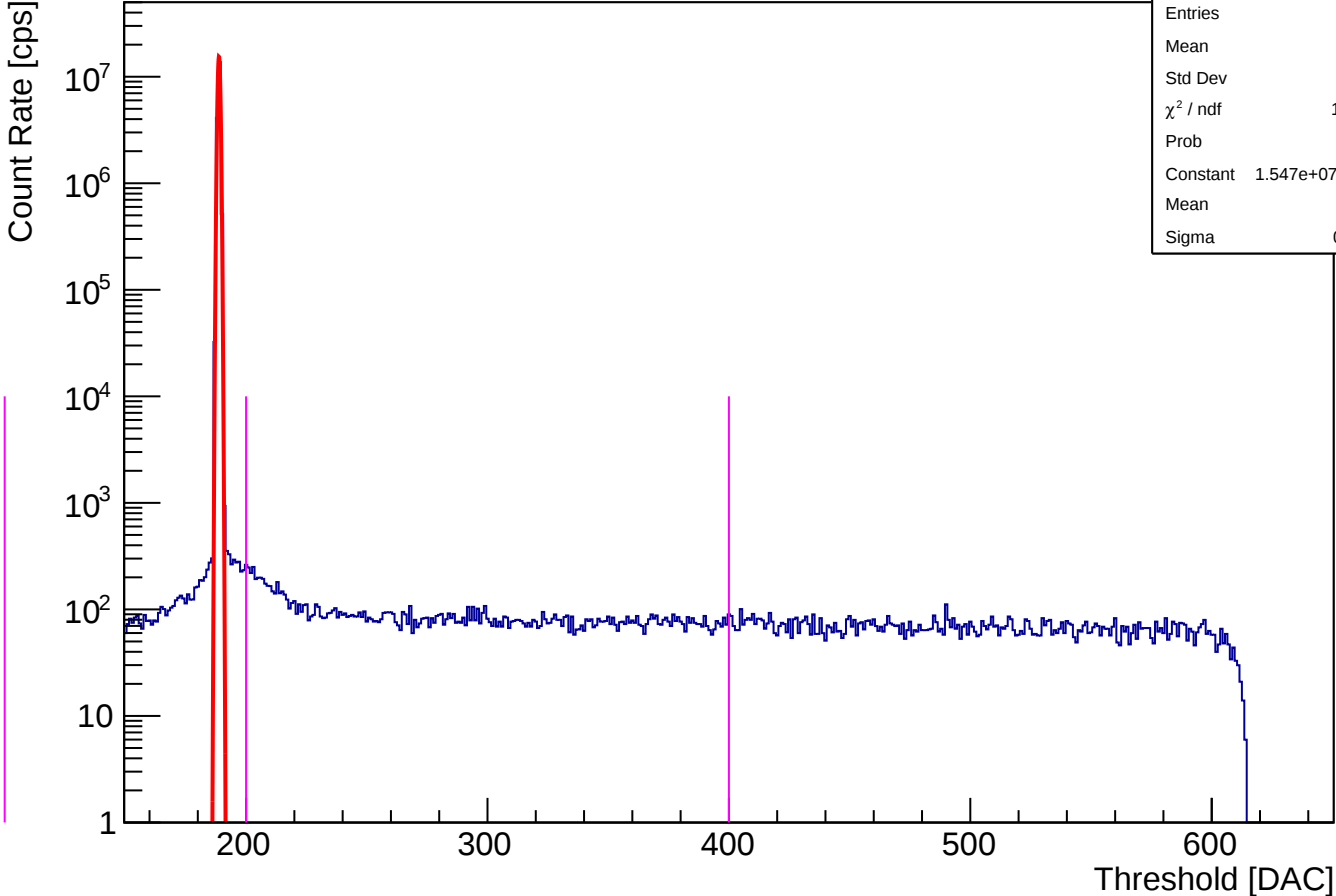
400

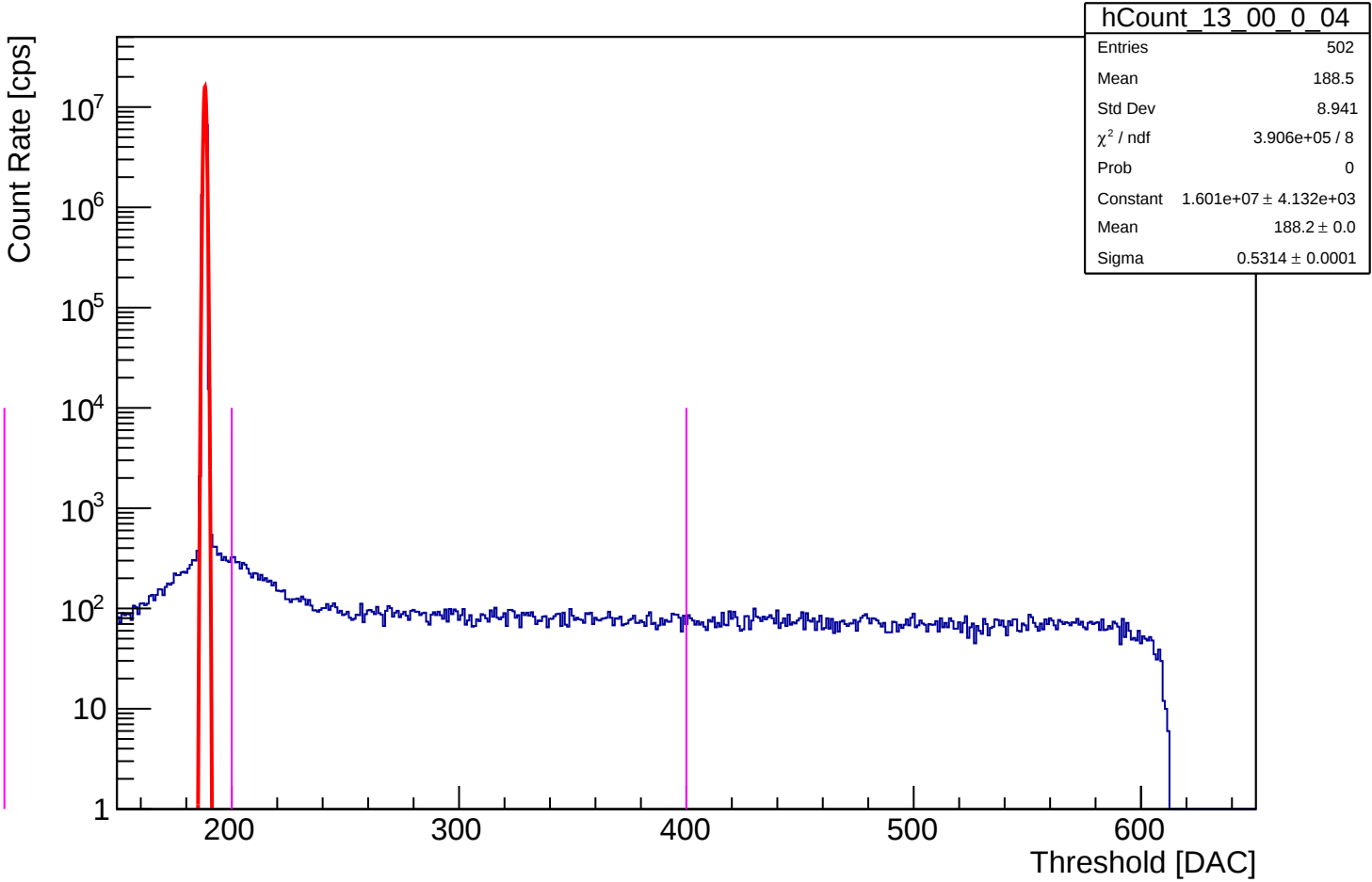
500

600

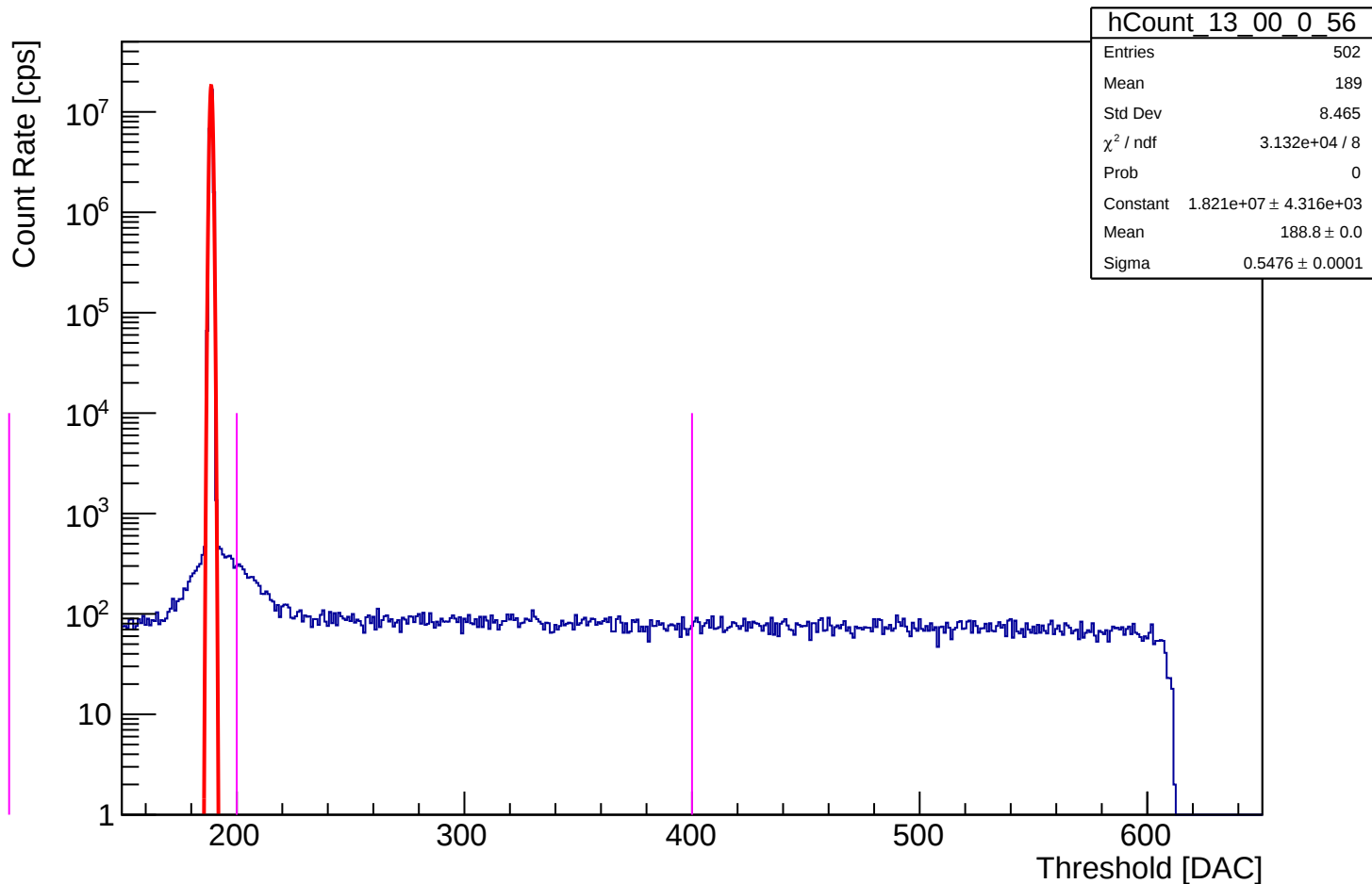
Threshold [DAC]

hCount_13_00_0_06	
Entries	502
Mean	189.1
Std Dev	9.59
χ^2 / ndf	1.277e+04 / 8
Prob	0
Constant	1.547e+07 $\pm$ 4.518e+03
Mean	188.8 $\pm$ 0.0
Sigma	0.473 $\pm$ 0.000

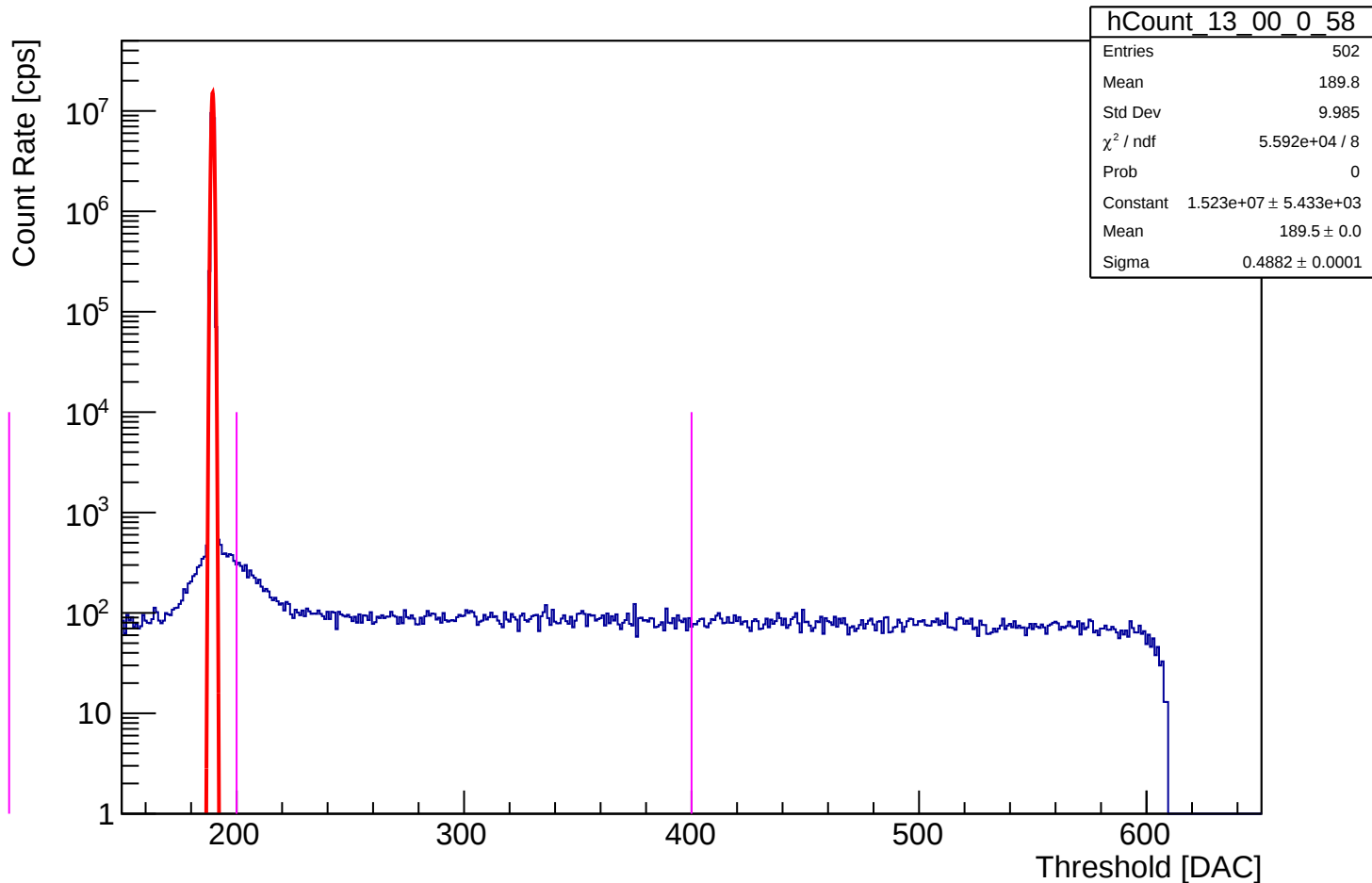




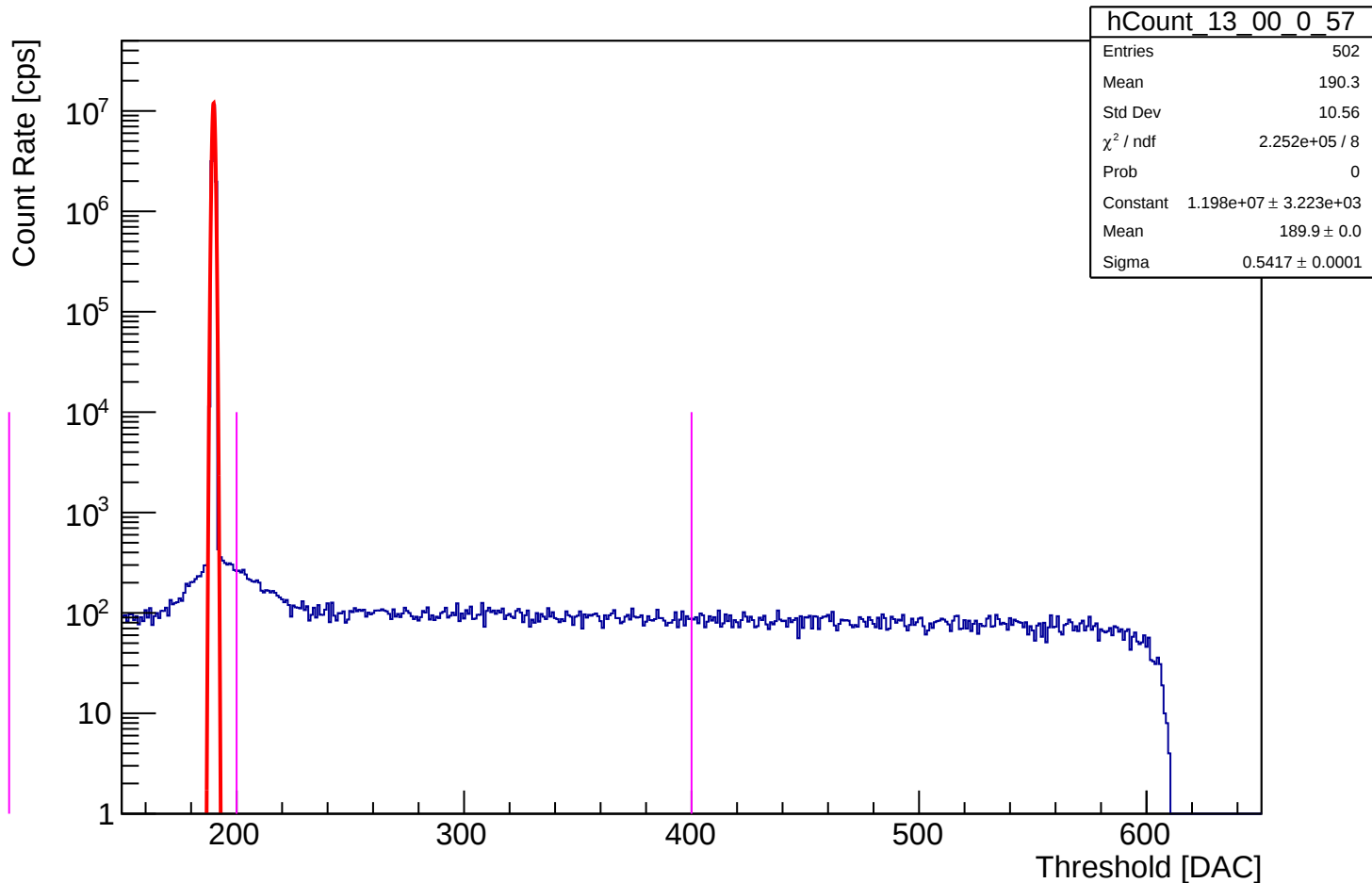
Row 1 Tile 1 Pmt 1 Pixel 53 Slot 13 Fiber 0 Asic 0 Channel 56



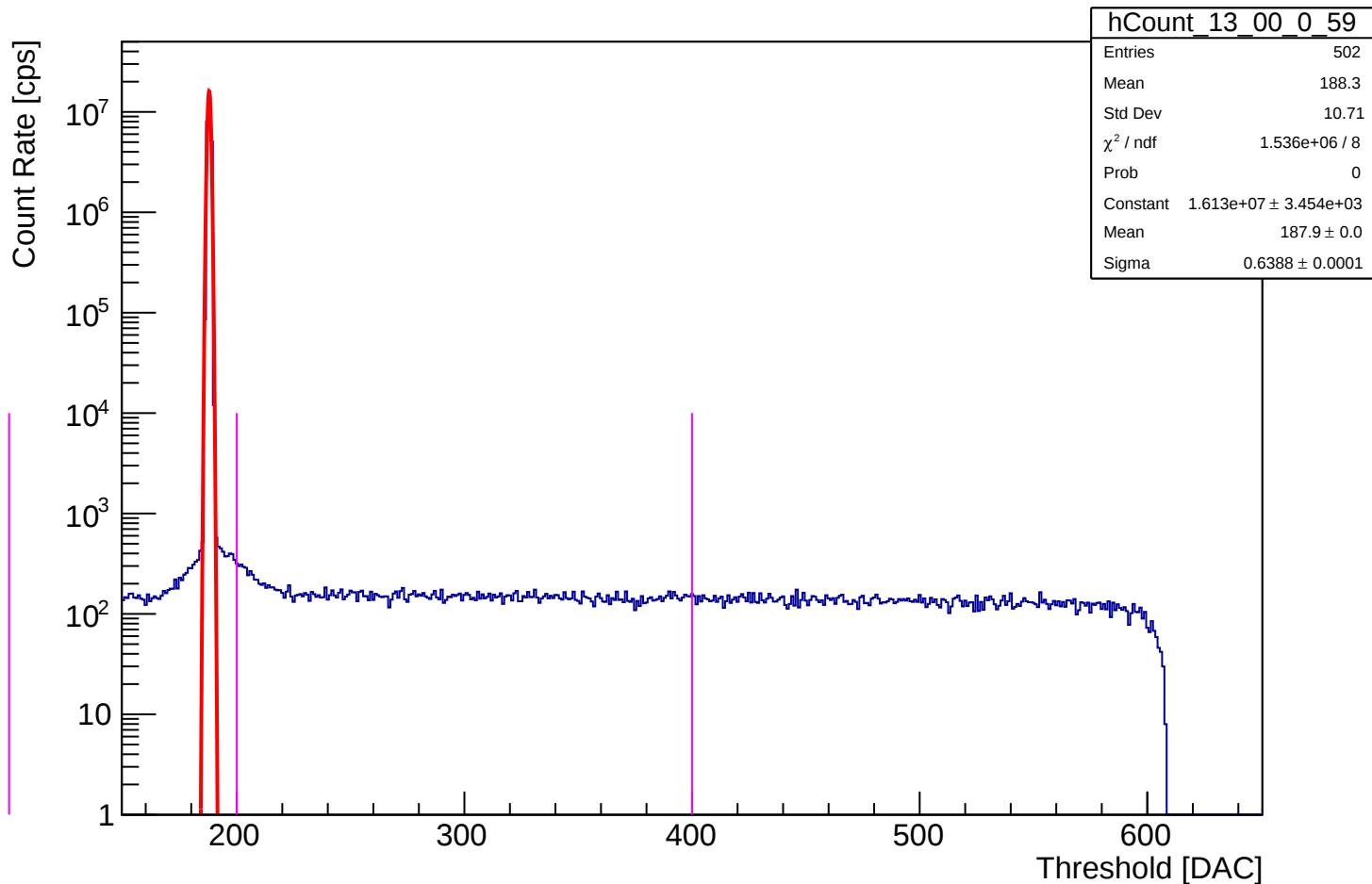
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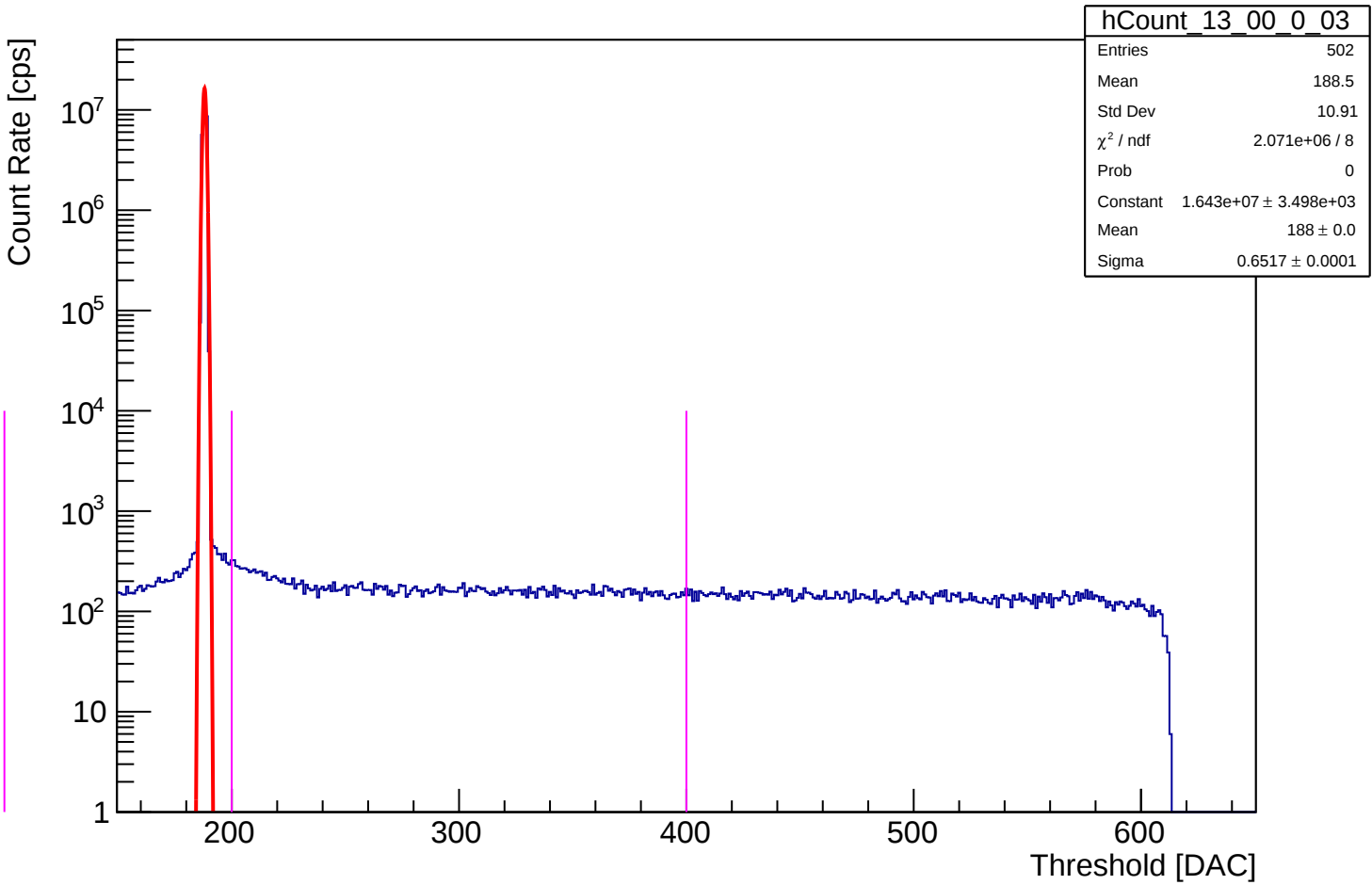


Row 1 Tile 1 Pmt 1 Pixel 55 Slot 13 Fiber 0 Asic 0 Channel 57

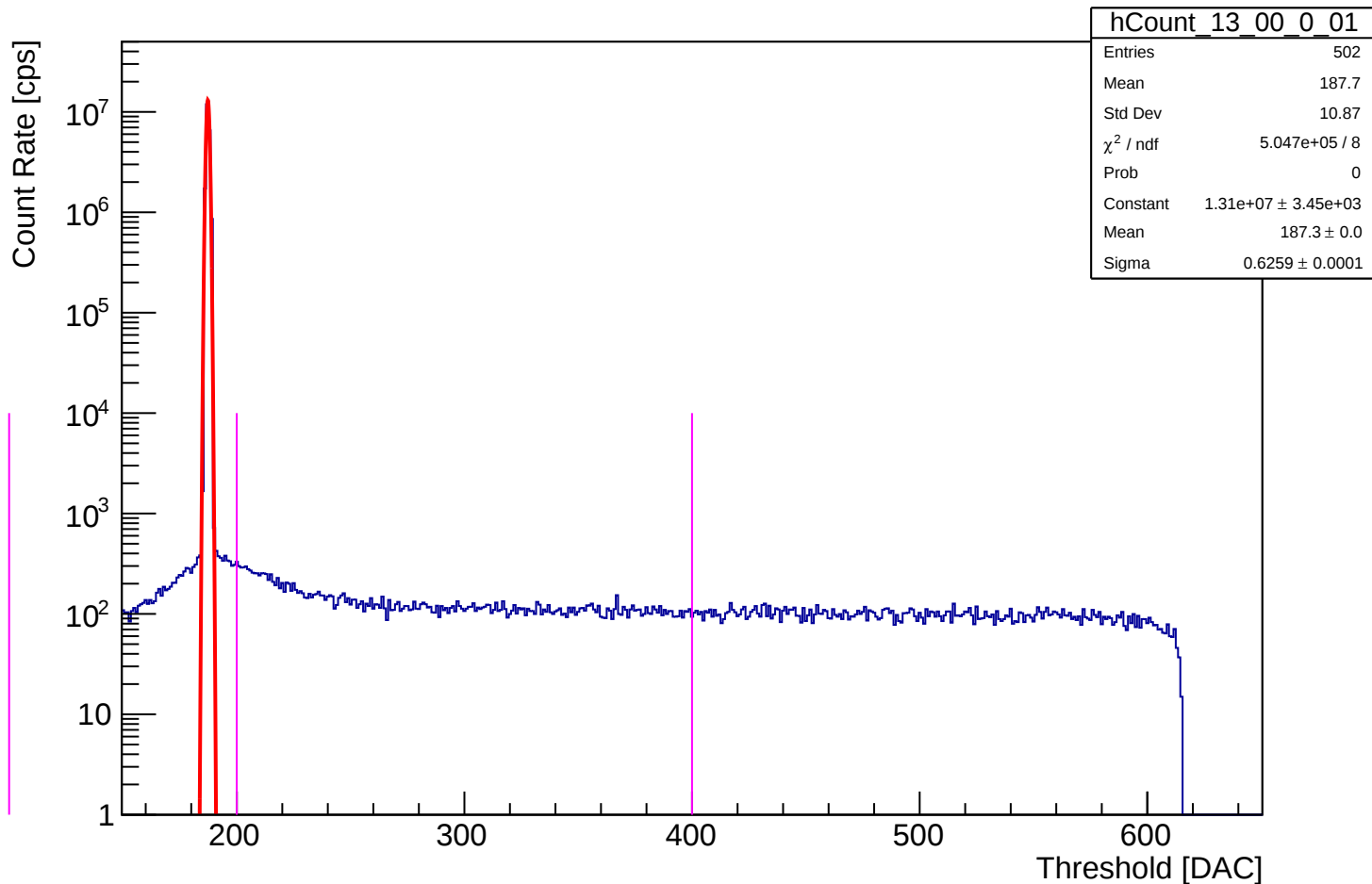


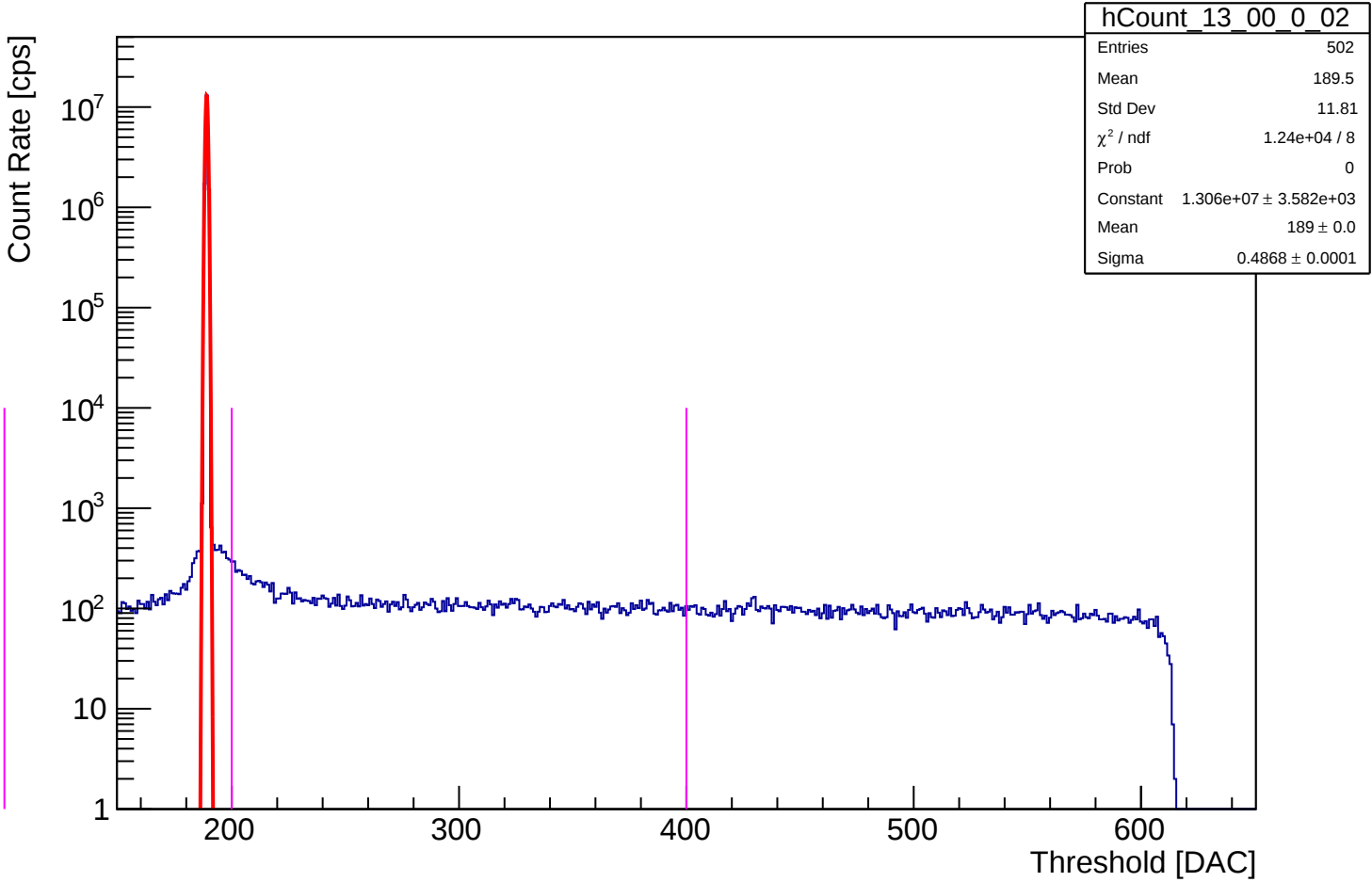
Row 1 Tile 1 Pmt 1 Pixel 56 Slot 13 Fiber 0 Asic 0 Channel 59

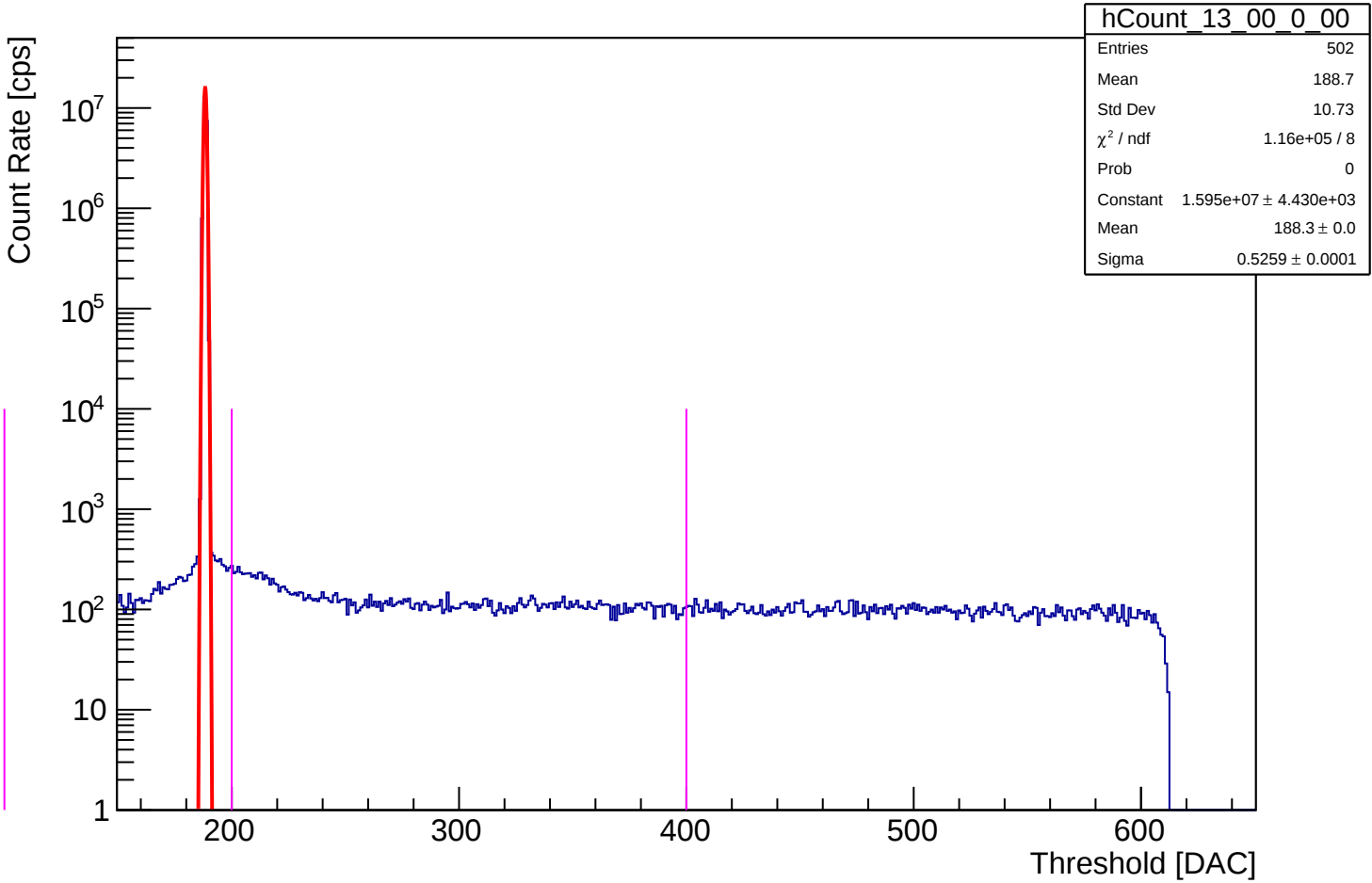




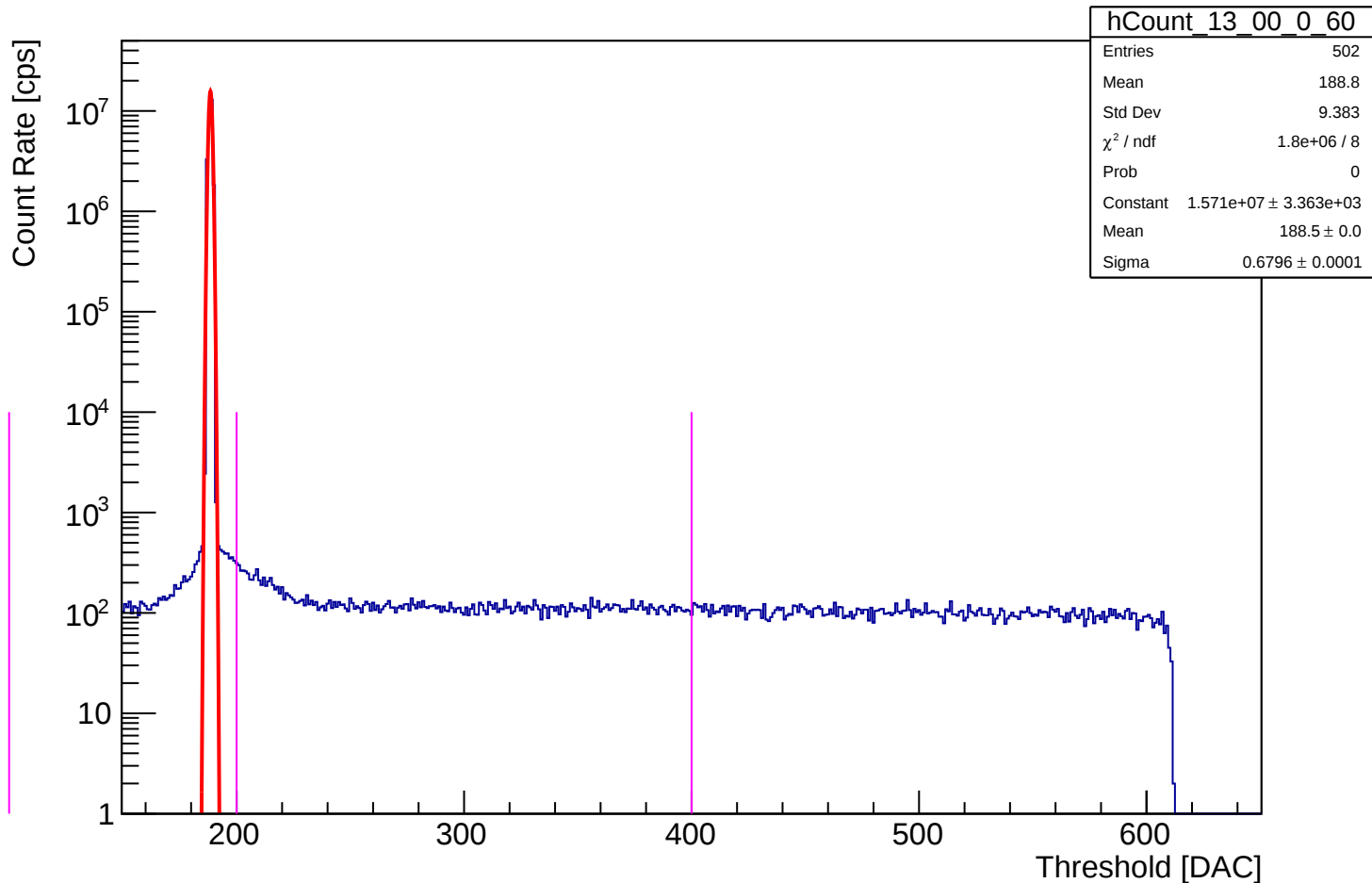
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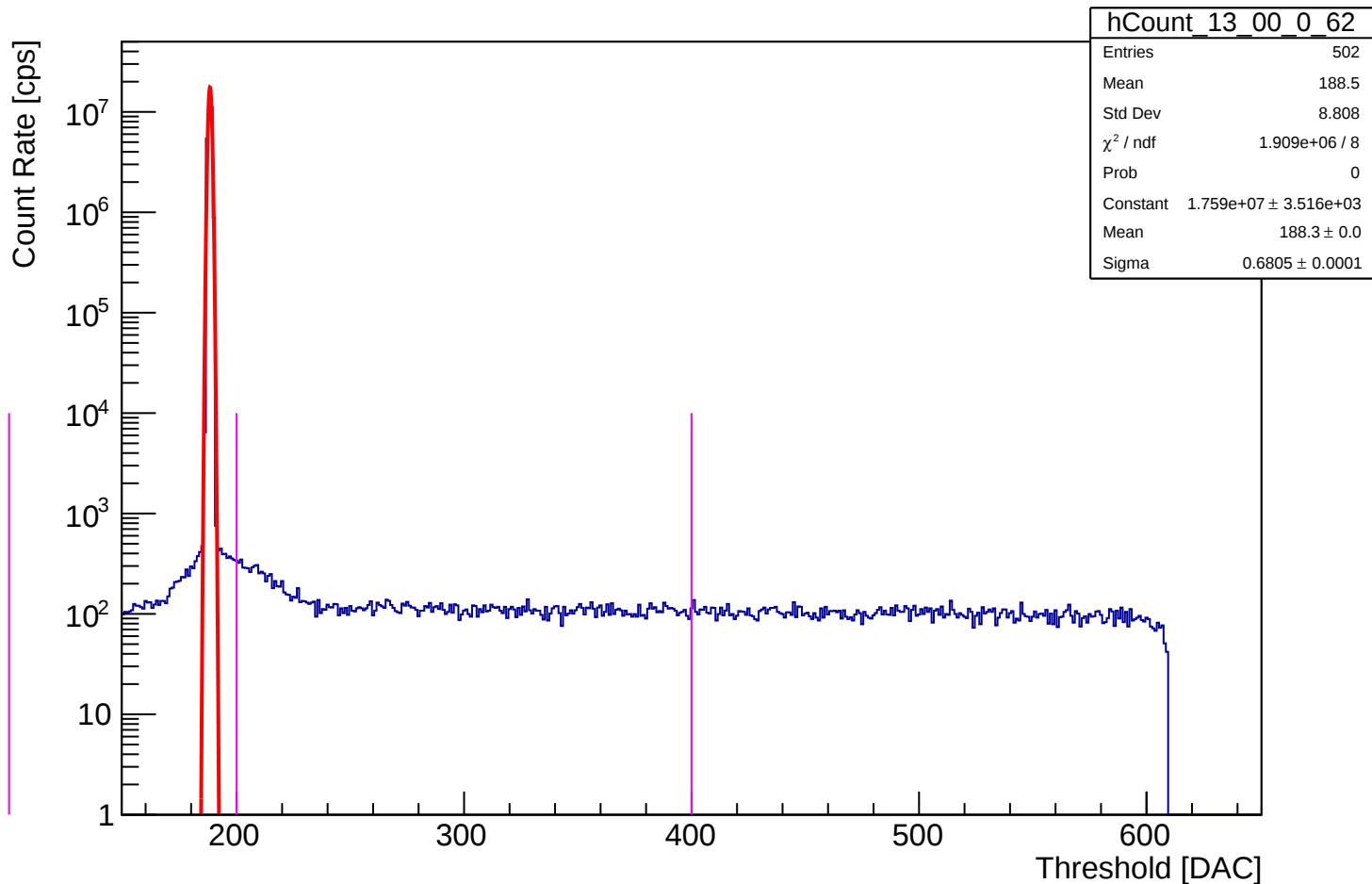




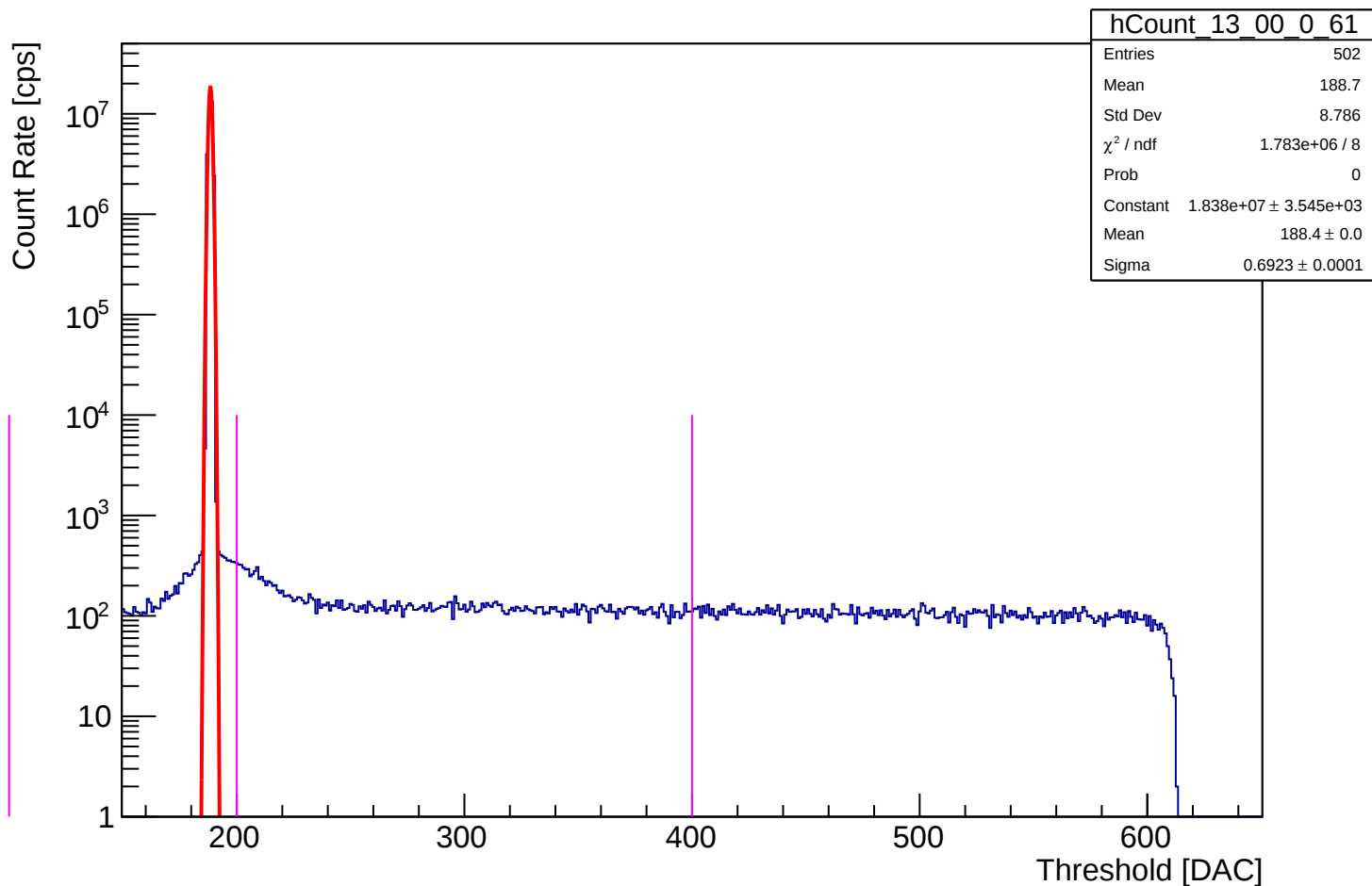
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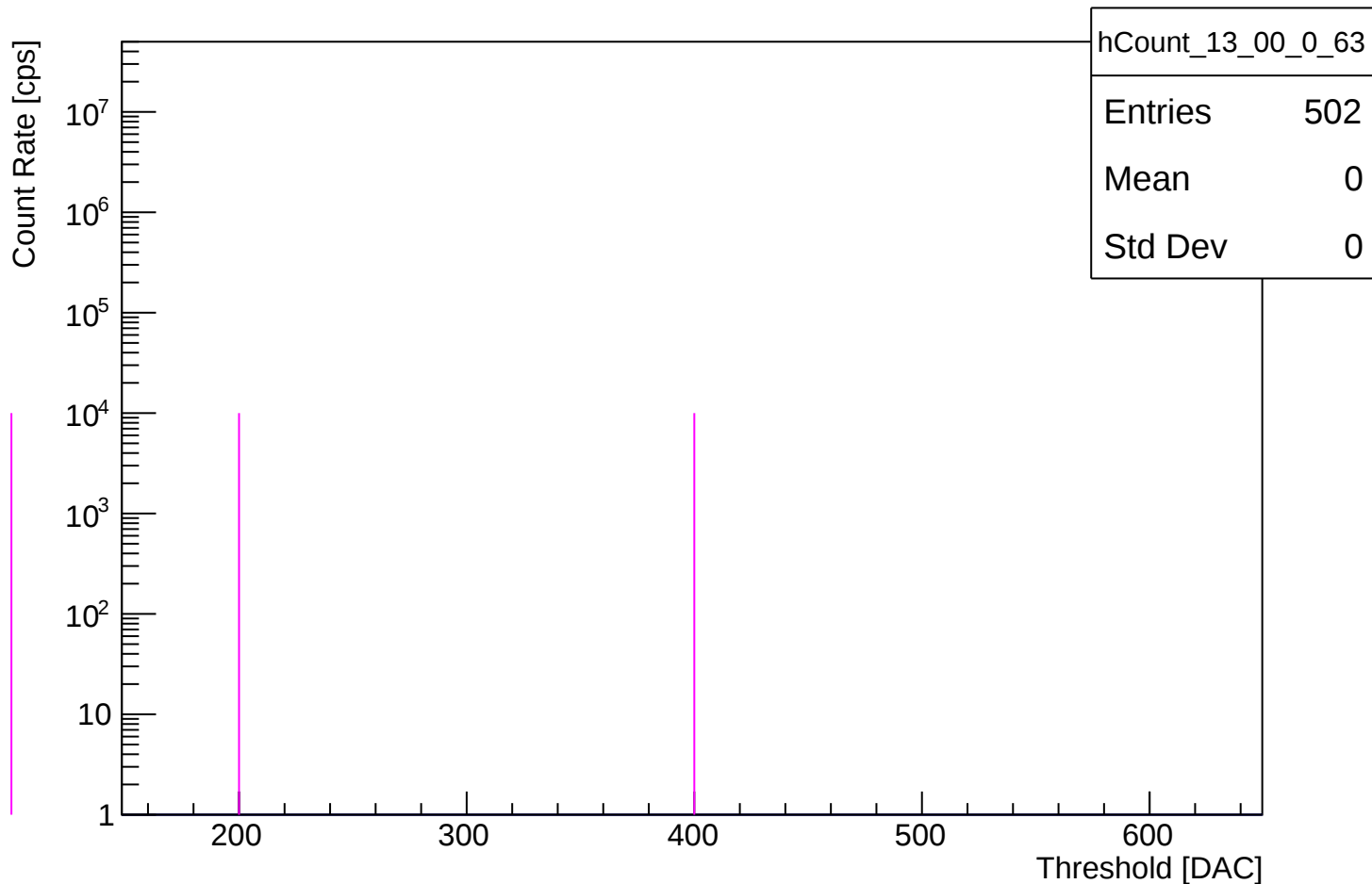
Row 1 Tile 1 Pmt 1 Pixel 62 Slot 13 Fiber 0 Asic 0 Channel 62



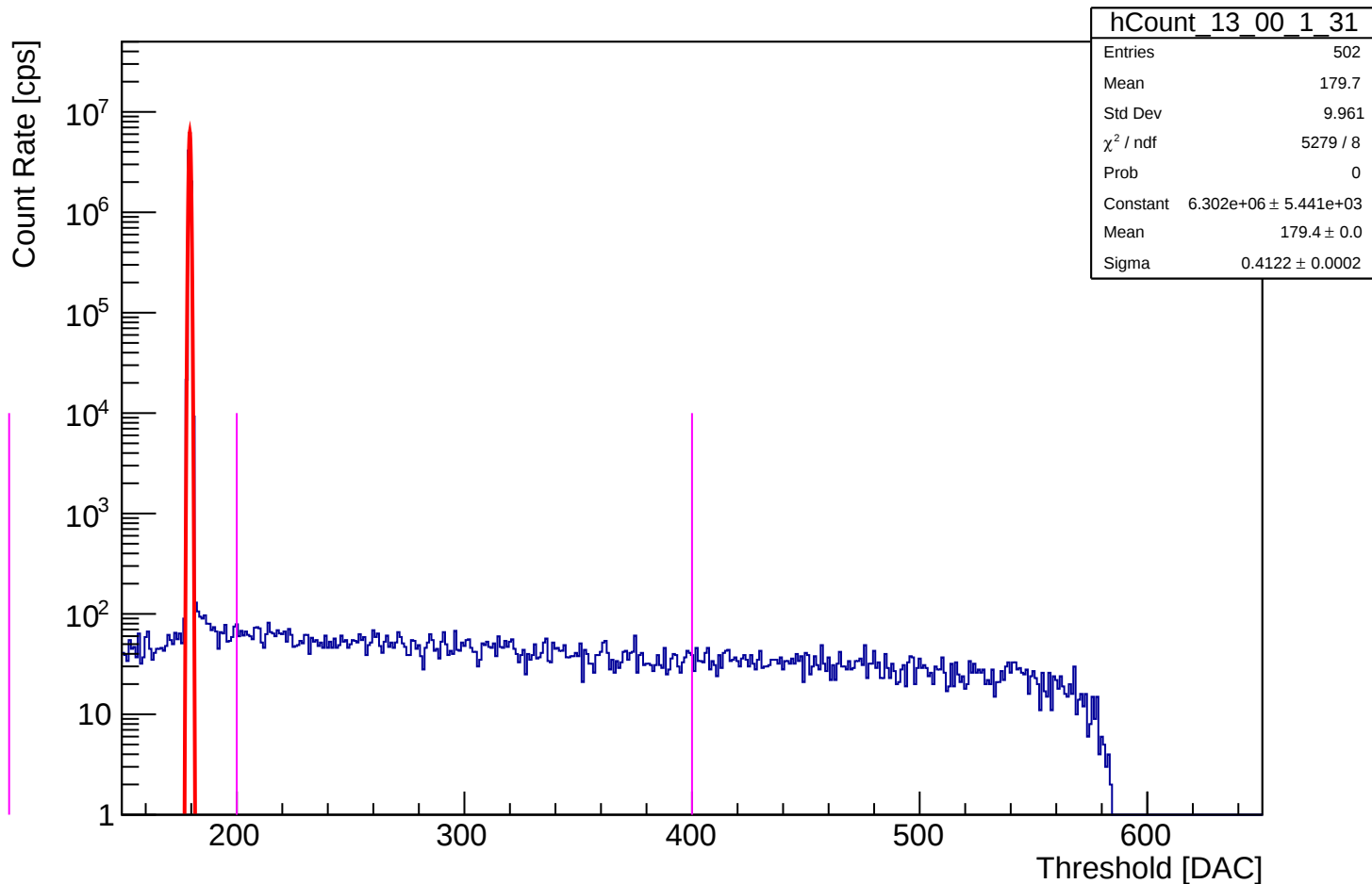
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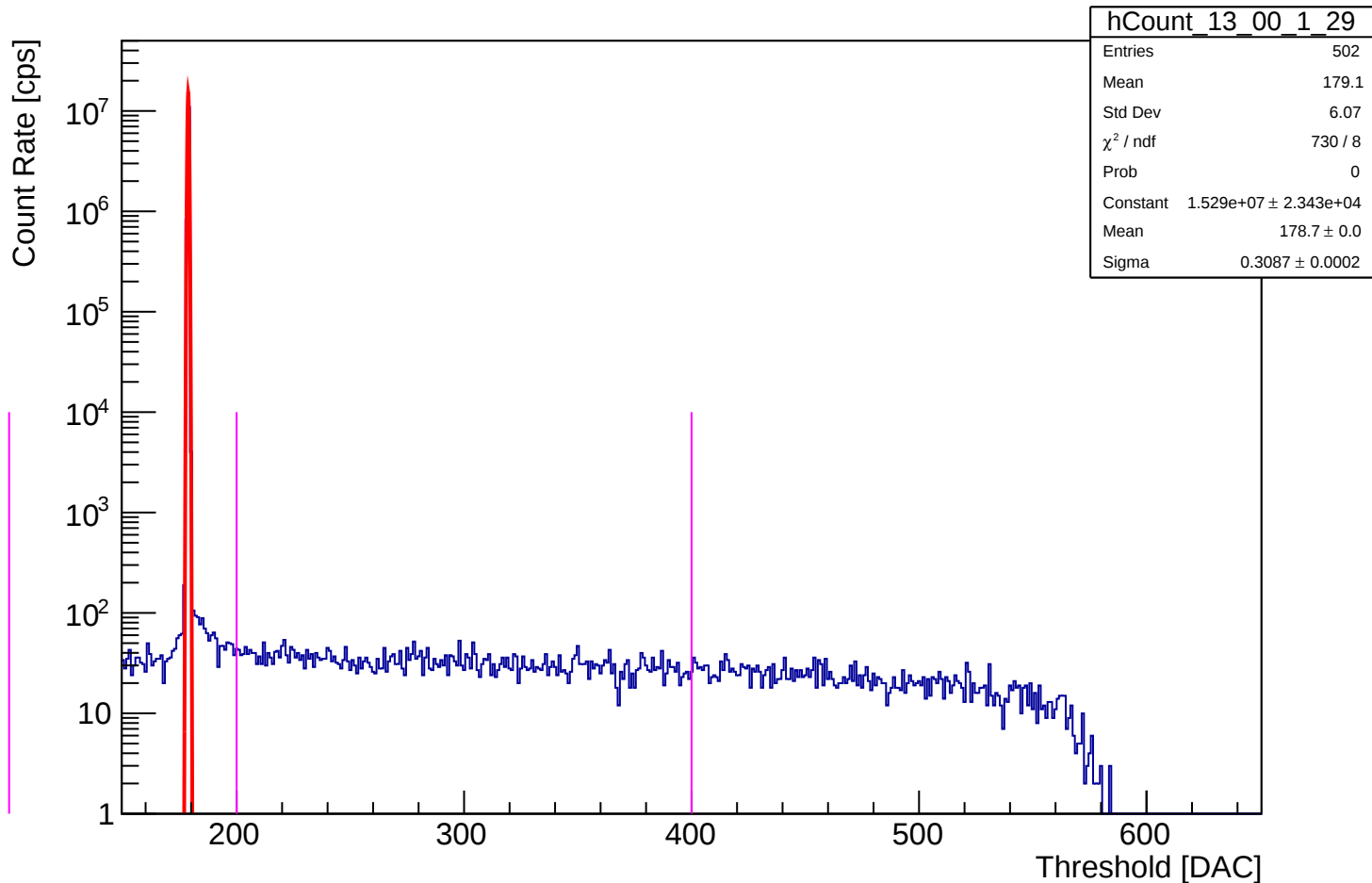
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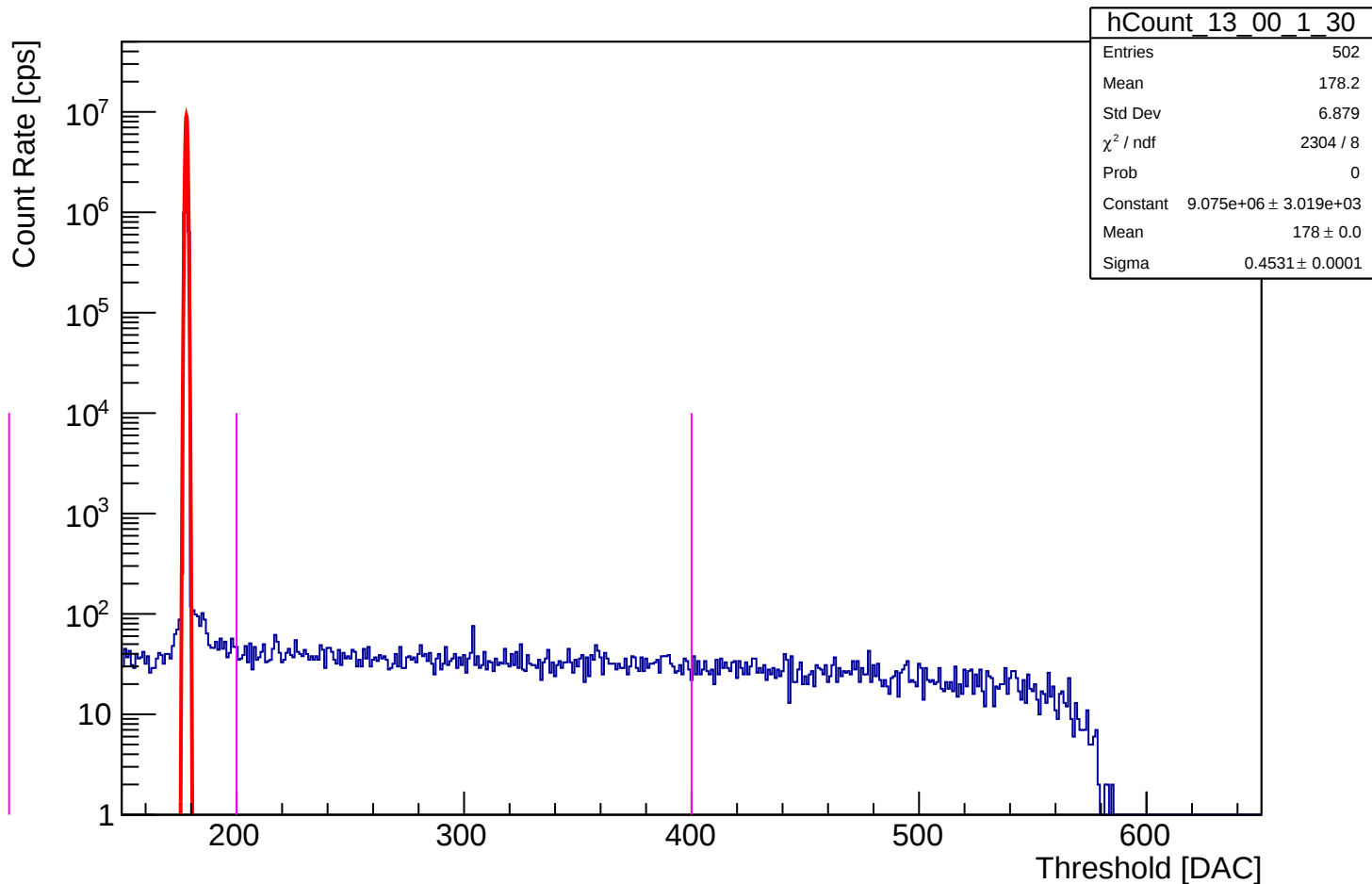
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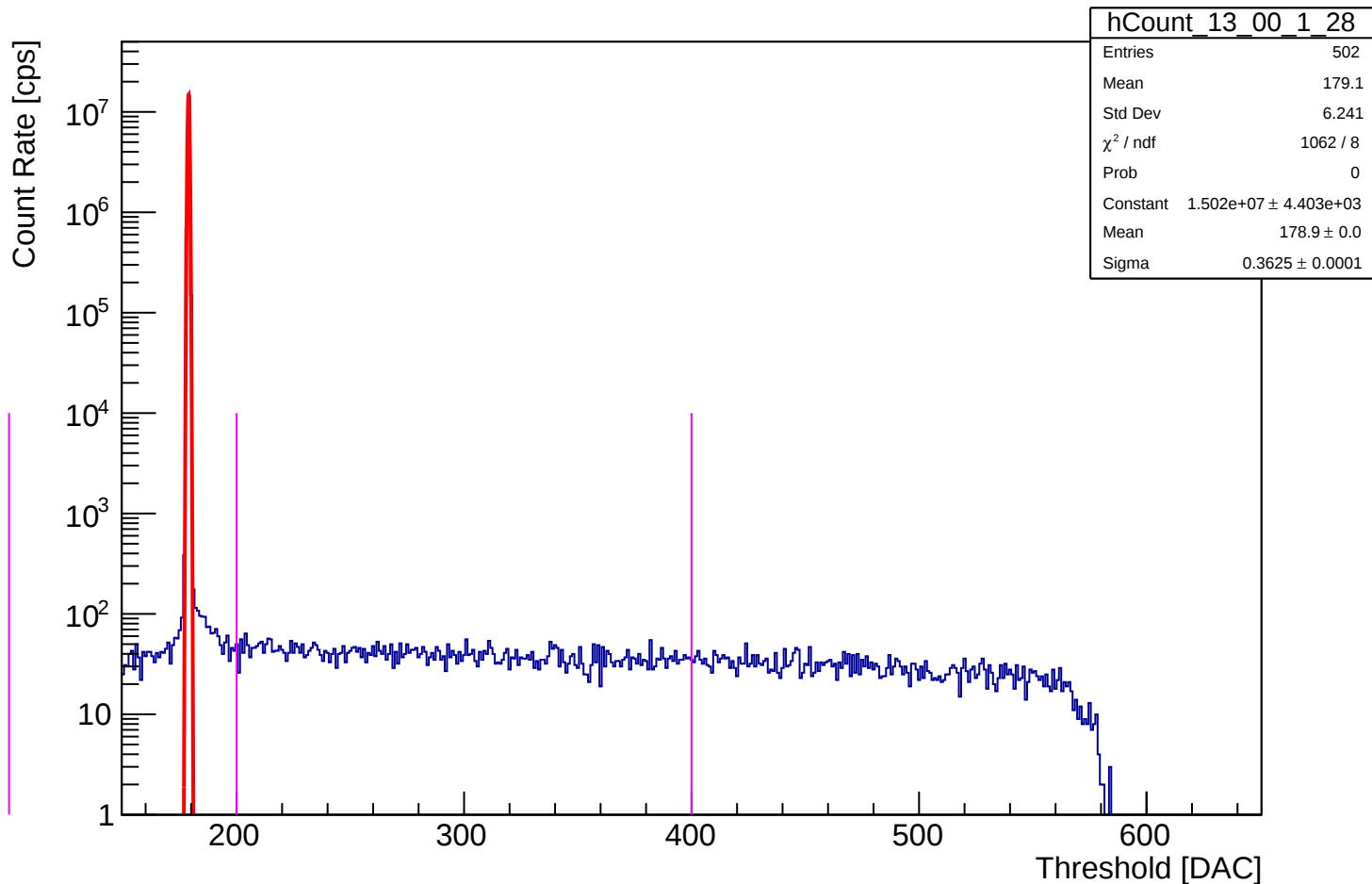
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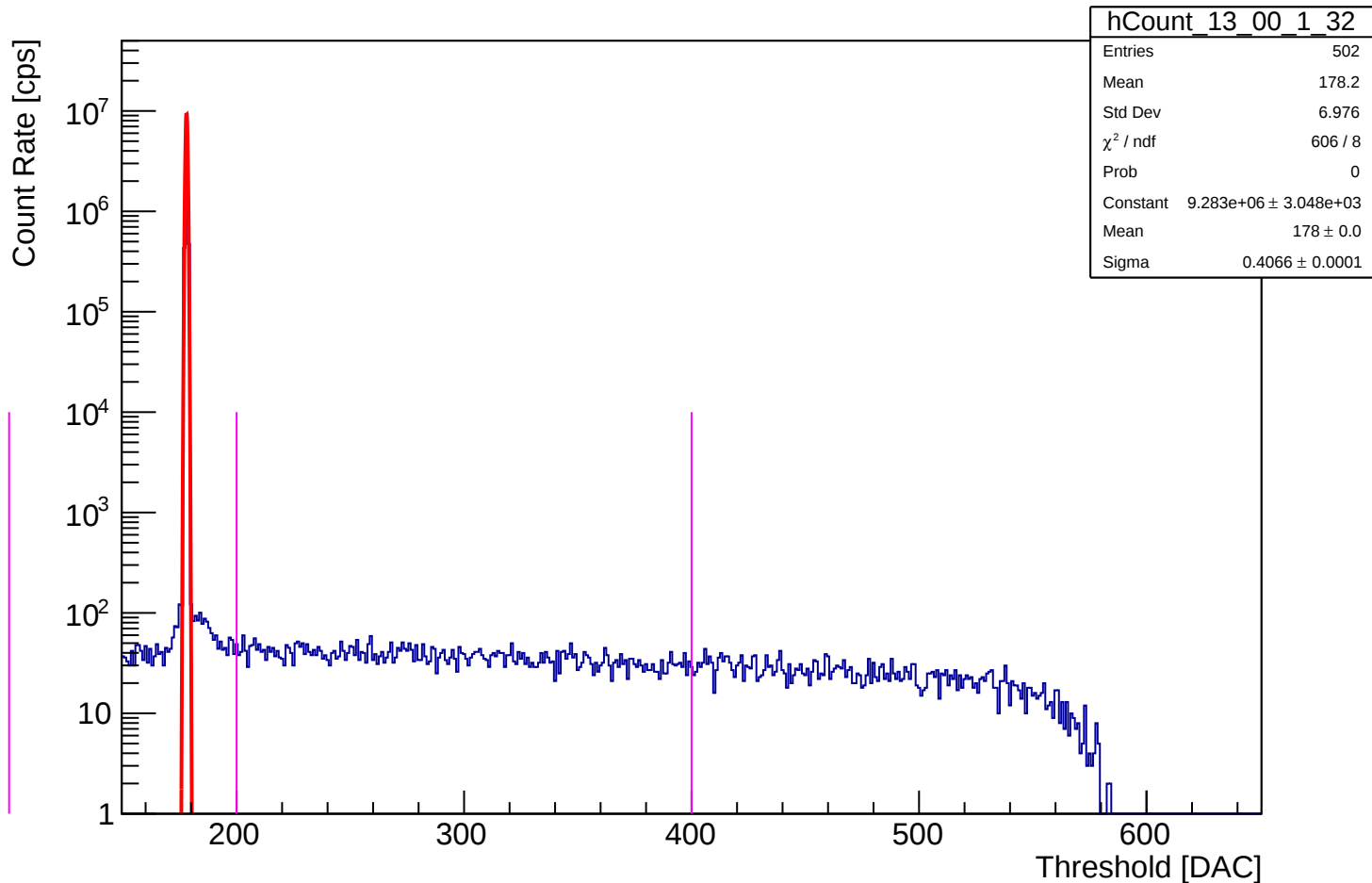
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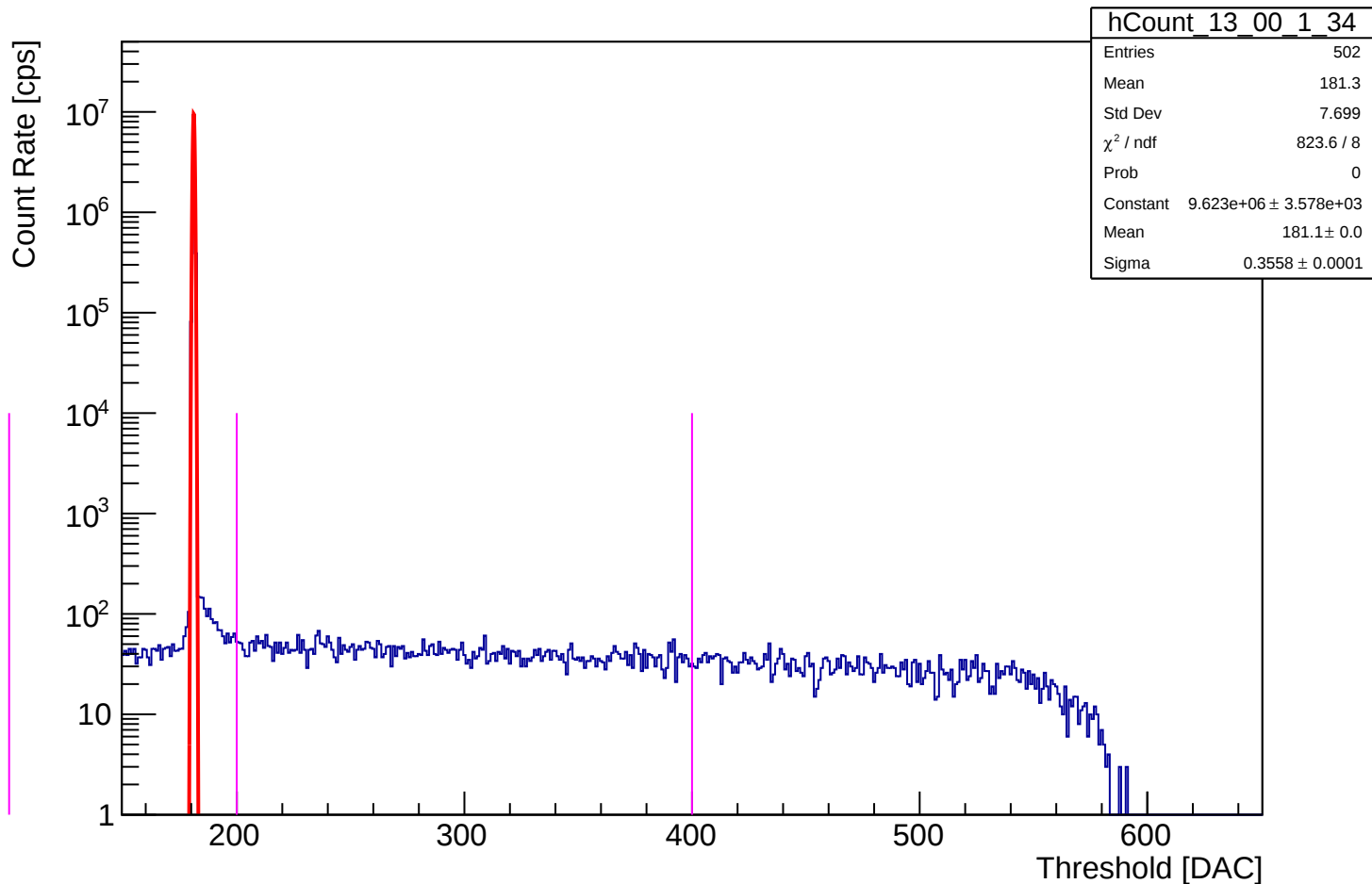
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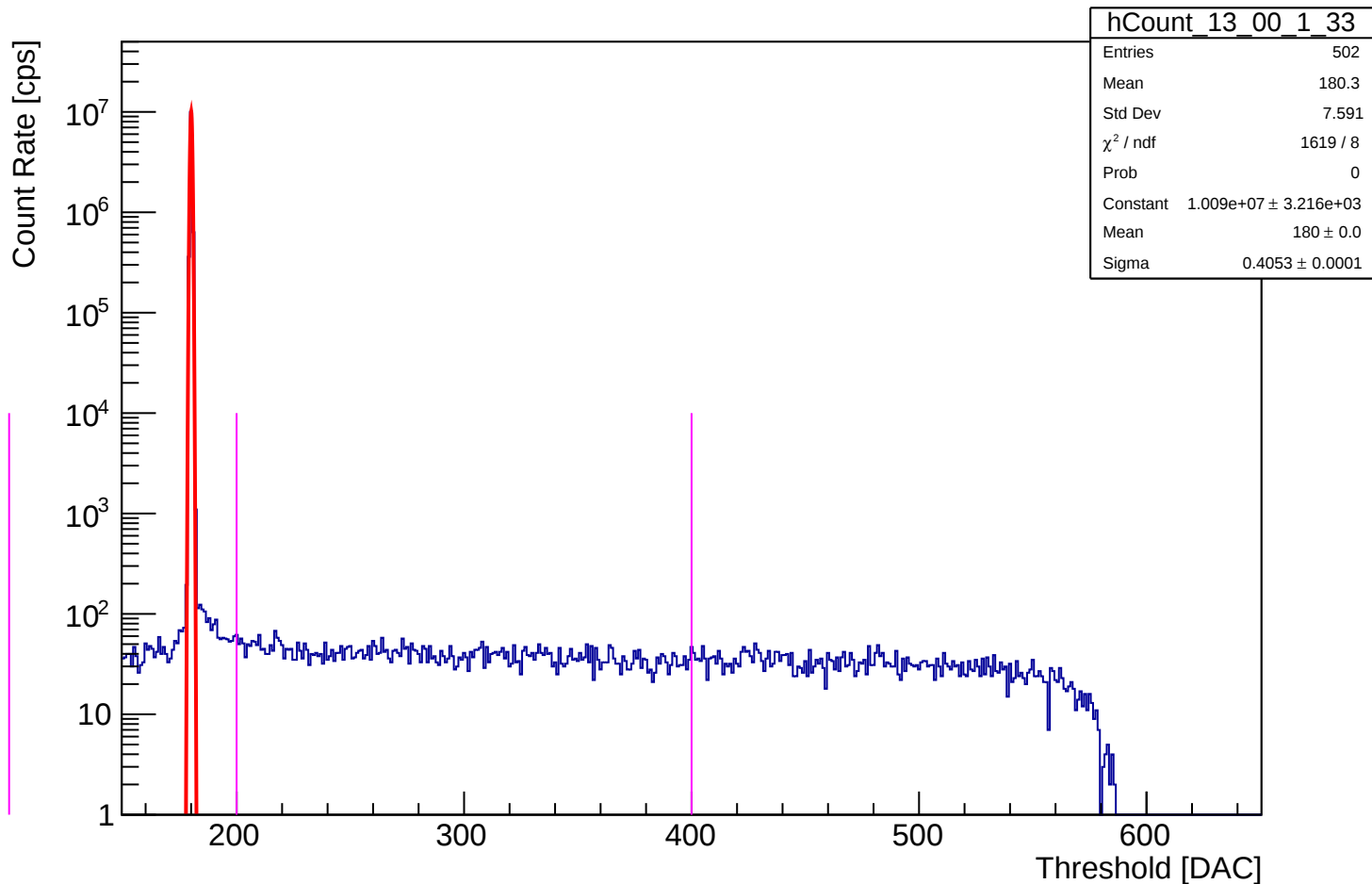
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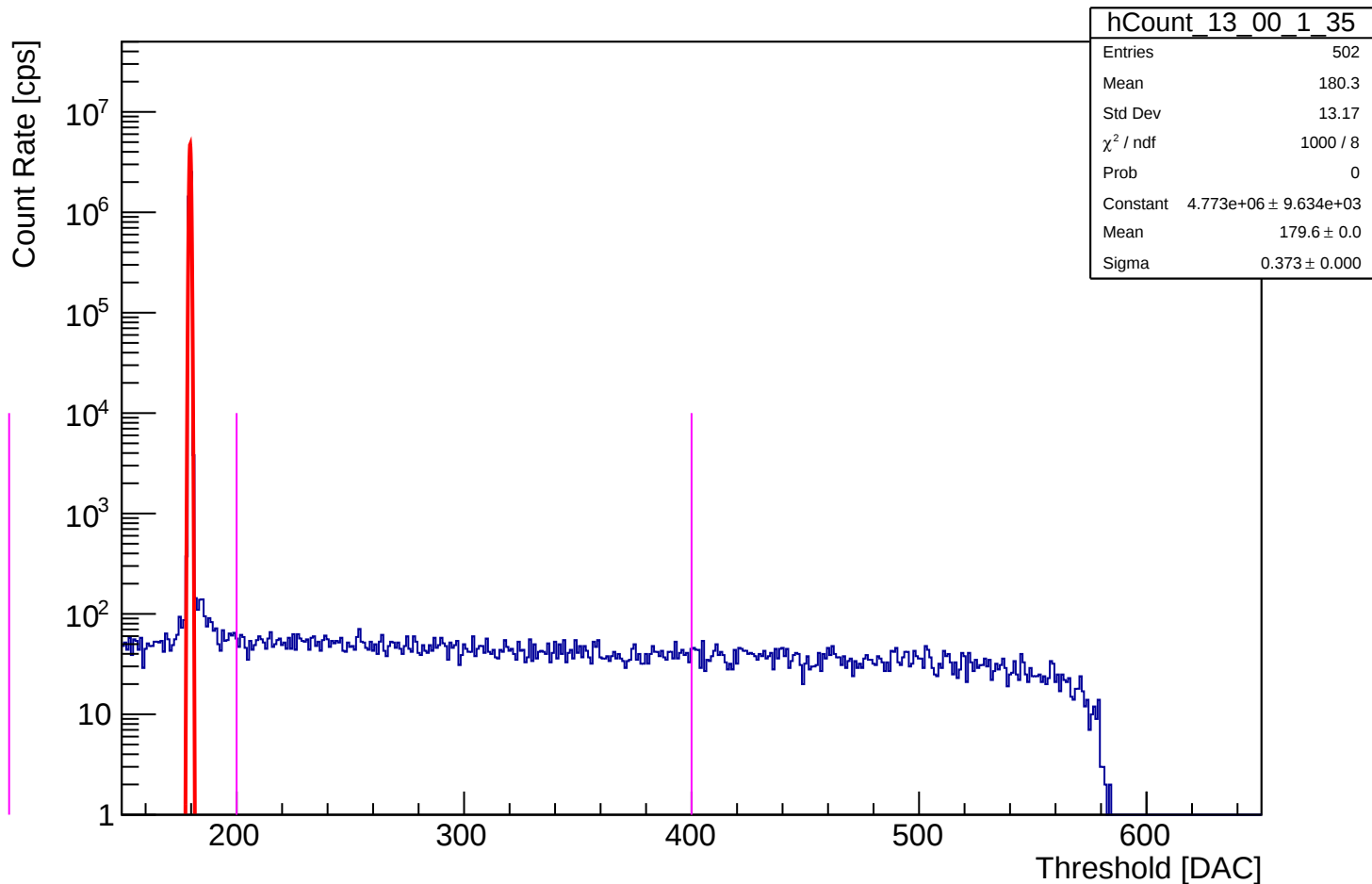
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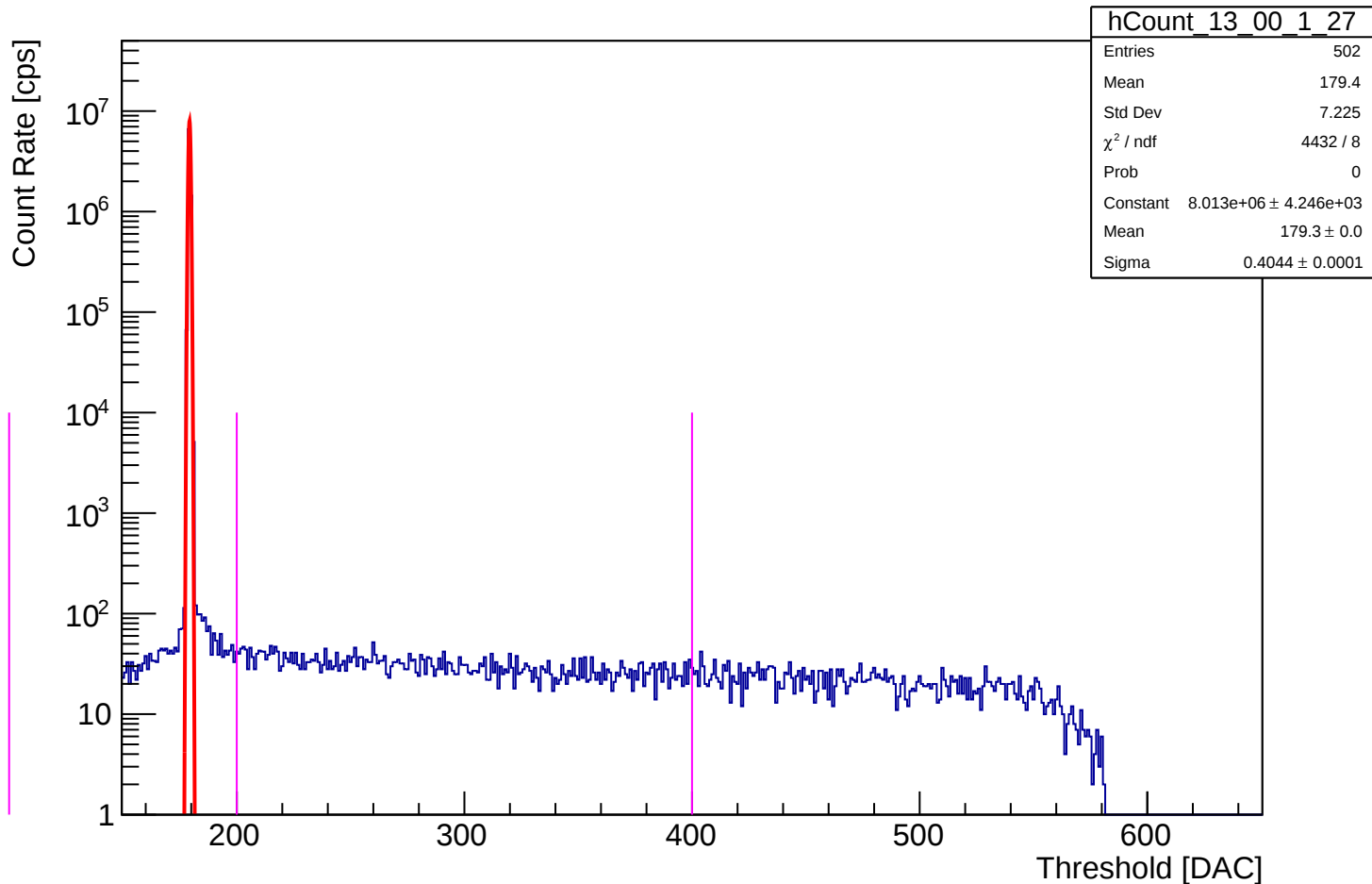
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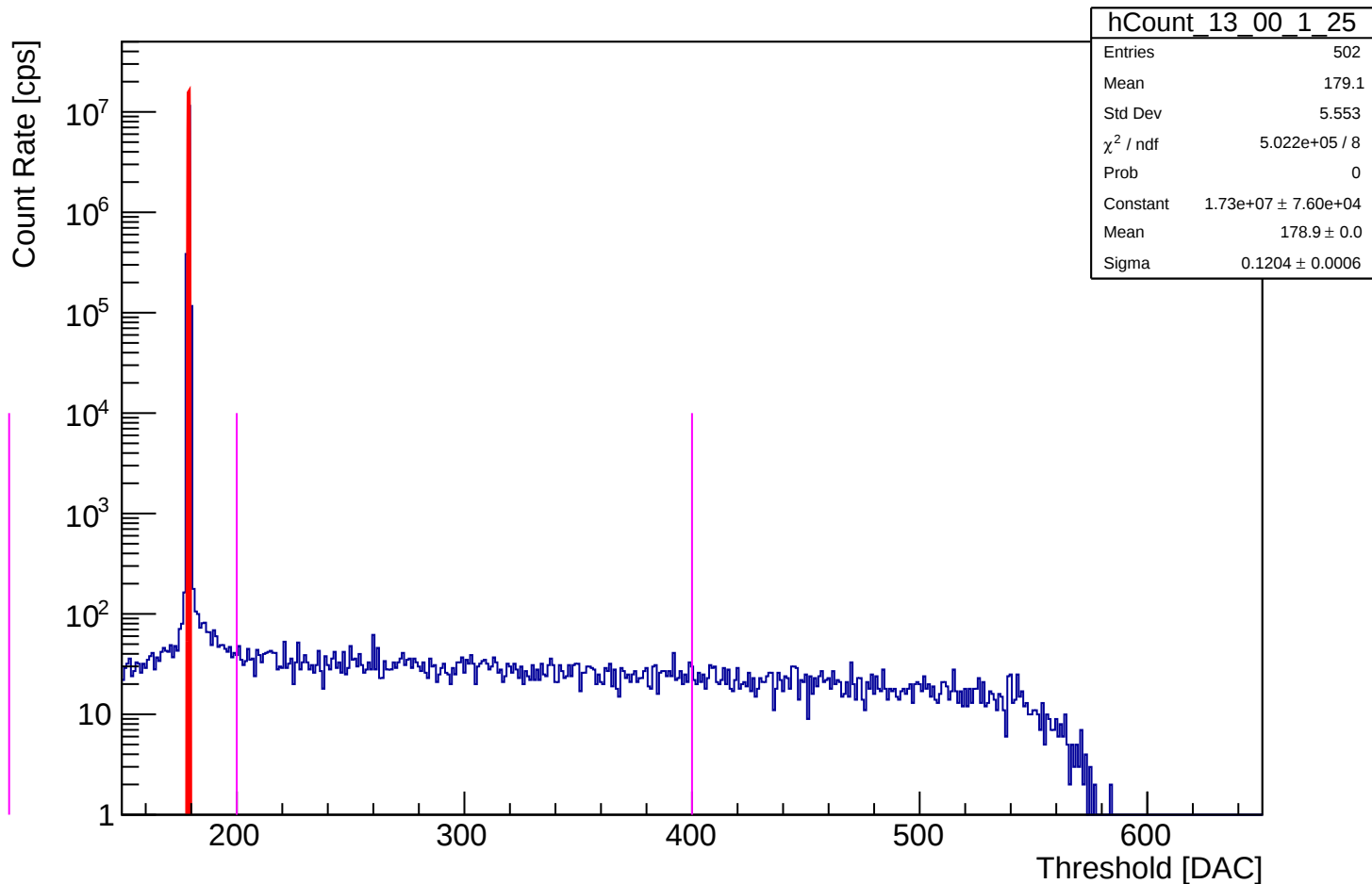
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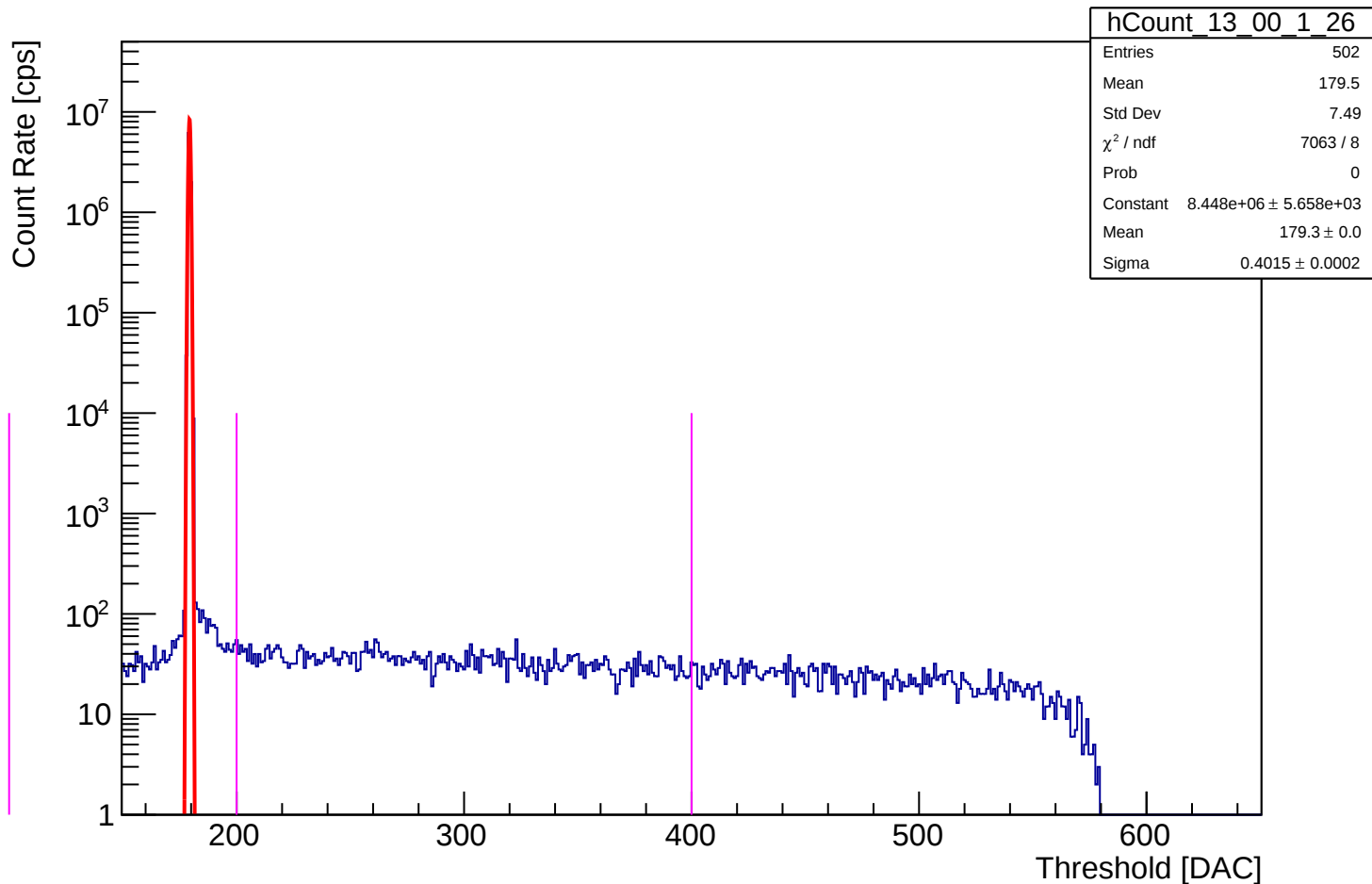
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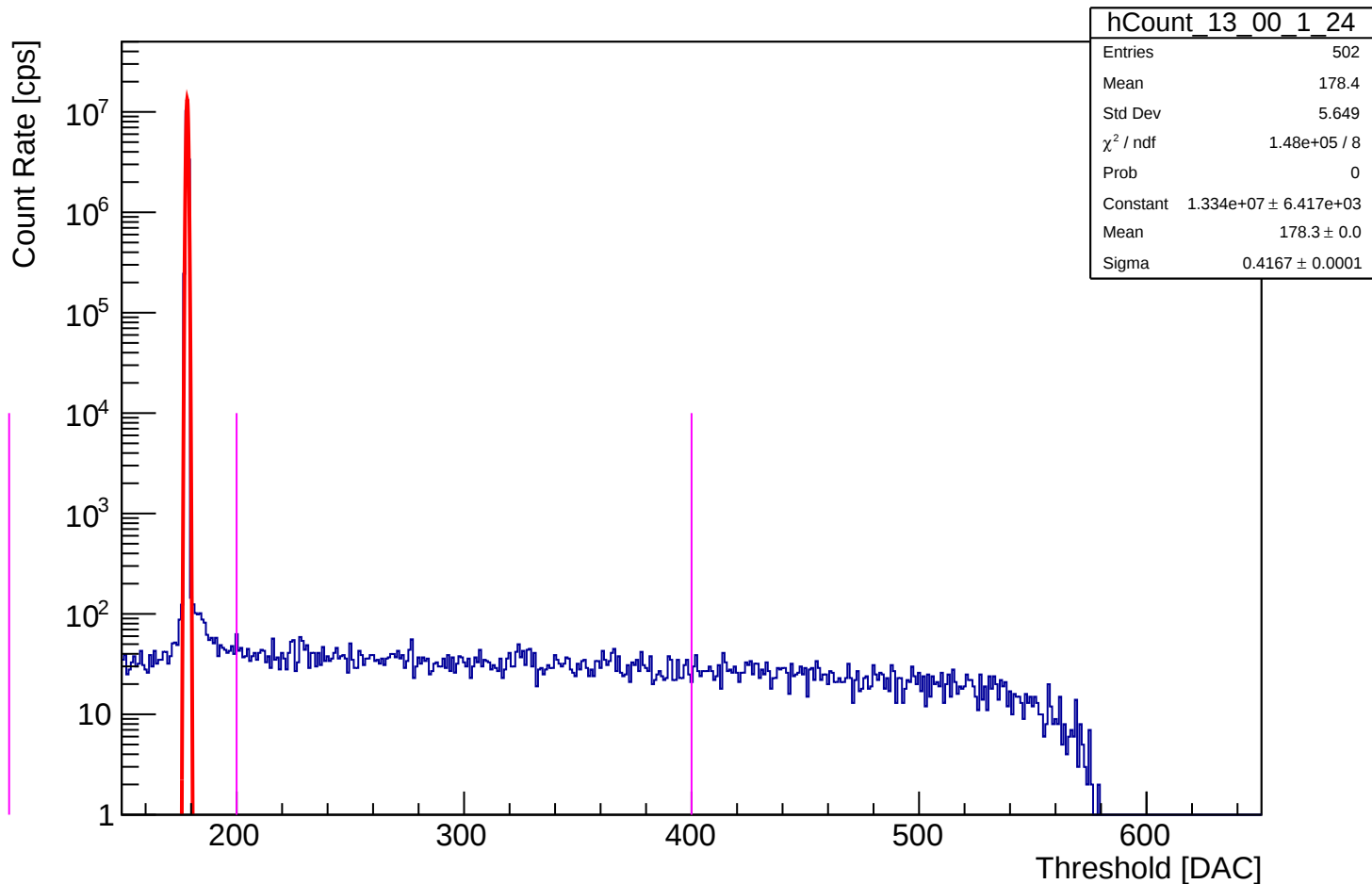
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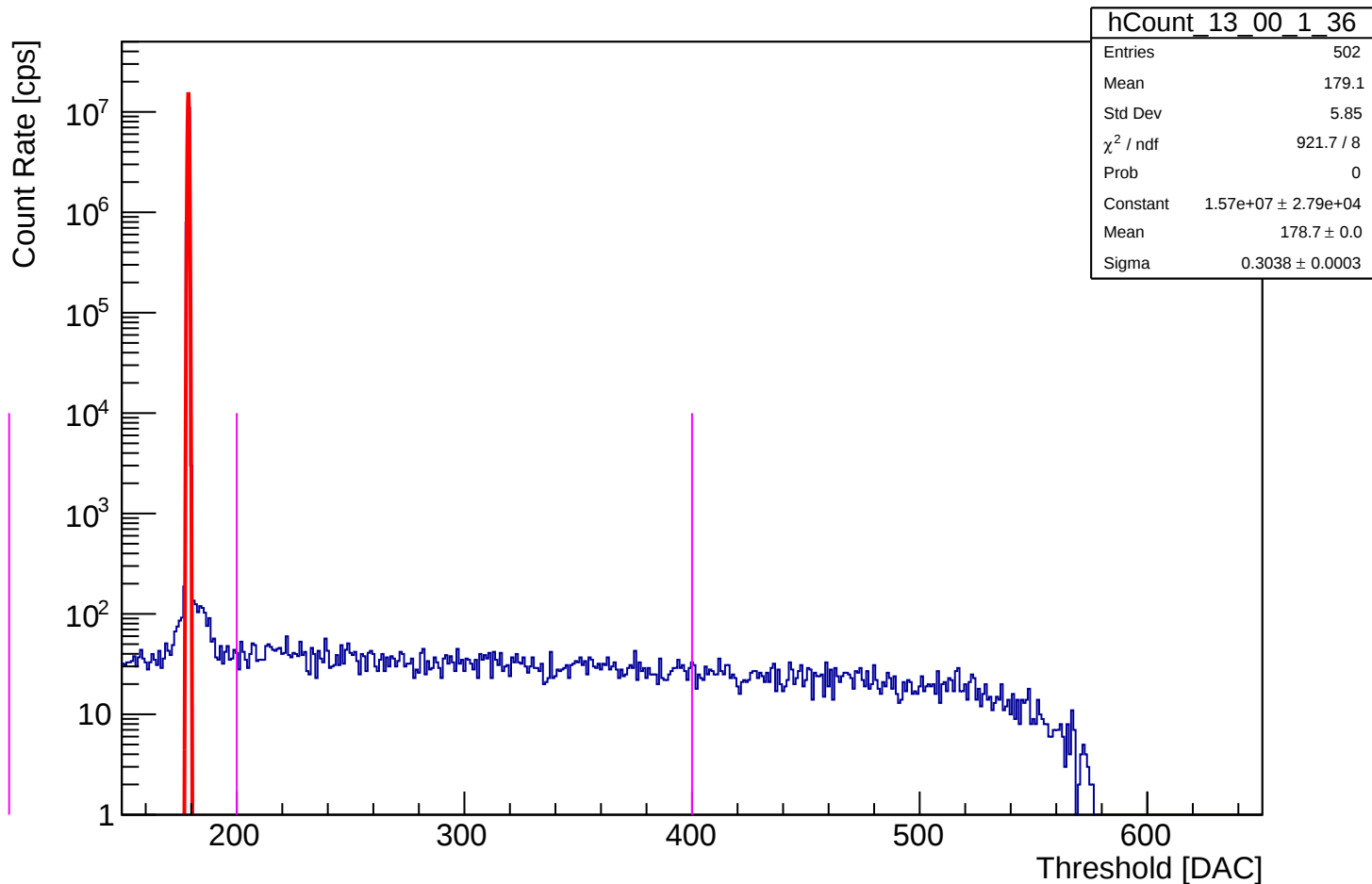
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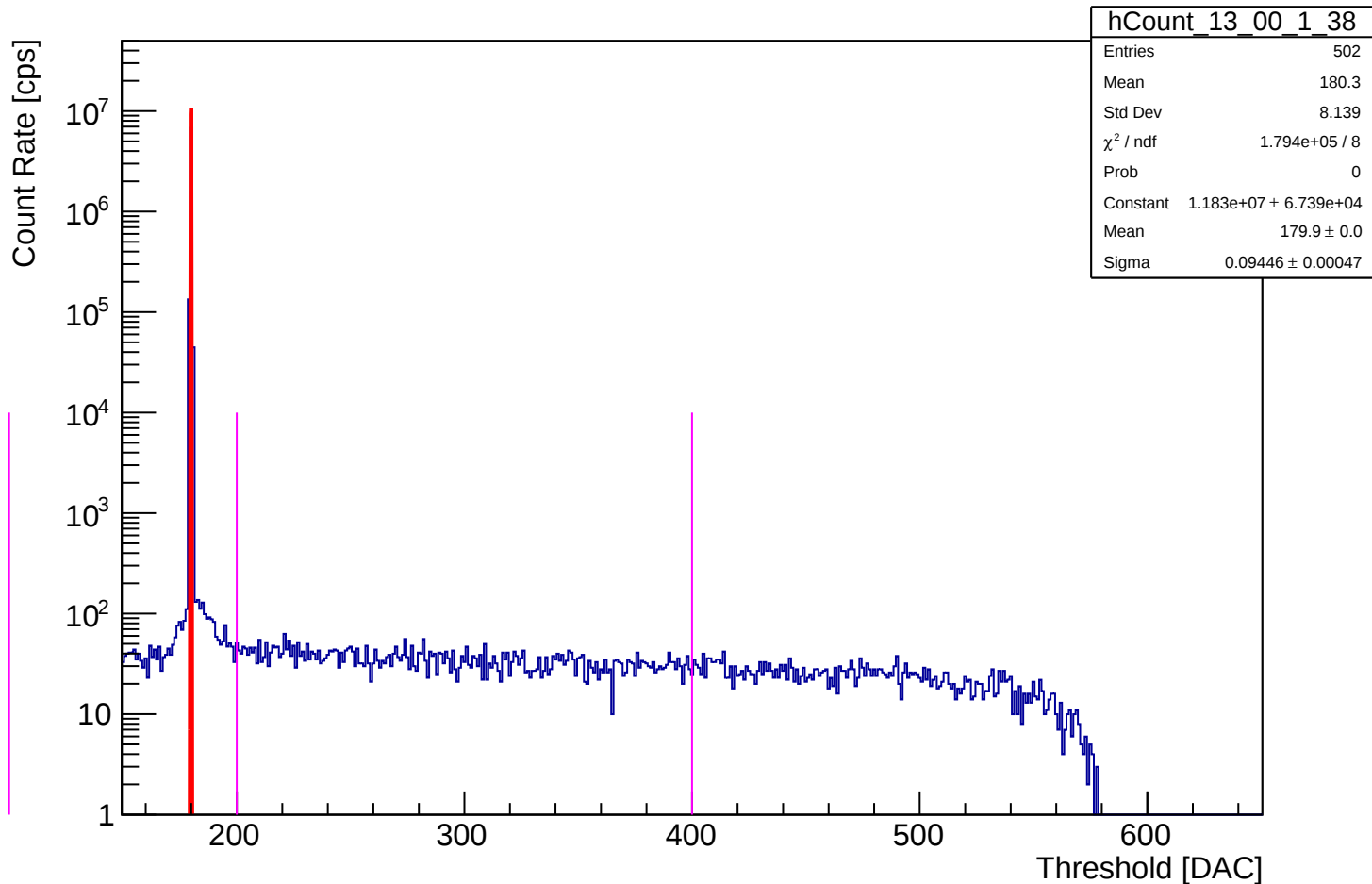
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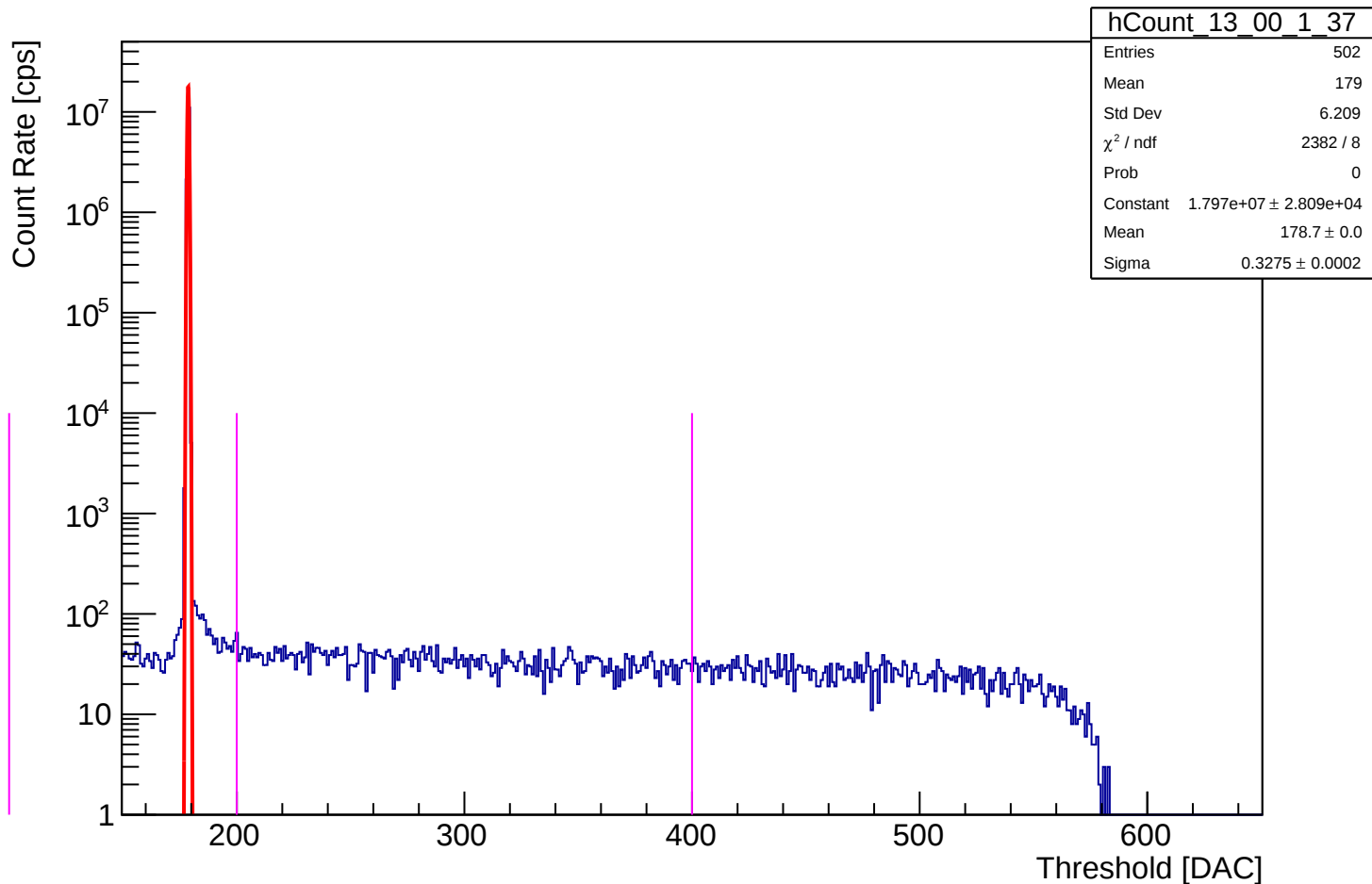
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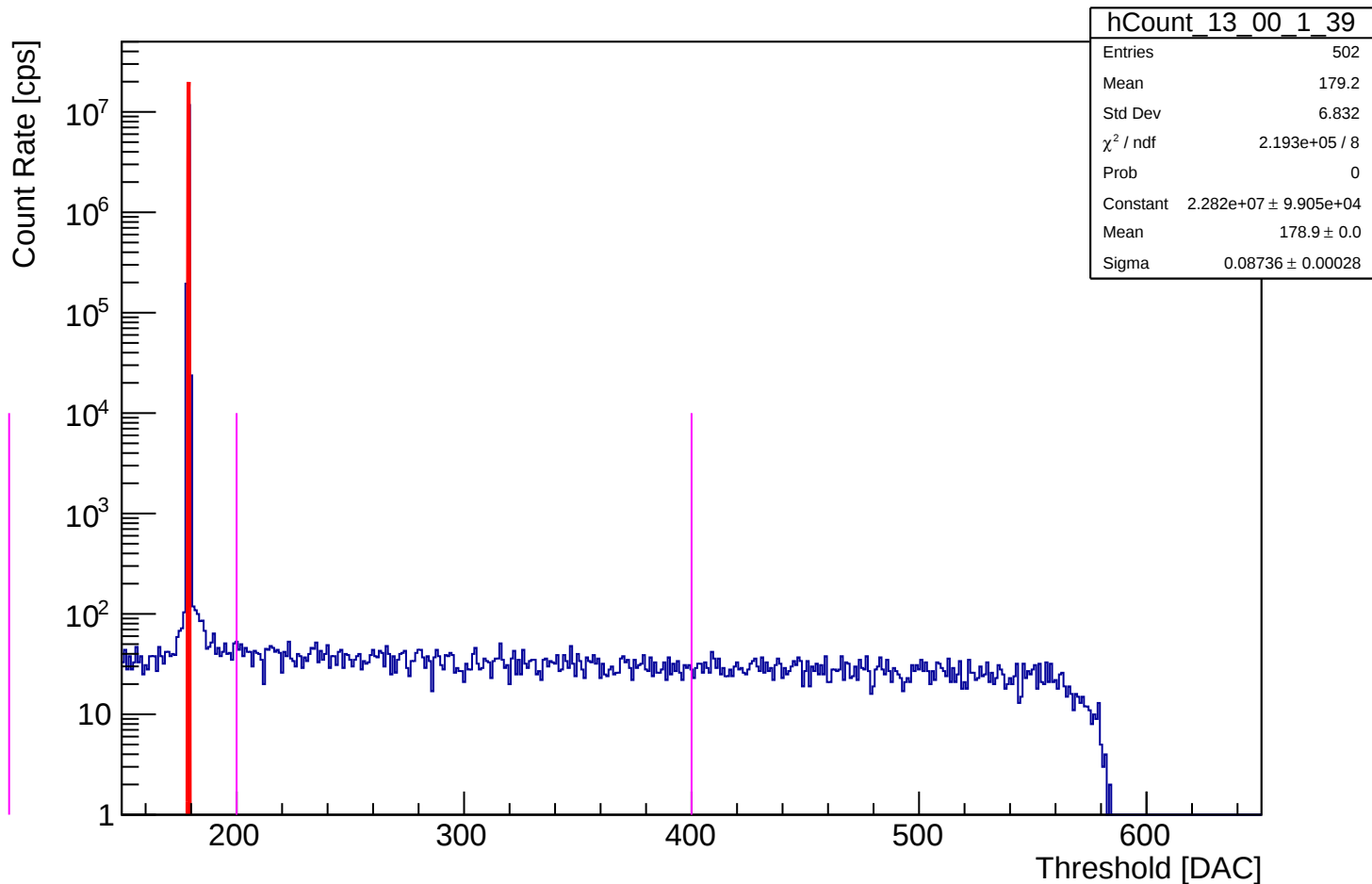
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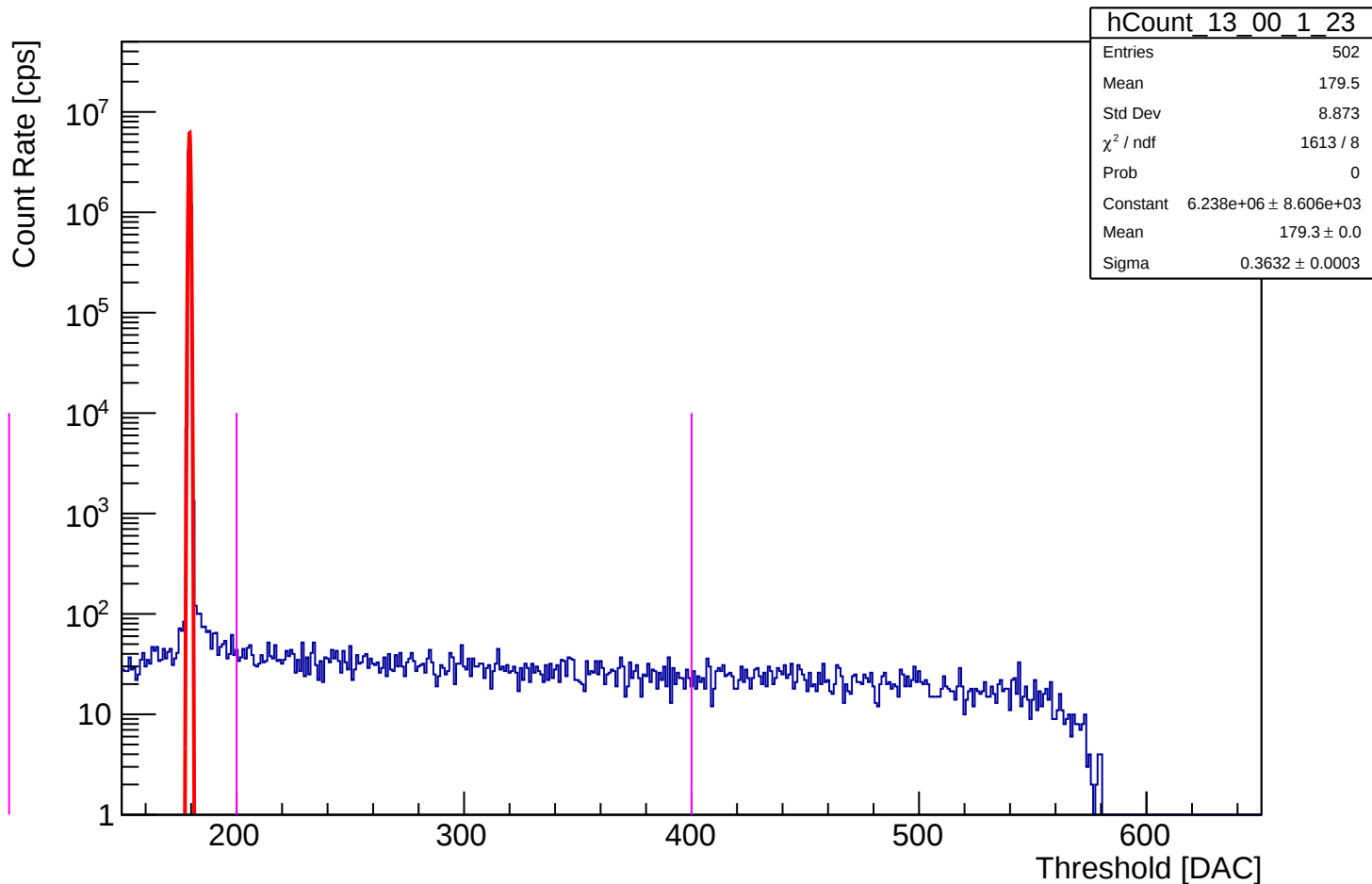
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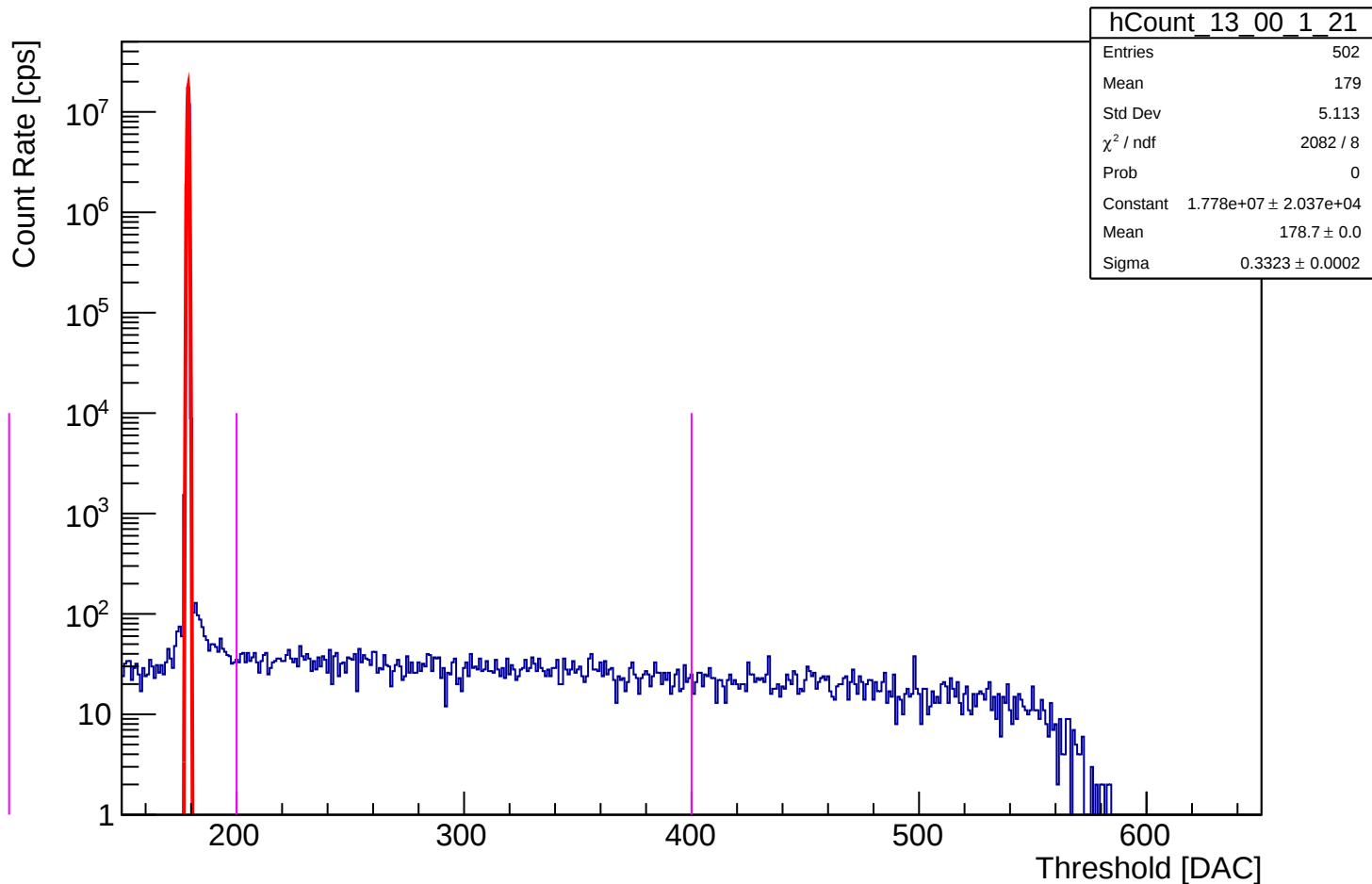
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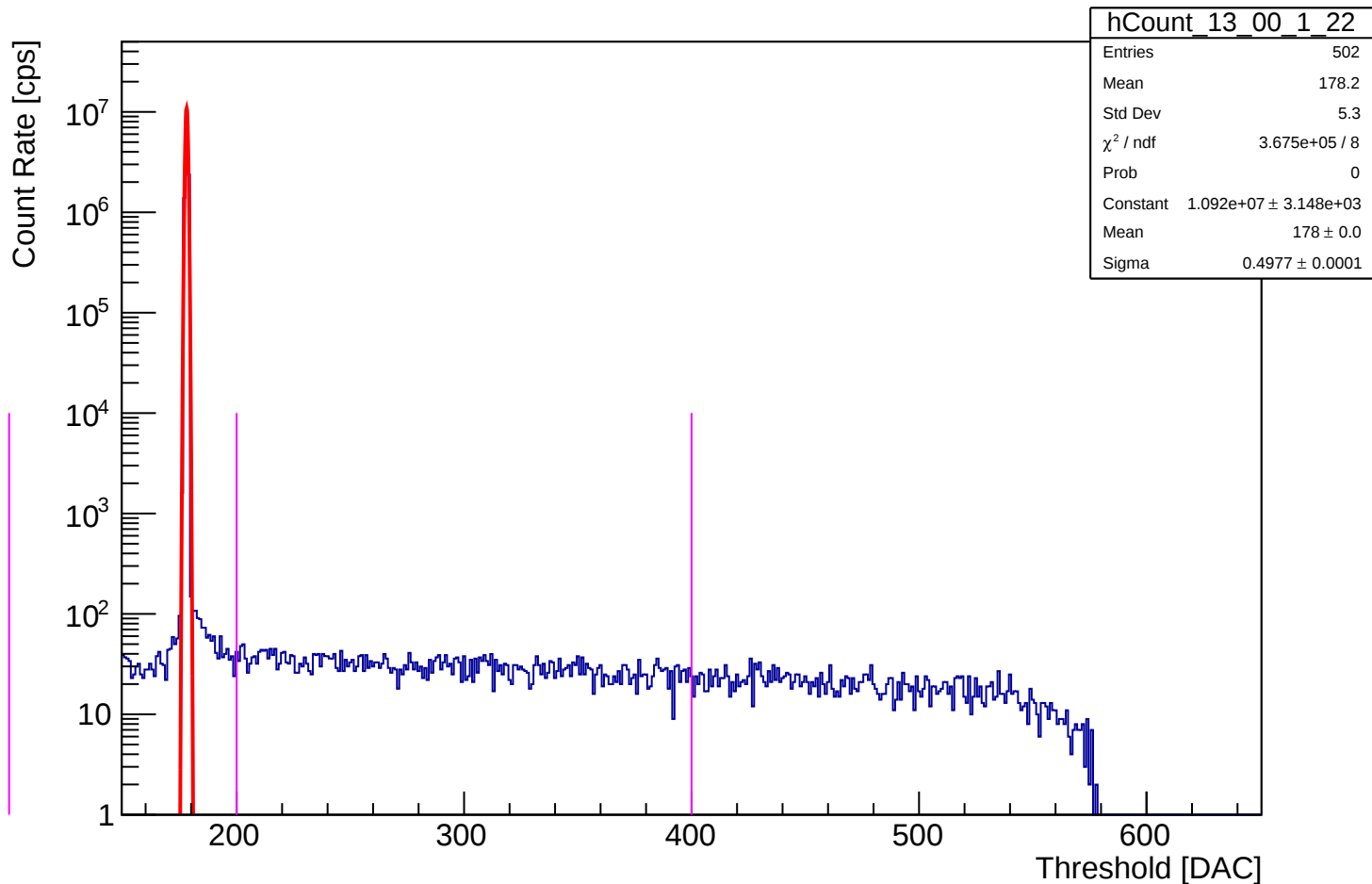
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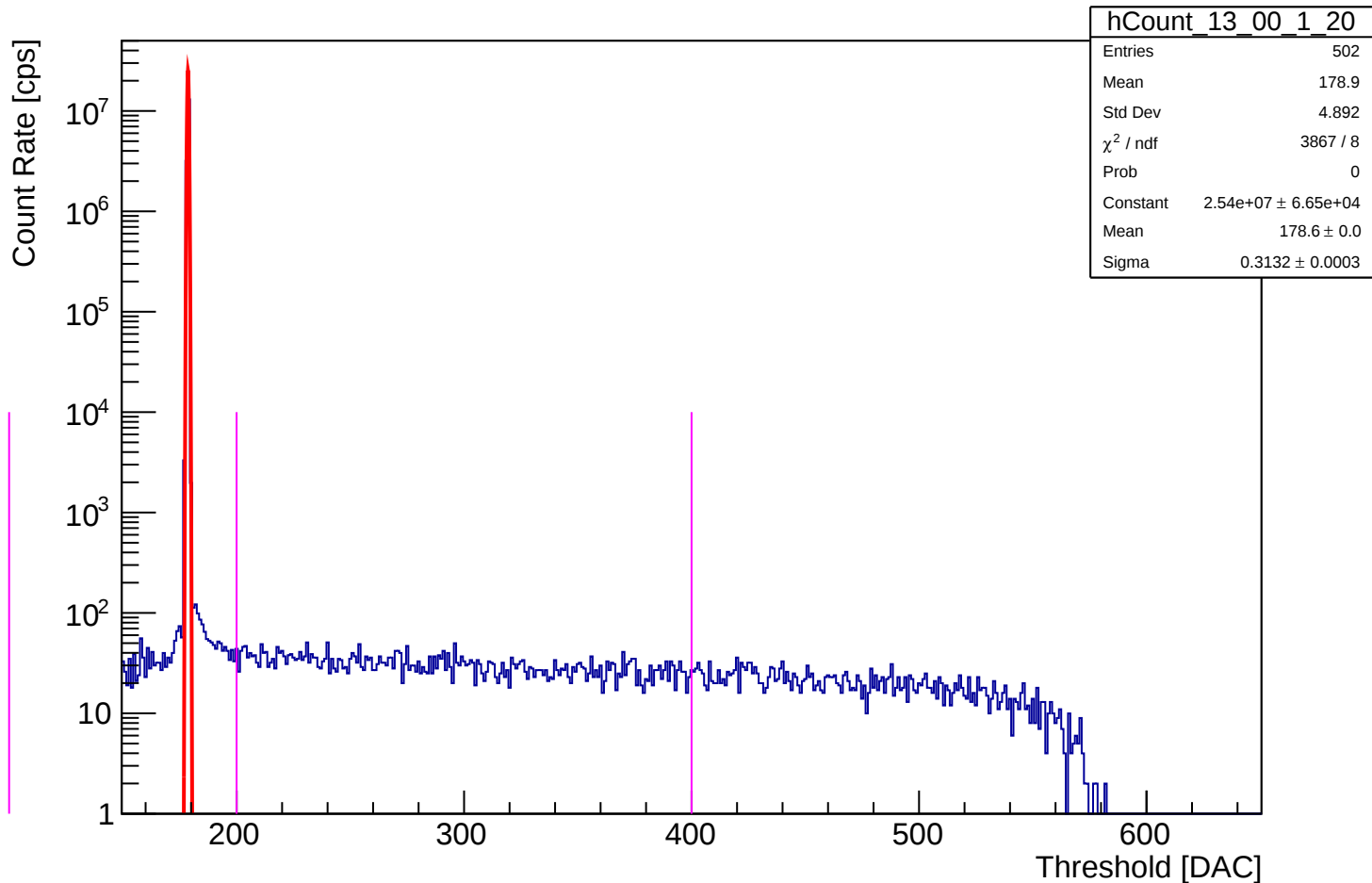
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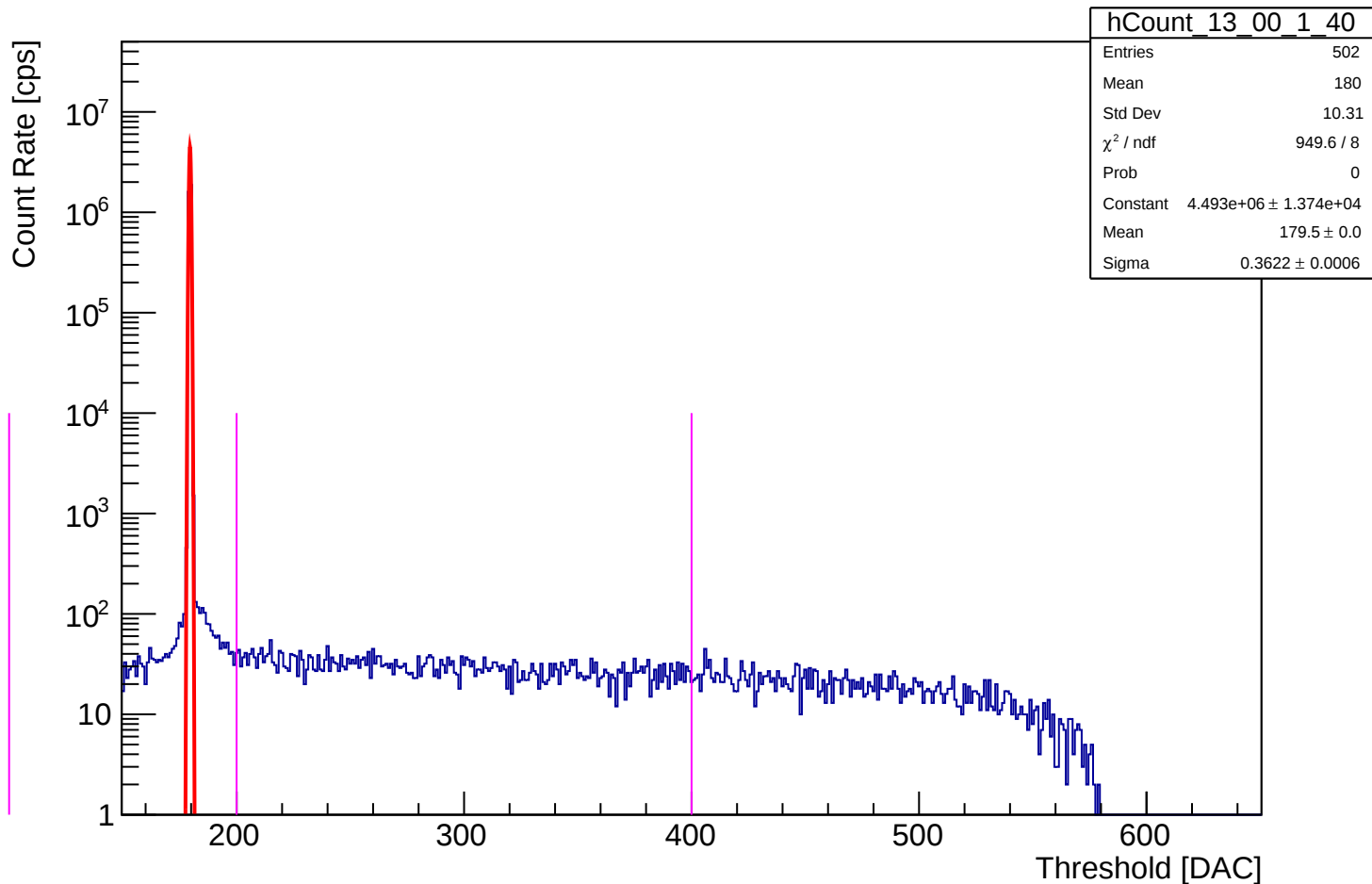
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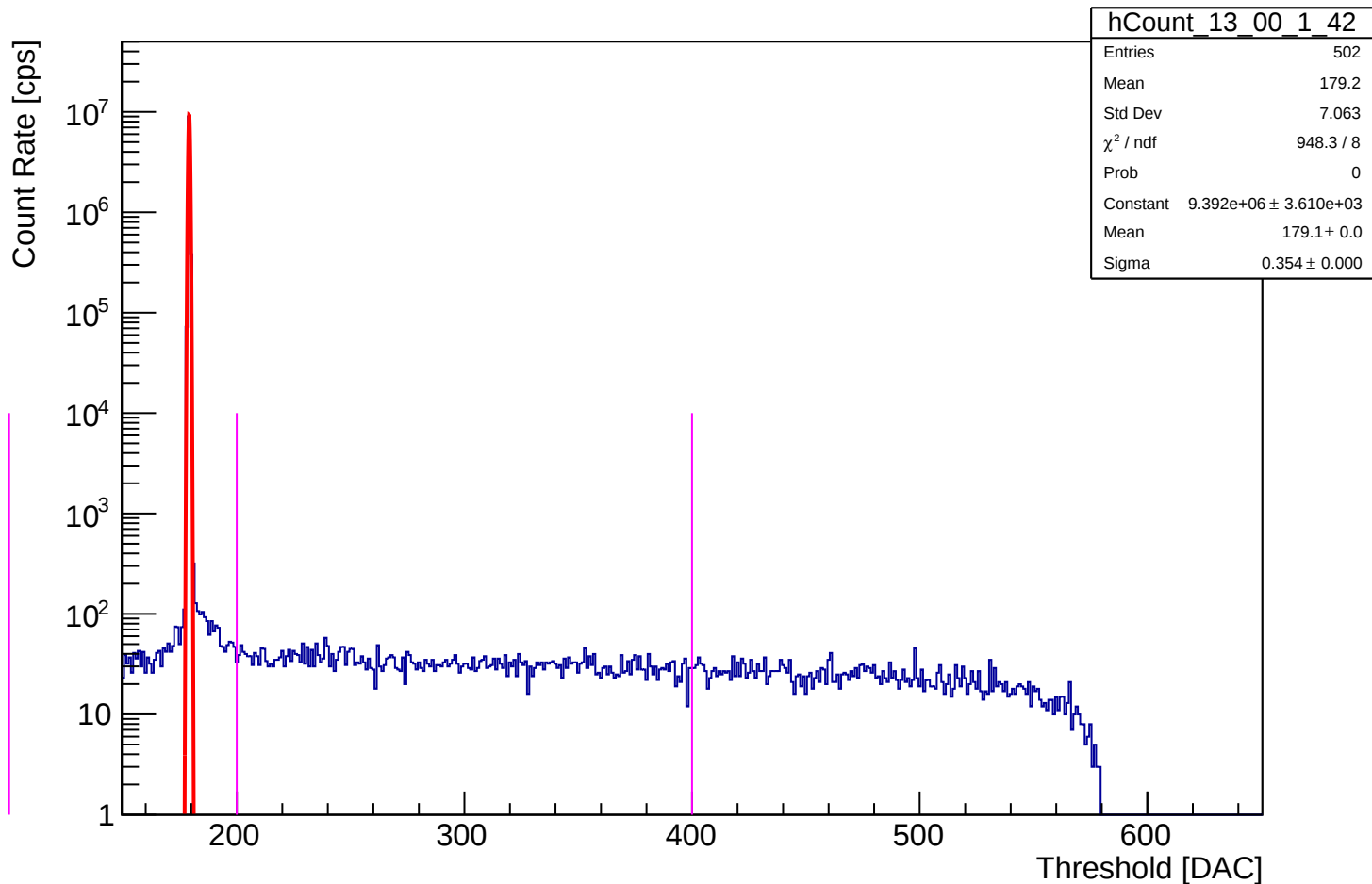
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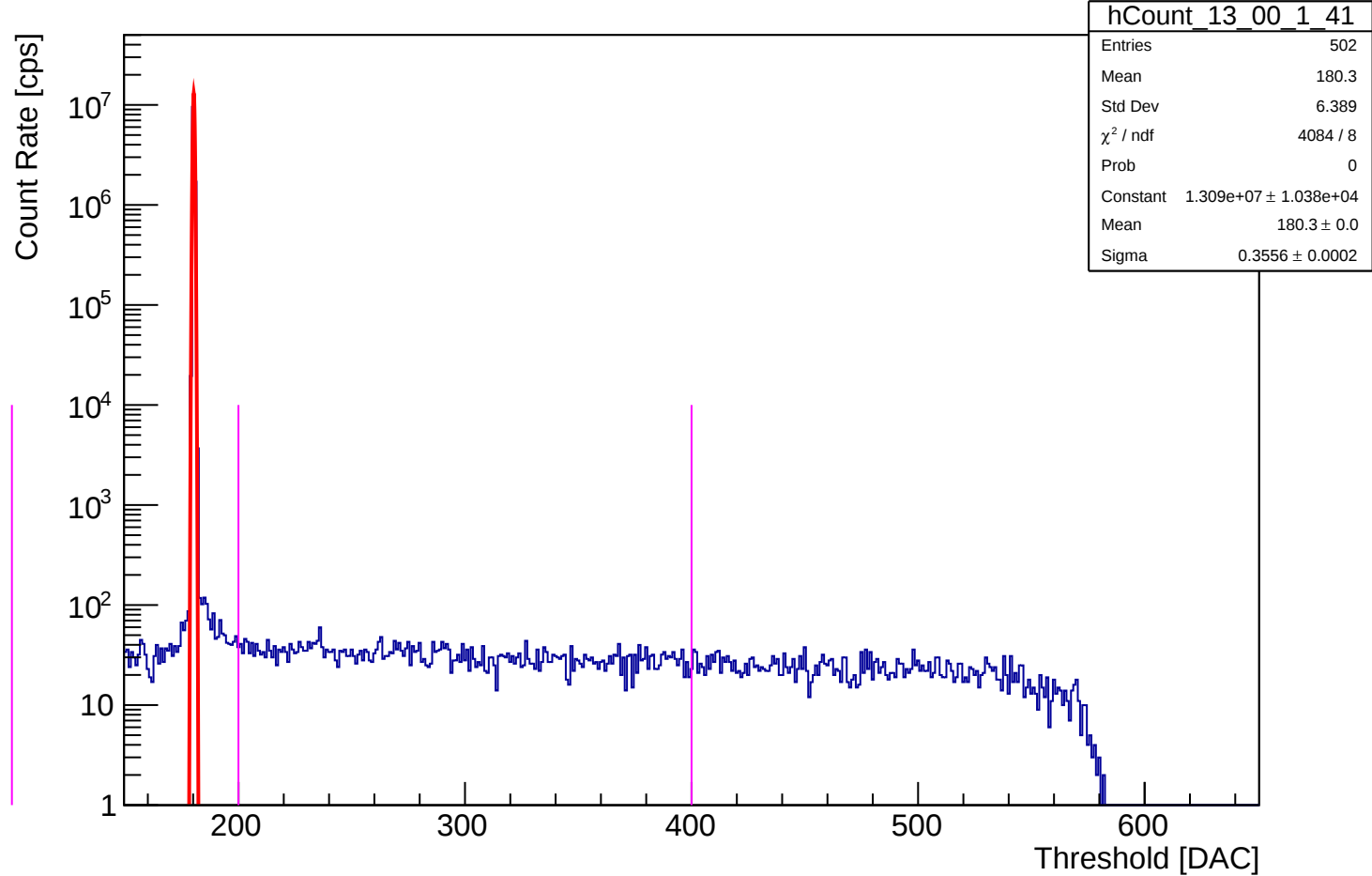


Row 1 Tile 1 Pmt 2 Pixel 21 Slot 13 Fiber 0 Asic 1 Channel 40

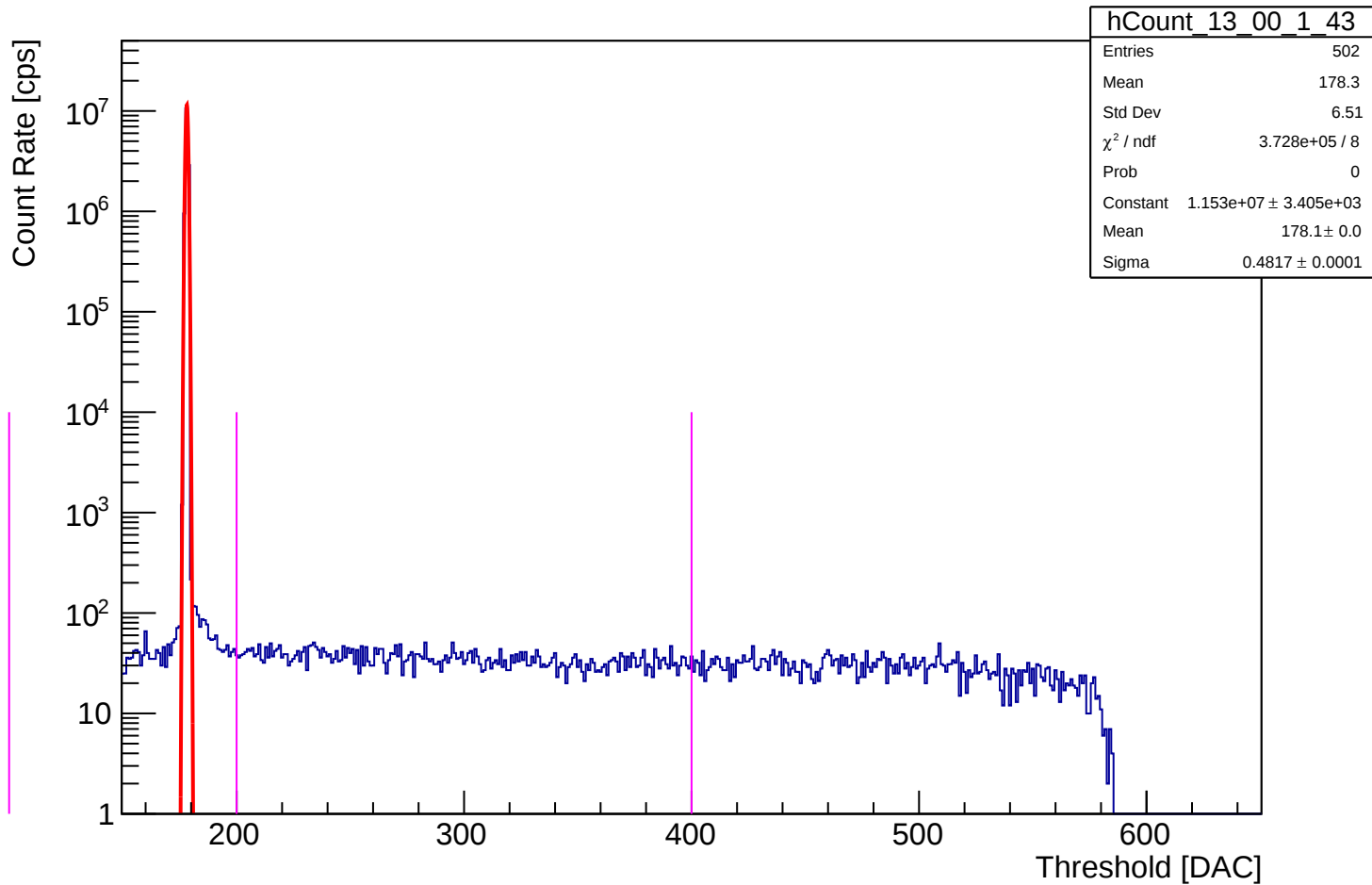


Row 1 Tile 1 Pmt 2 Pixel 22 Slot 13 Fiber 0 Asic 1 Channel 42

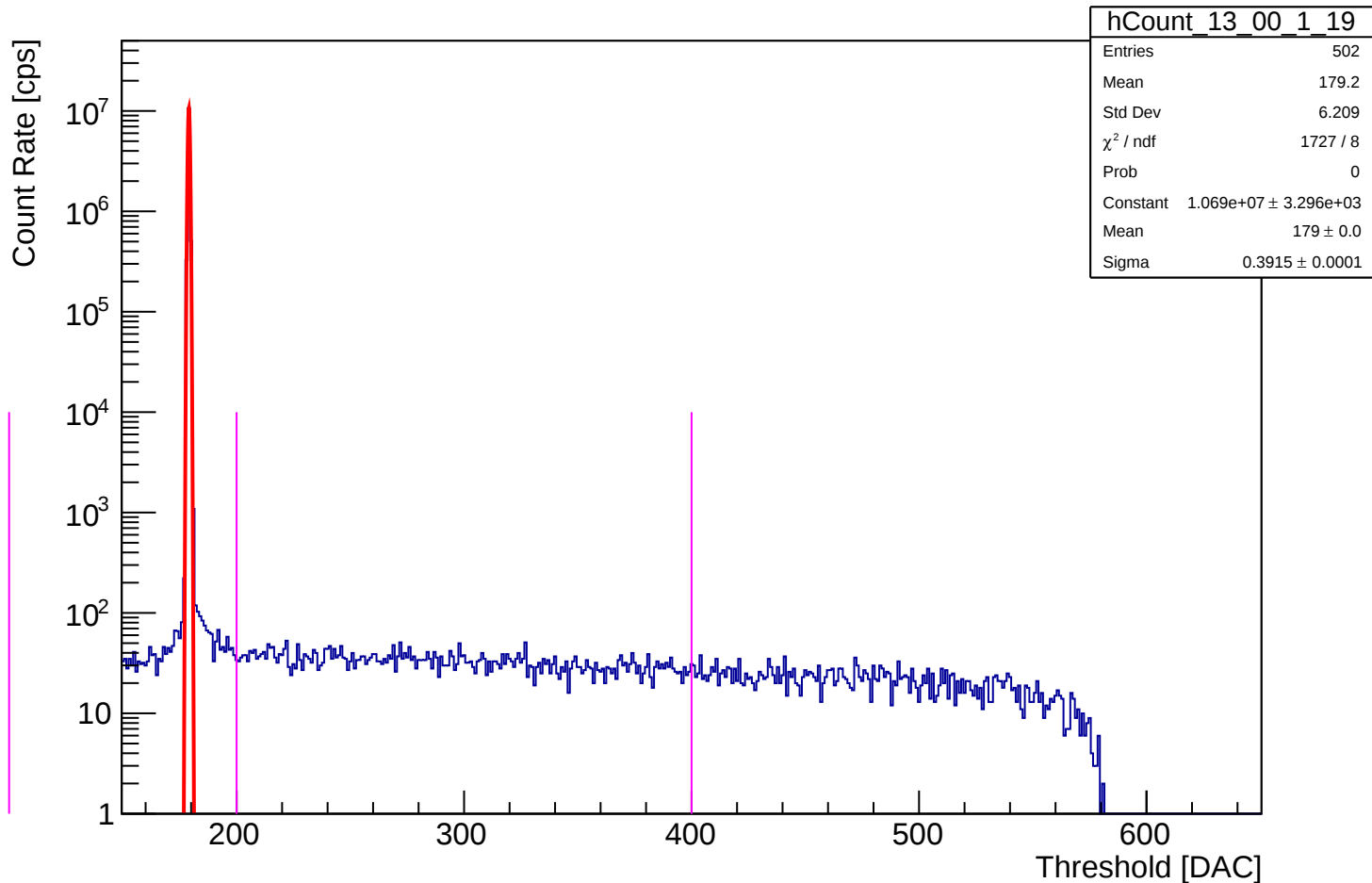




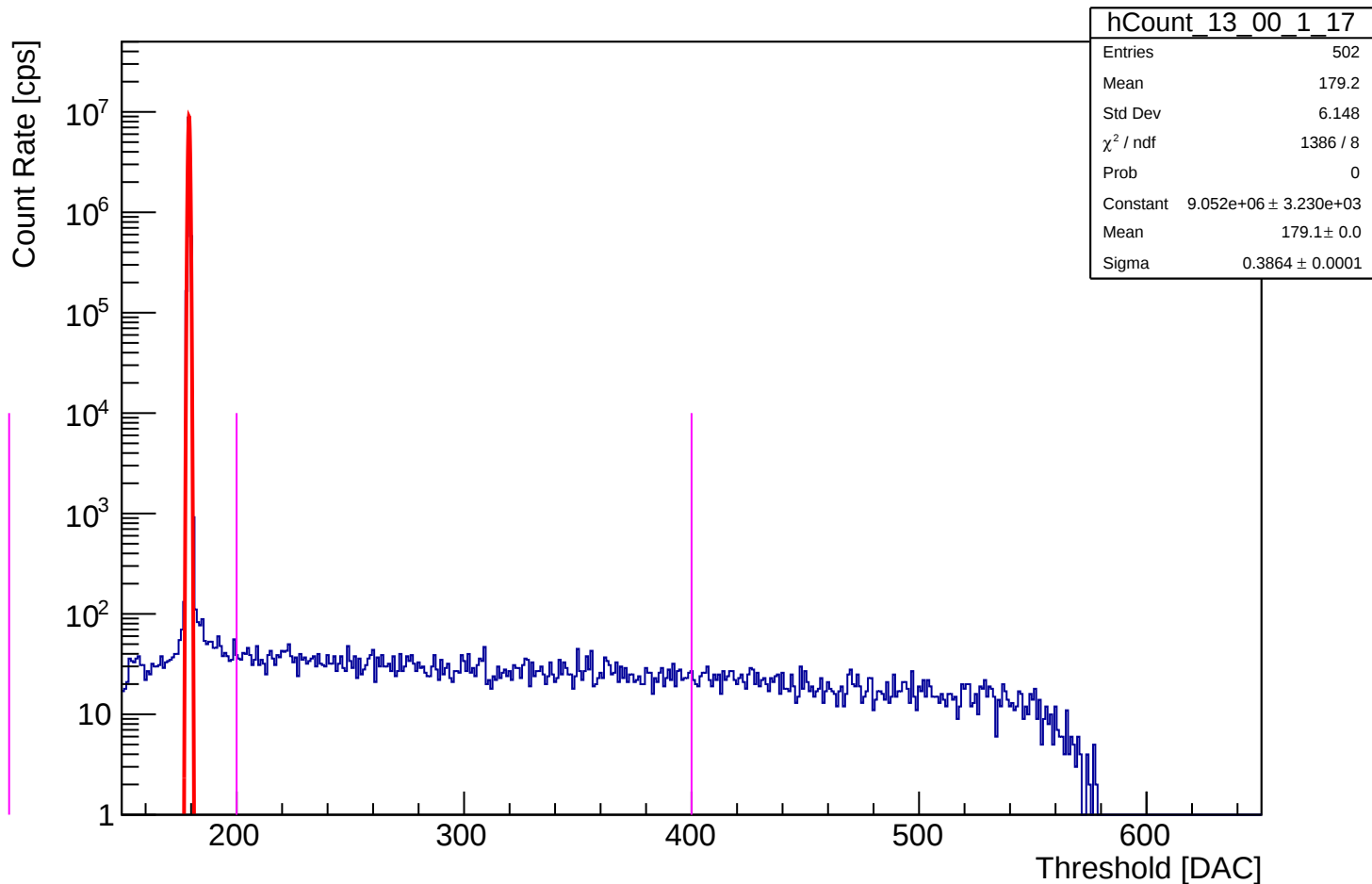
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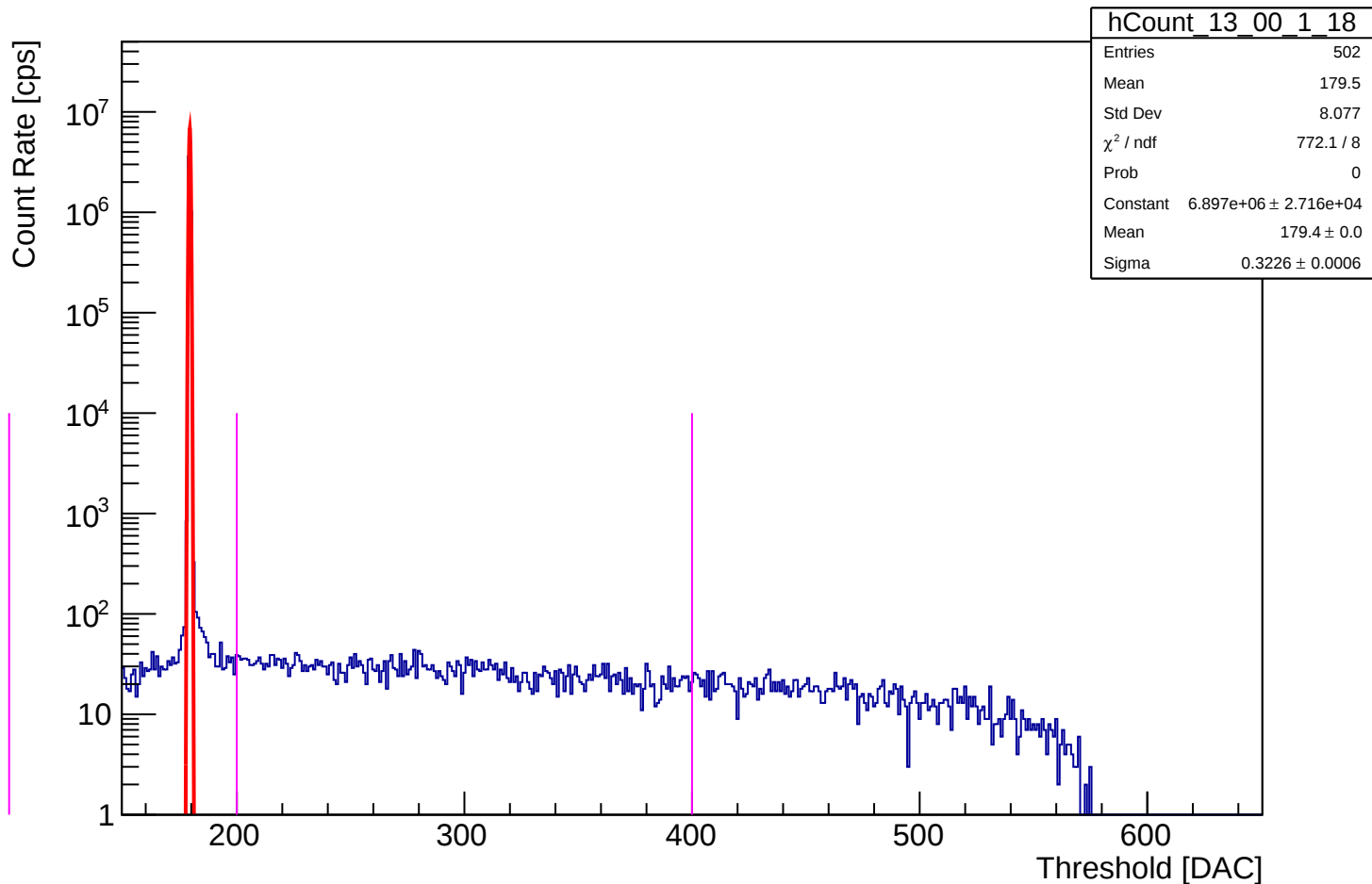
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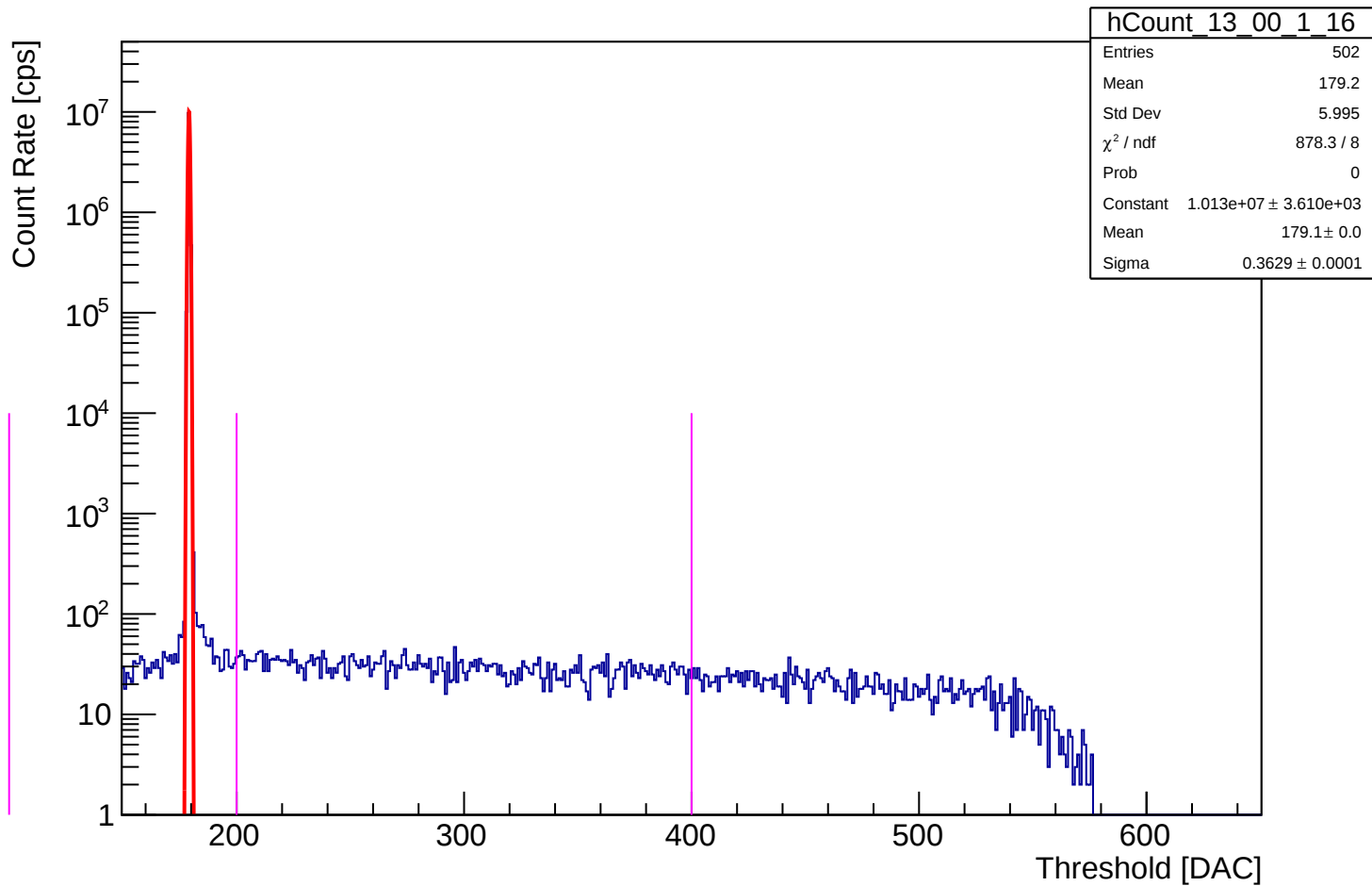
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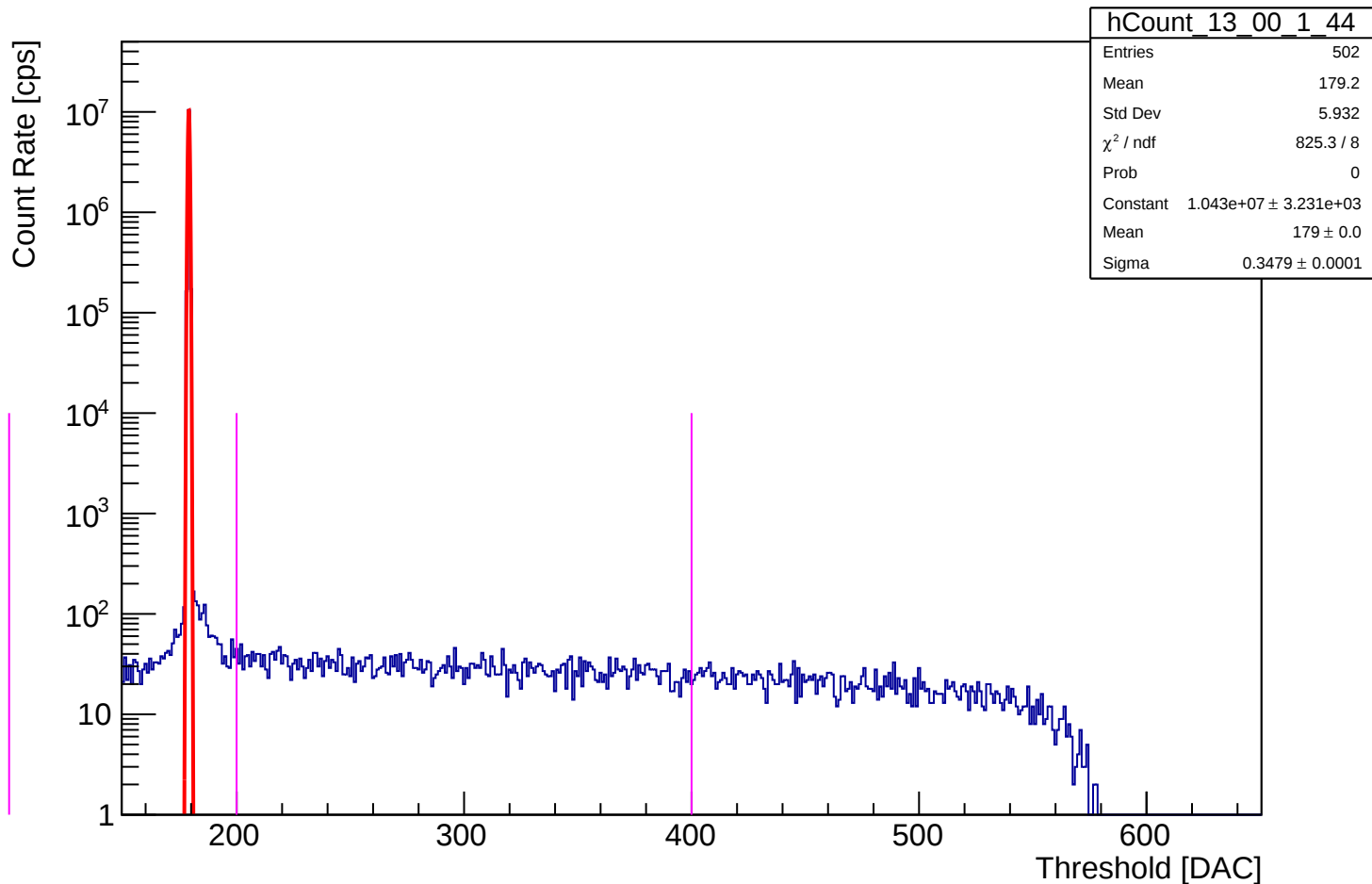
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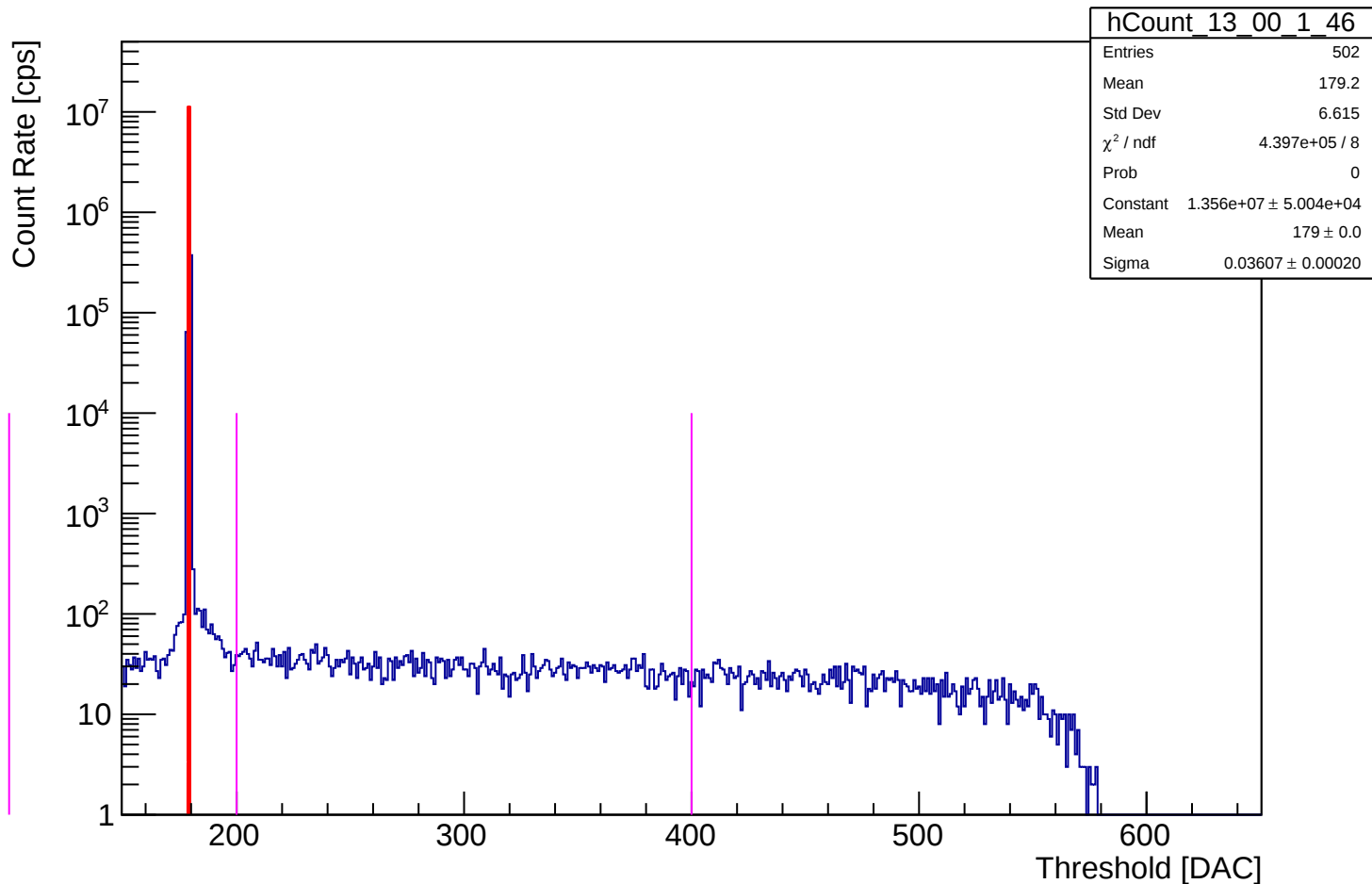
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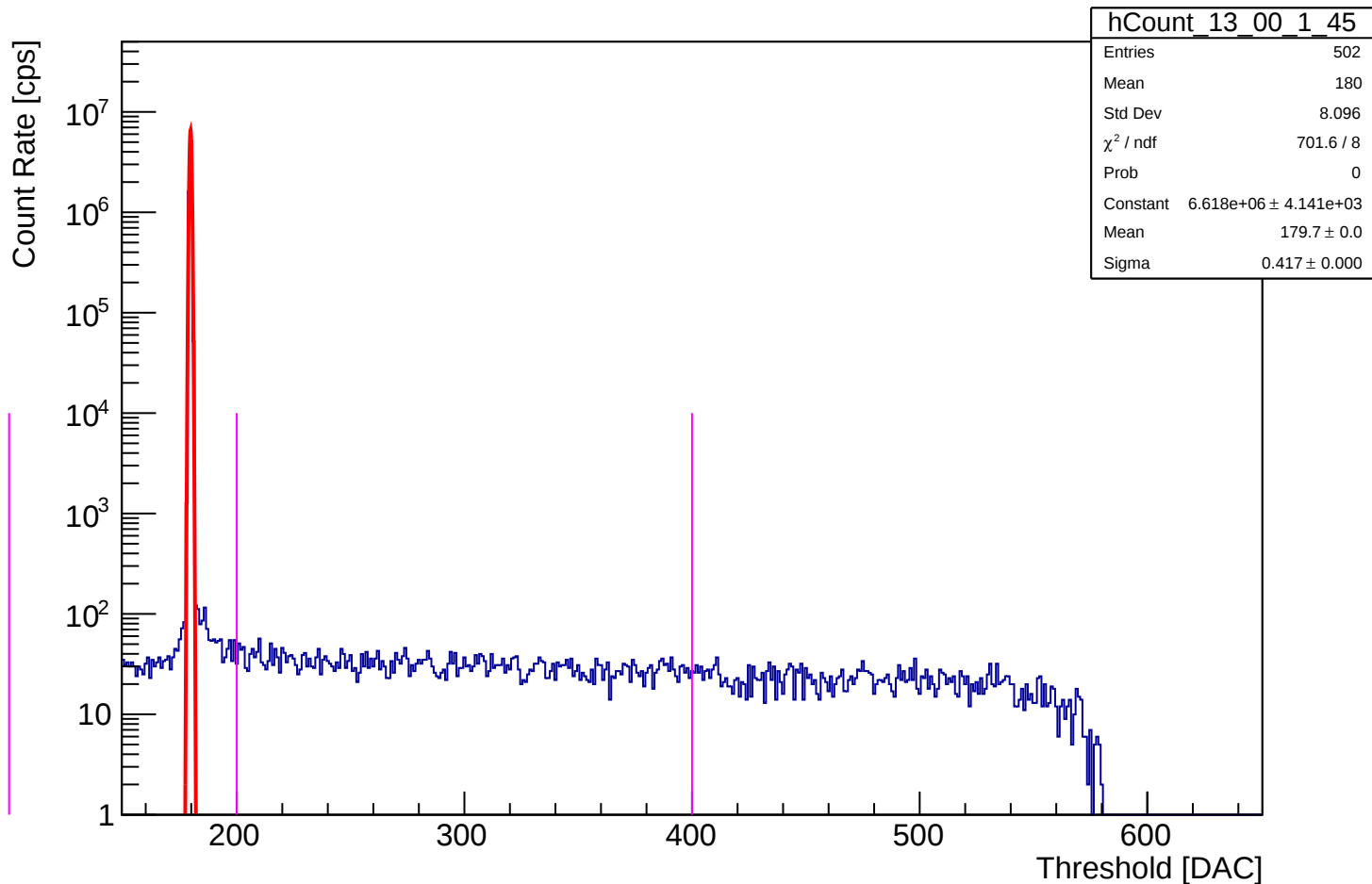
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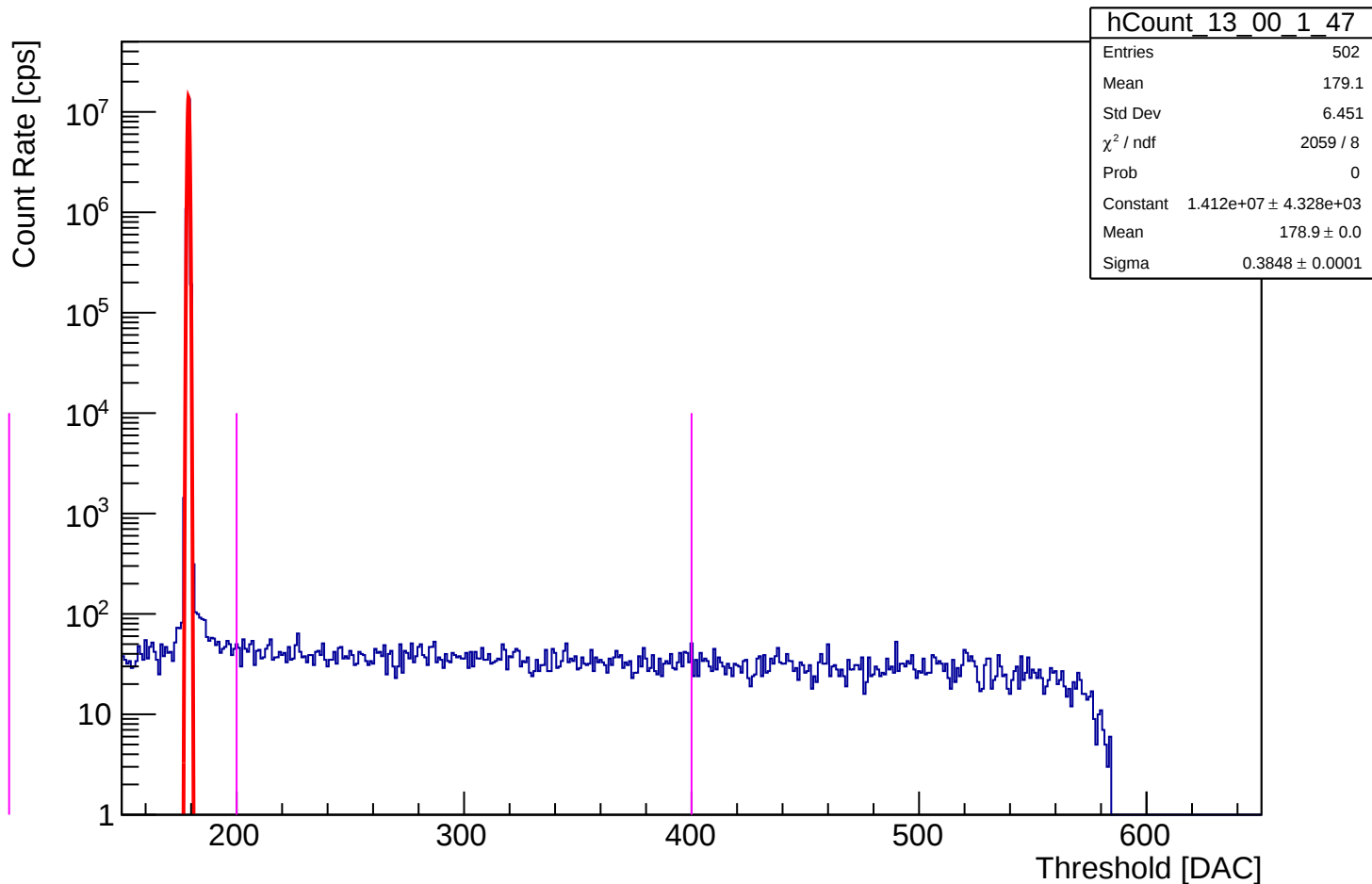
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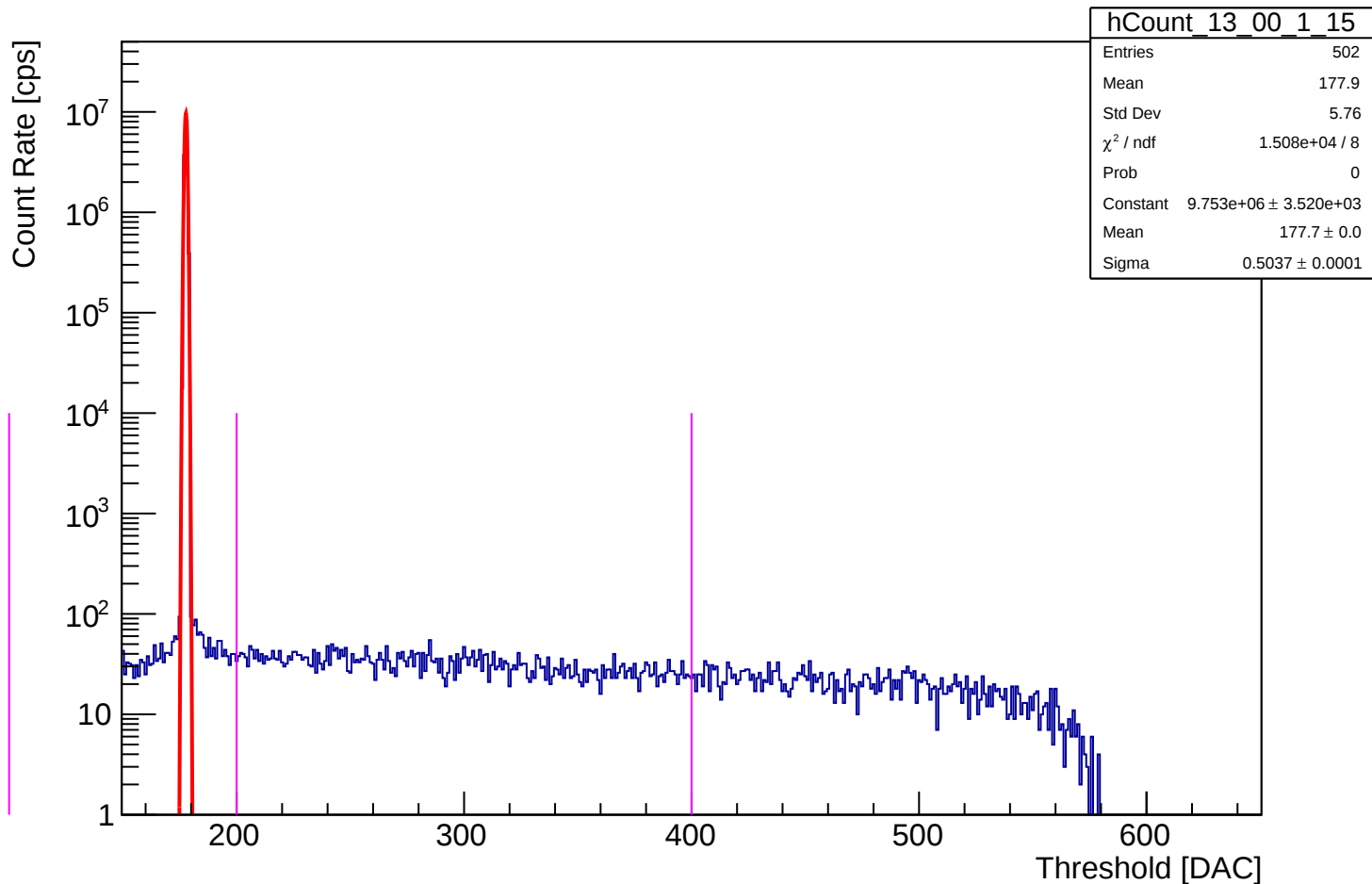
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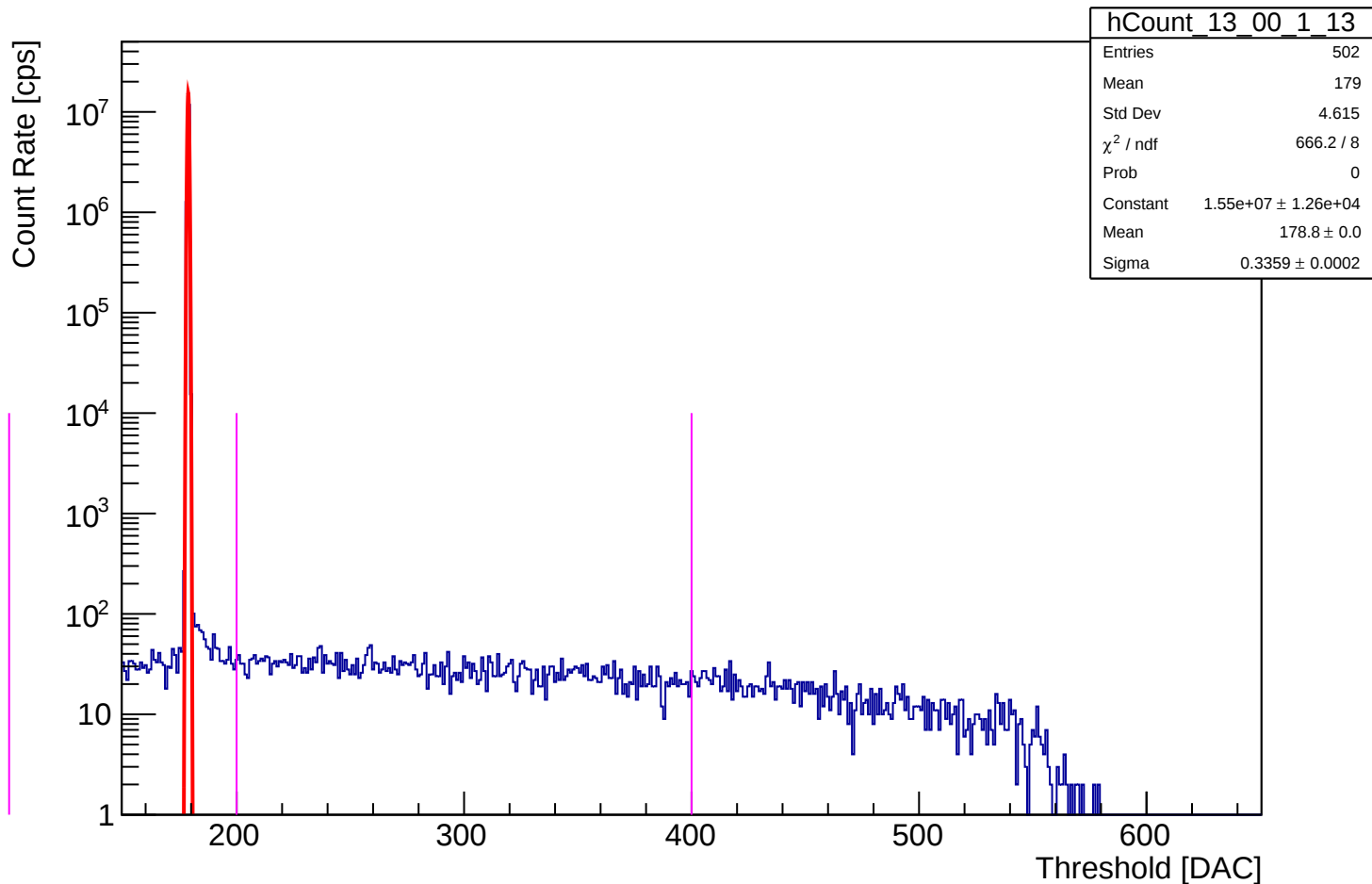
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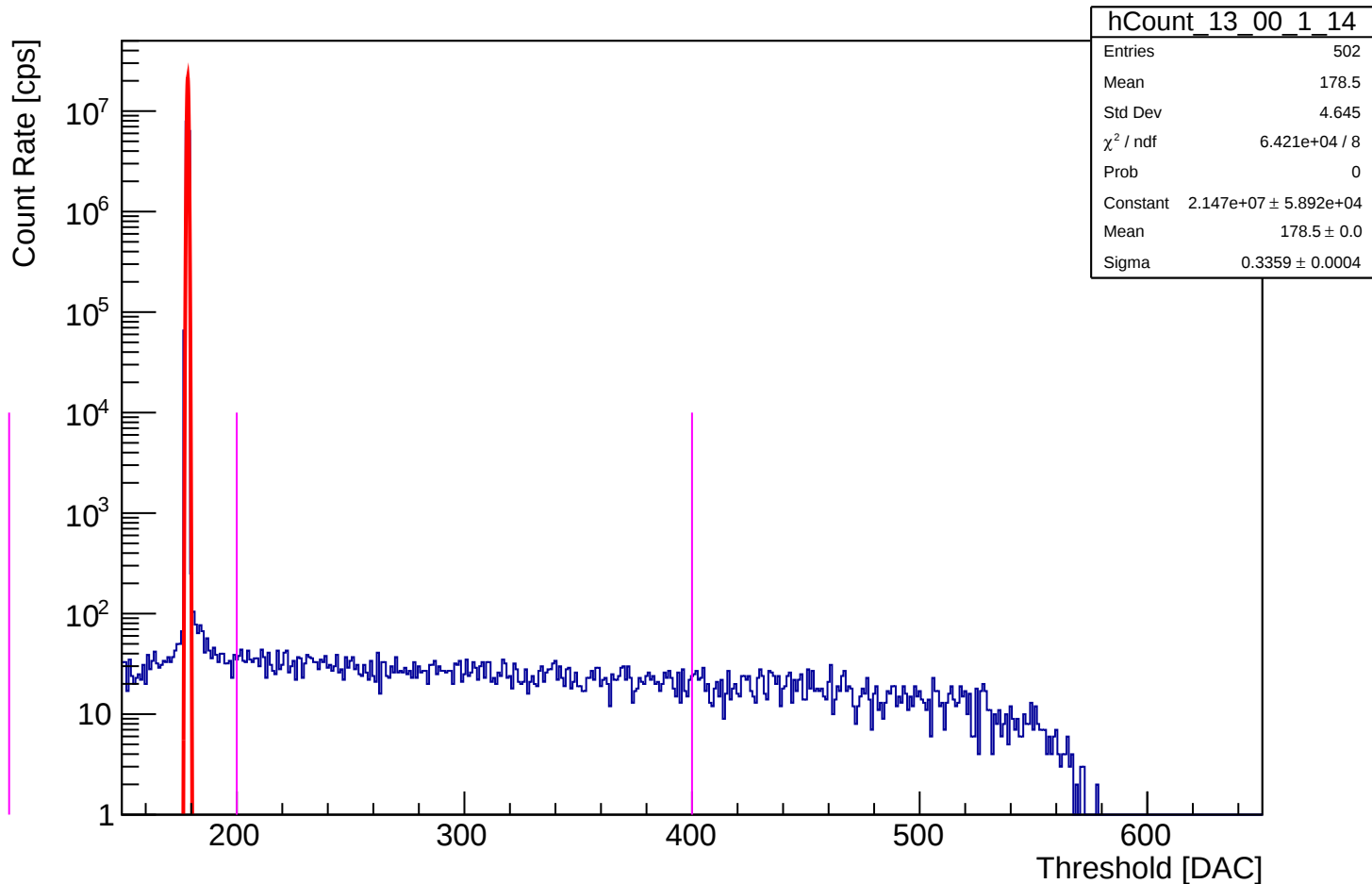
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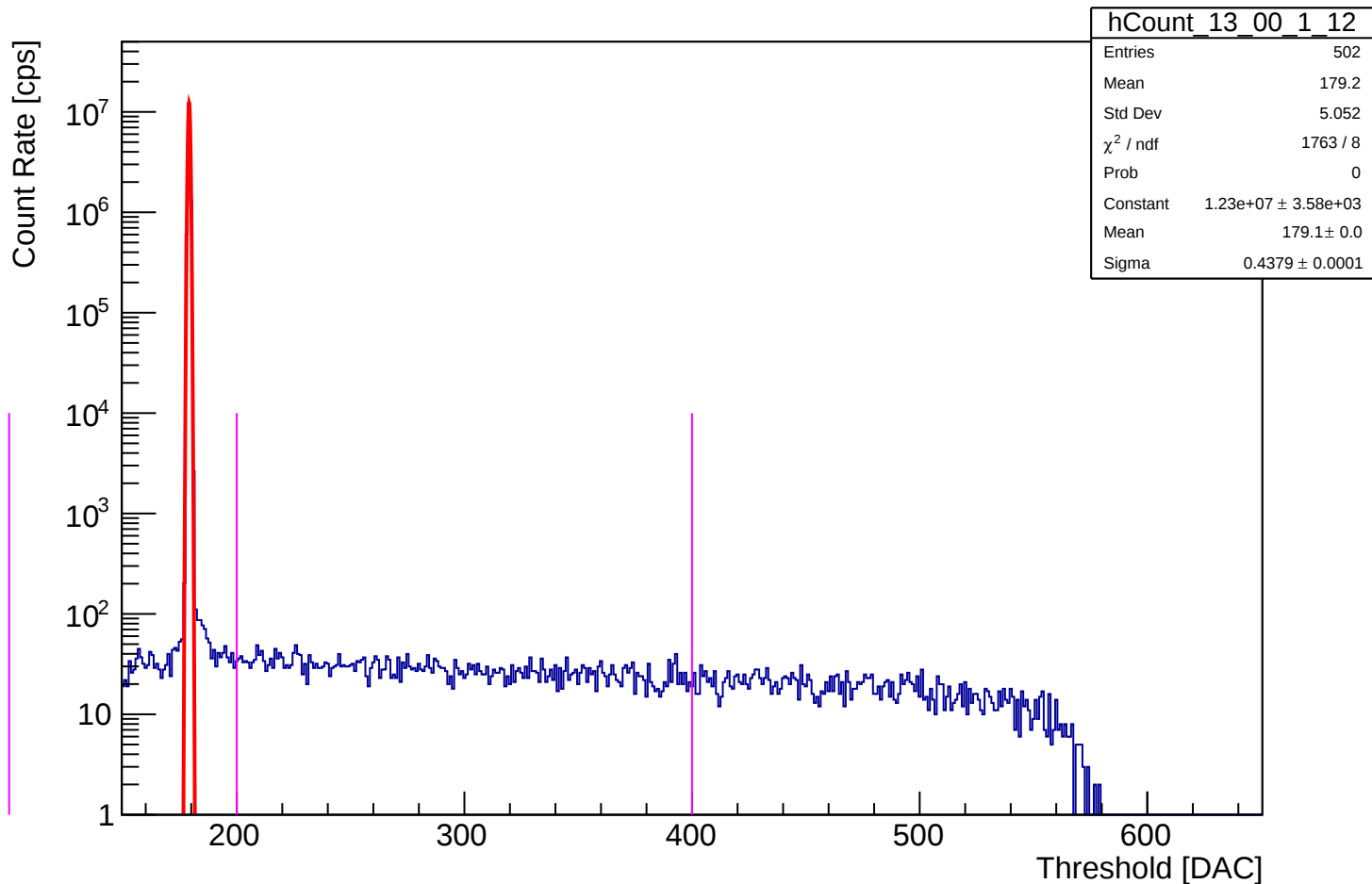
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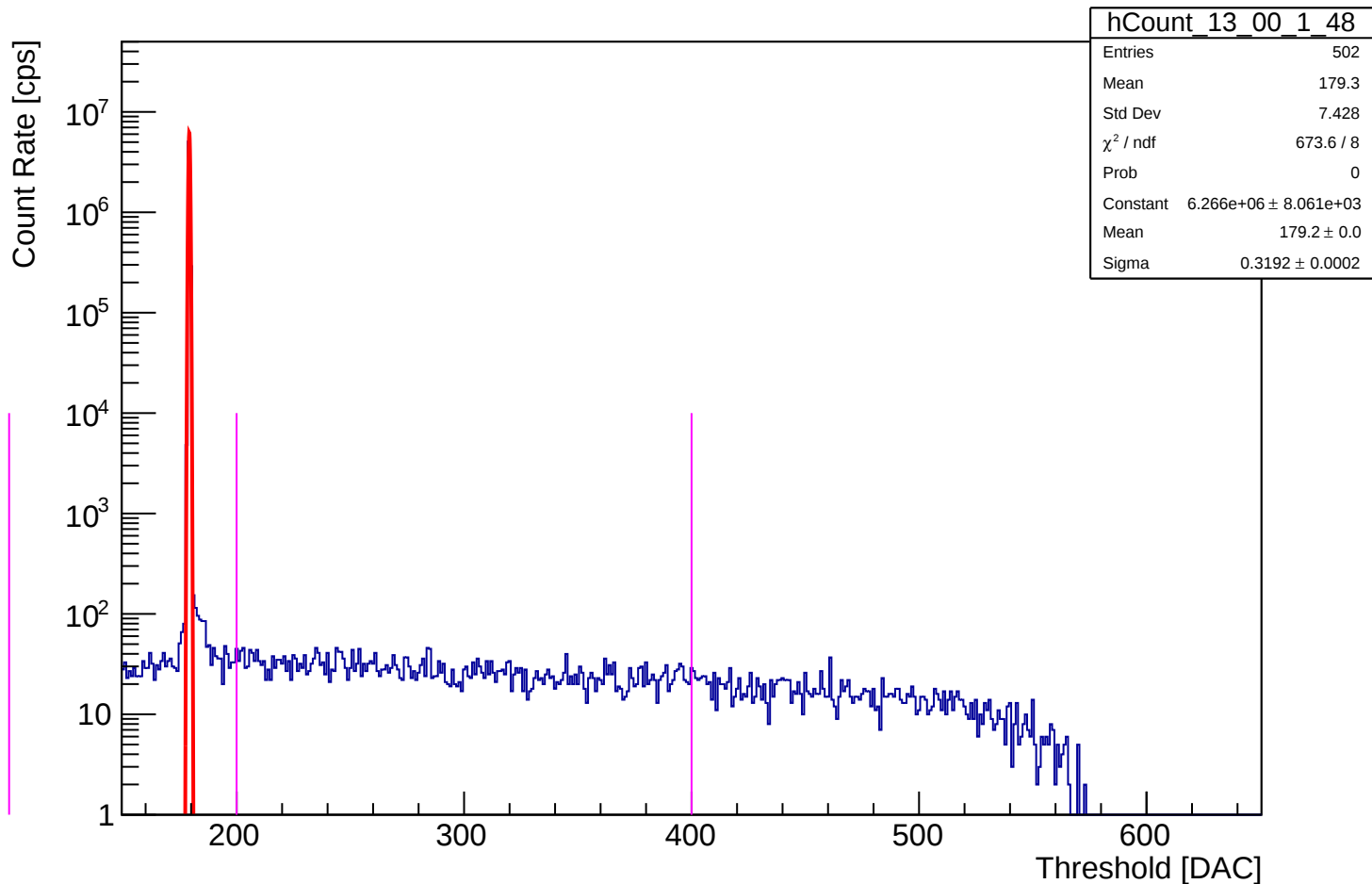
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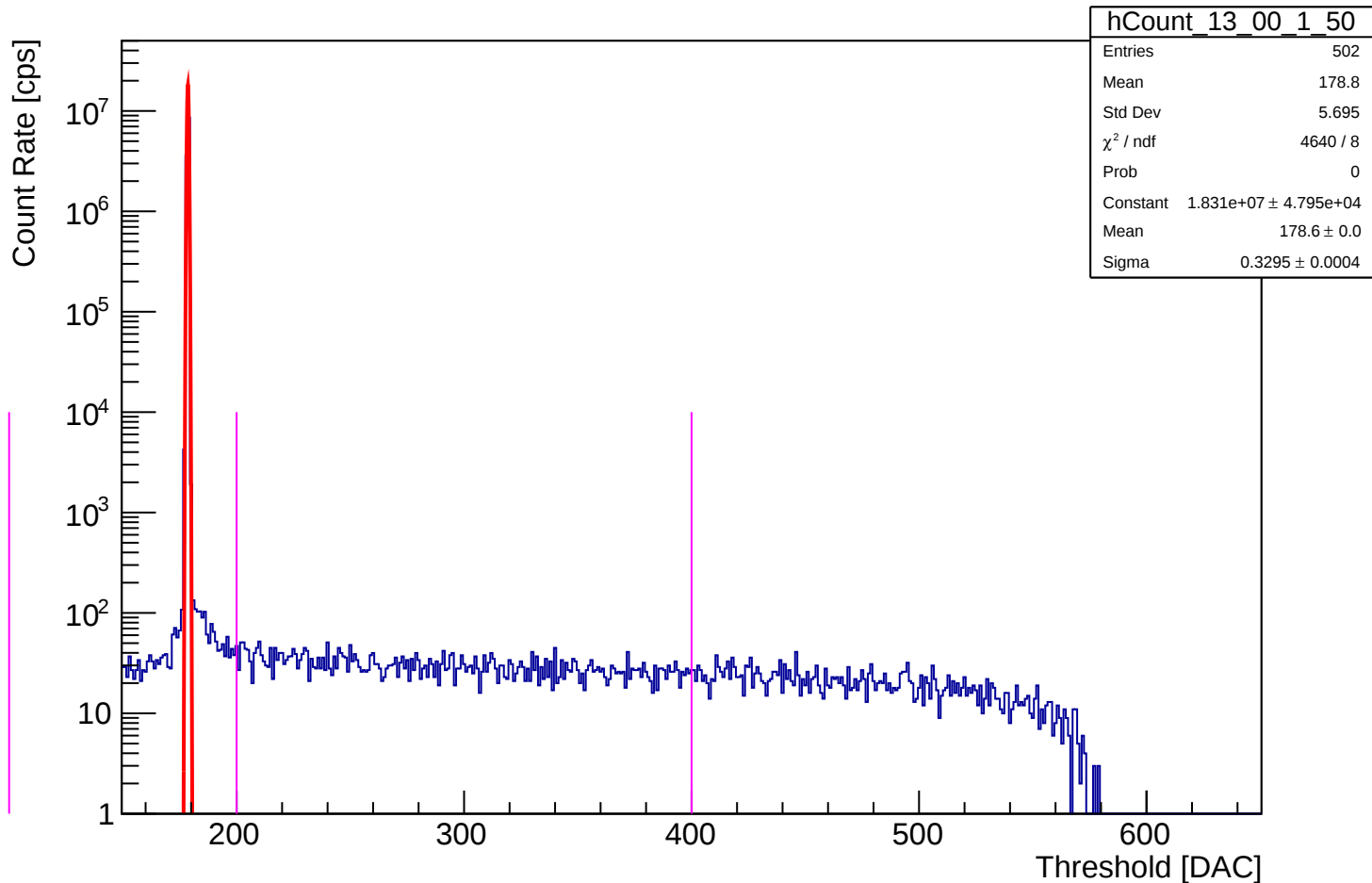
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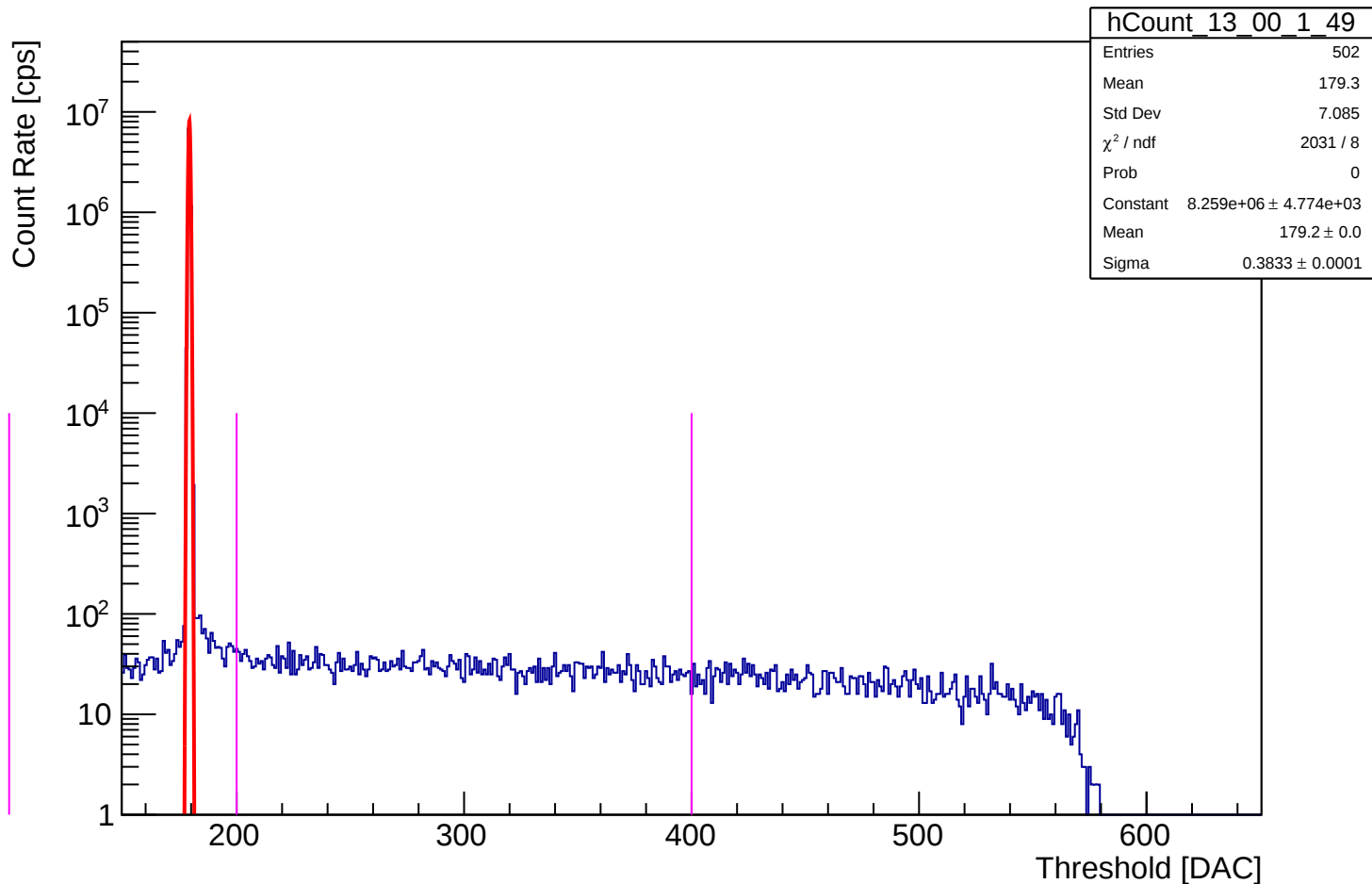
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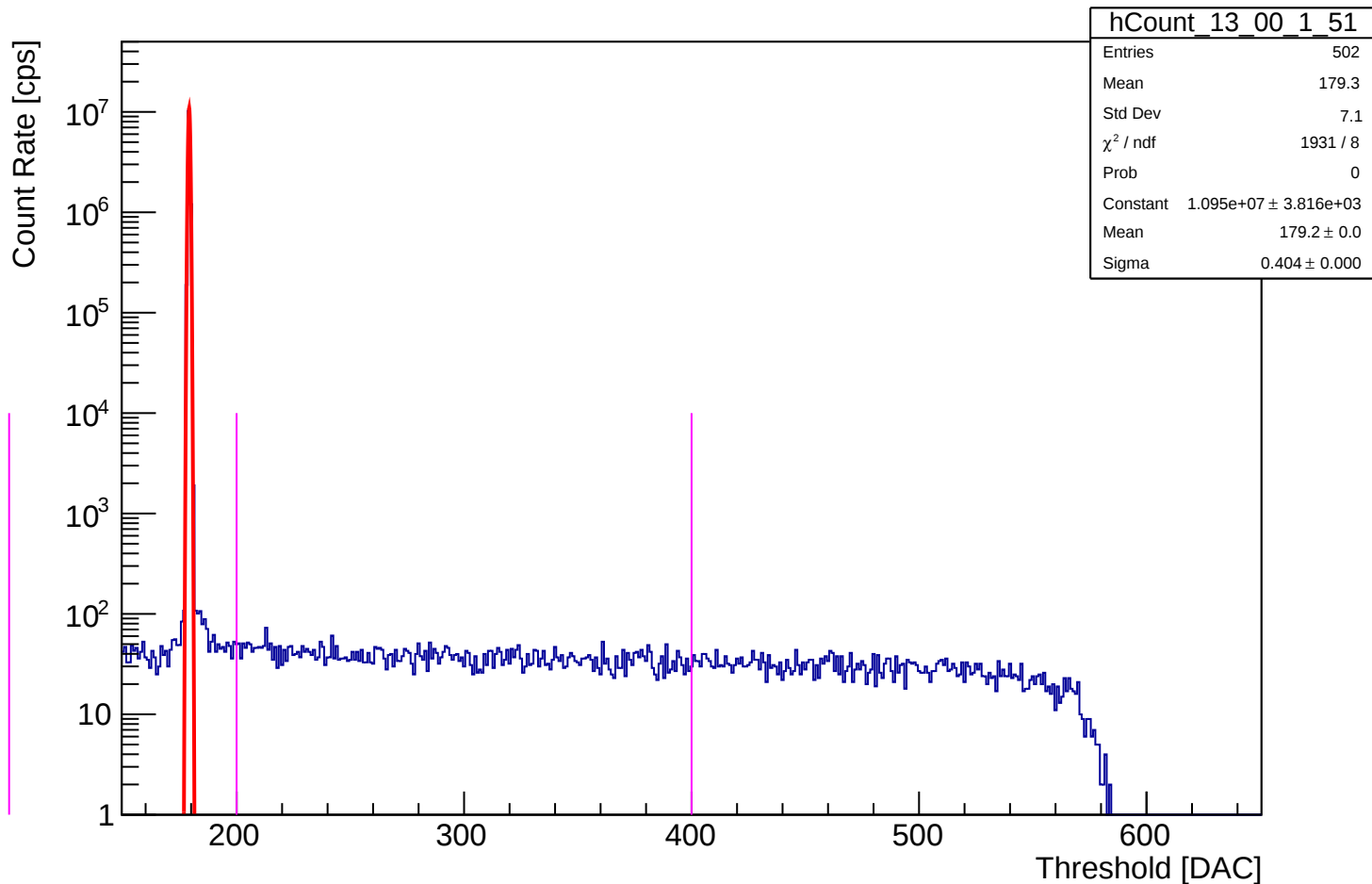
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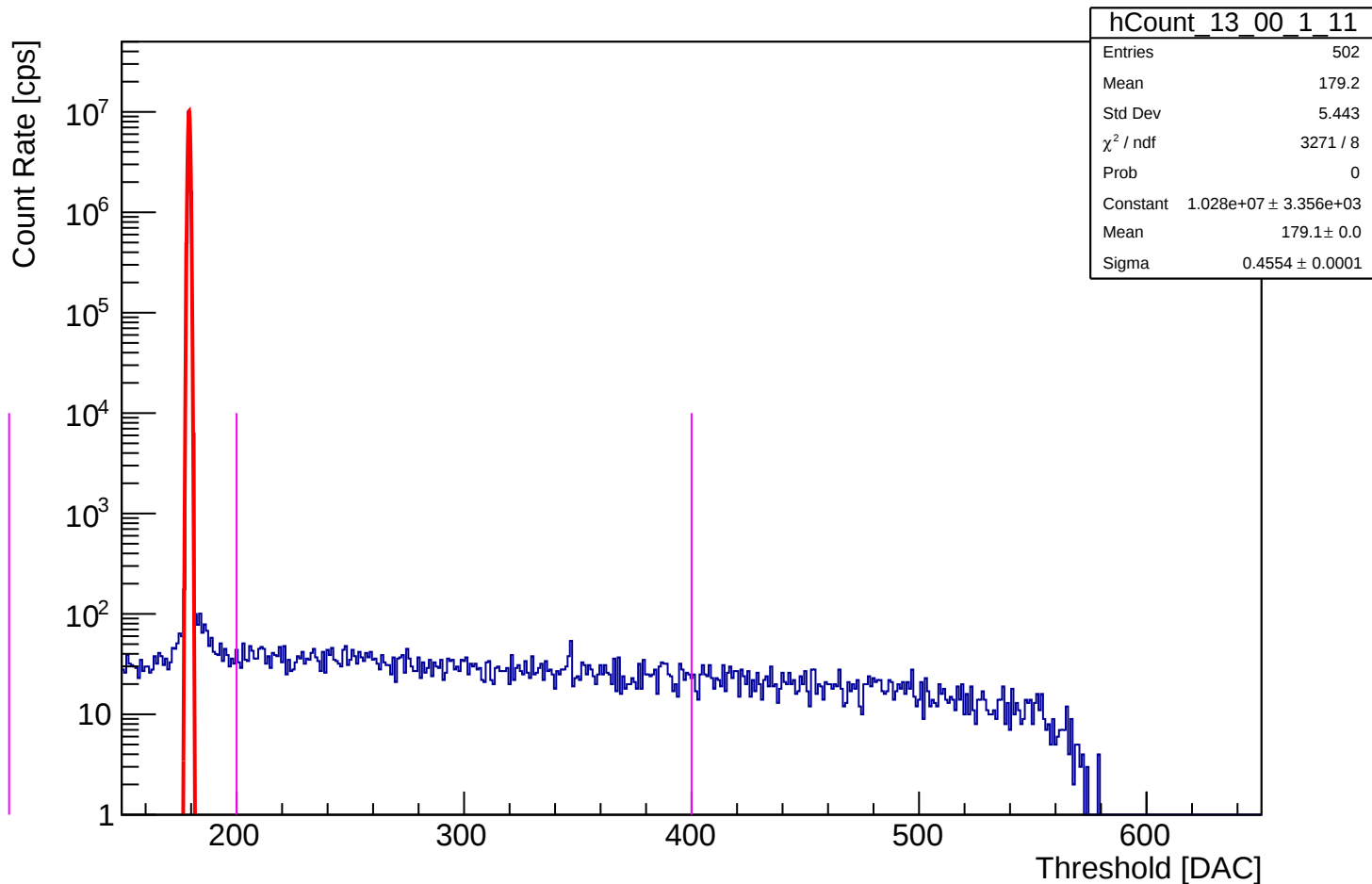
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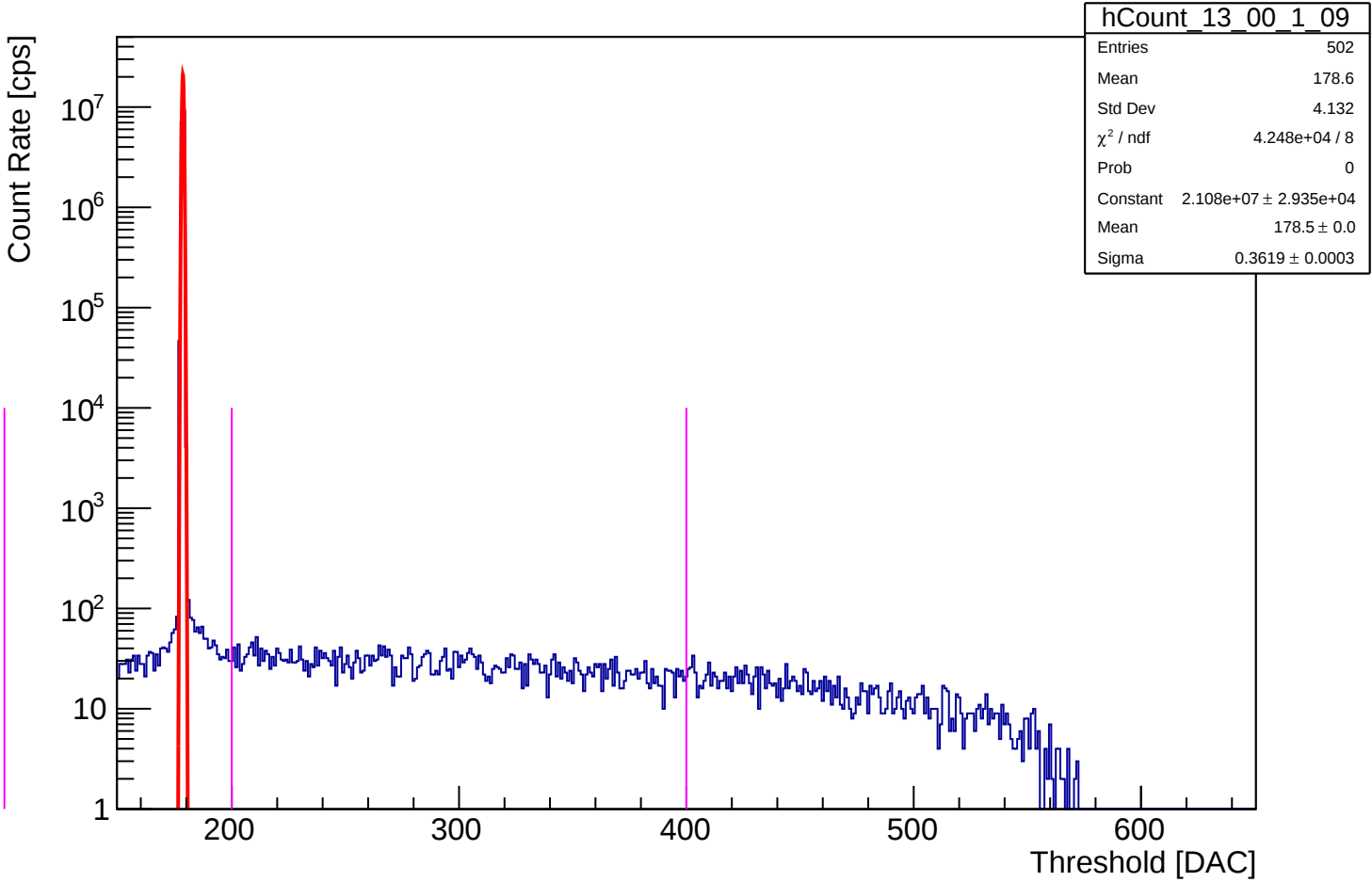


Row 1 Tile 1 Pmt 2 Pixel 40 Slot 13 Fiber 0 Asic 1 Channel 51

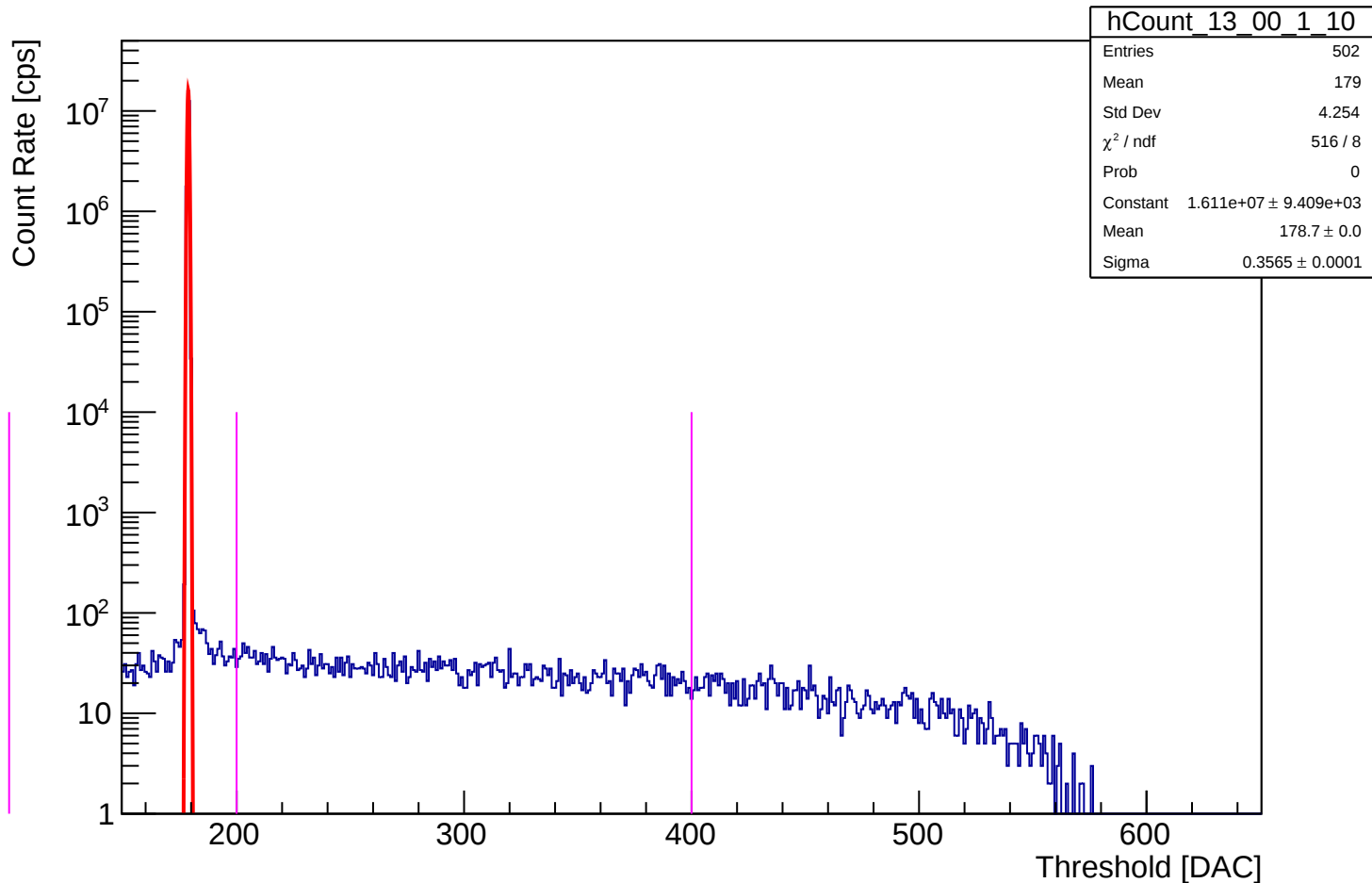


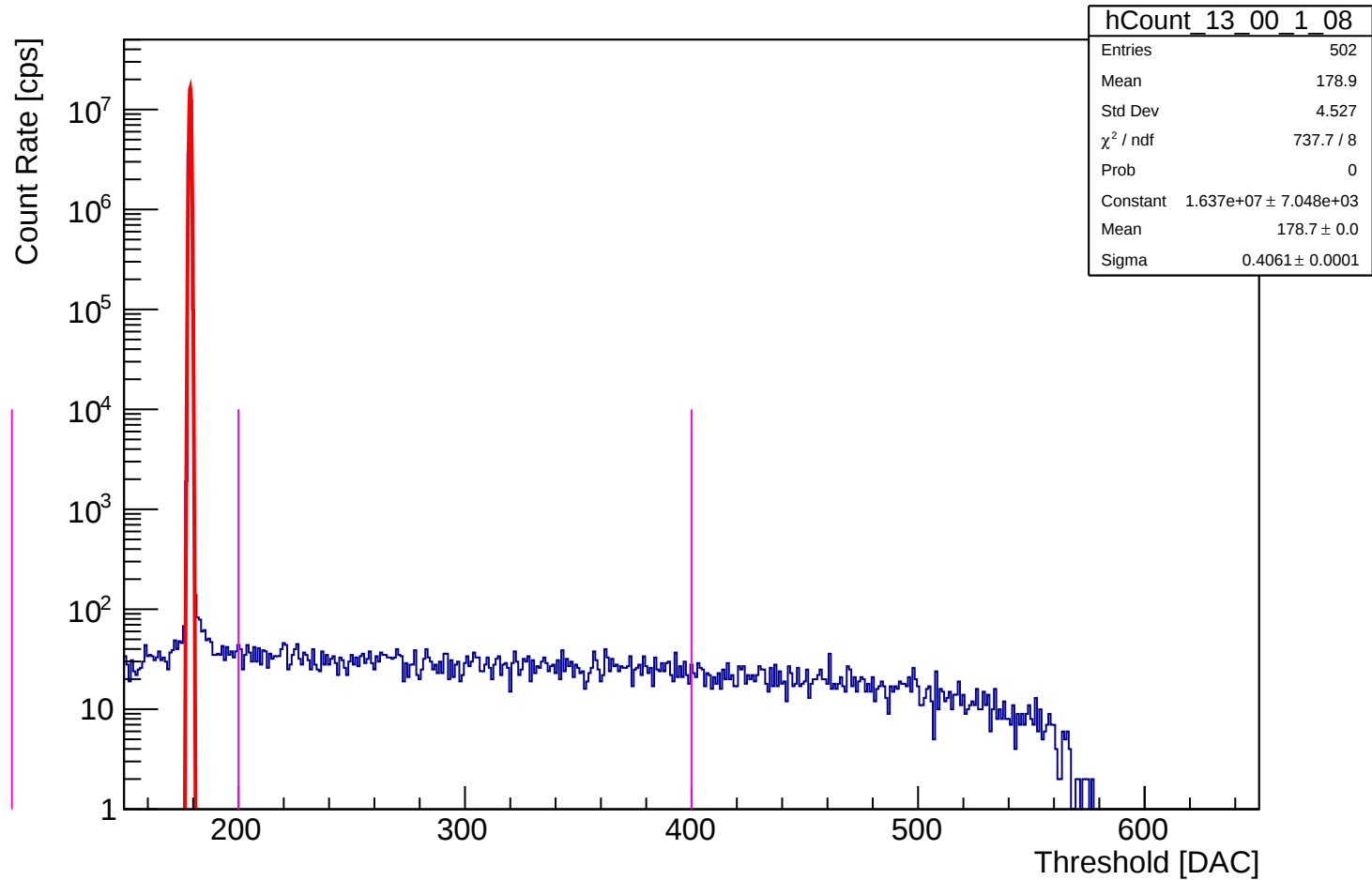
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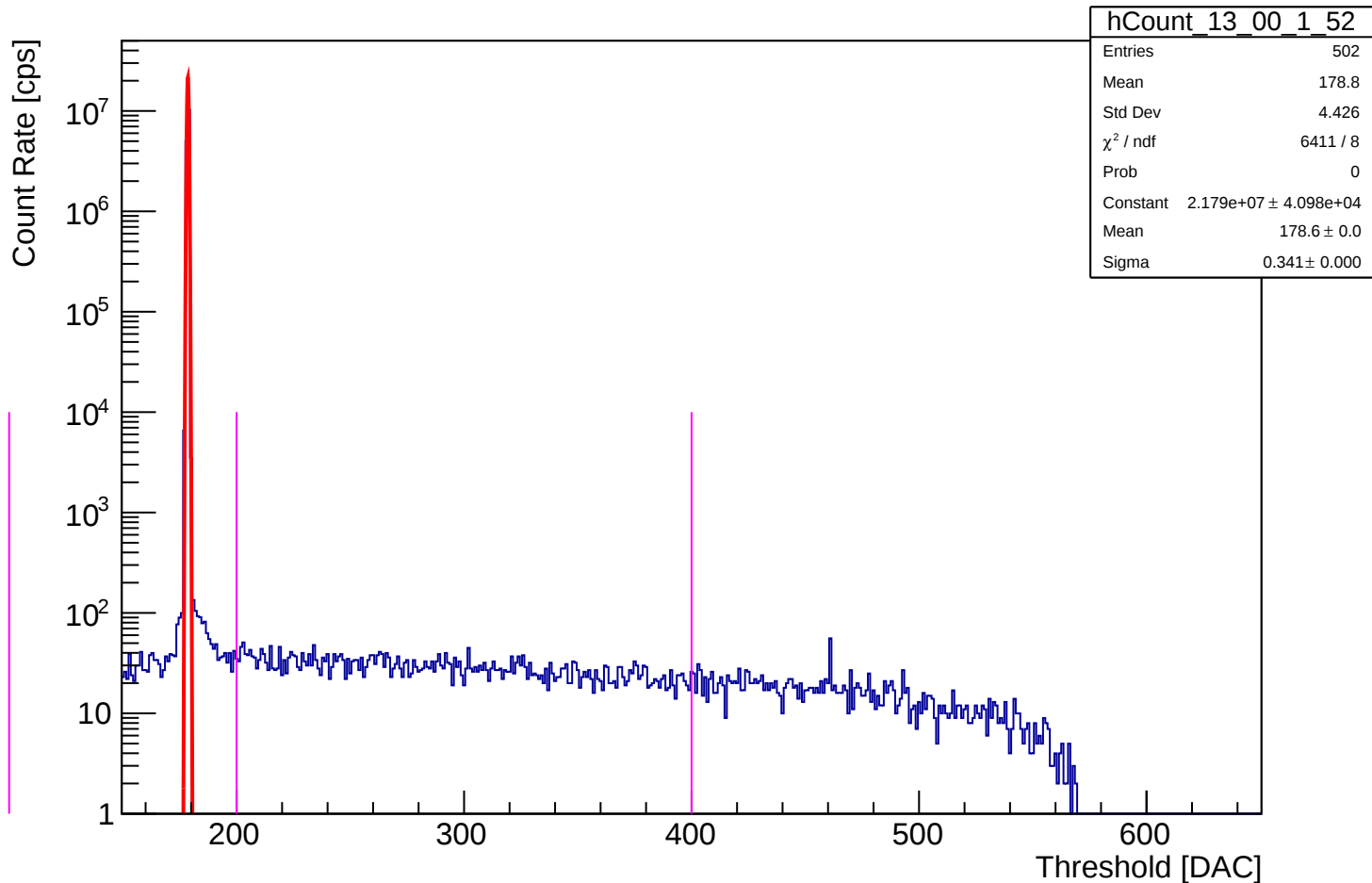


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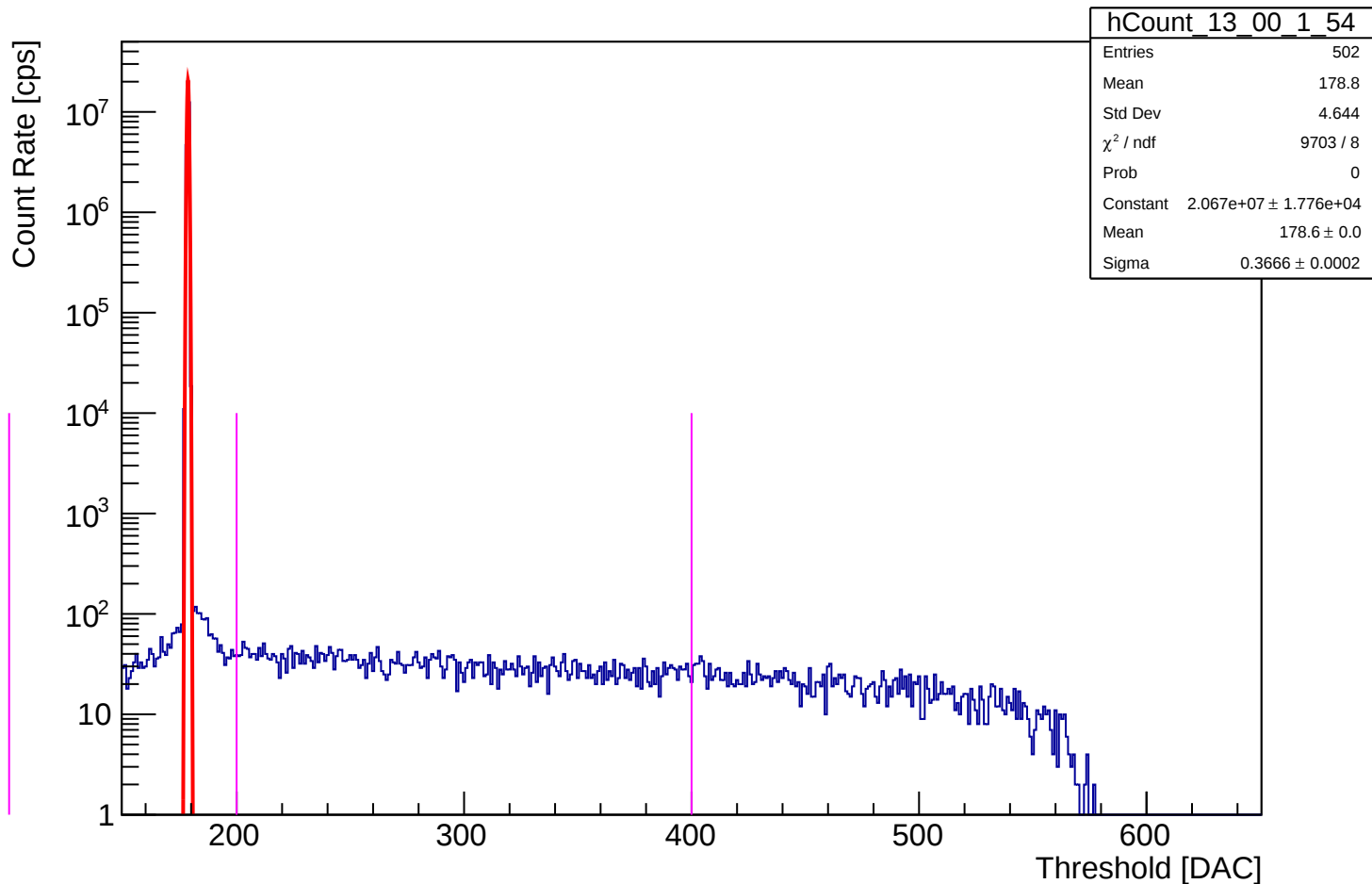




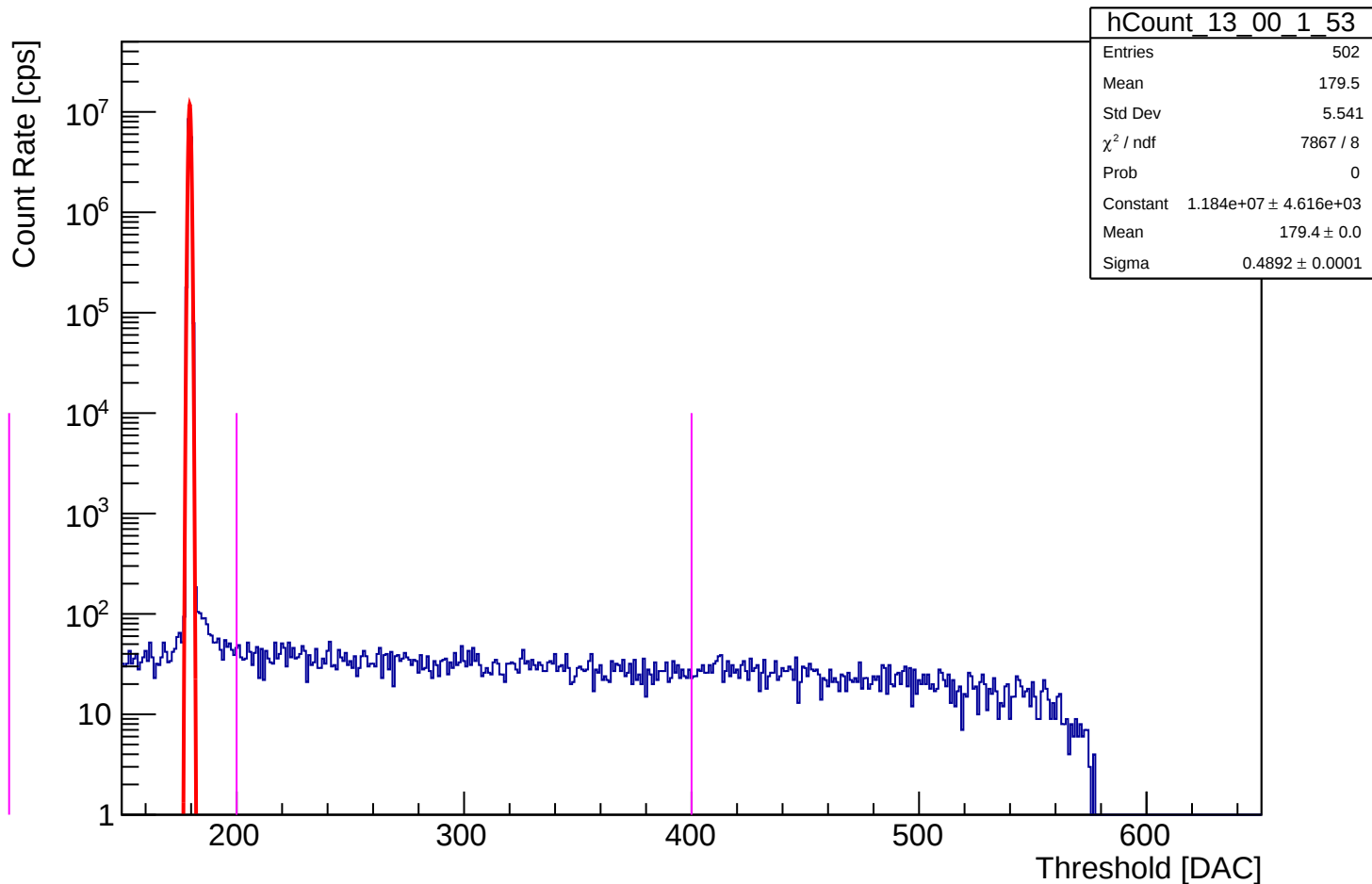
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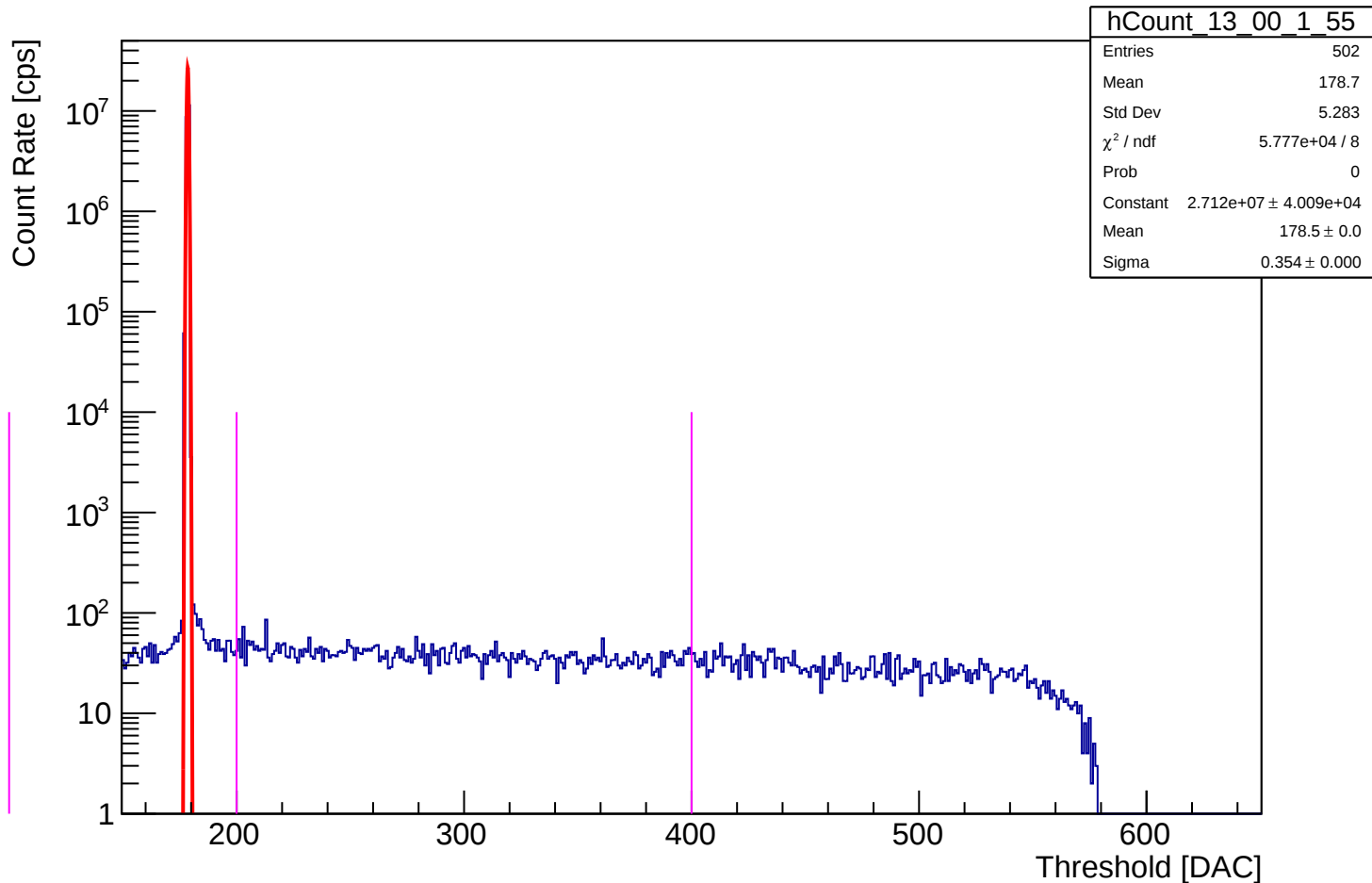
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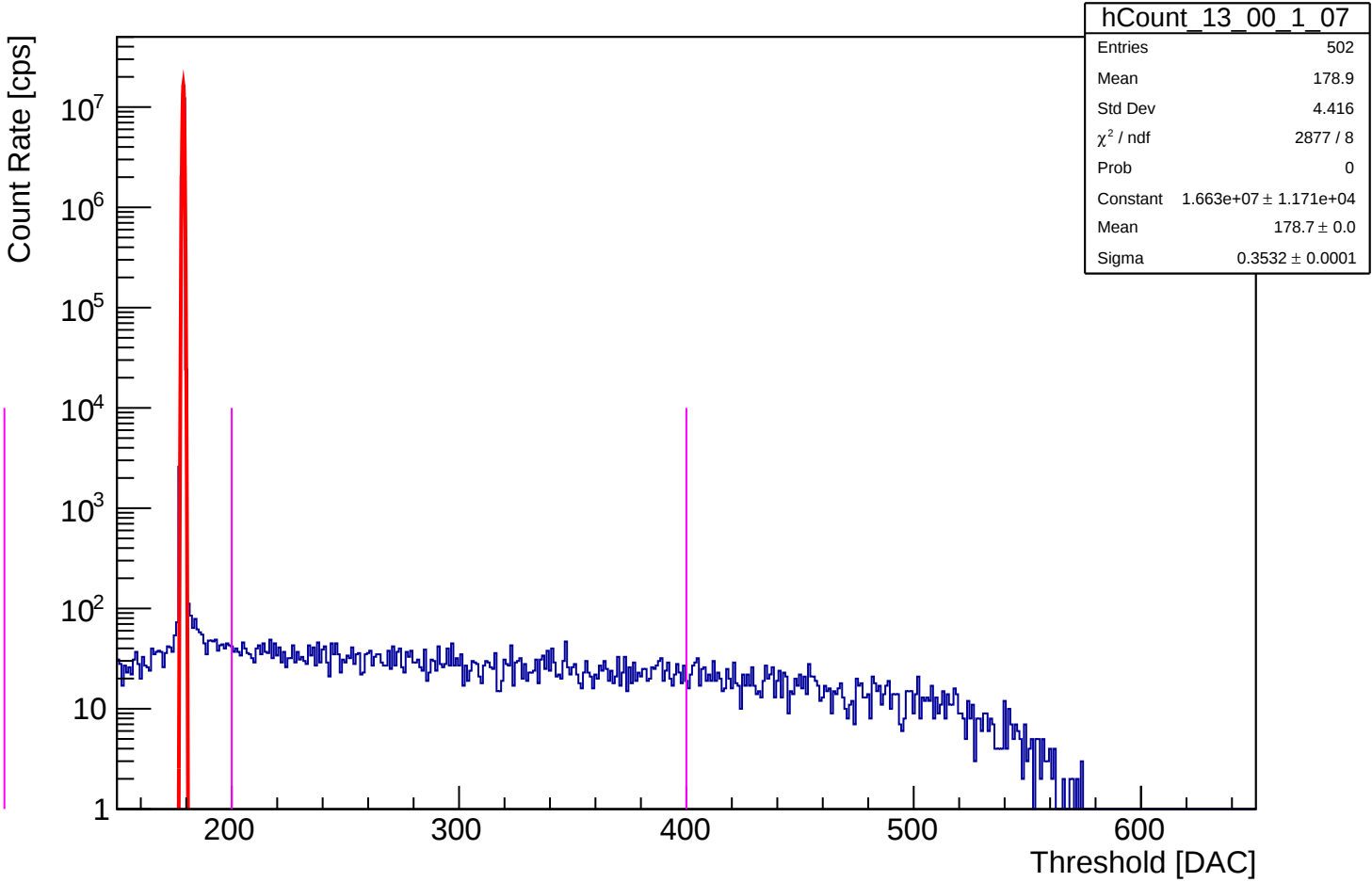


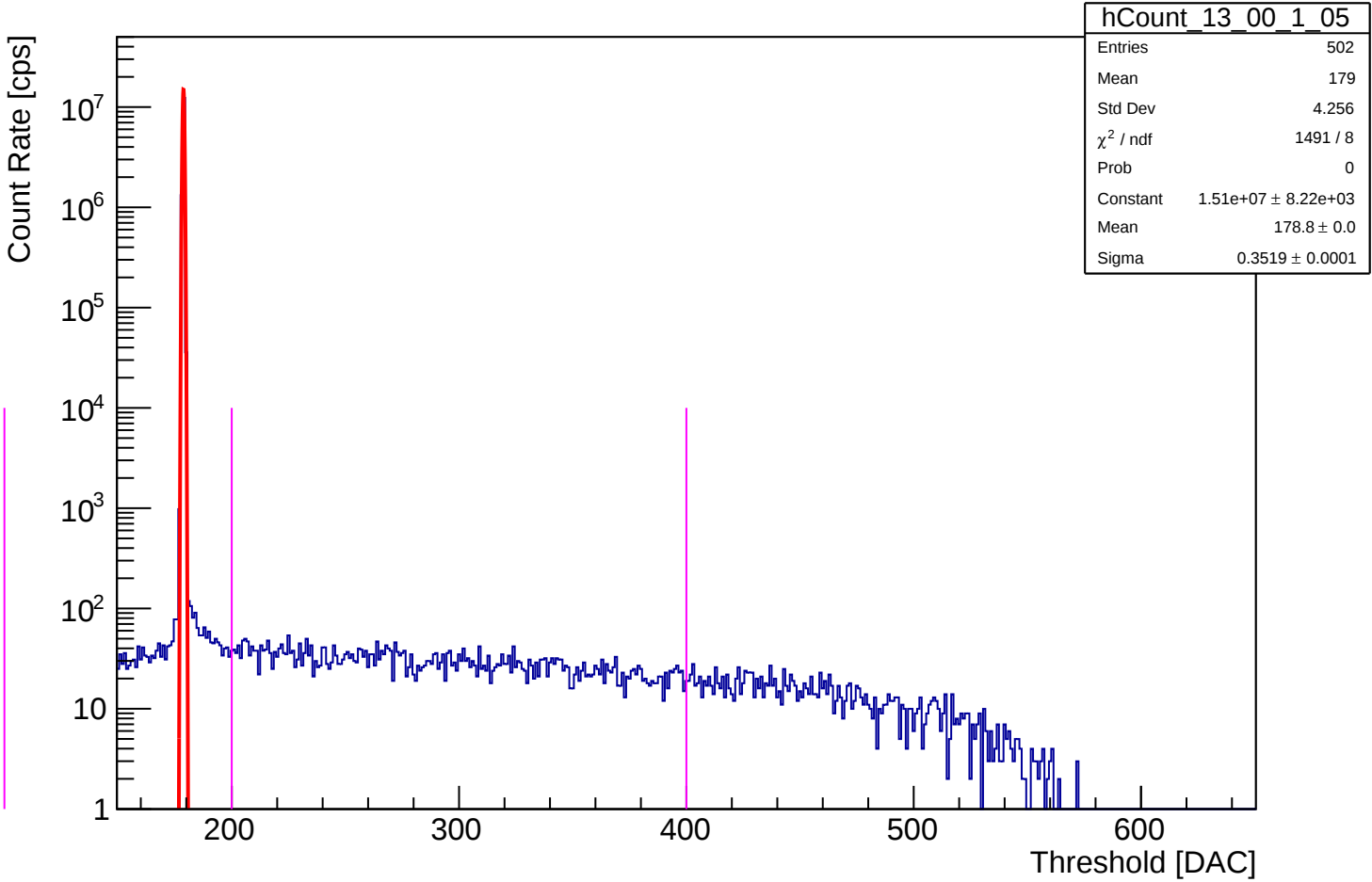
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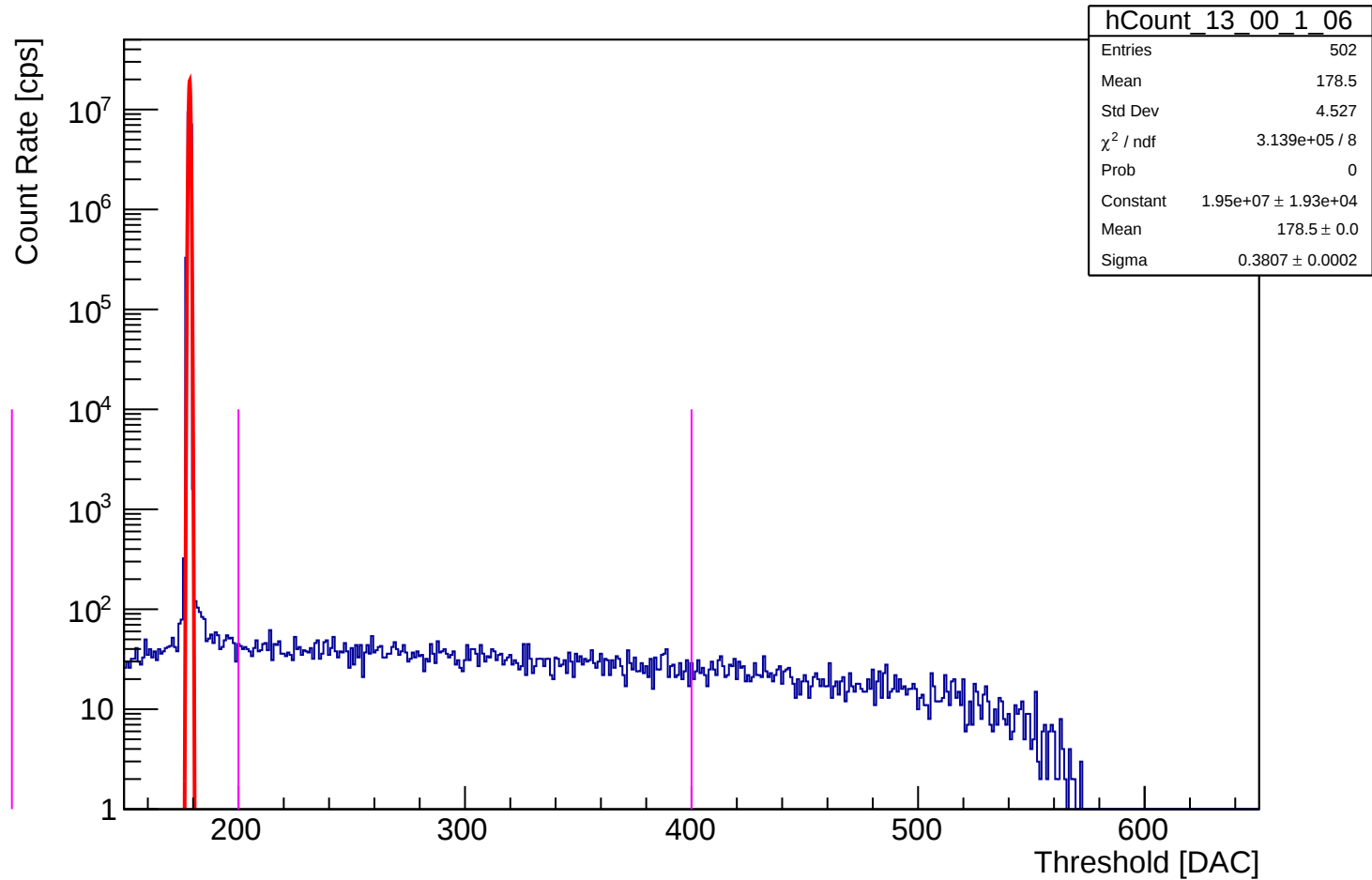


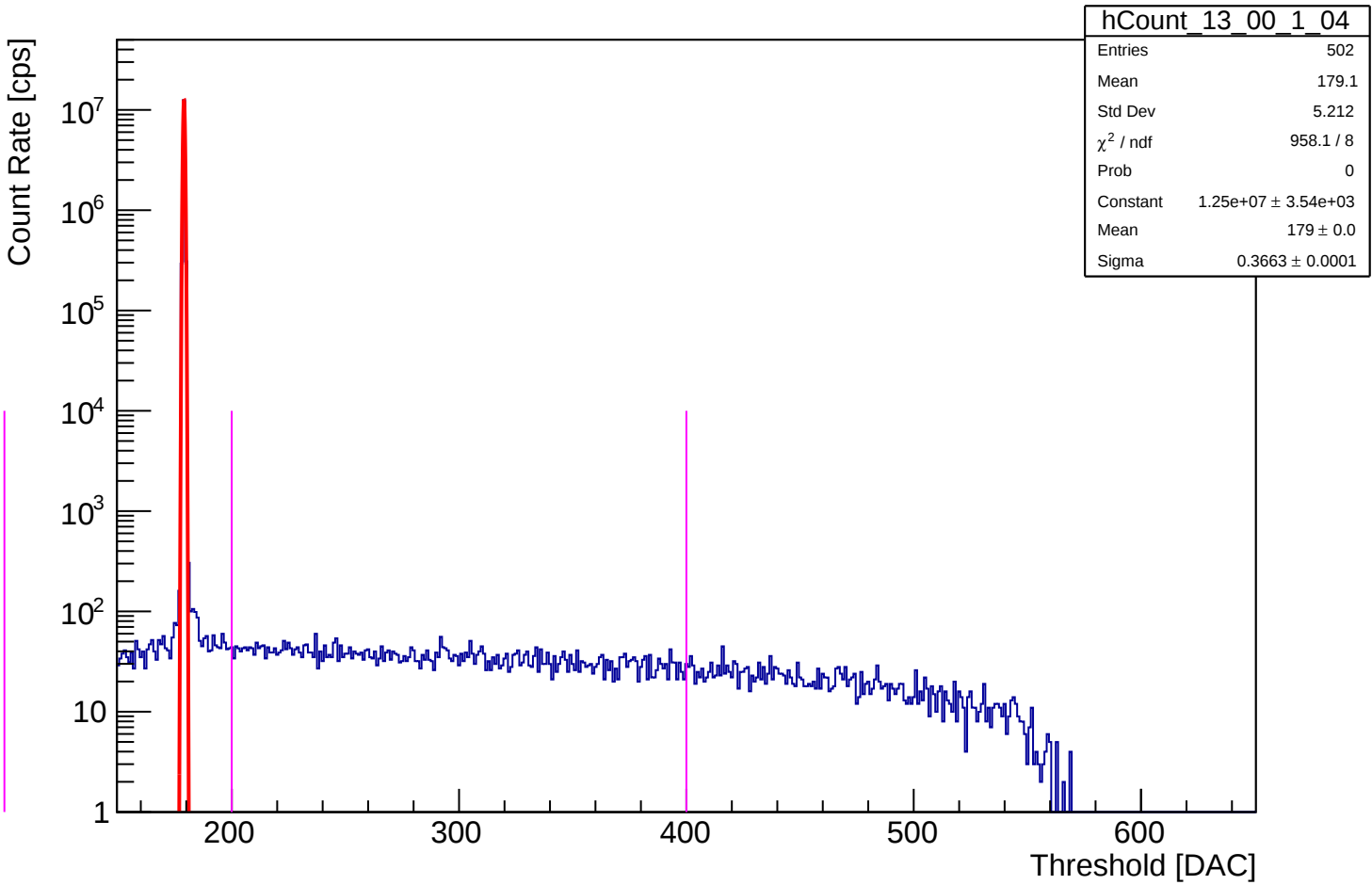
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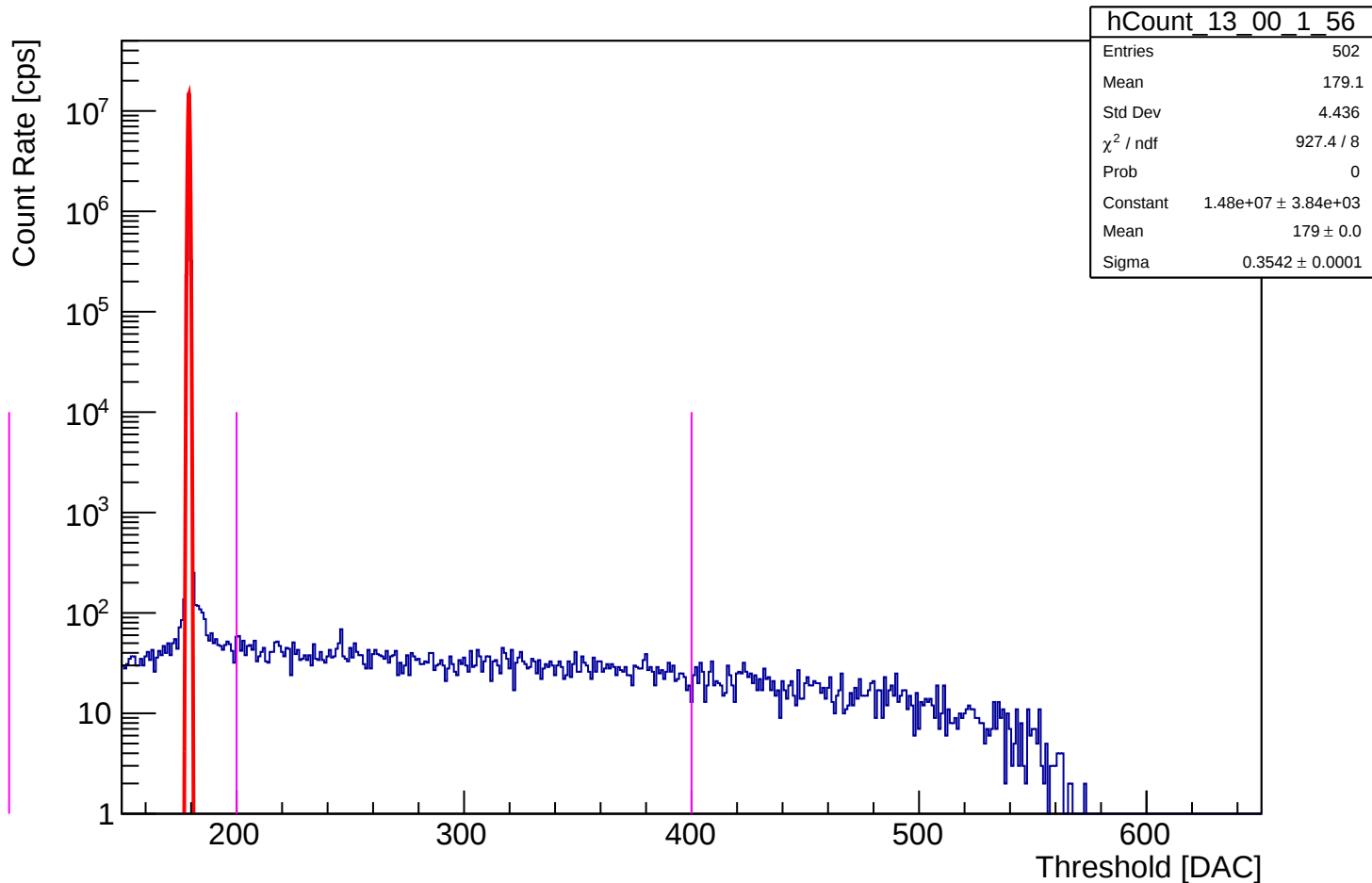




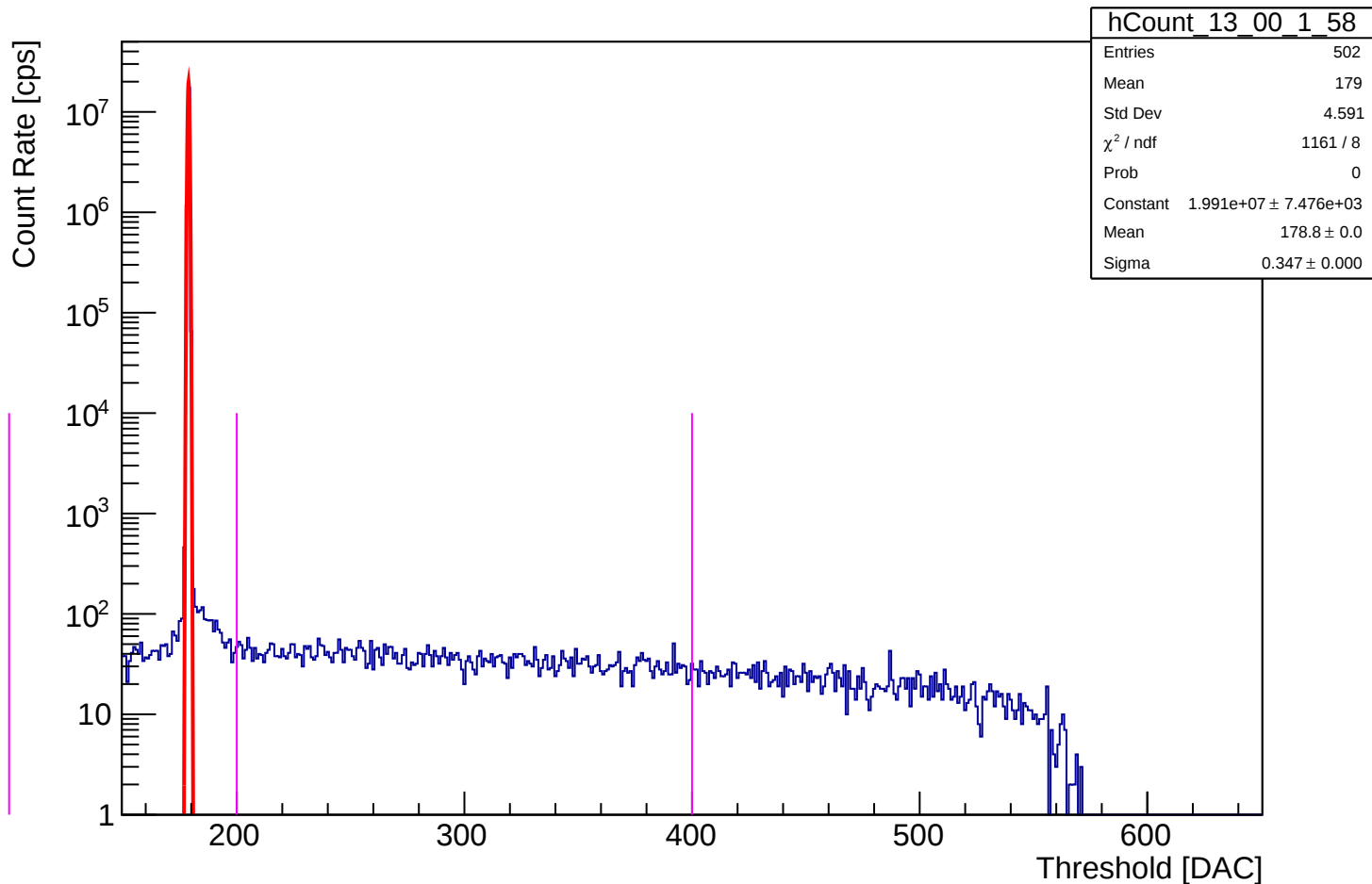




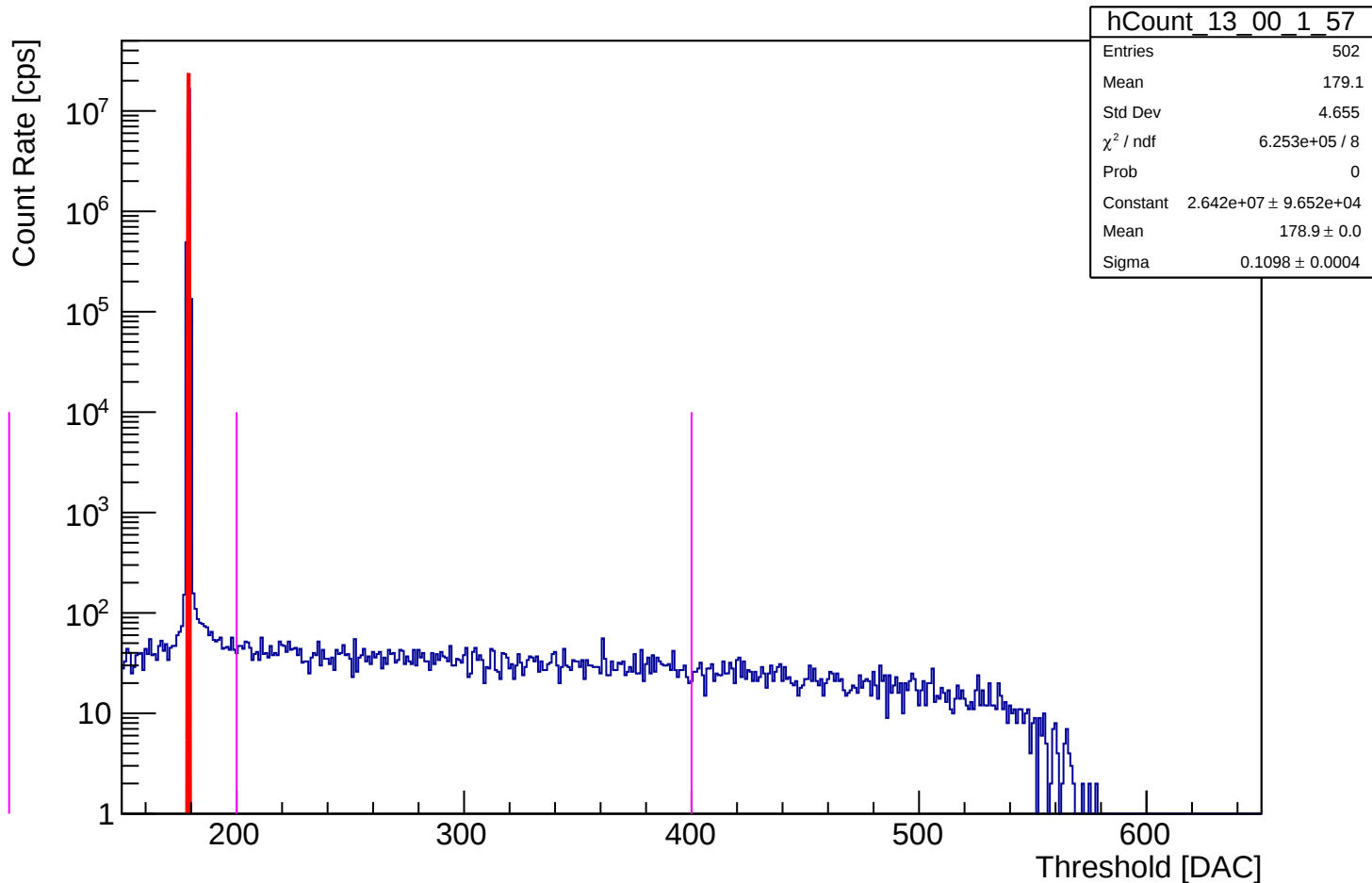
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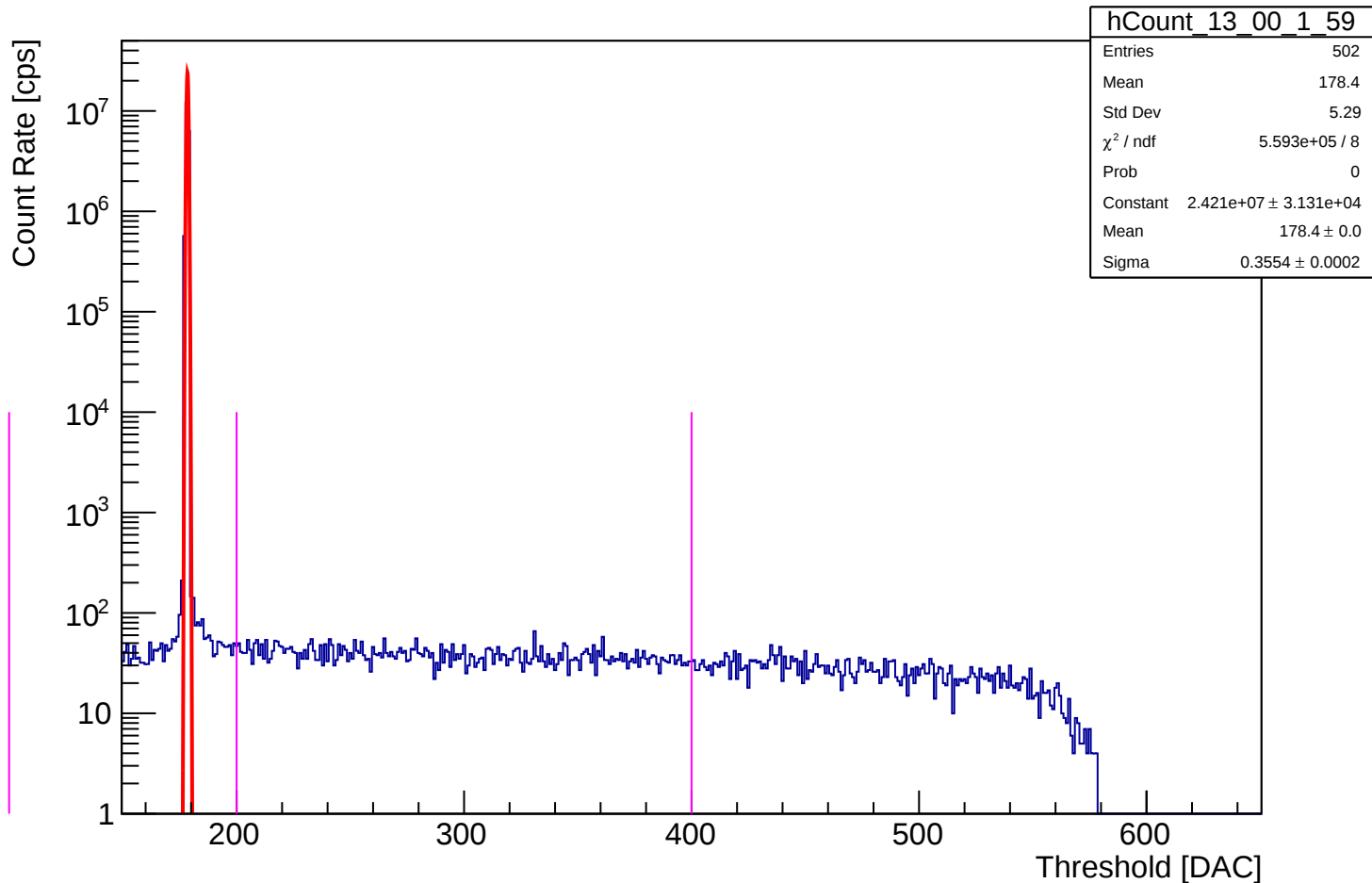
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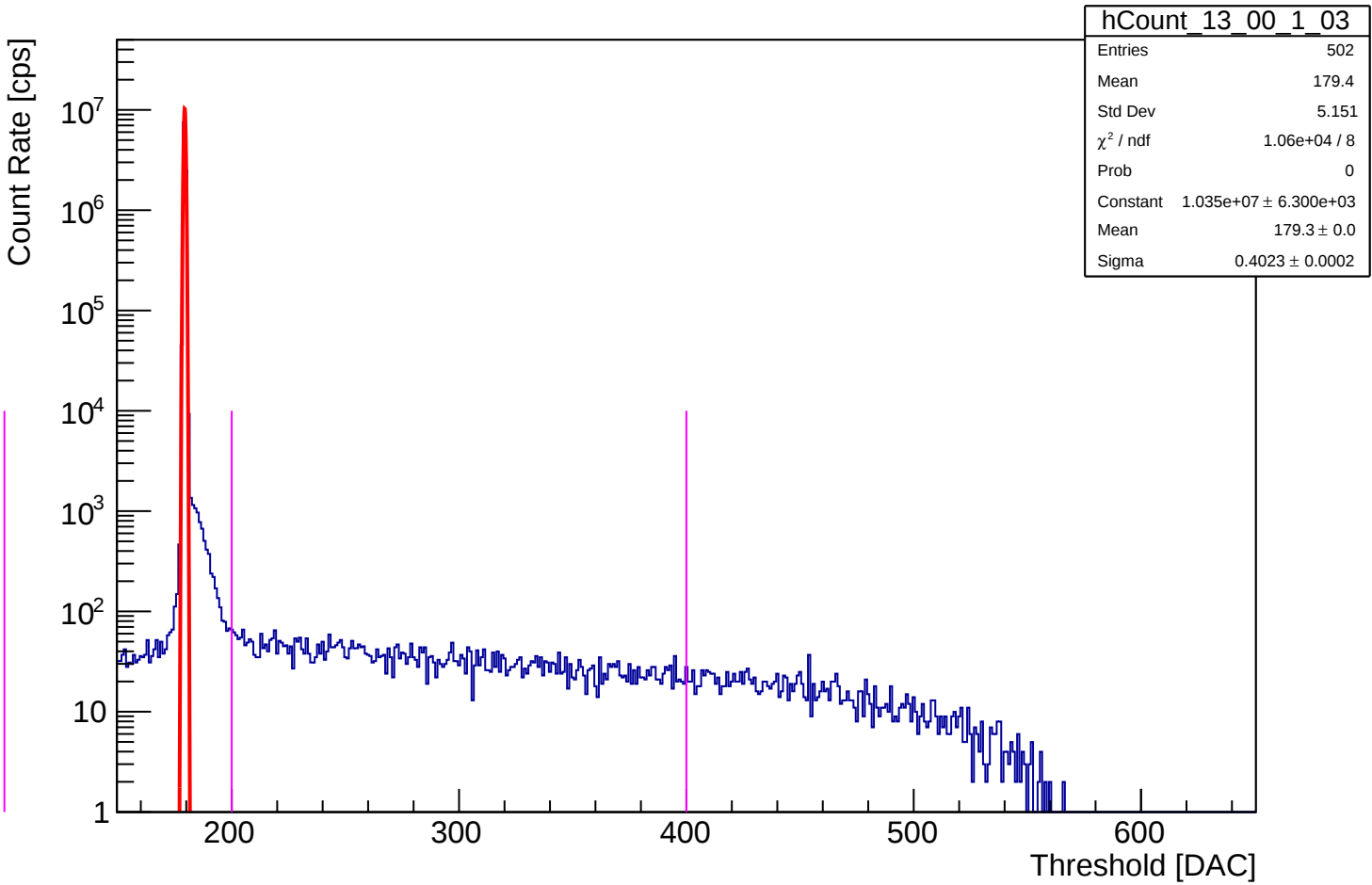


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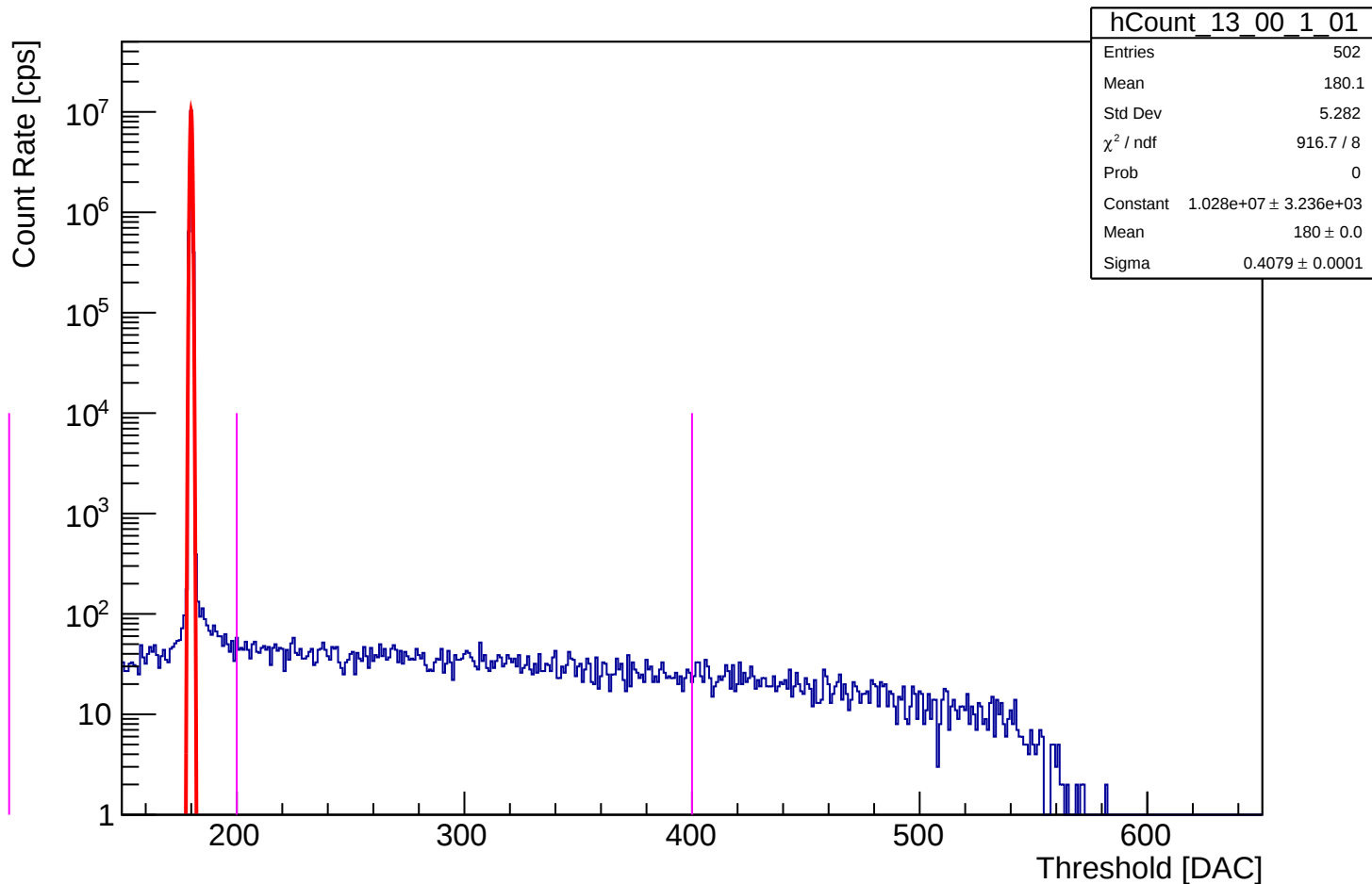


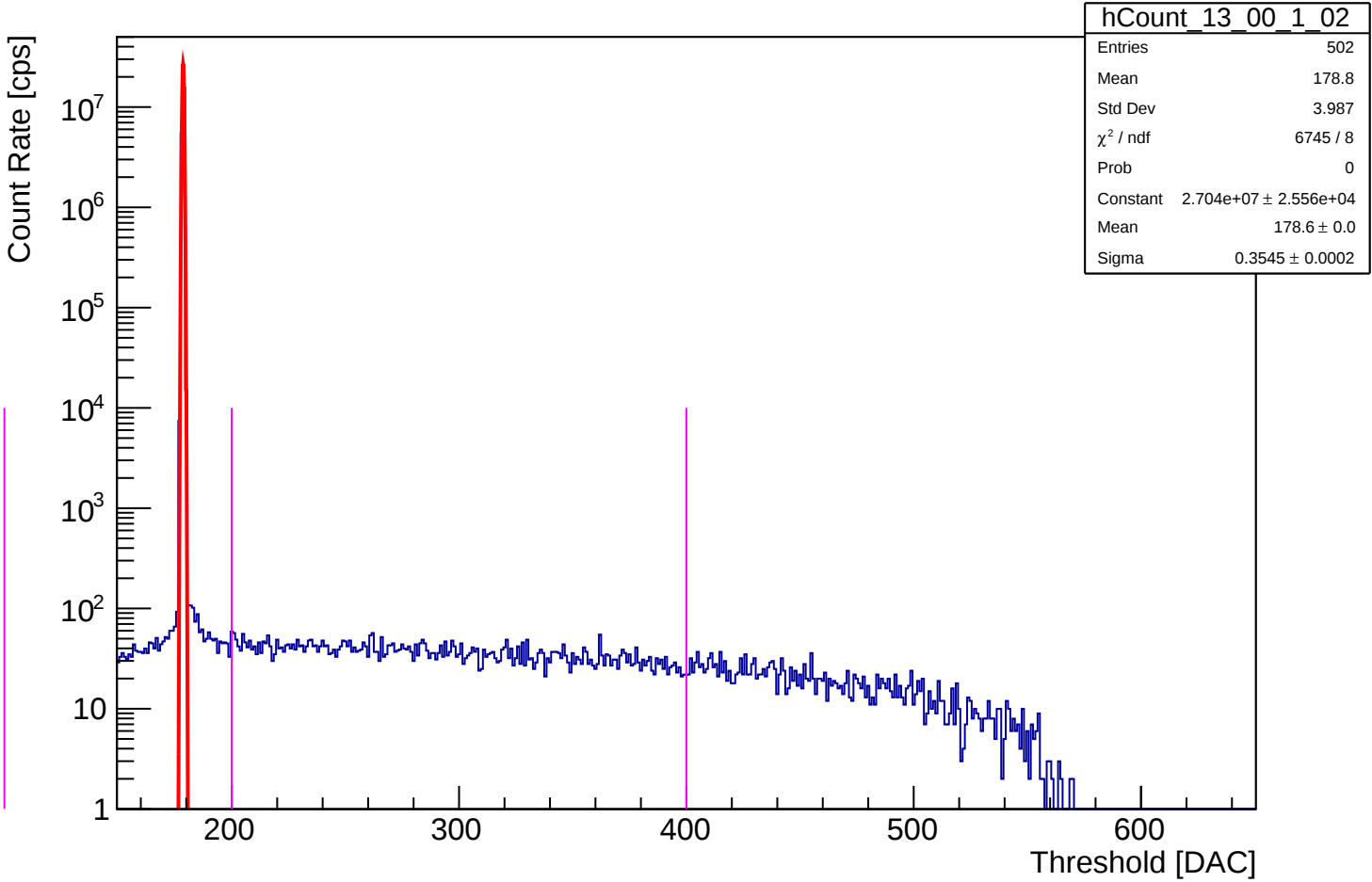
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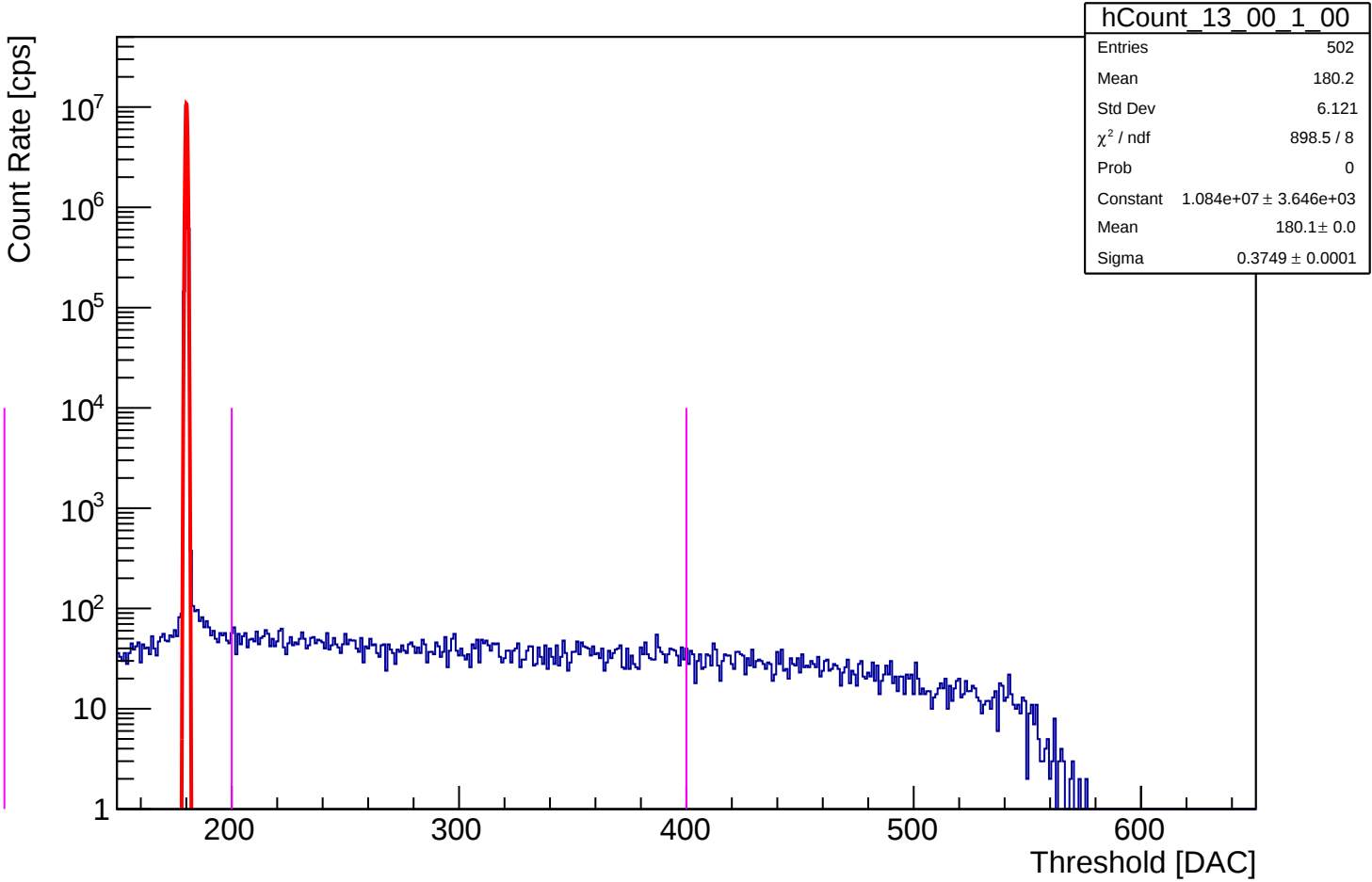




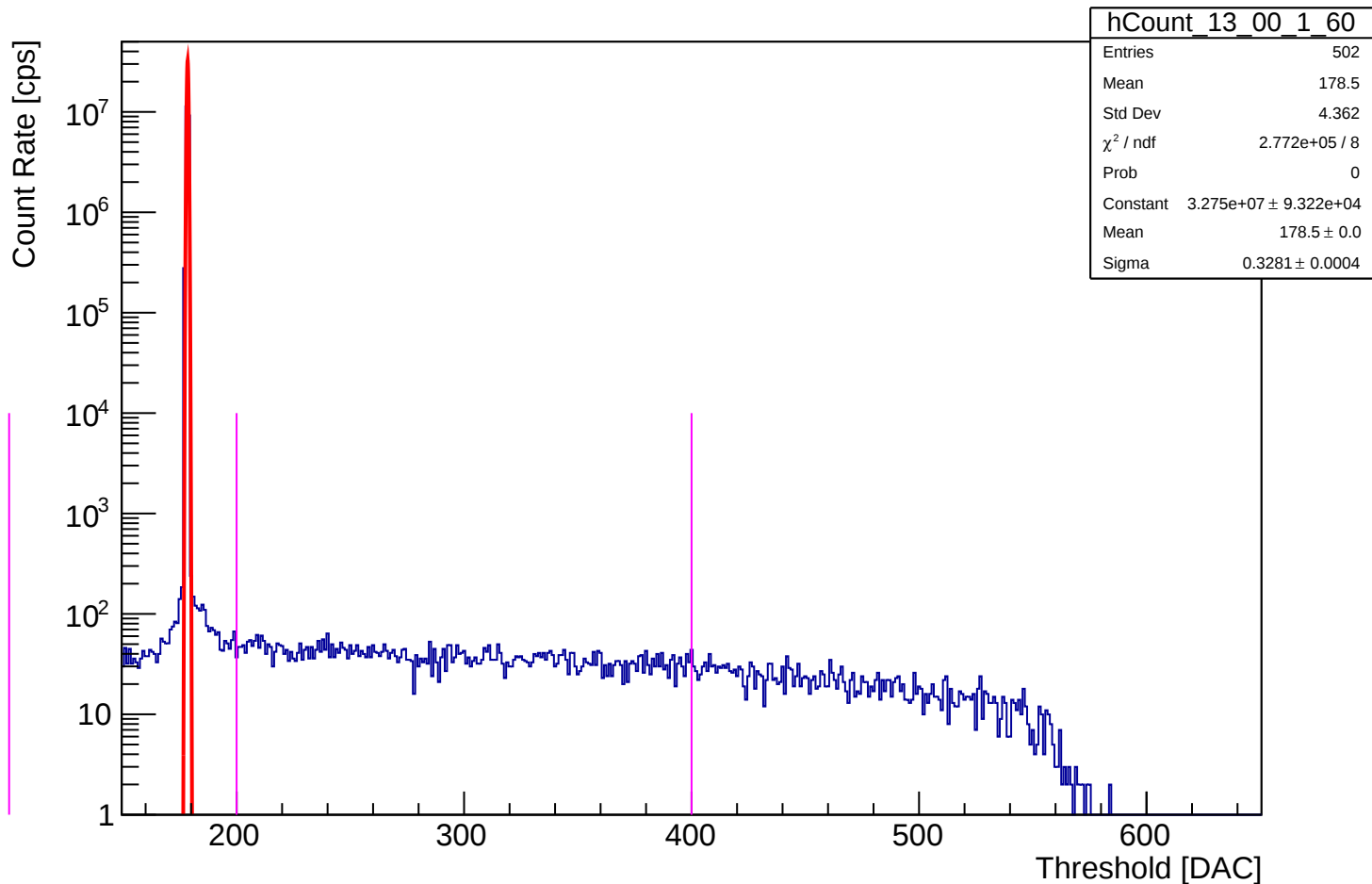
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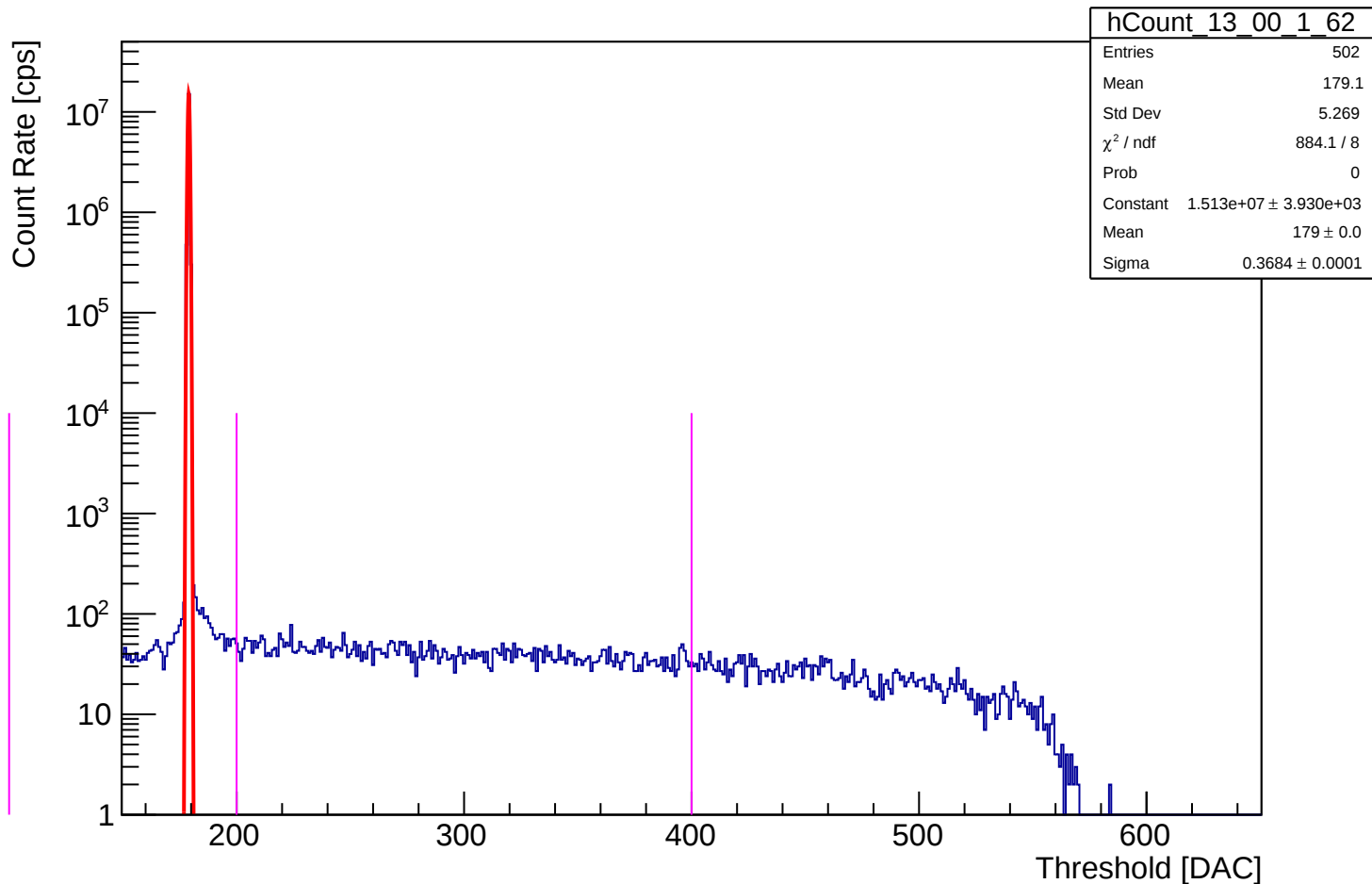


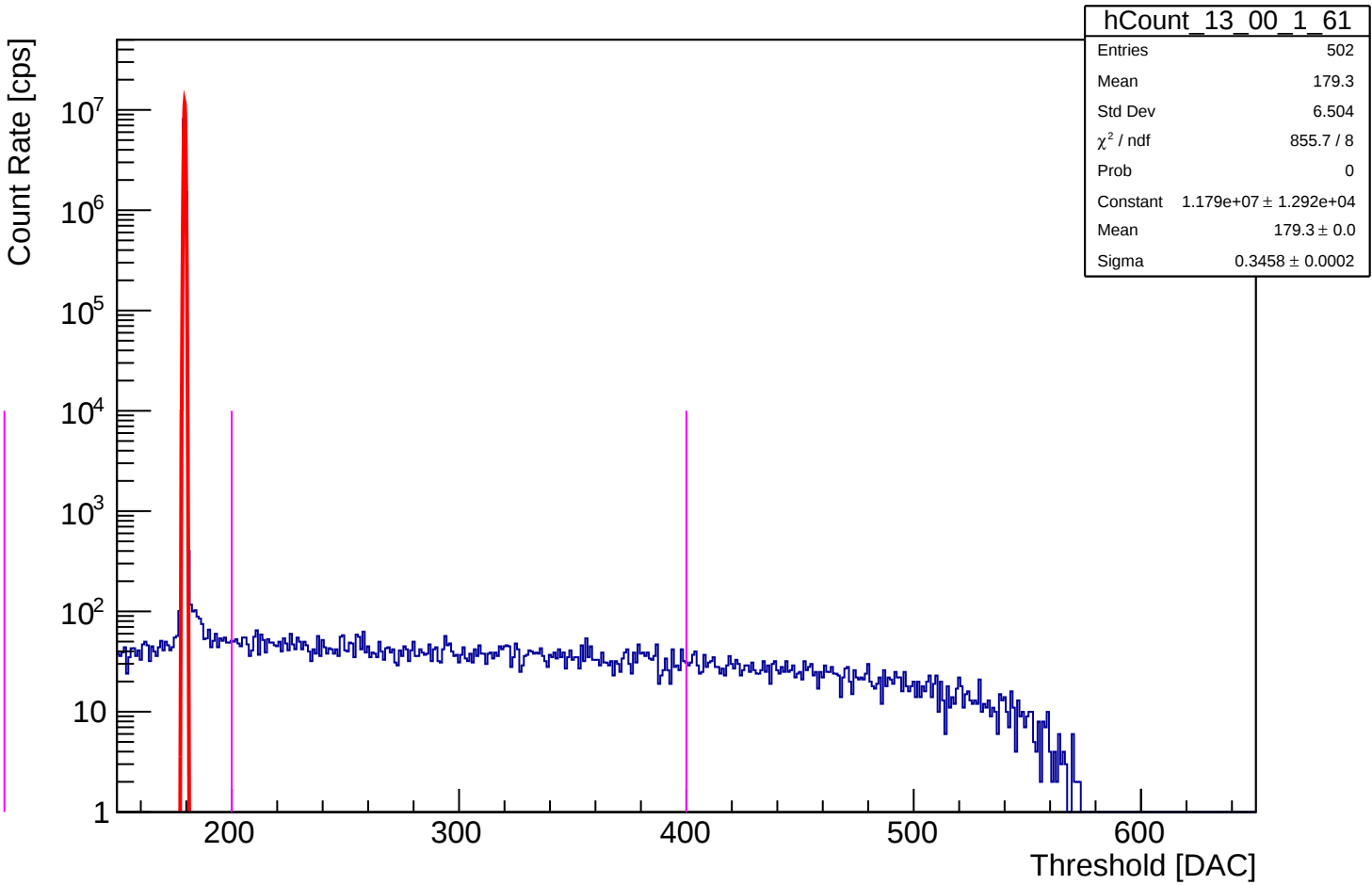


Row 1 Tile 1 Pmt 2 Pixel 61 Slot 13 Fiber 0 Asic 1 Channel 60

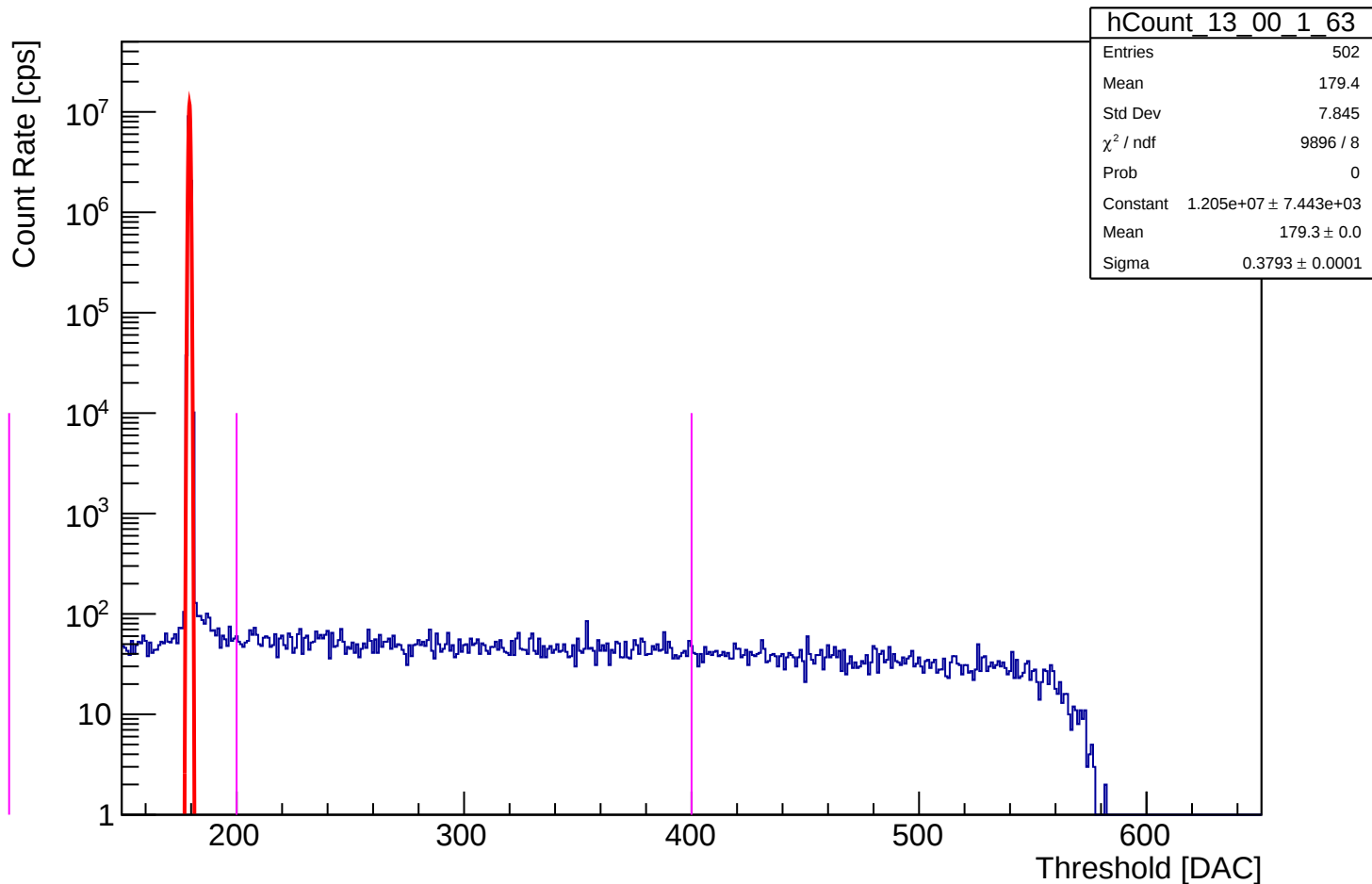


Row 1 Tile 1 Pmt 2 Pixel 62 Slot 13 Fiber 0 Asic 1 Channel 62

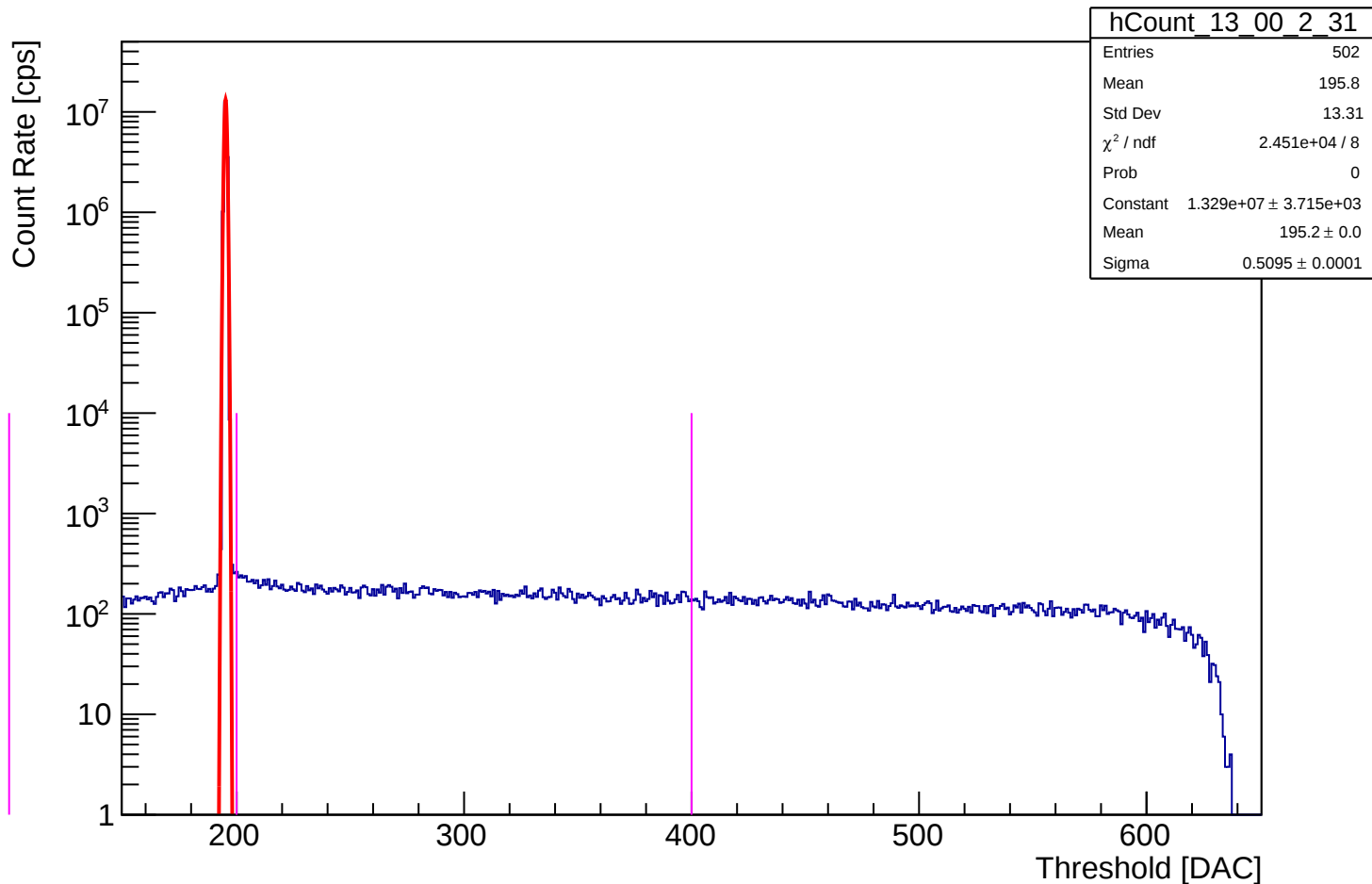




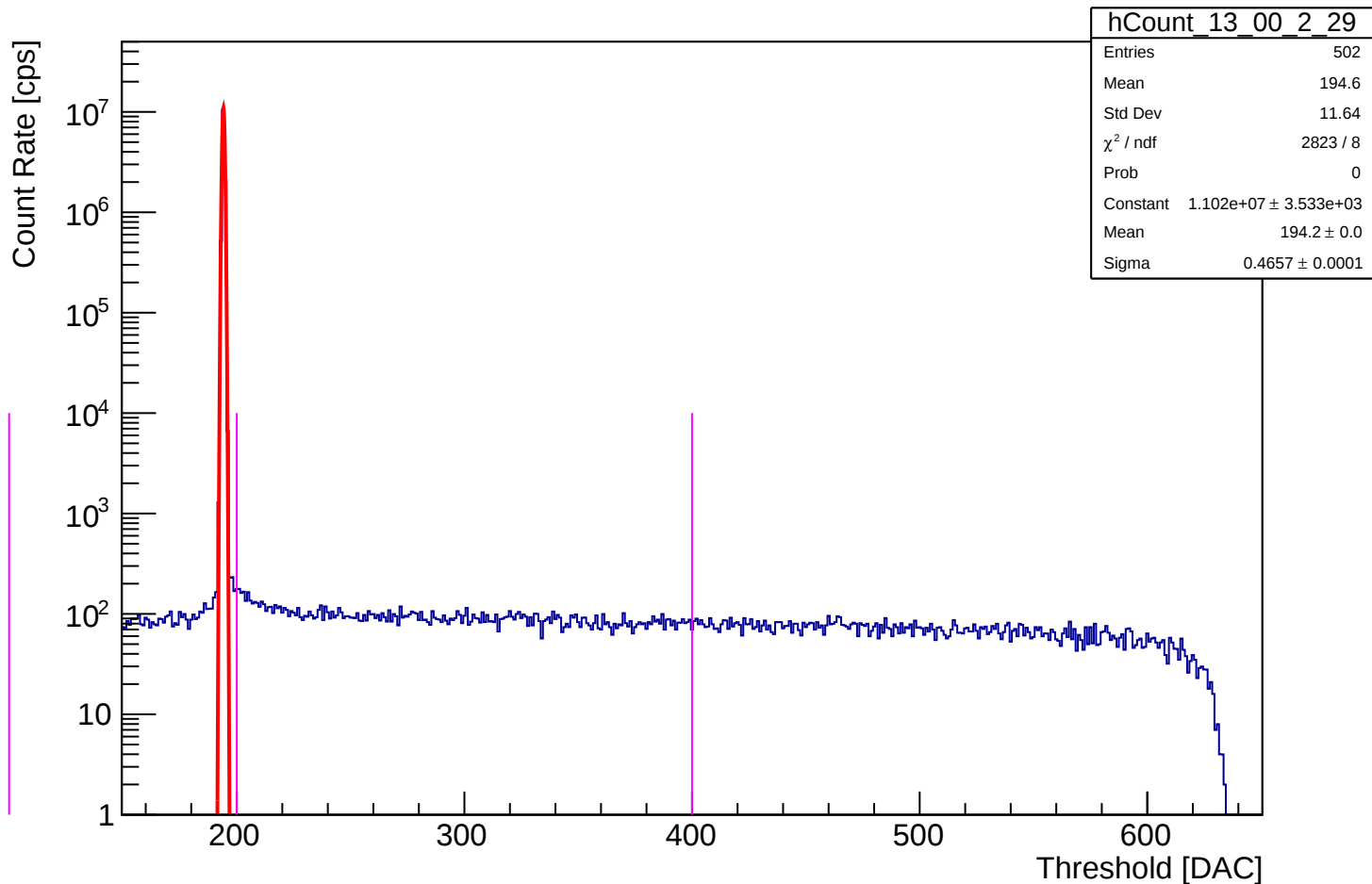
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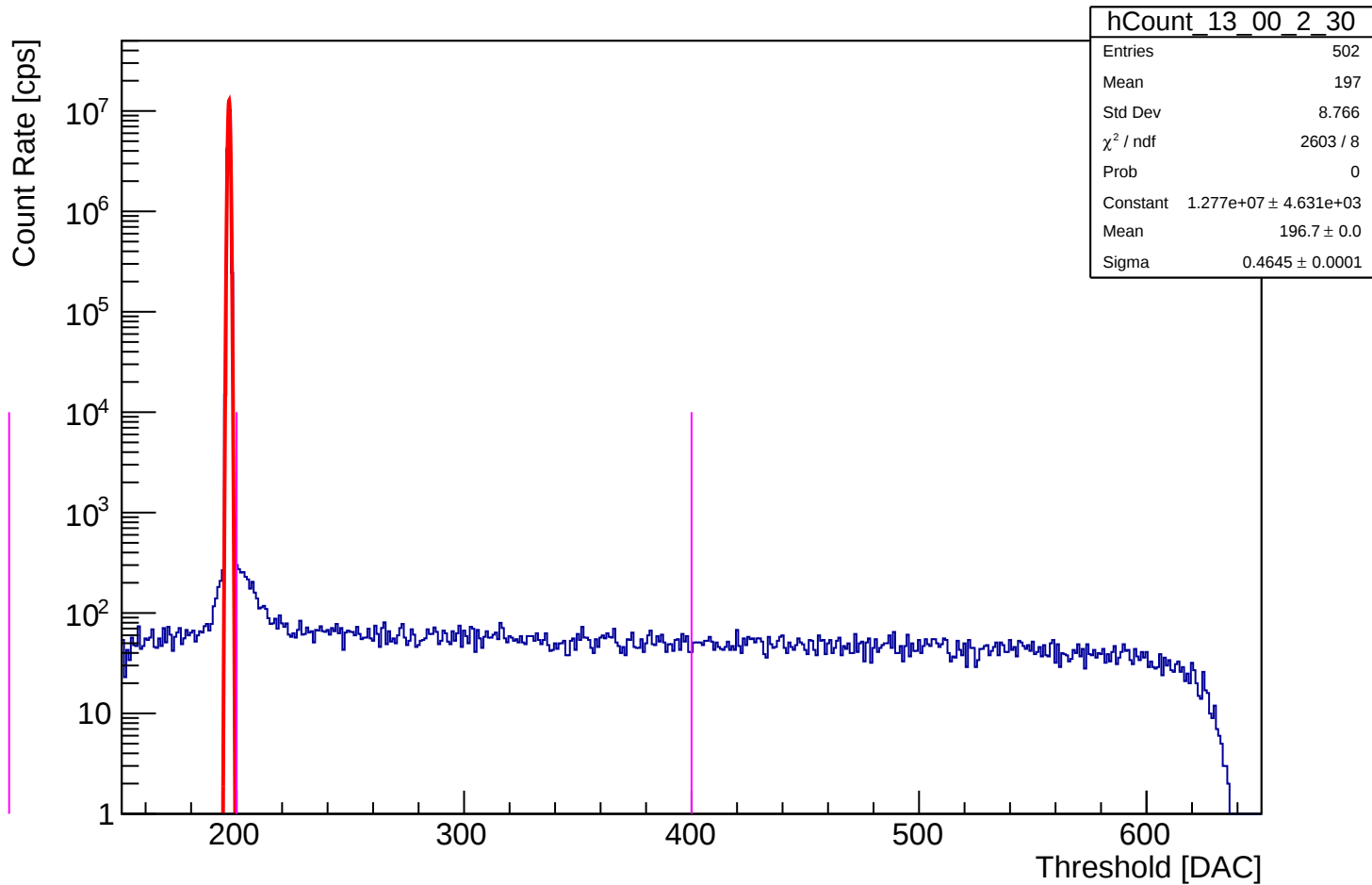
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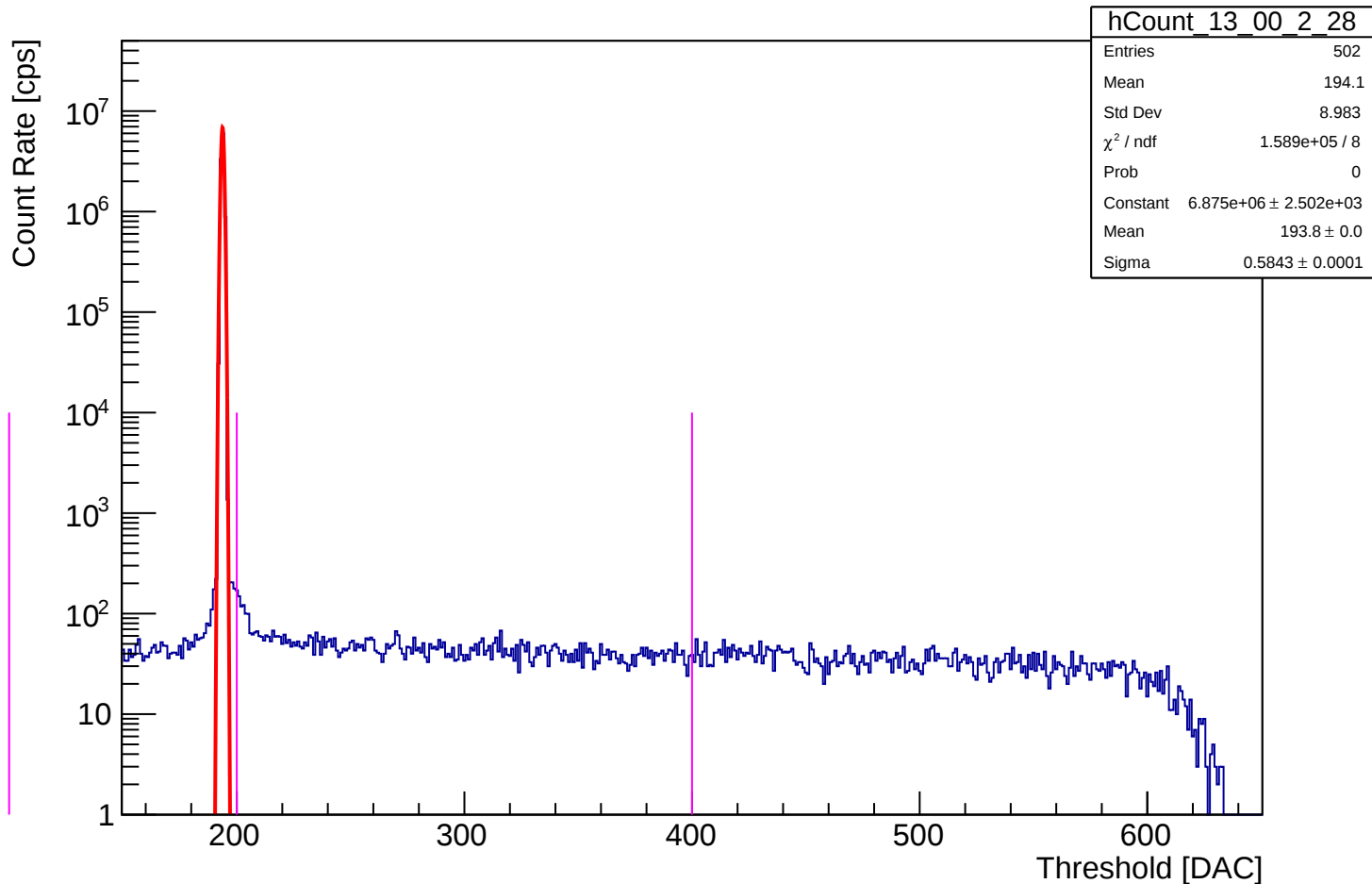
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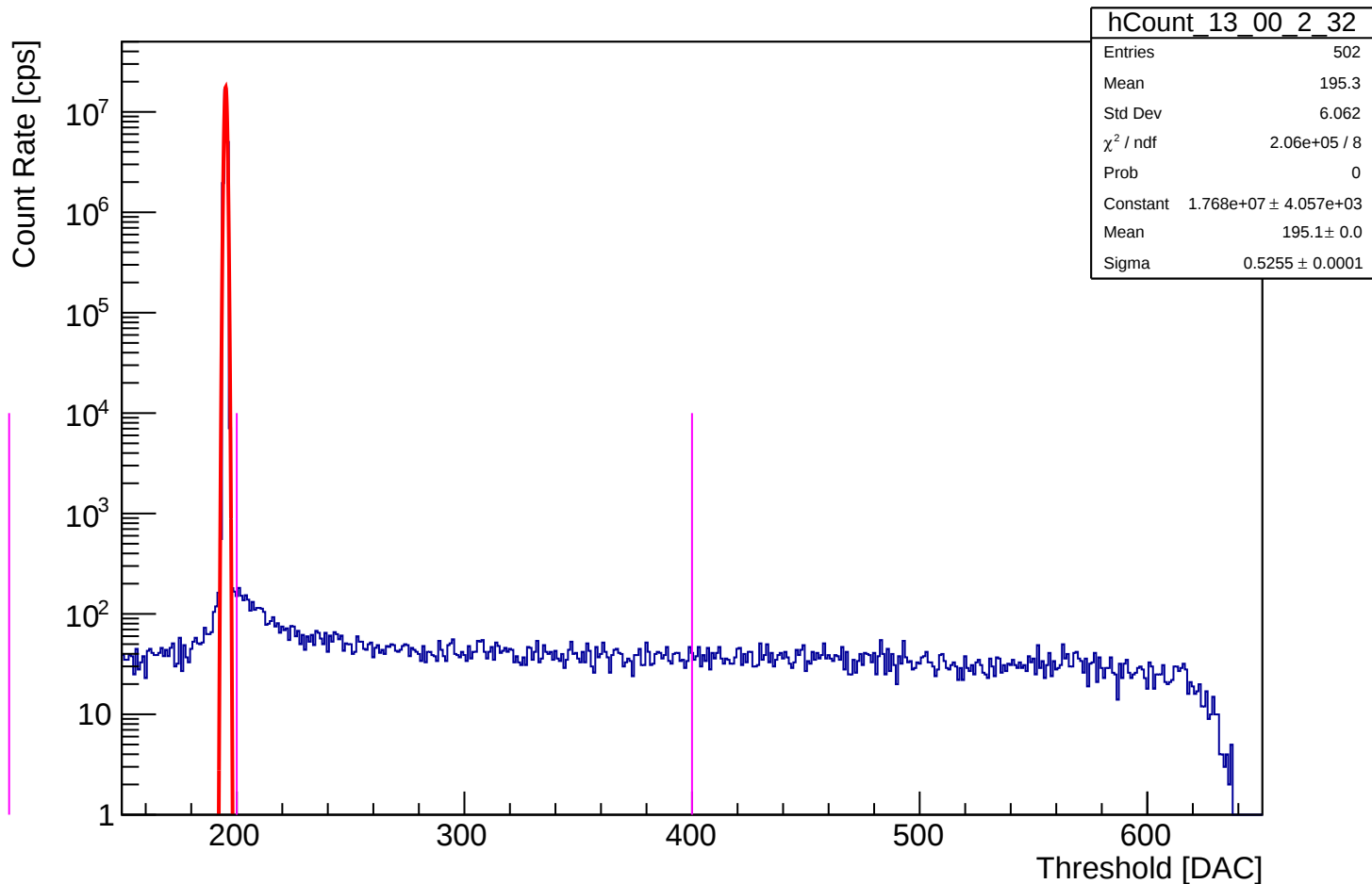
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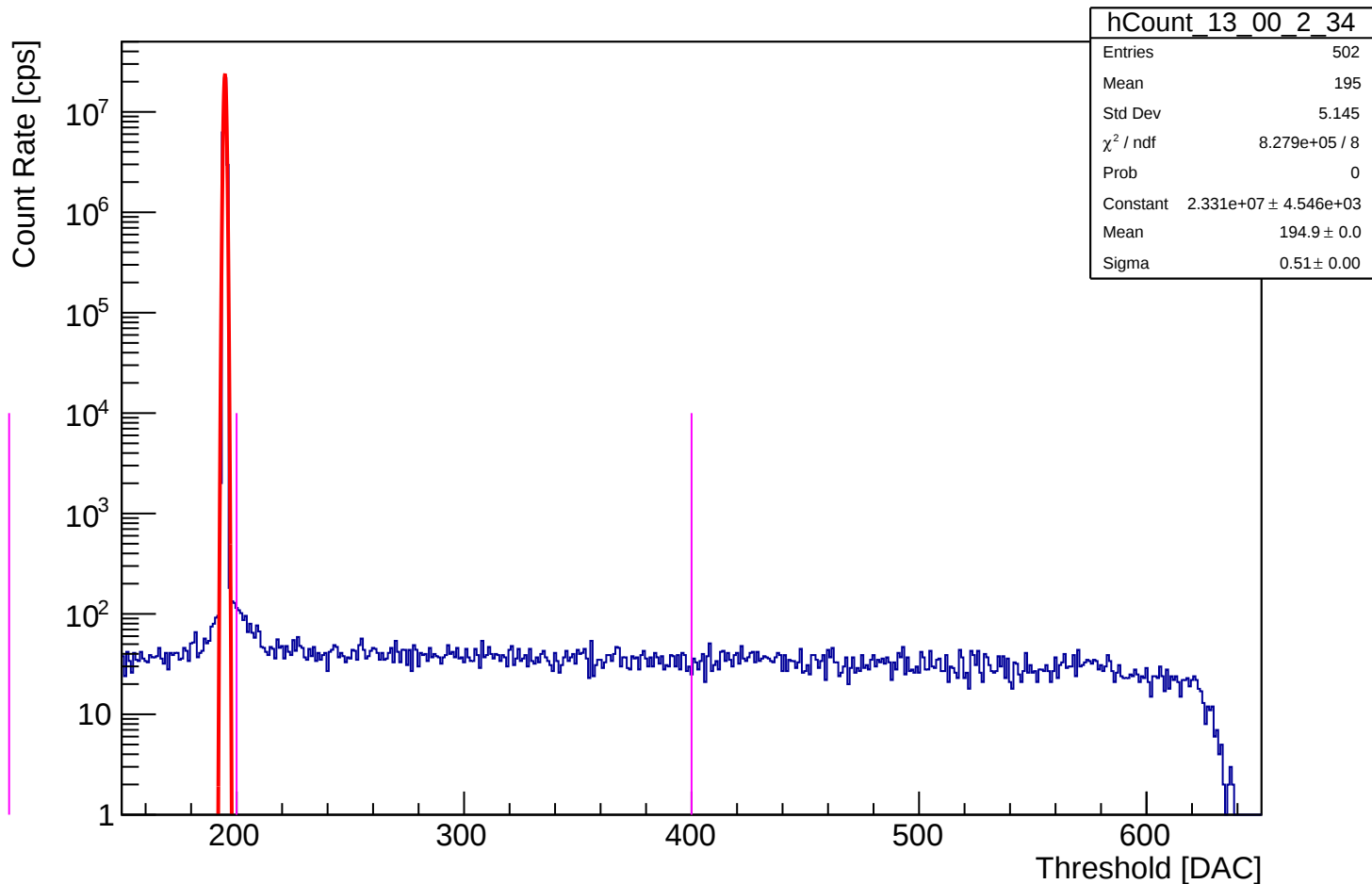
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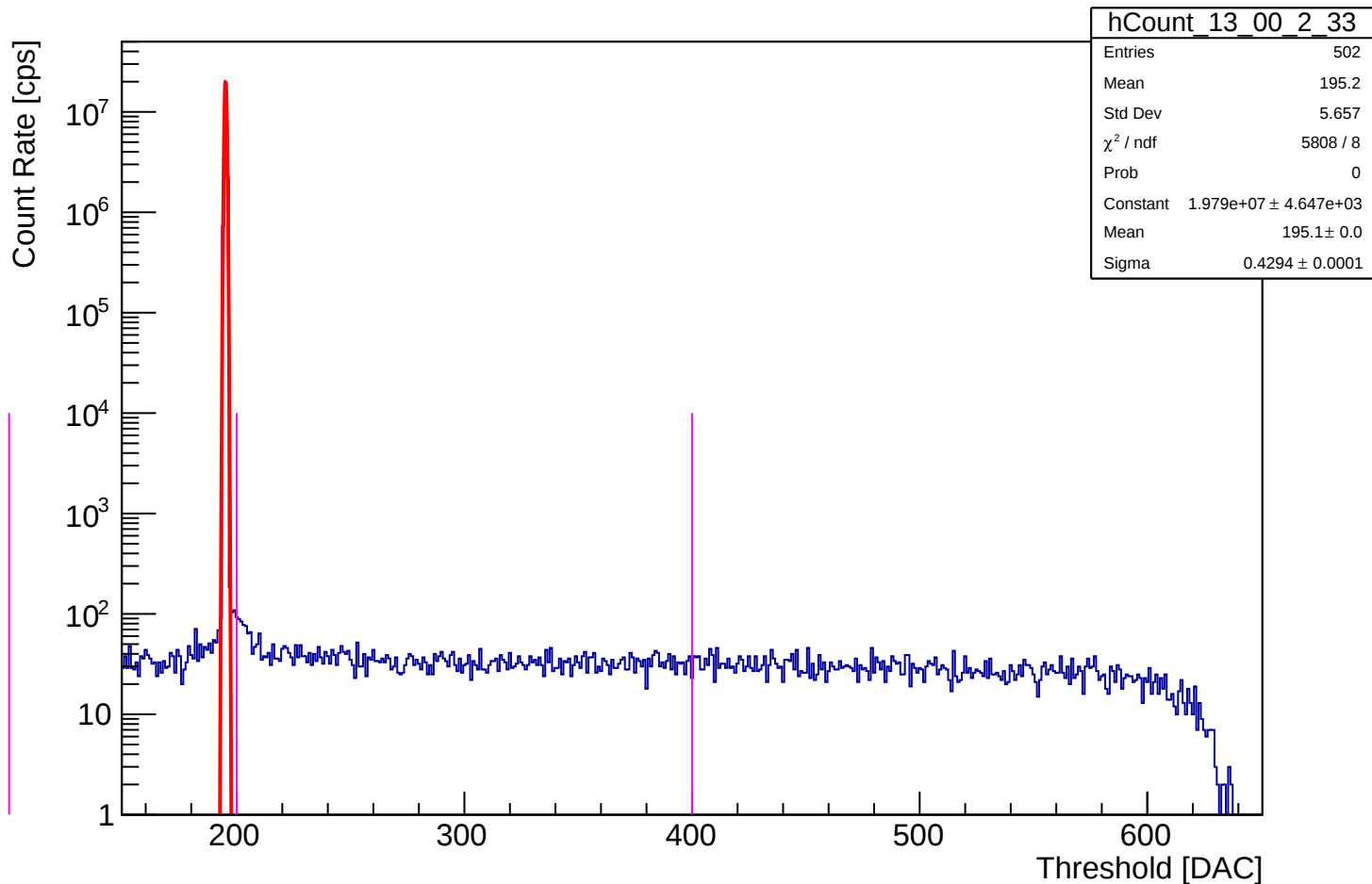
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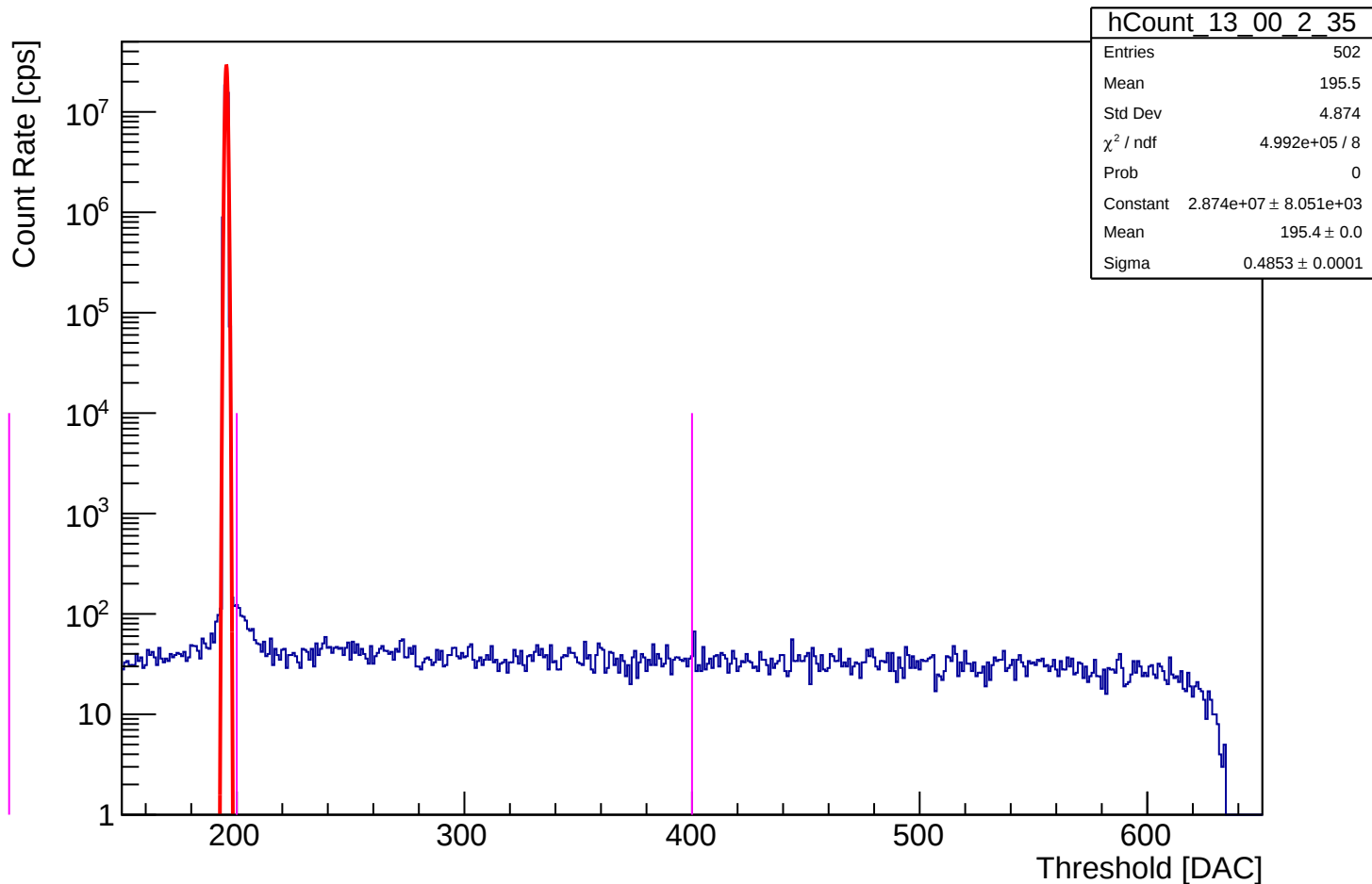
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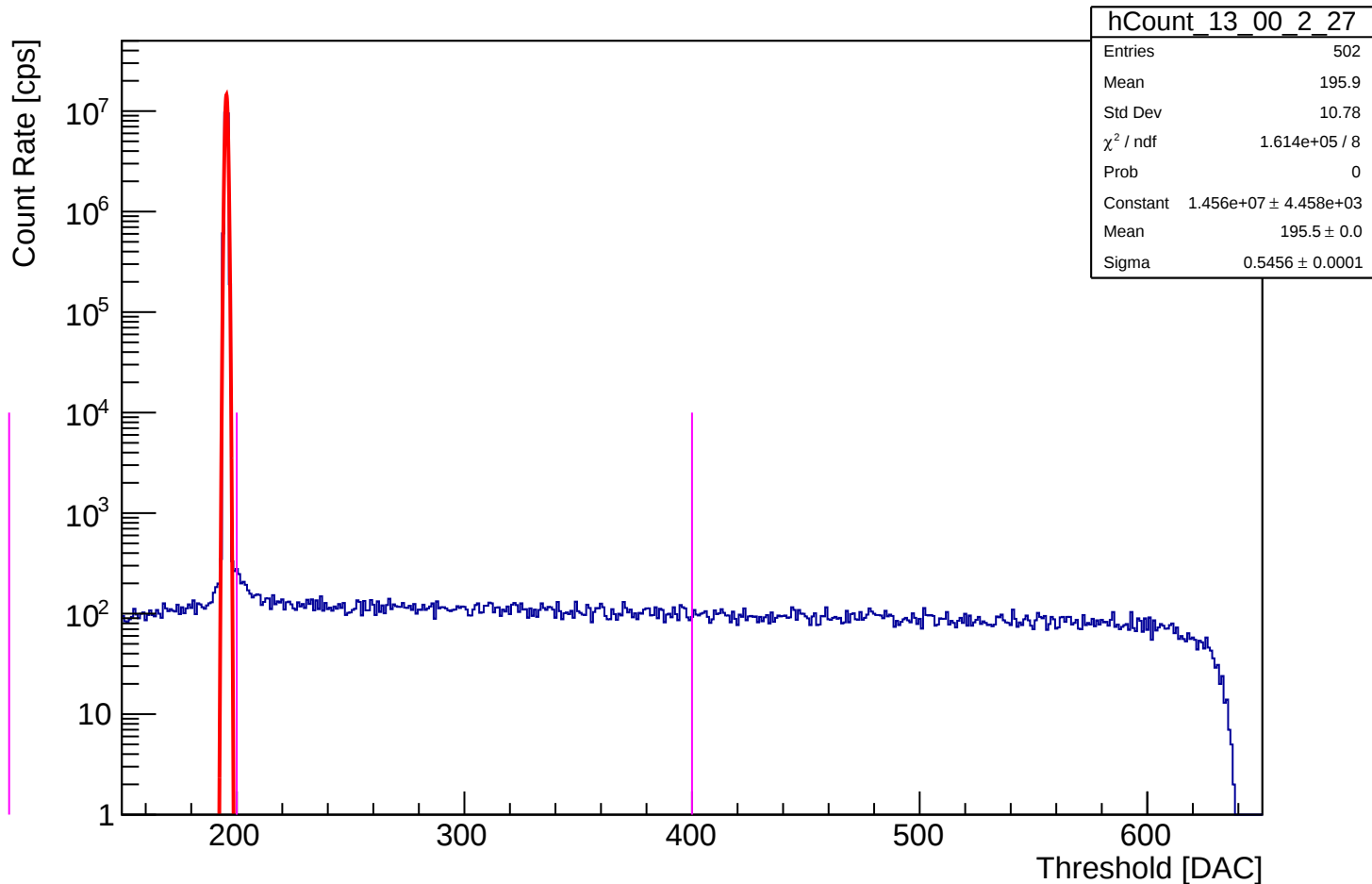
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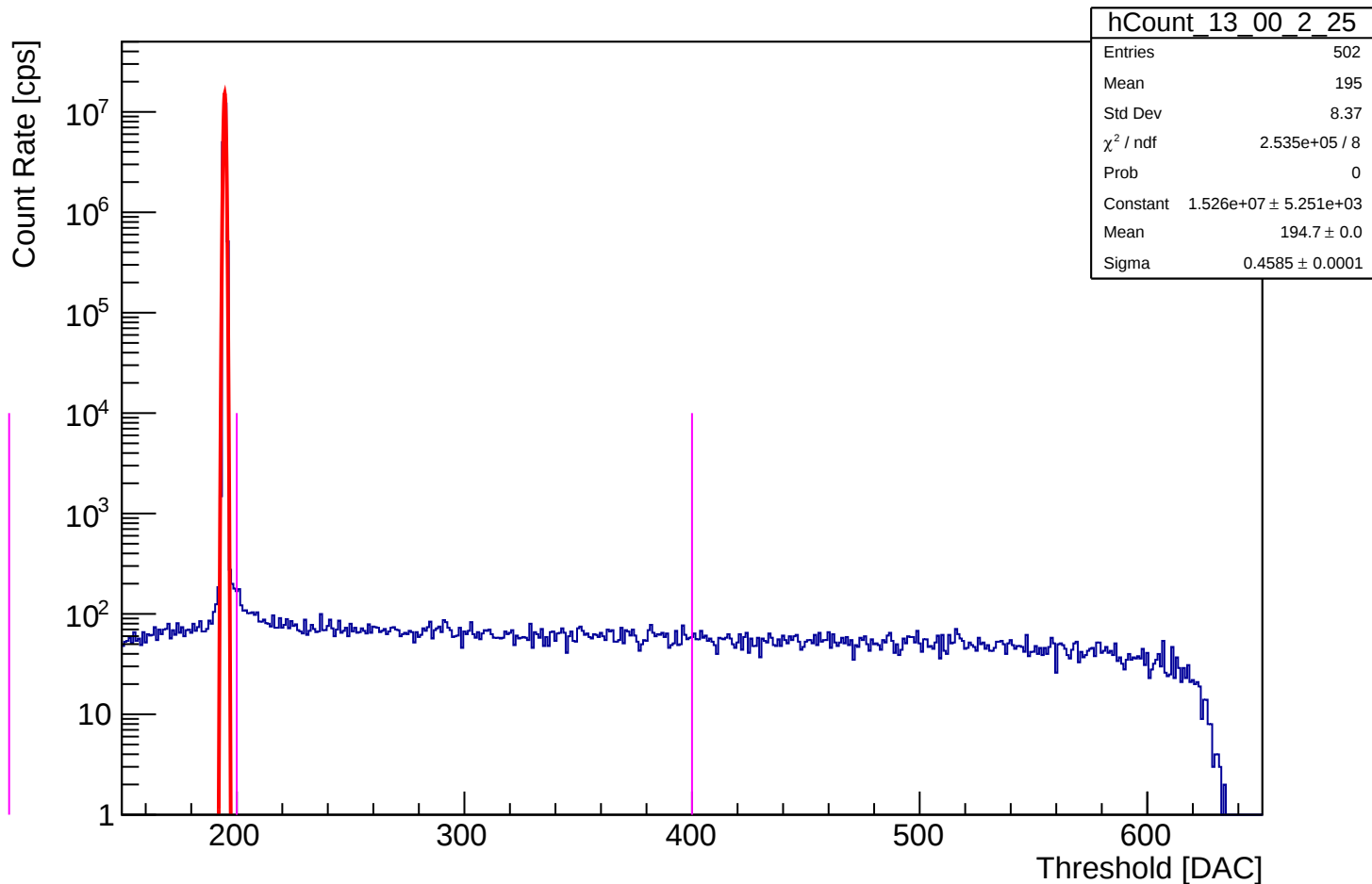
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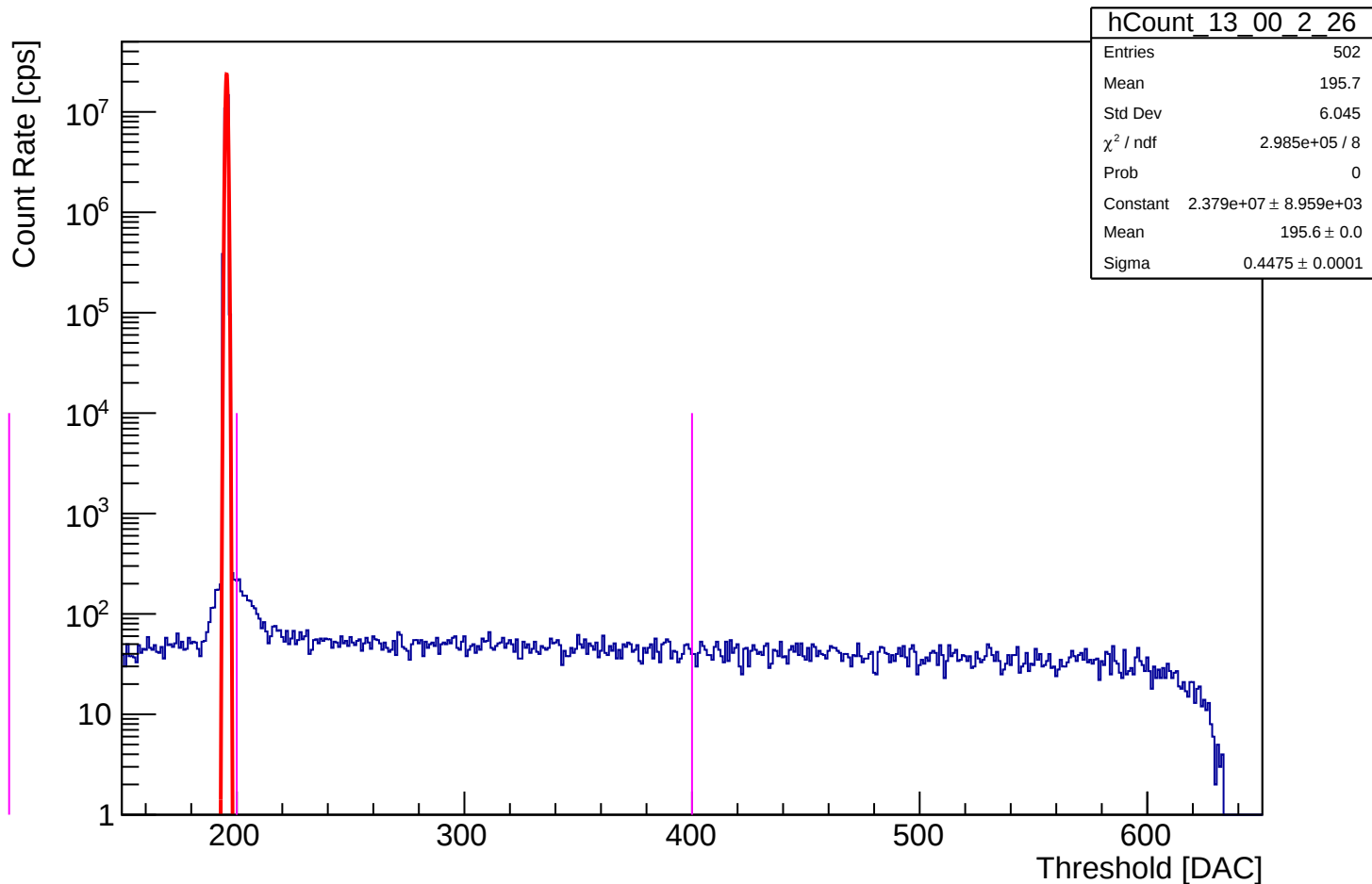
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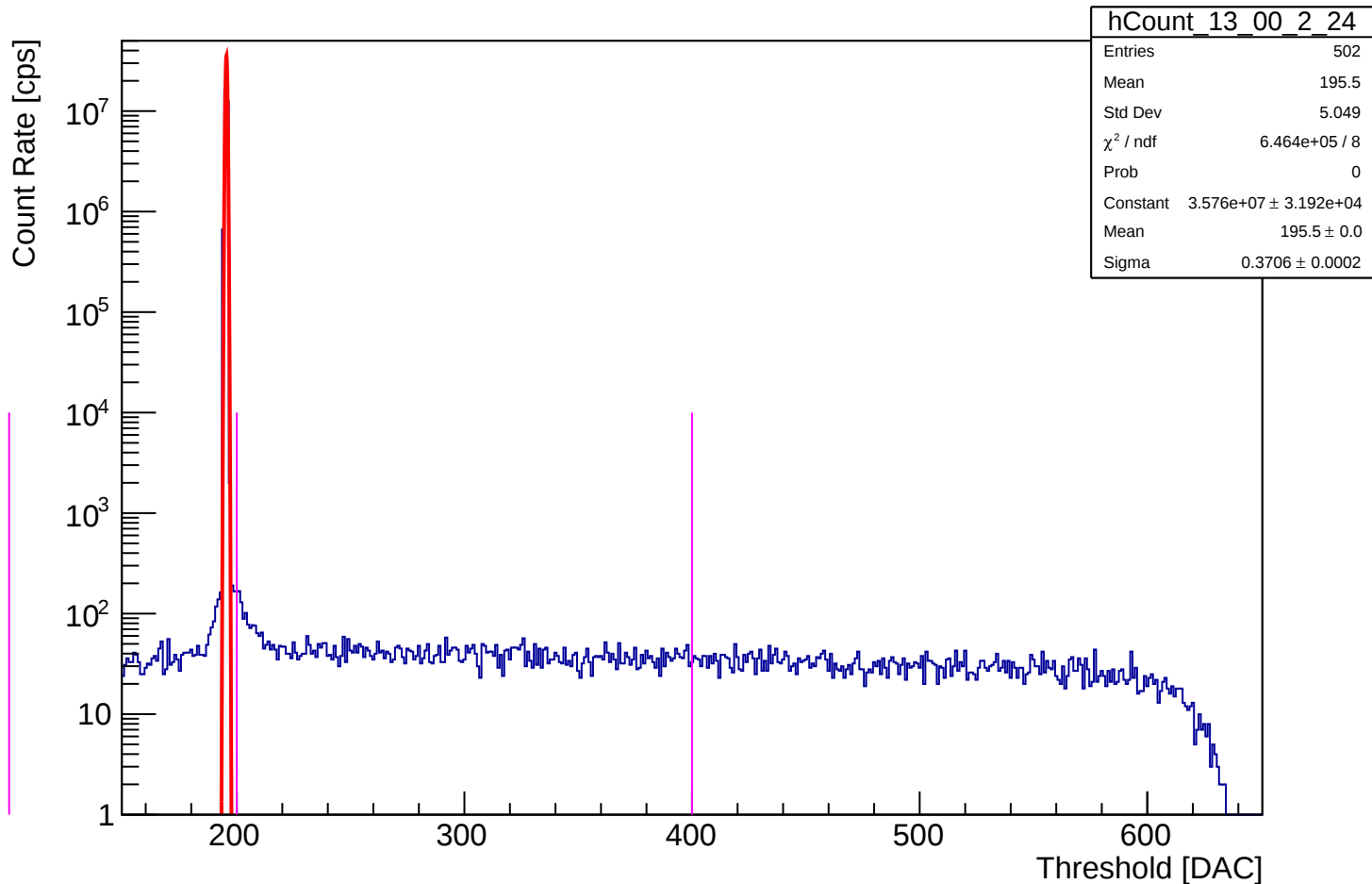
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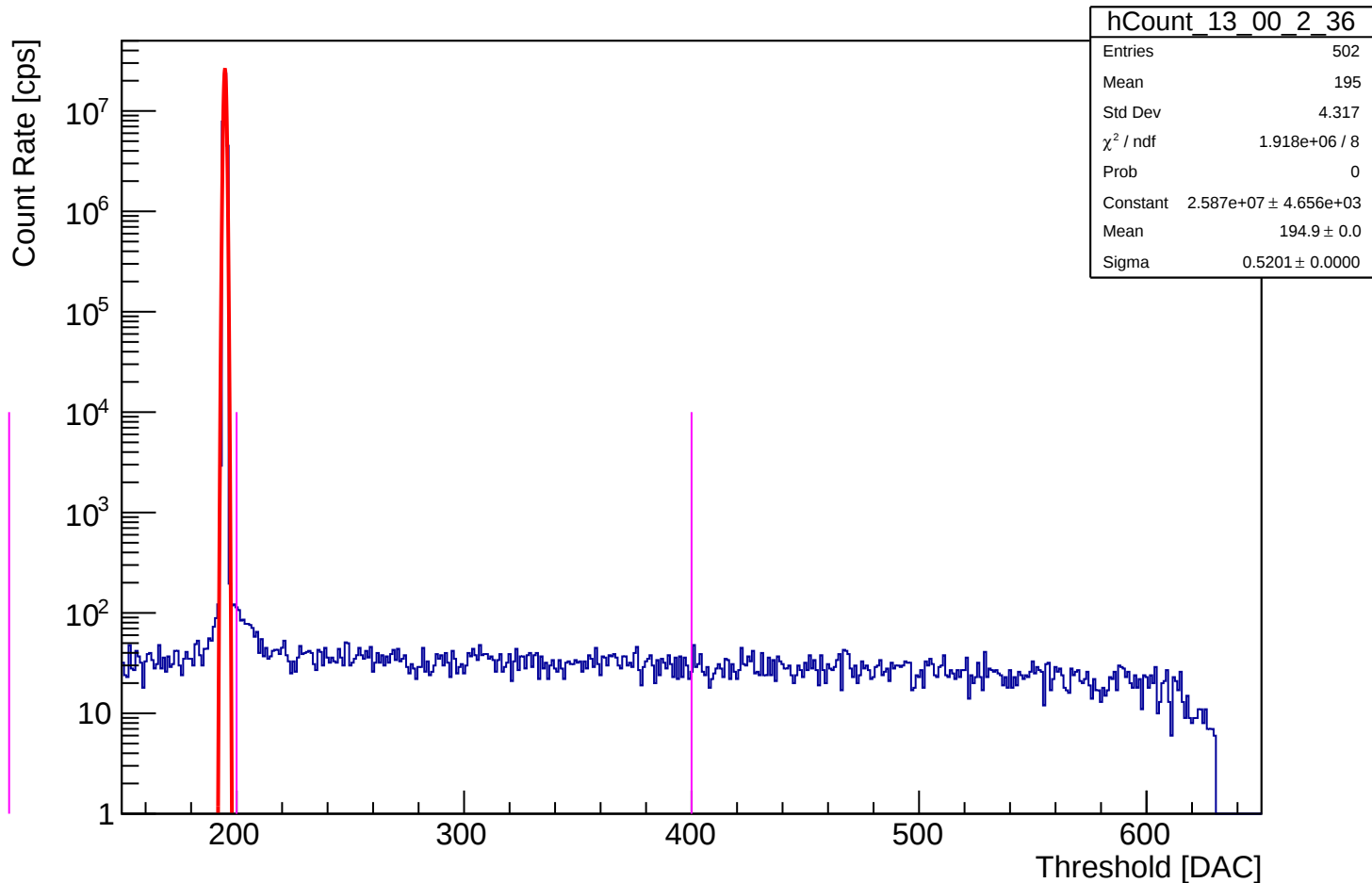
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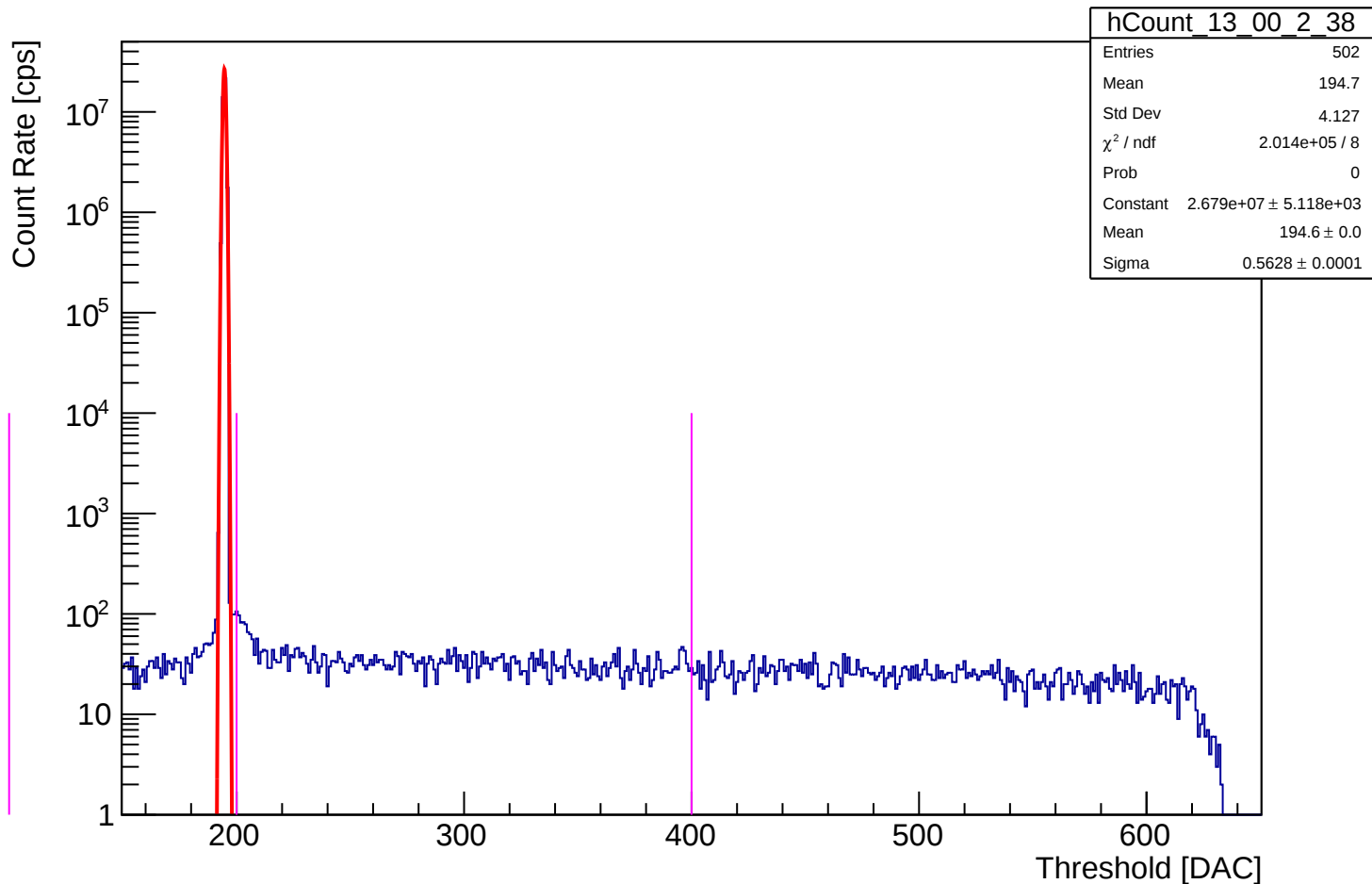
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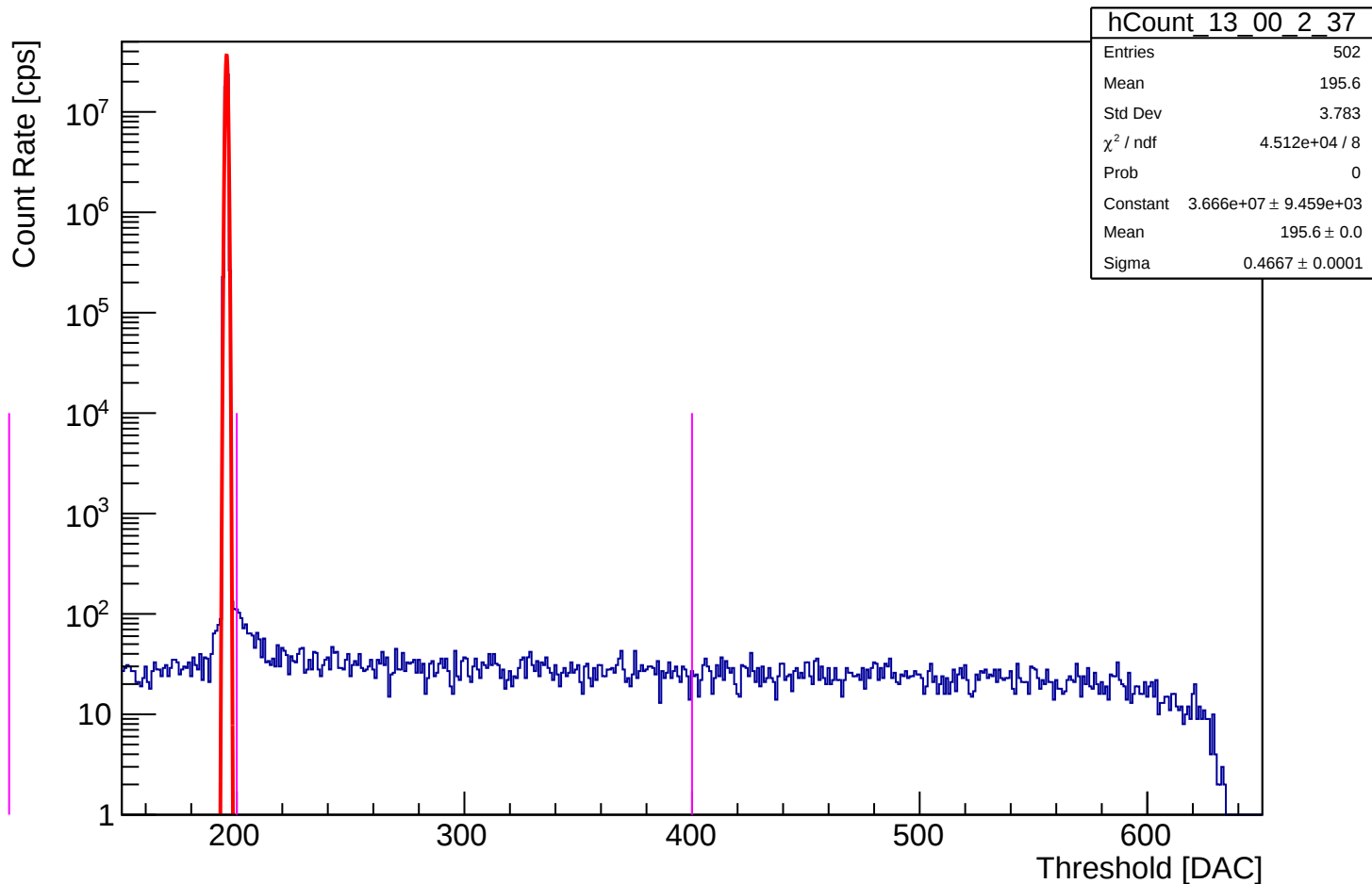
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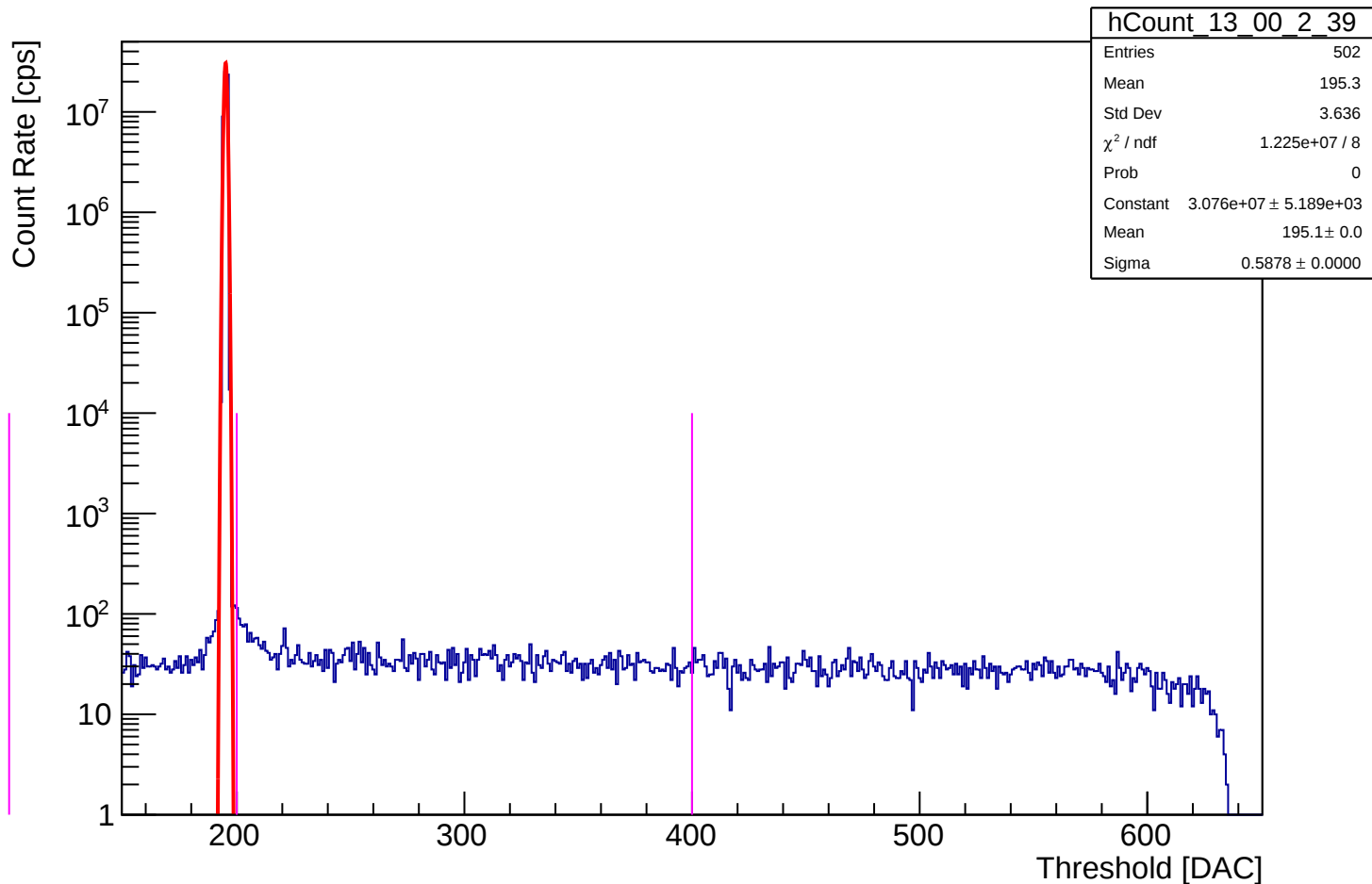
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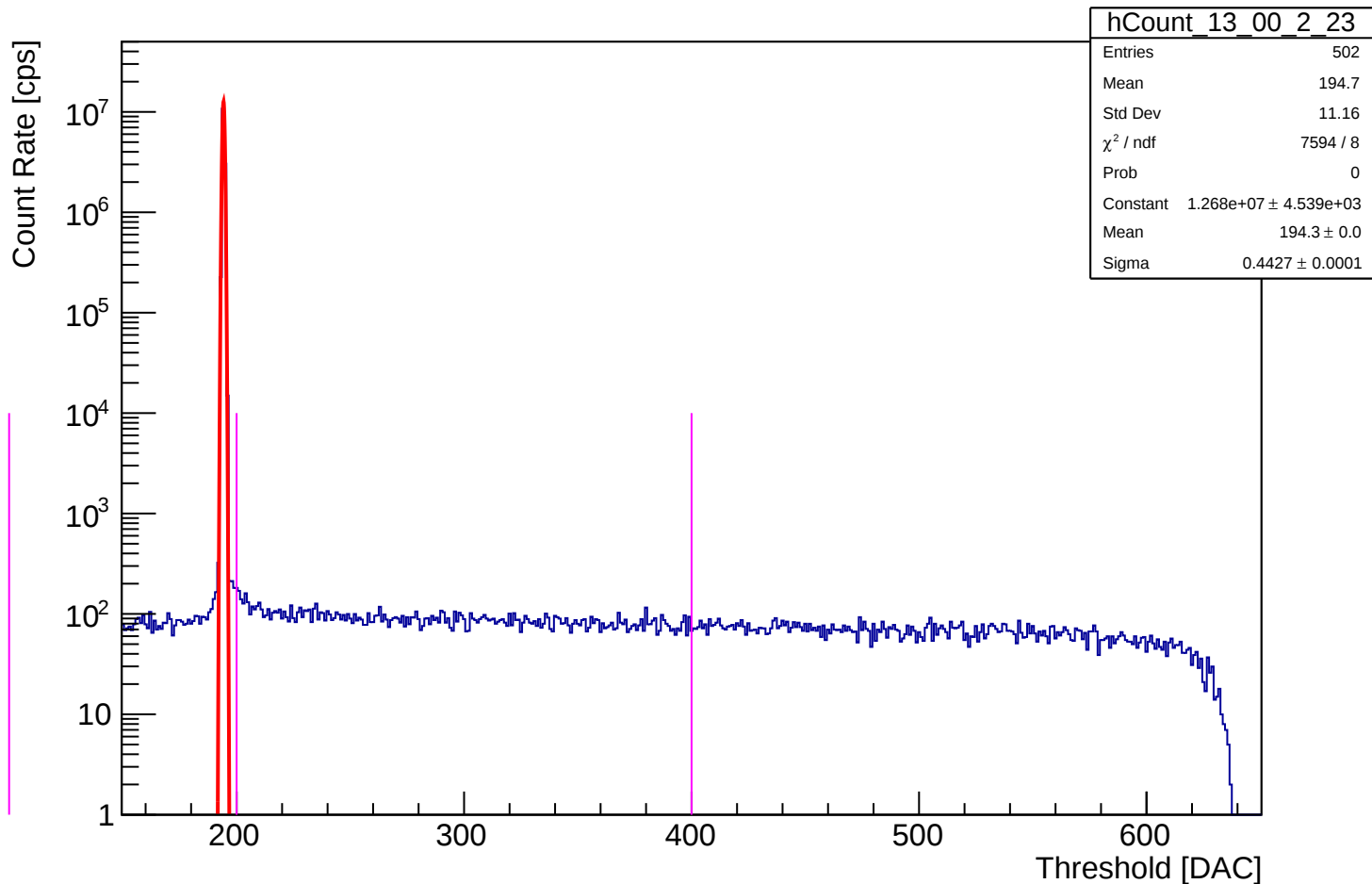
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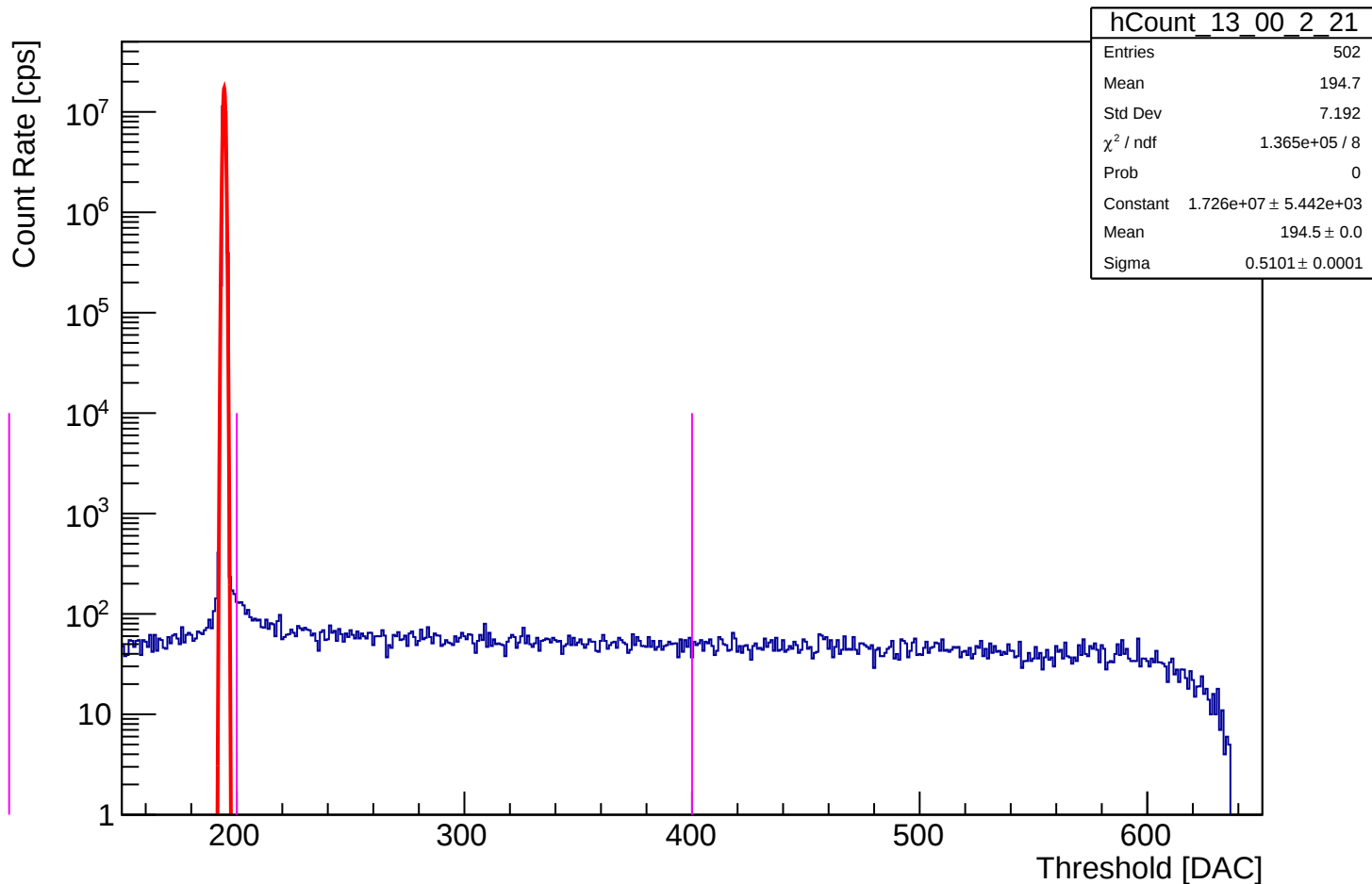
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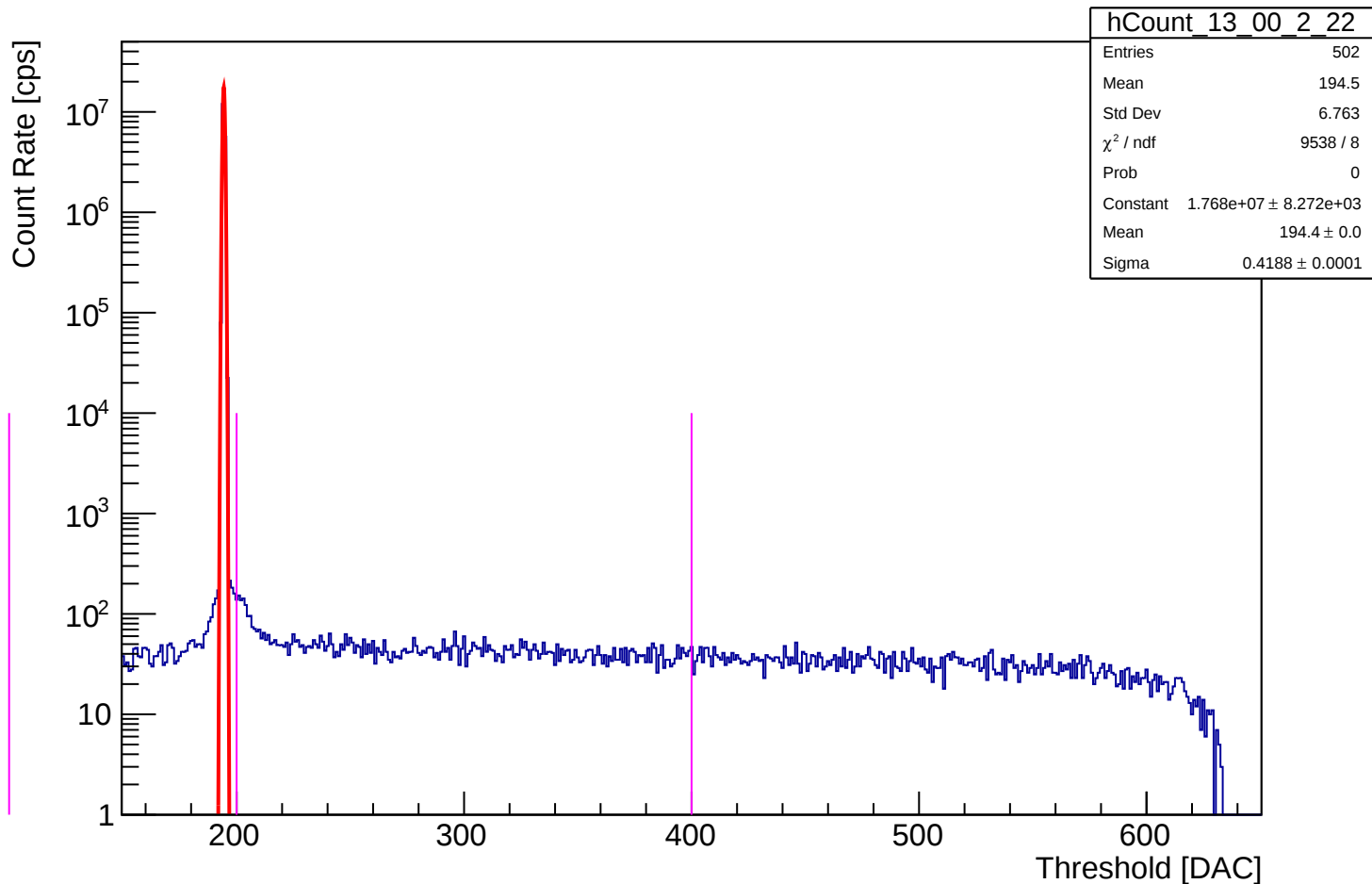
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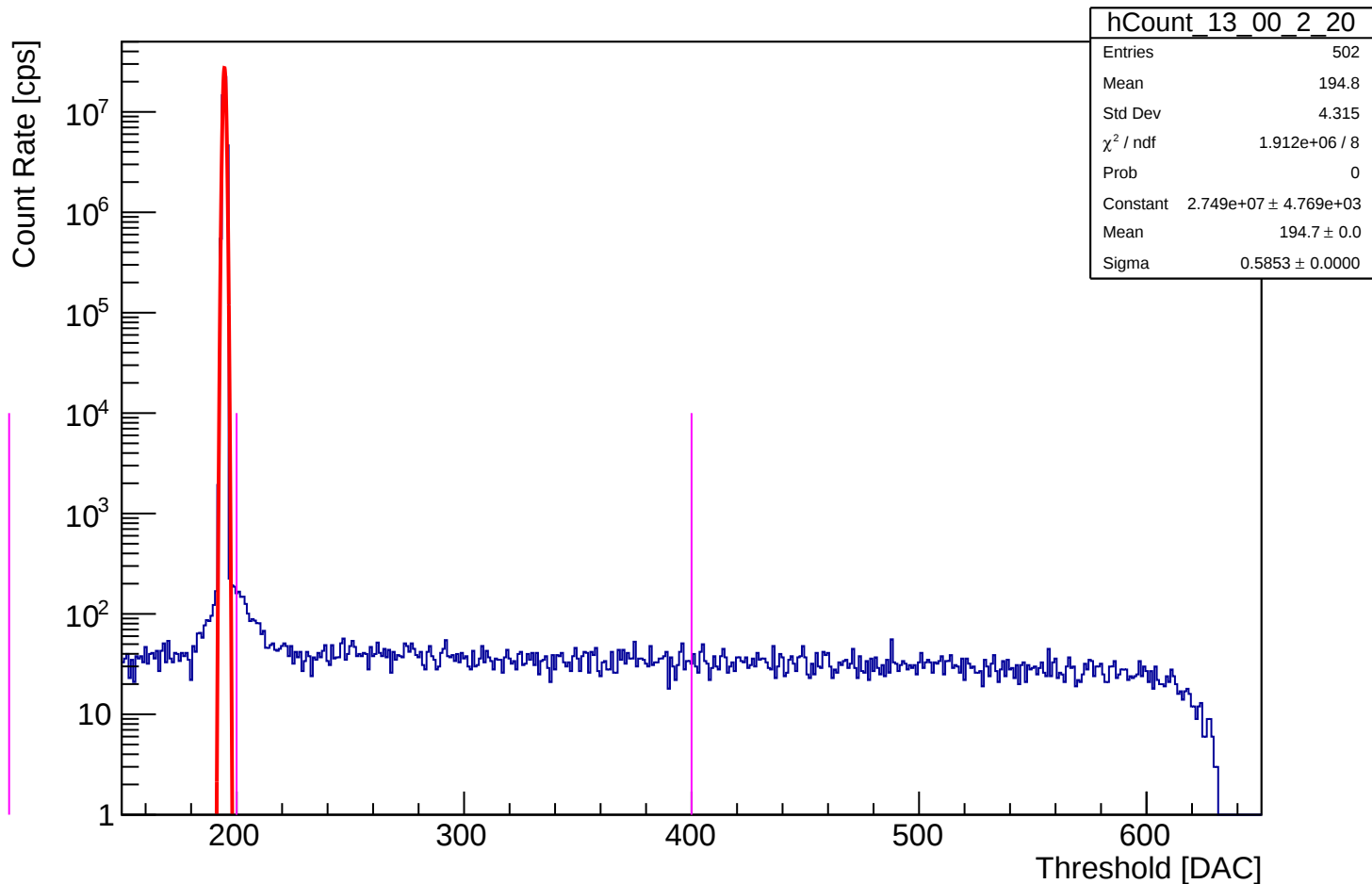
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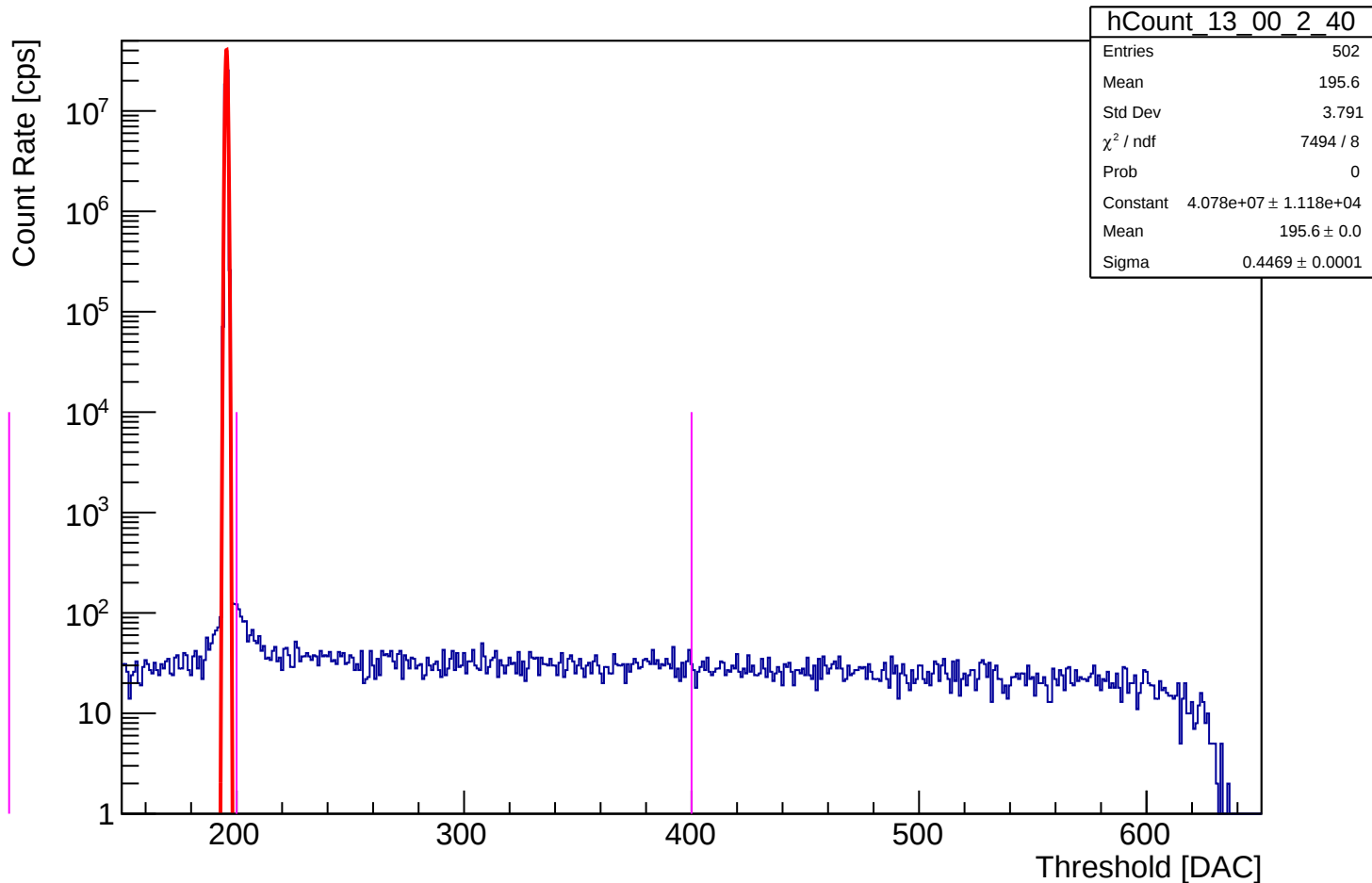
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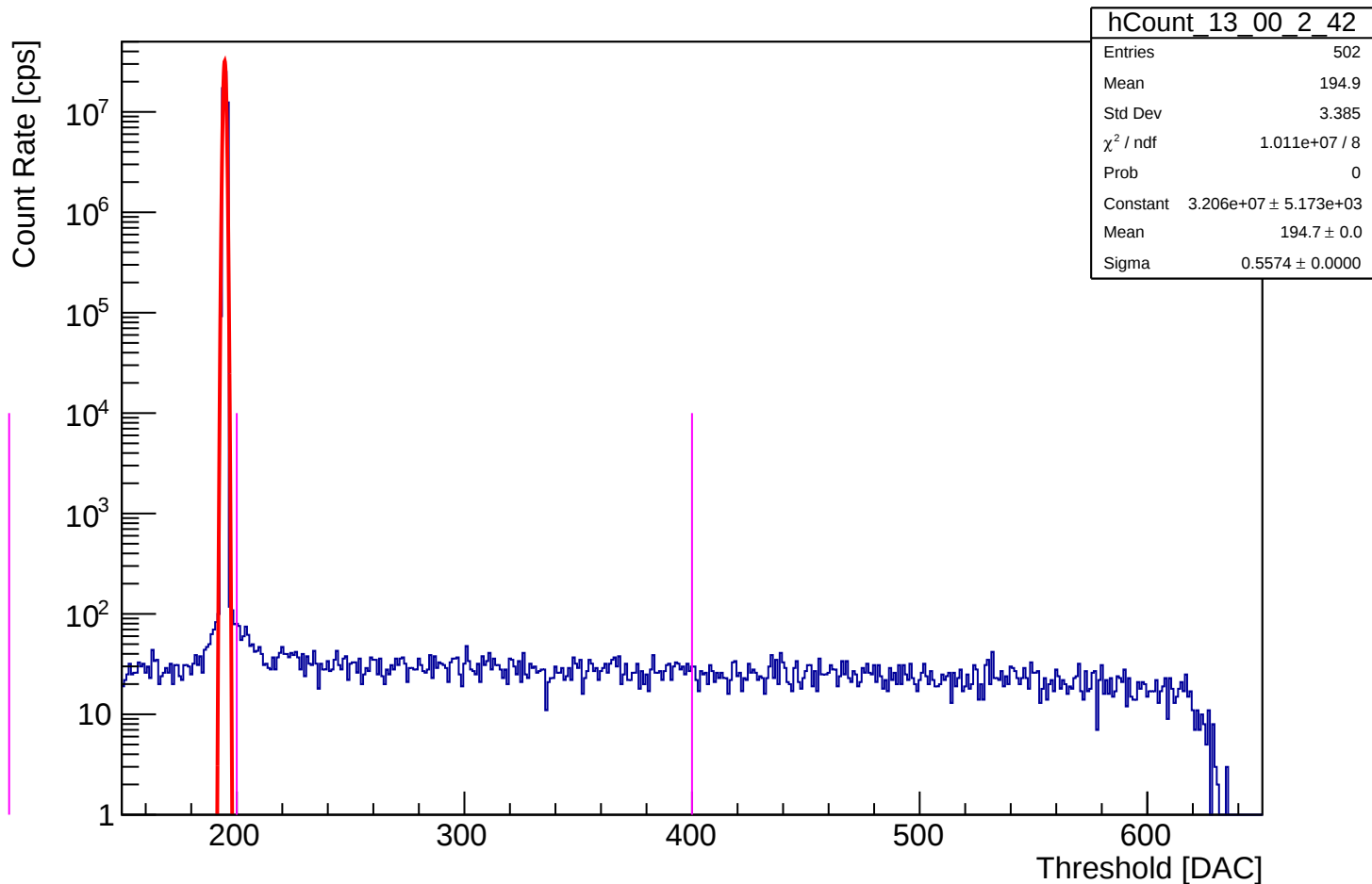
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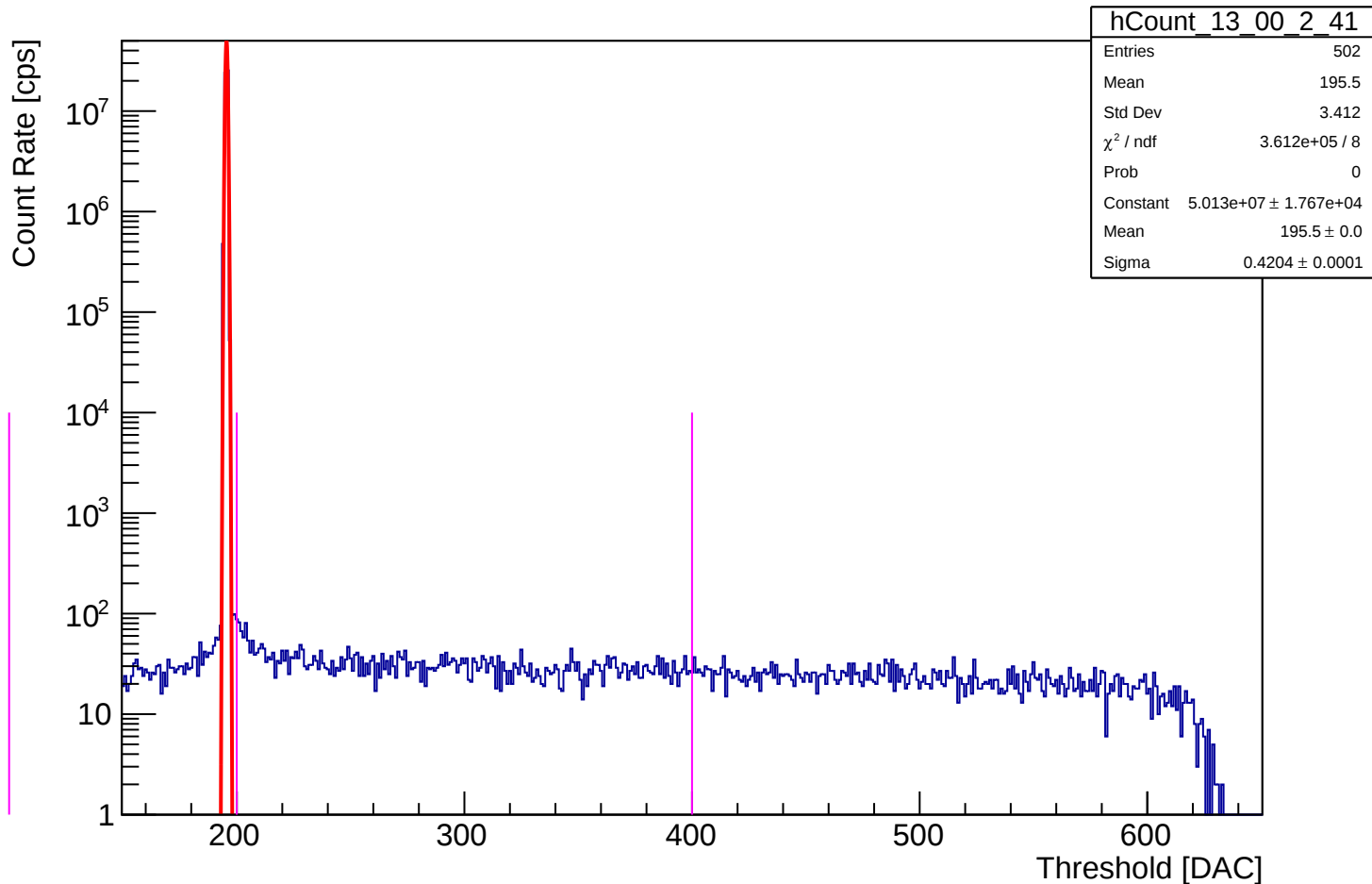
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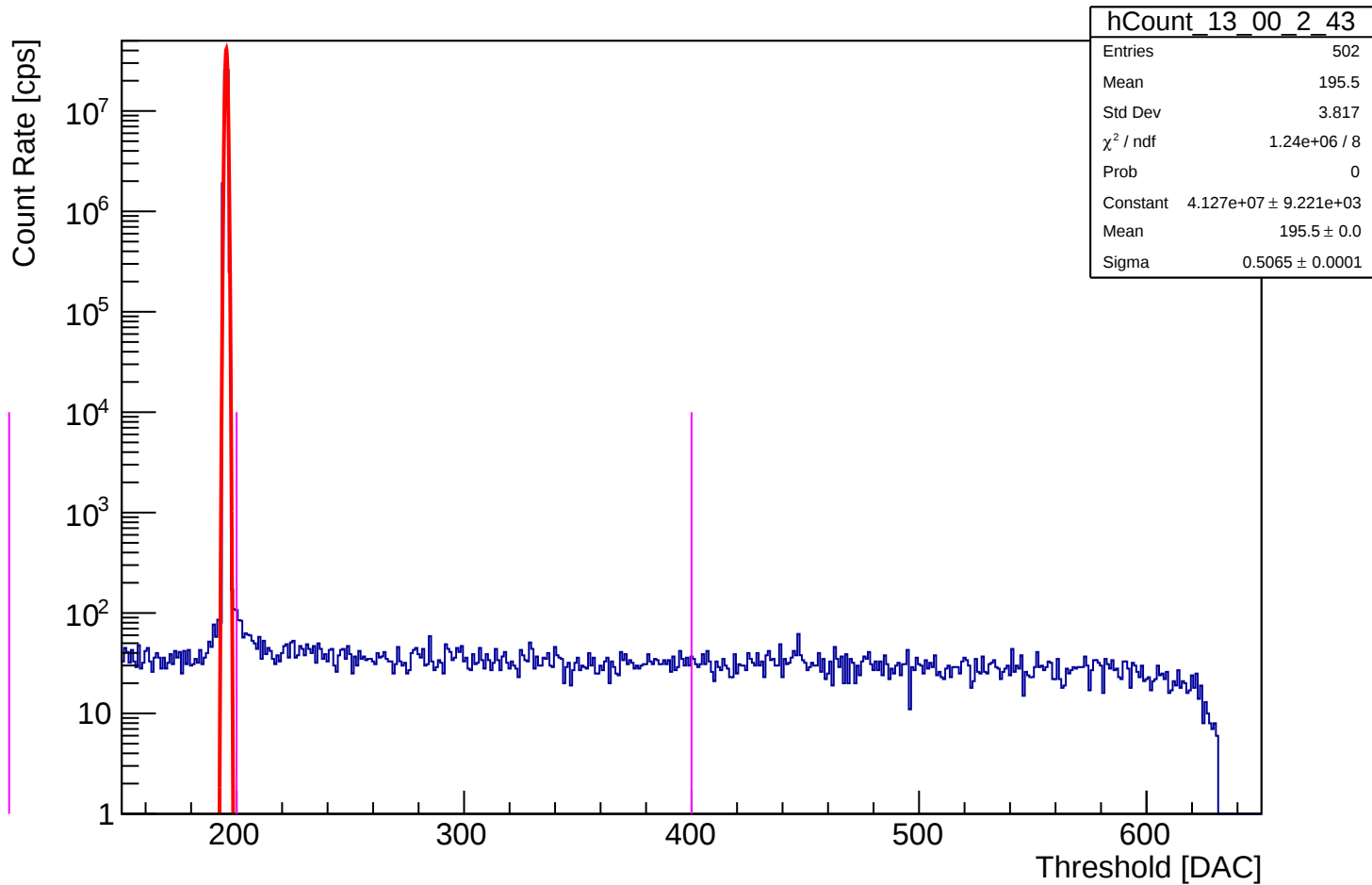
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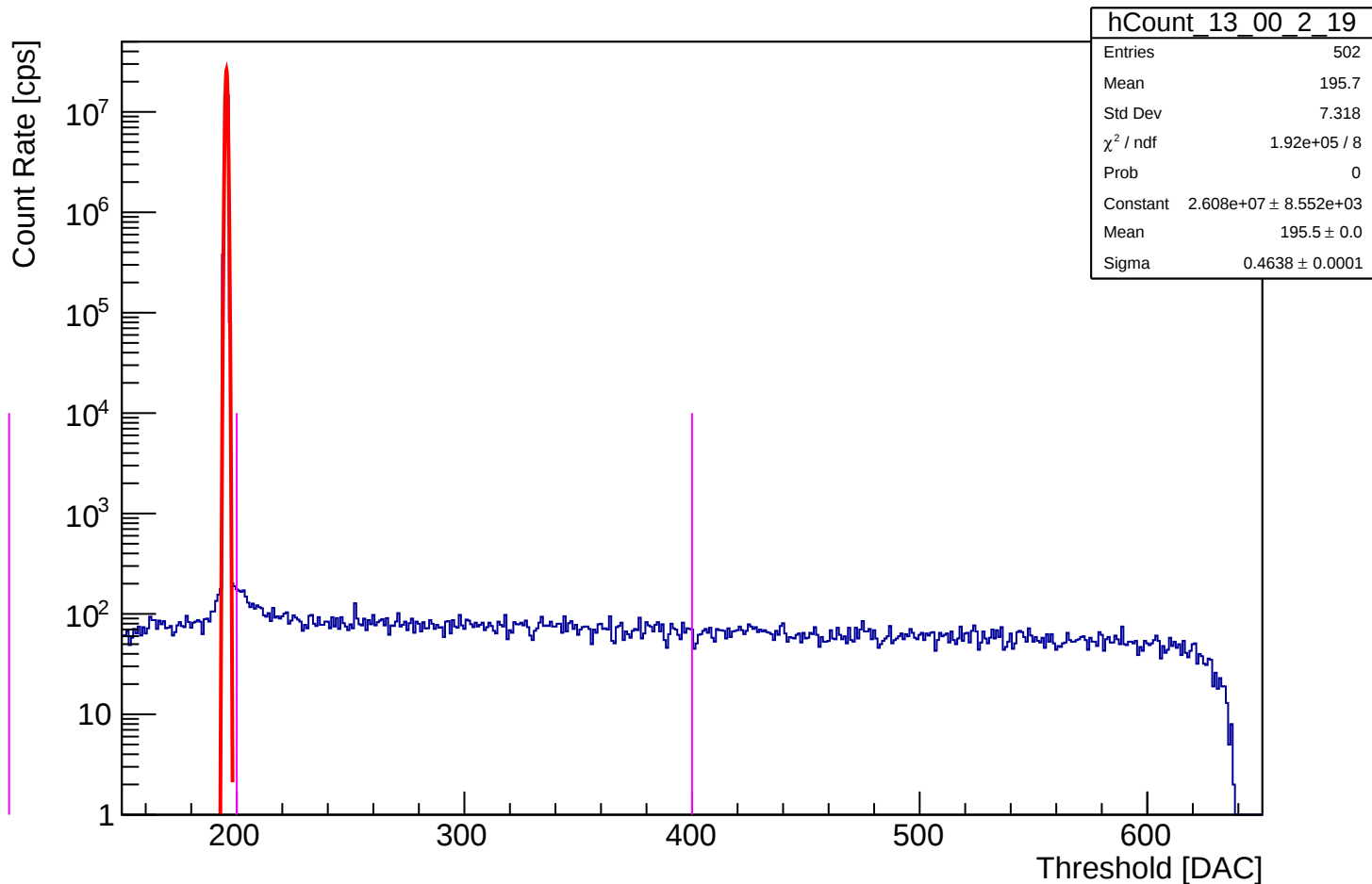
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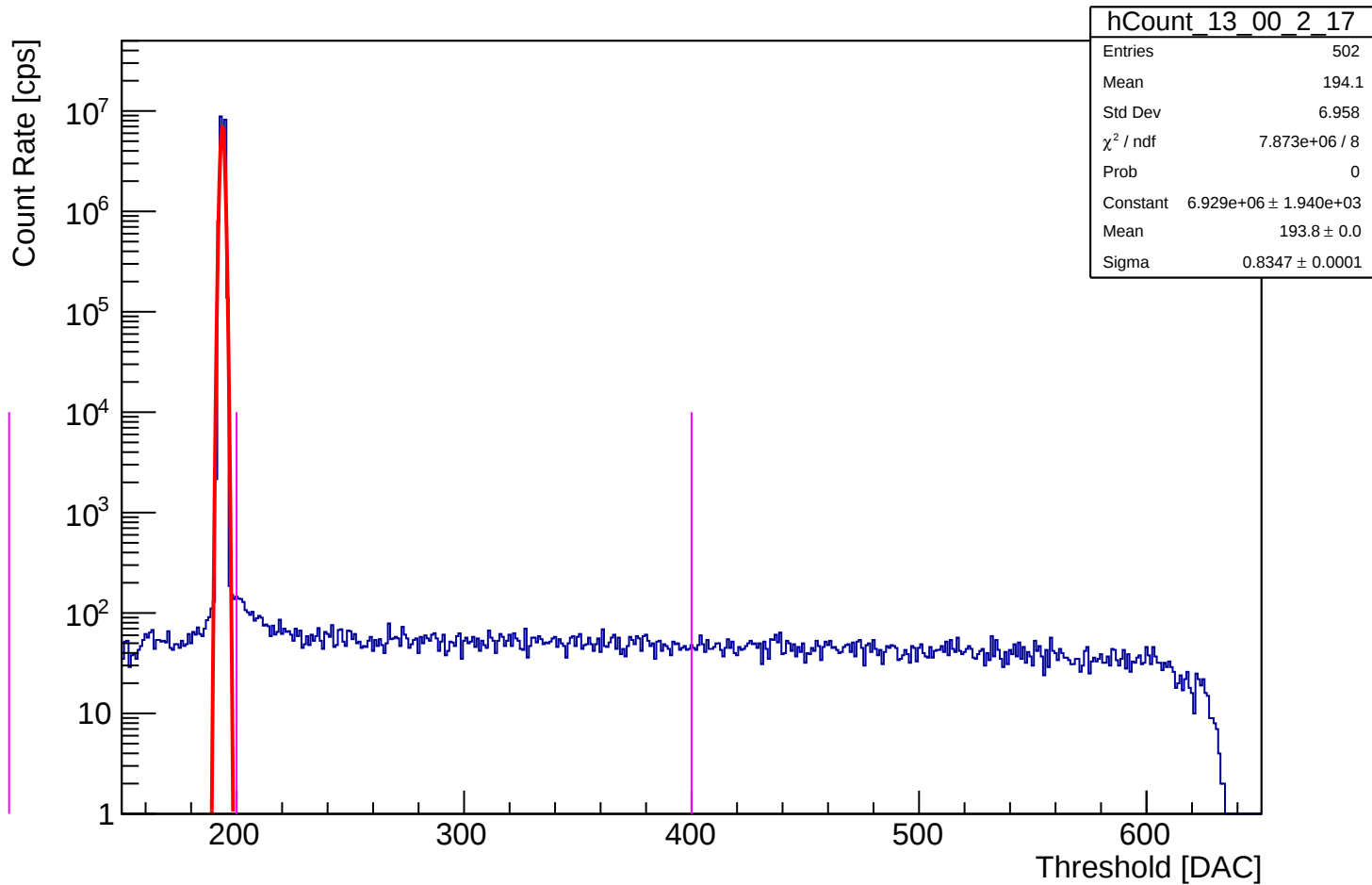
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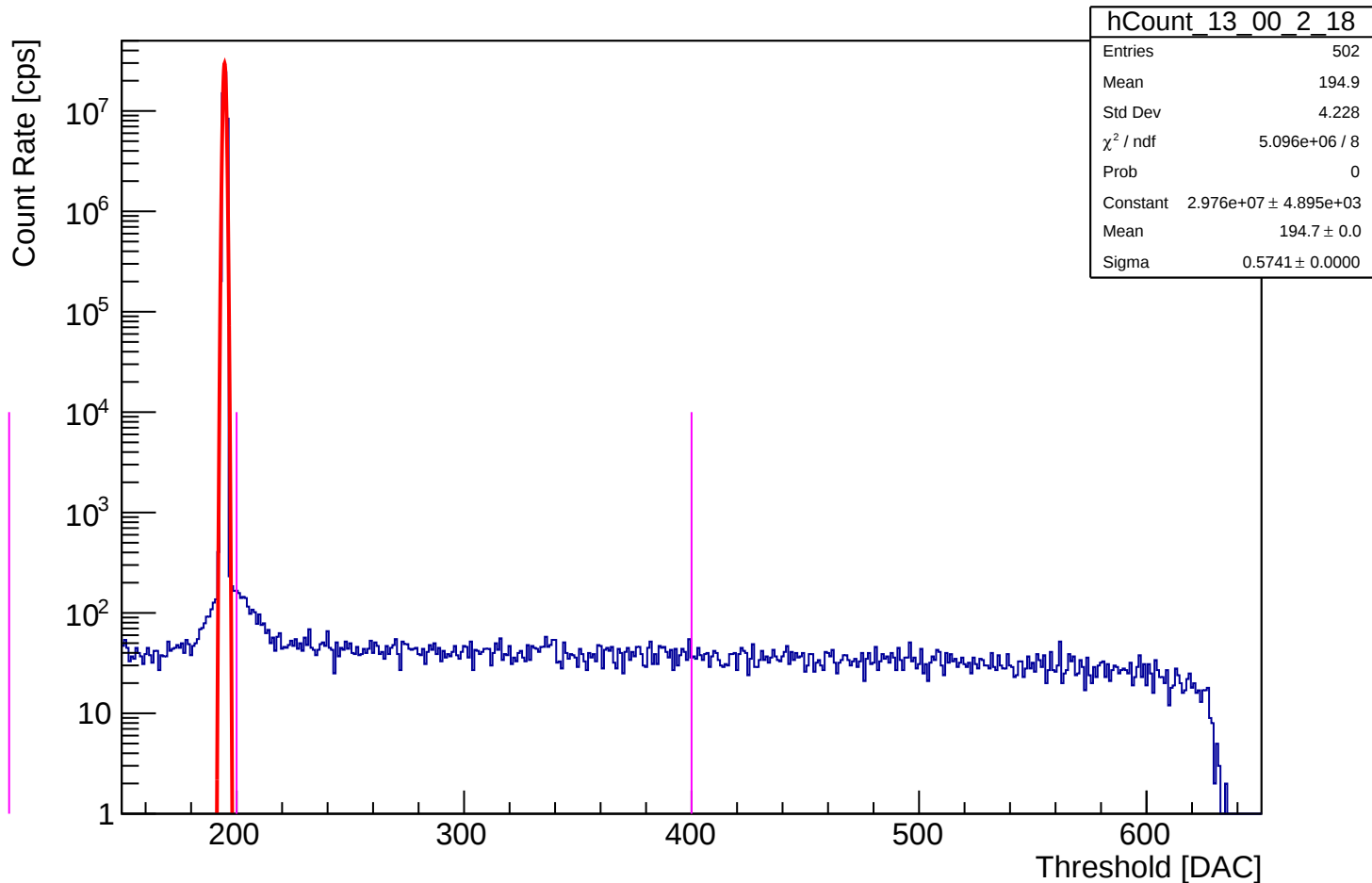
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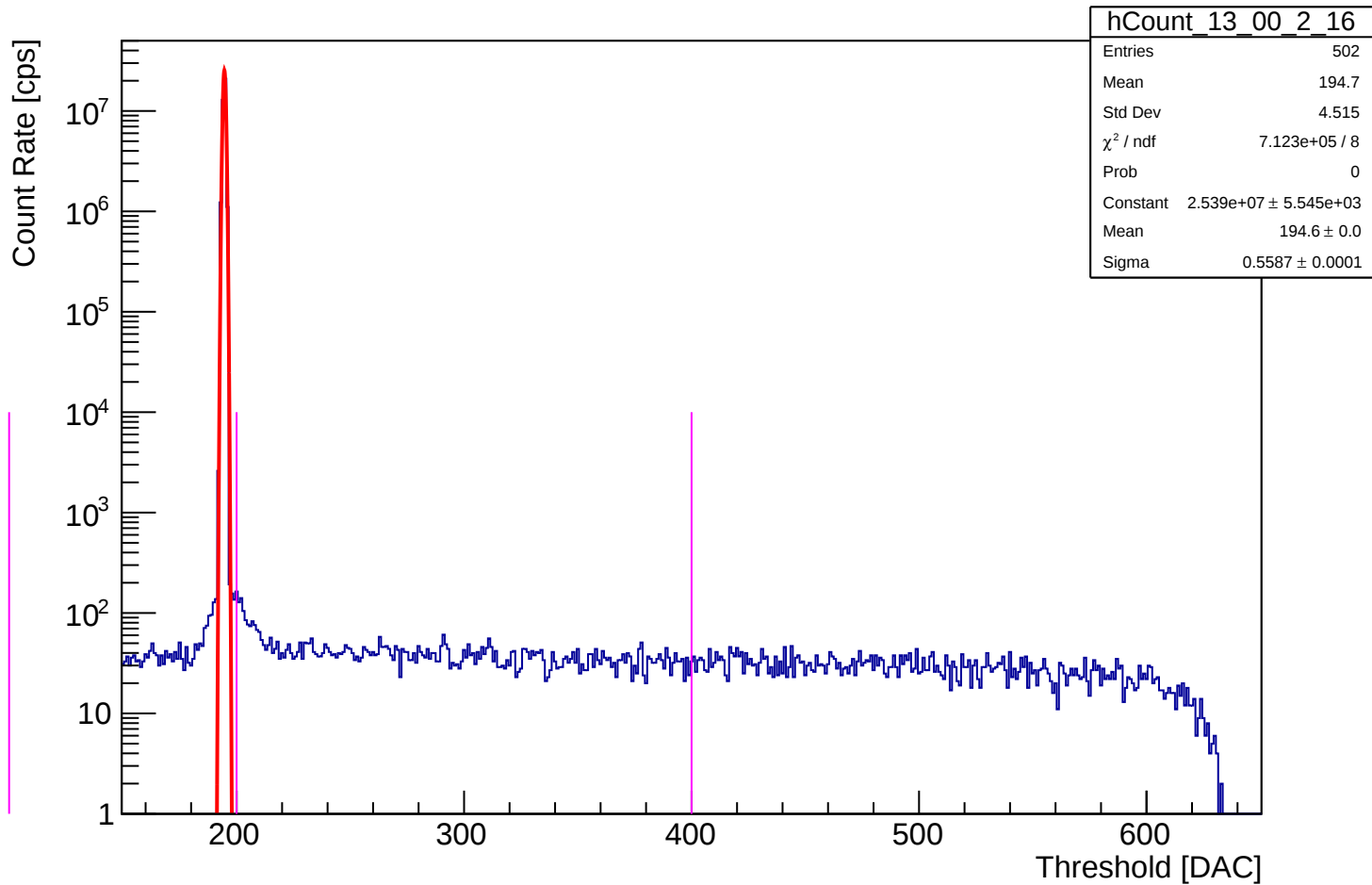
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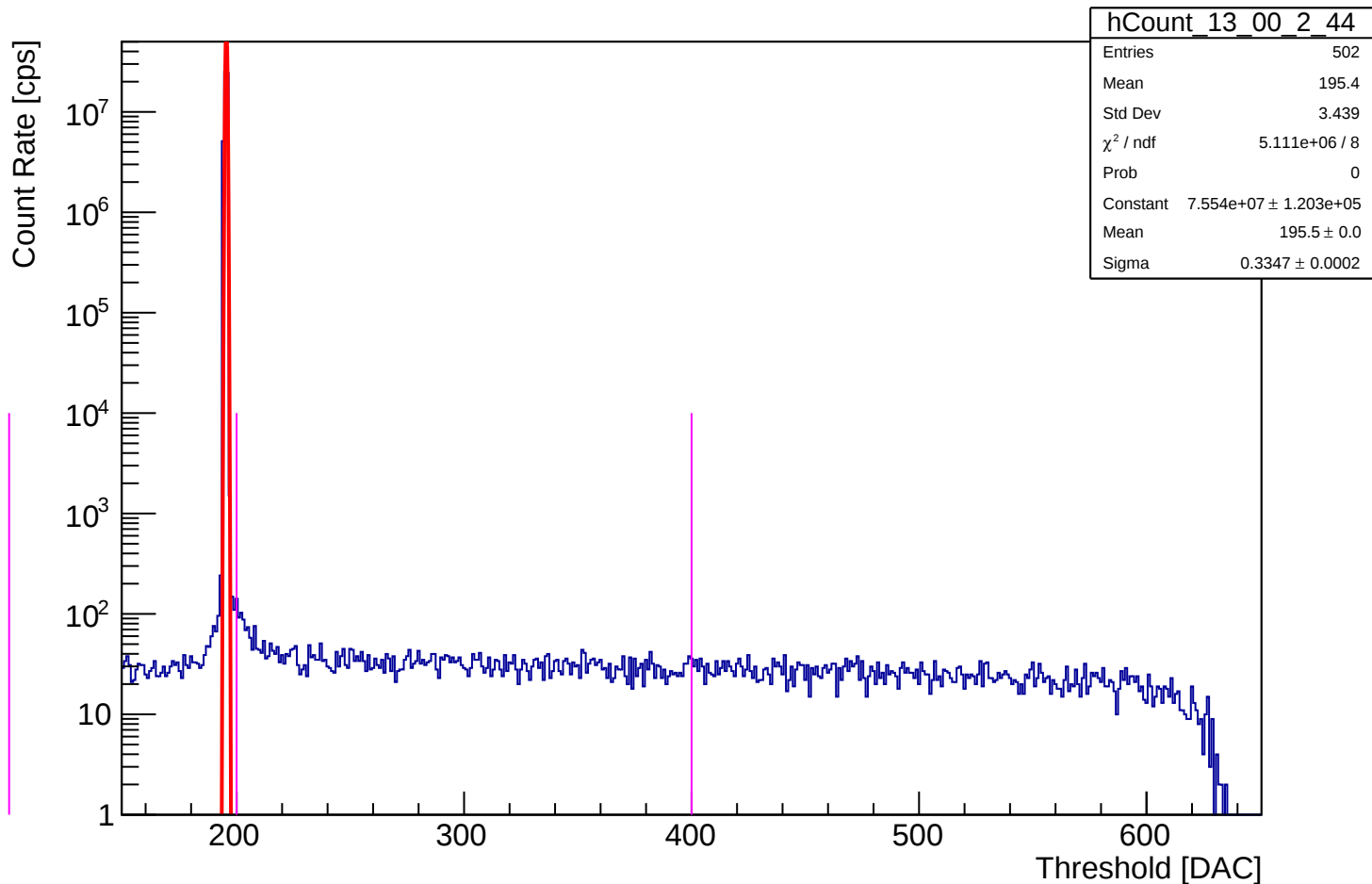
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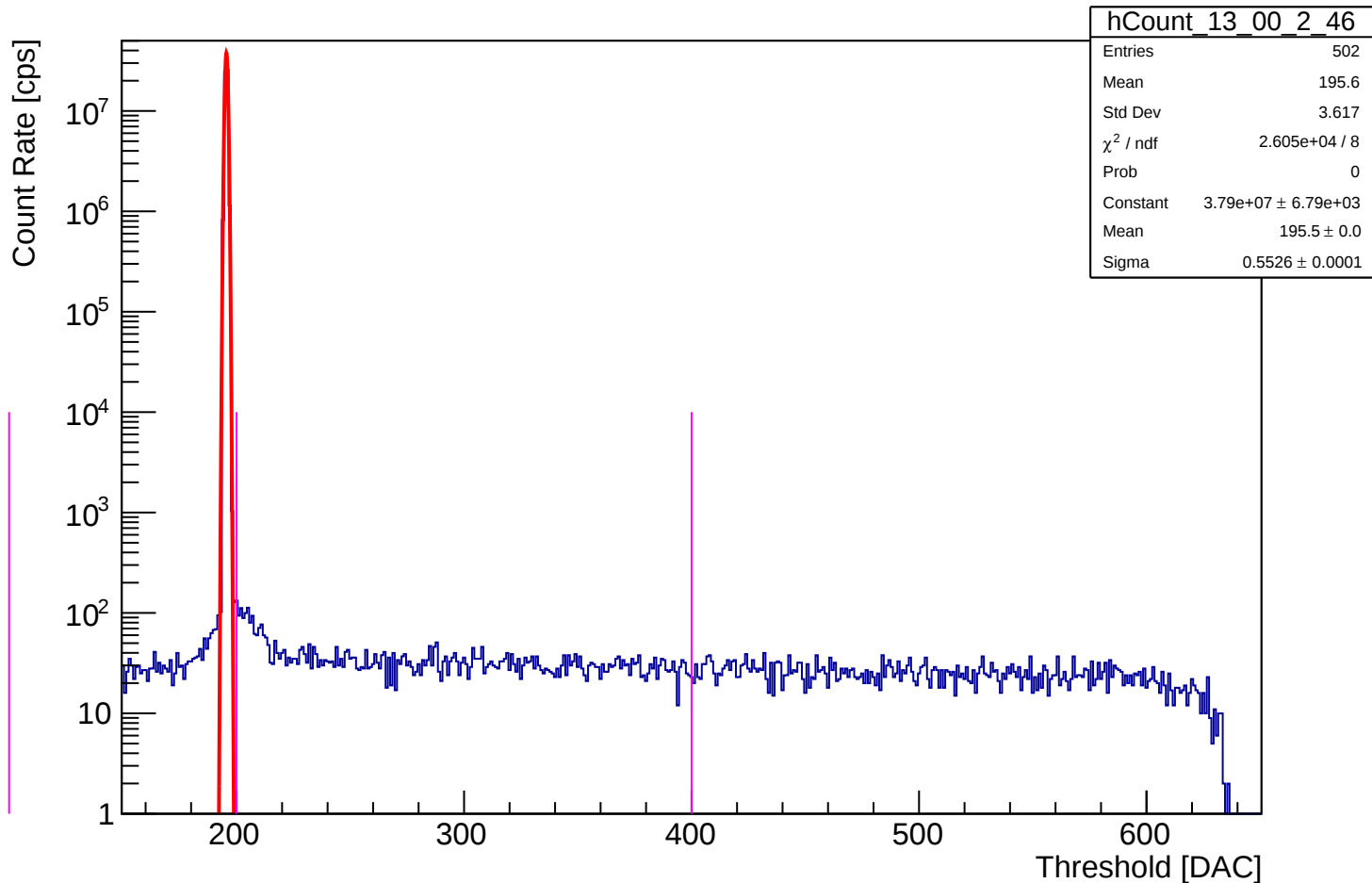
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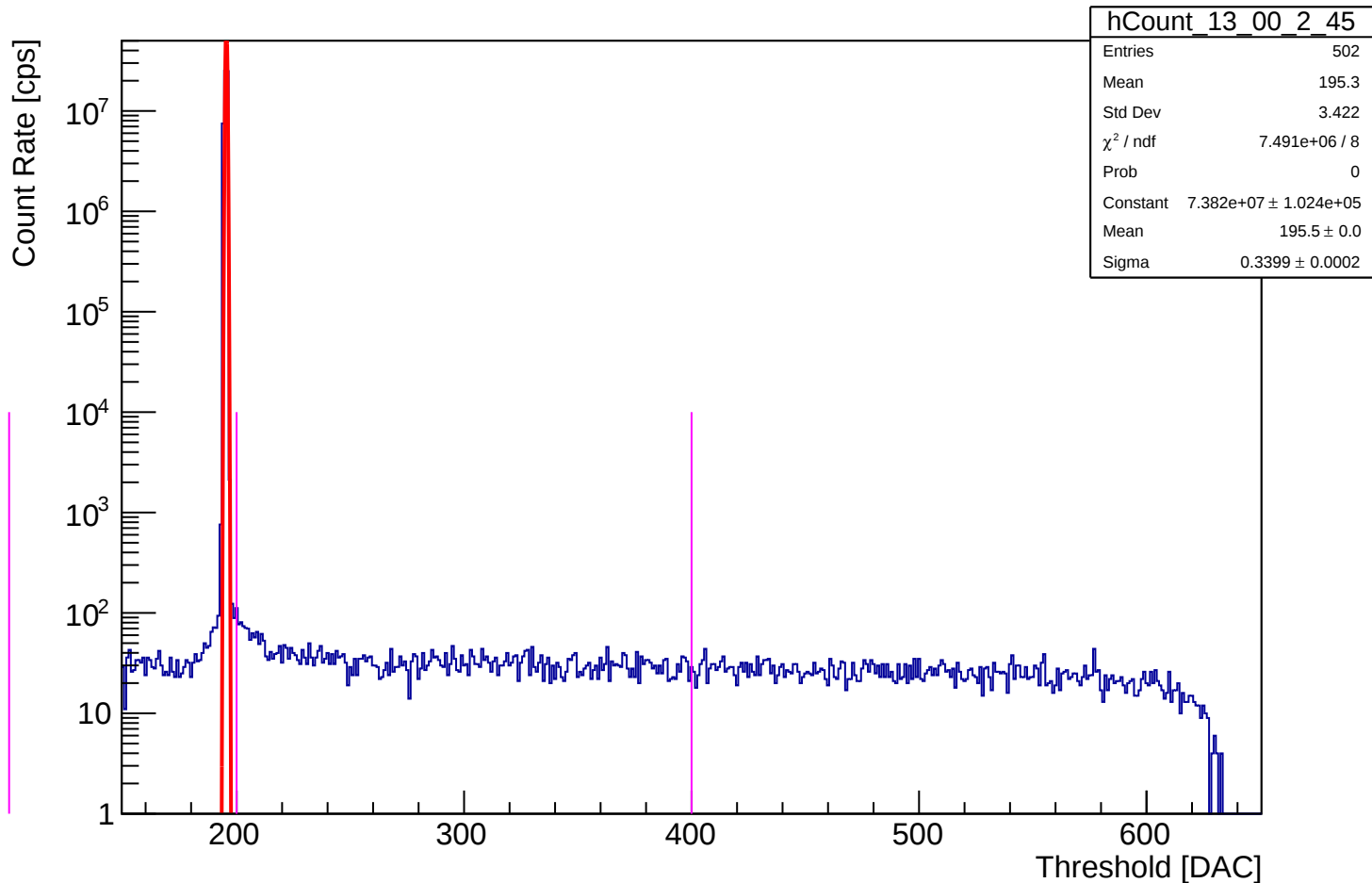
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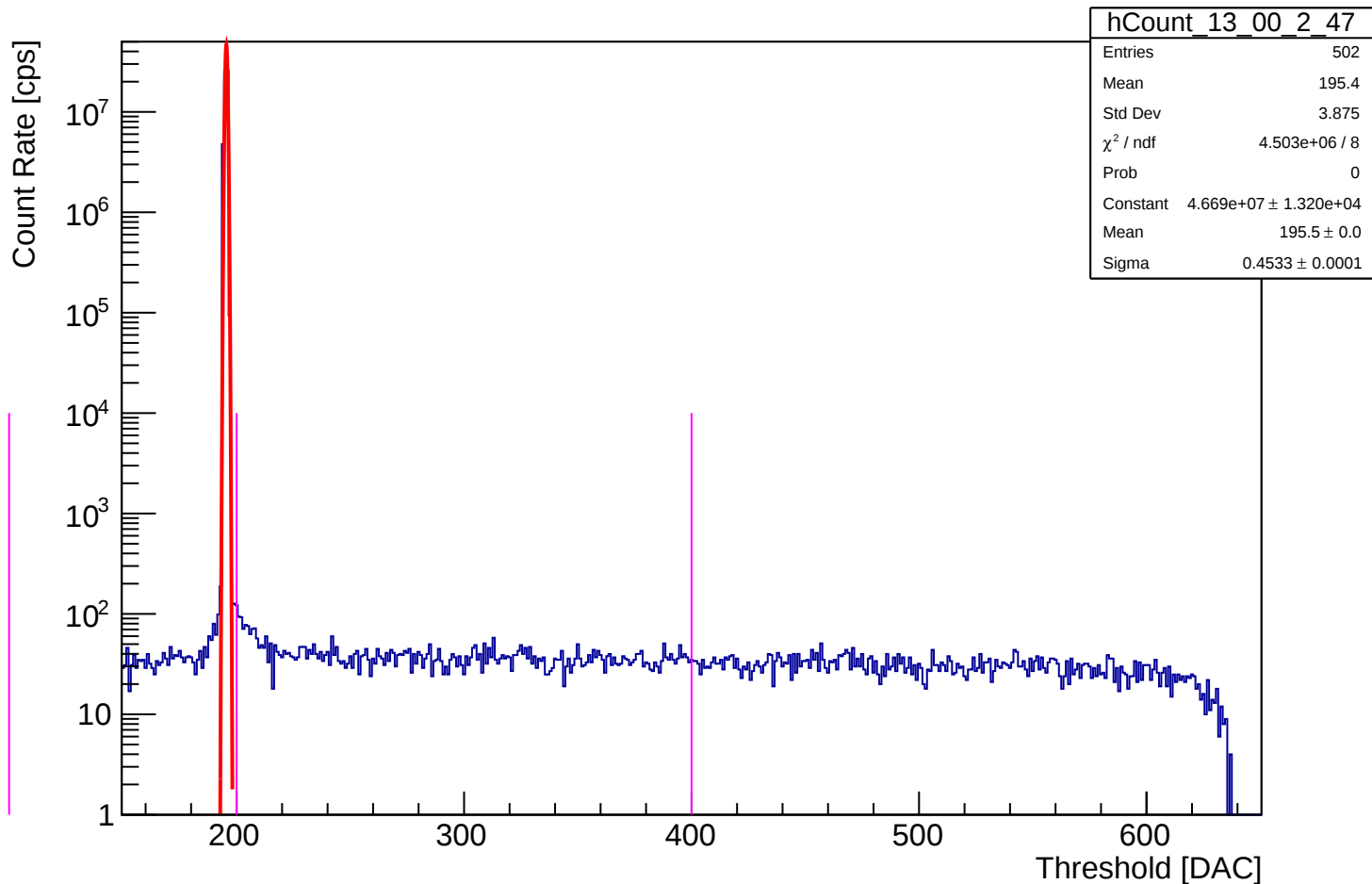
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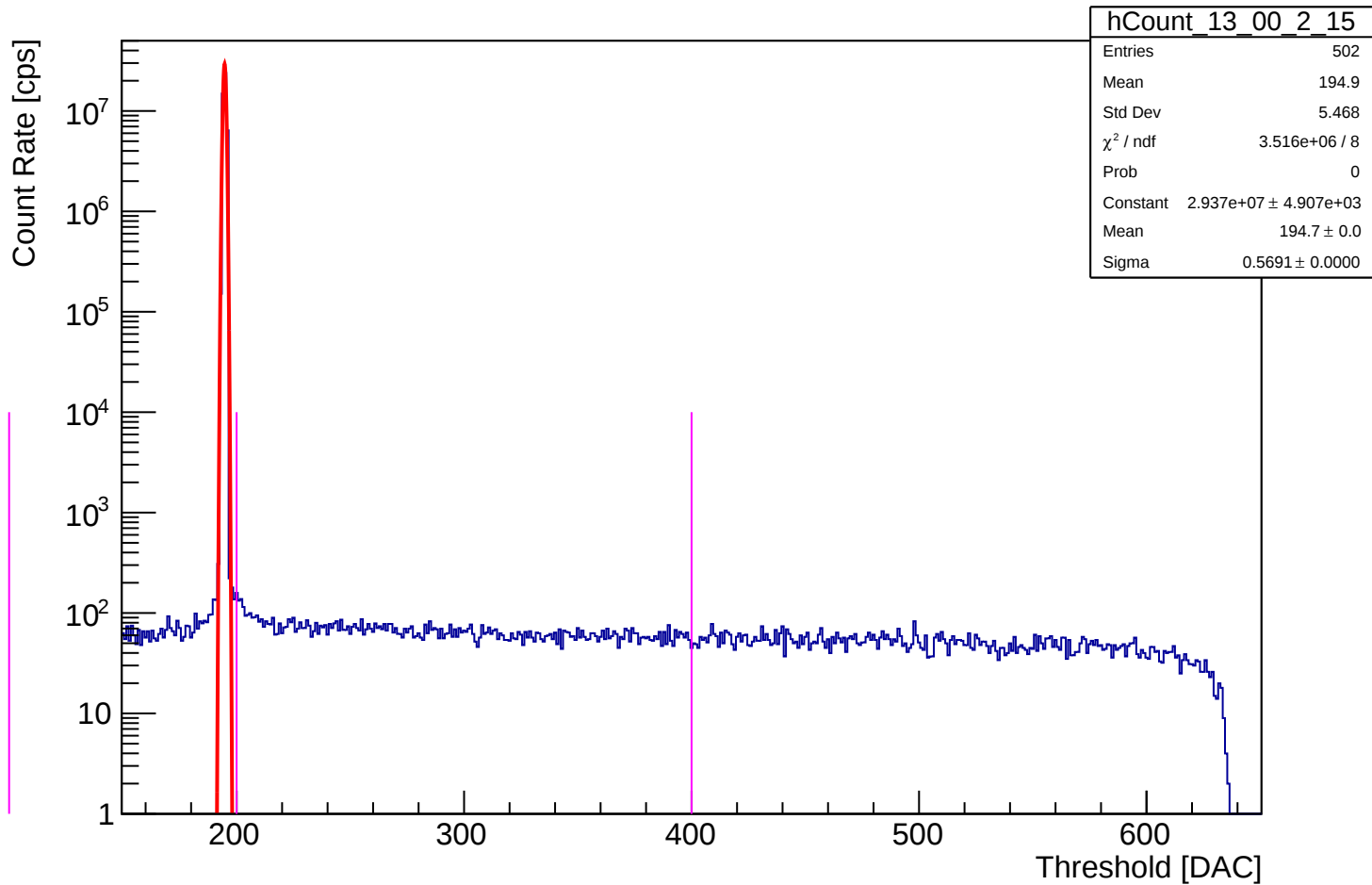
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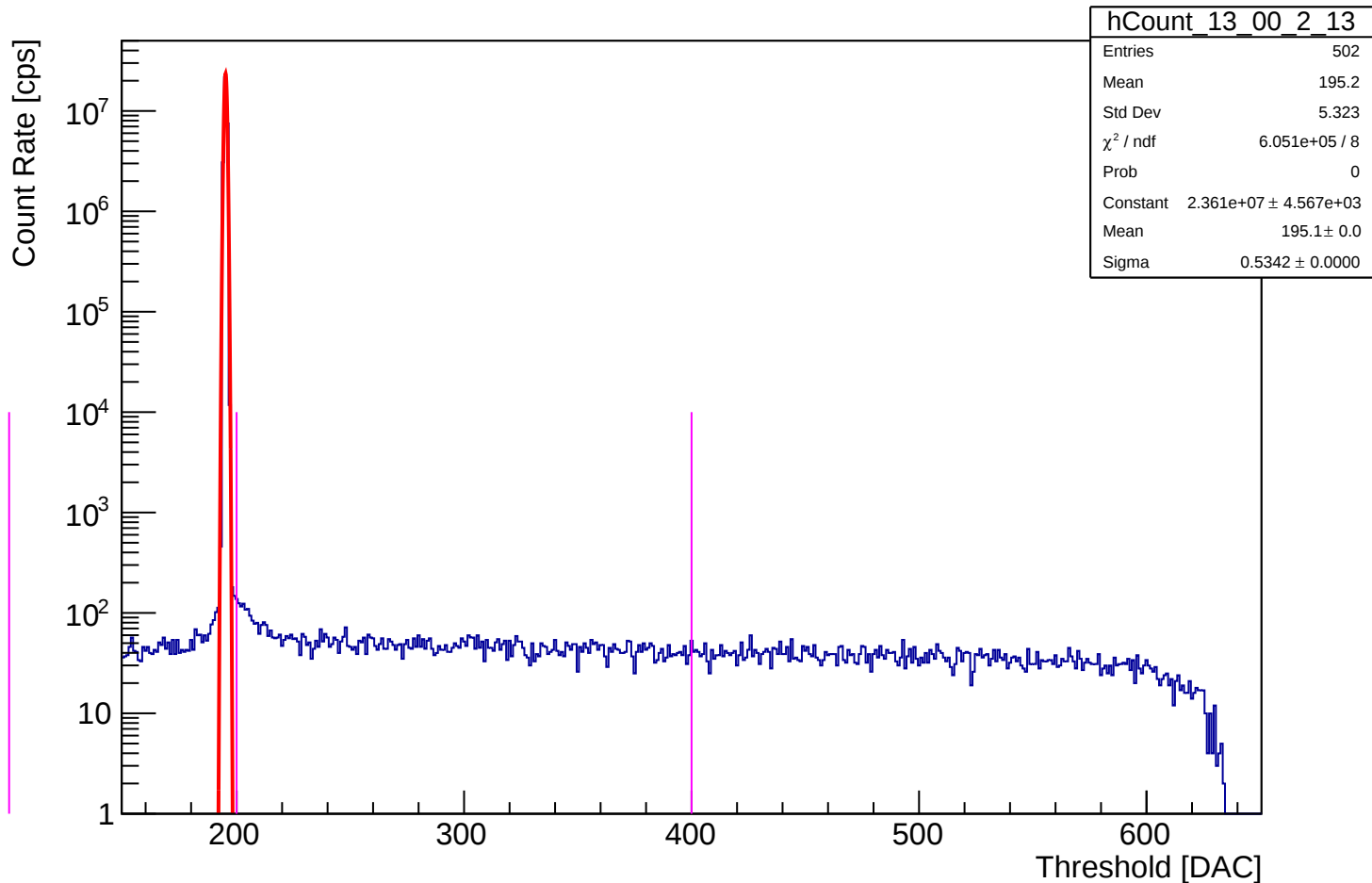
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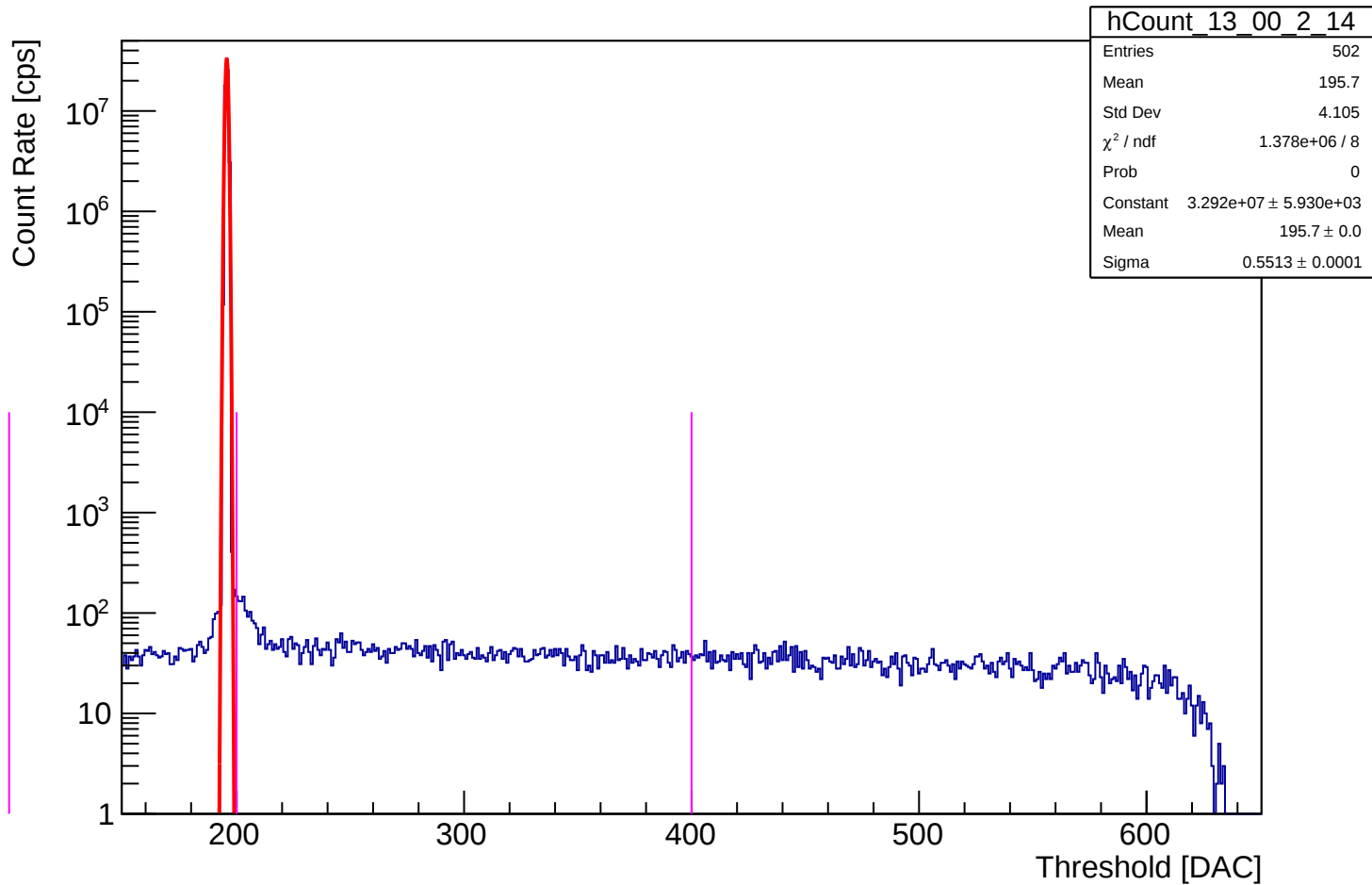
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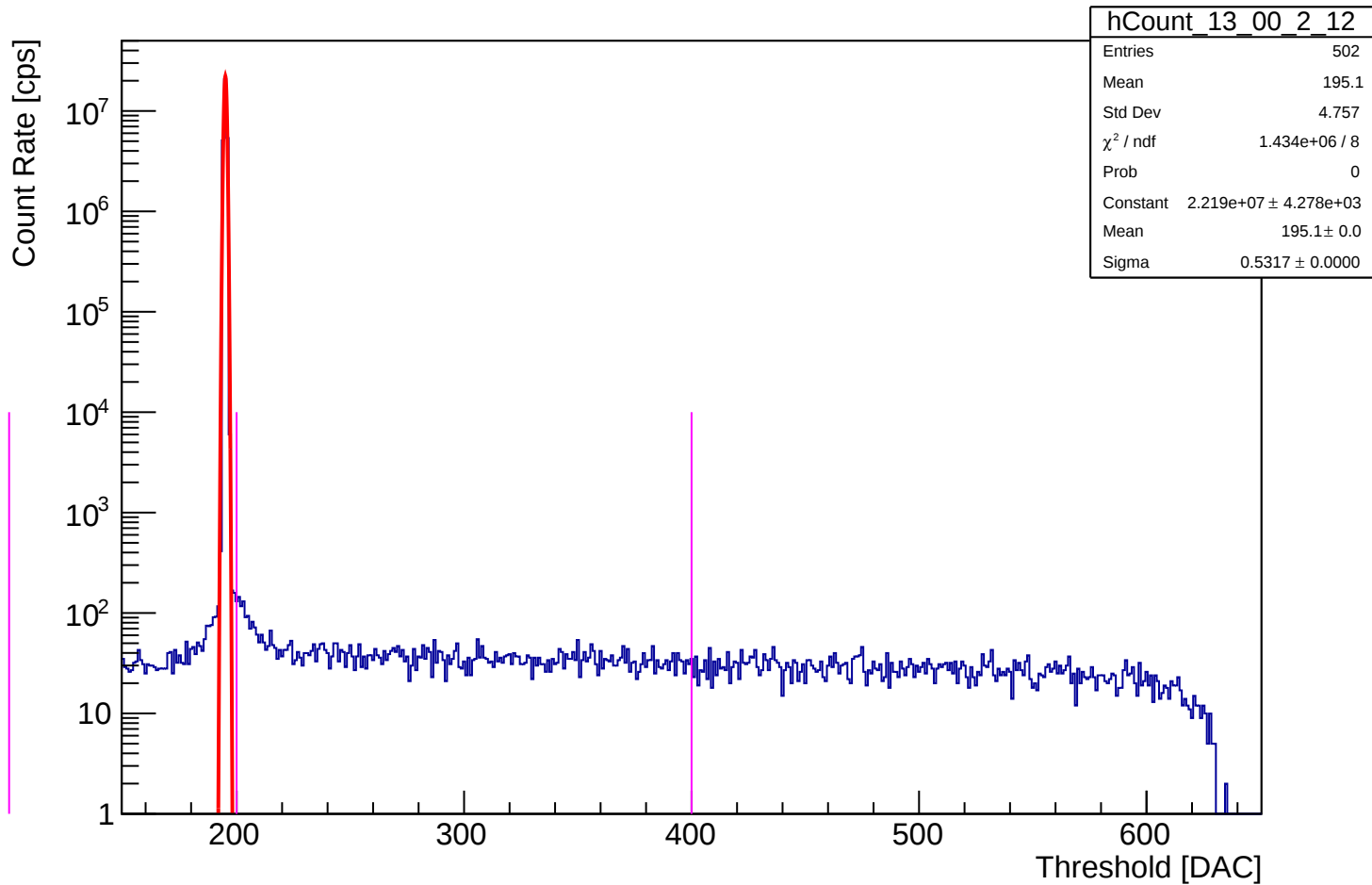
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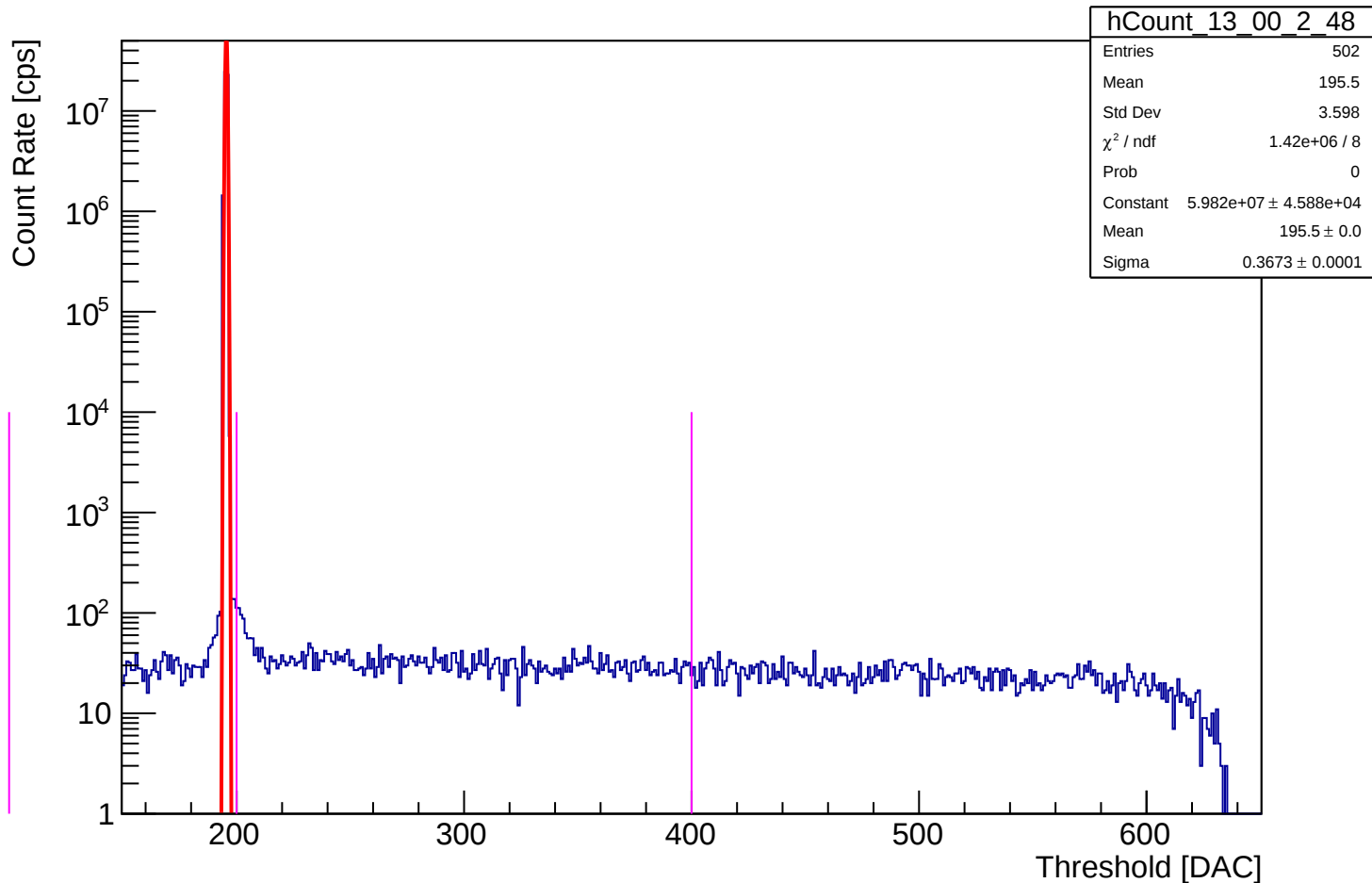
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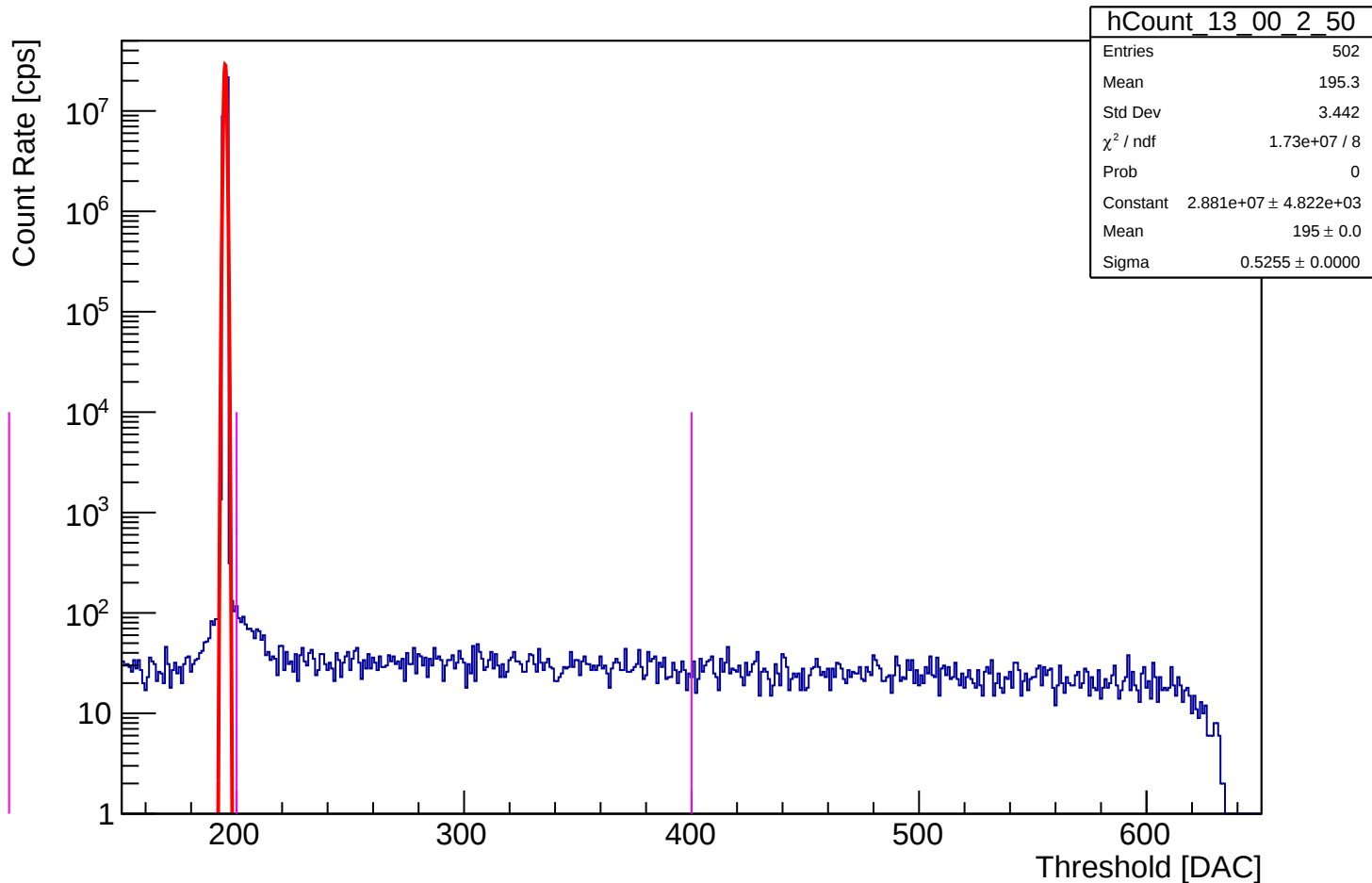
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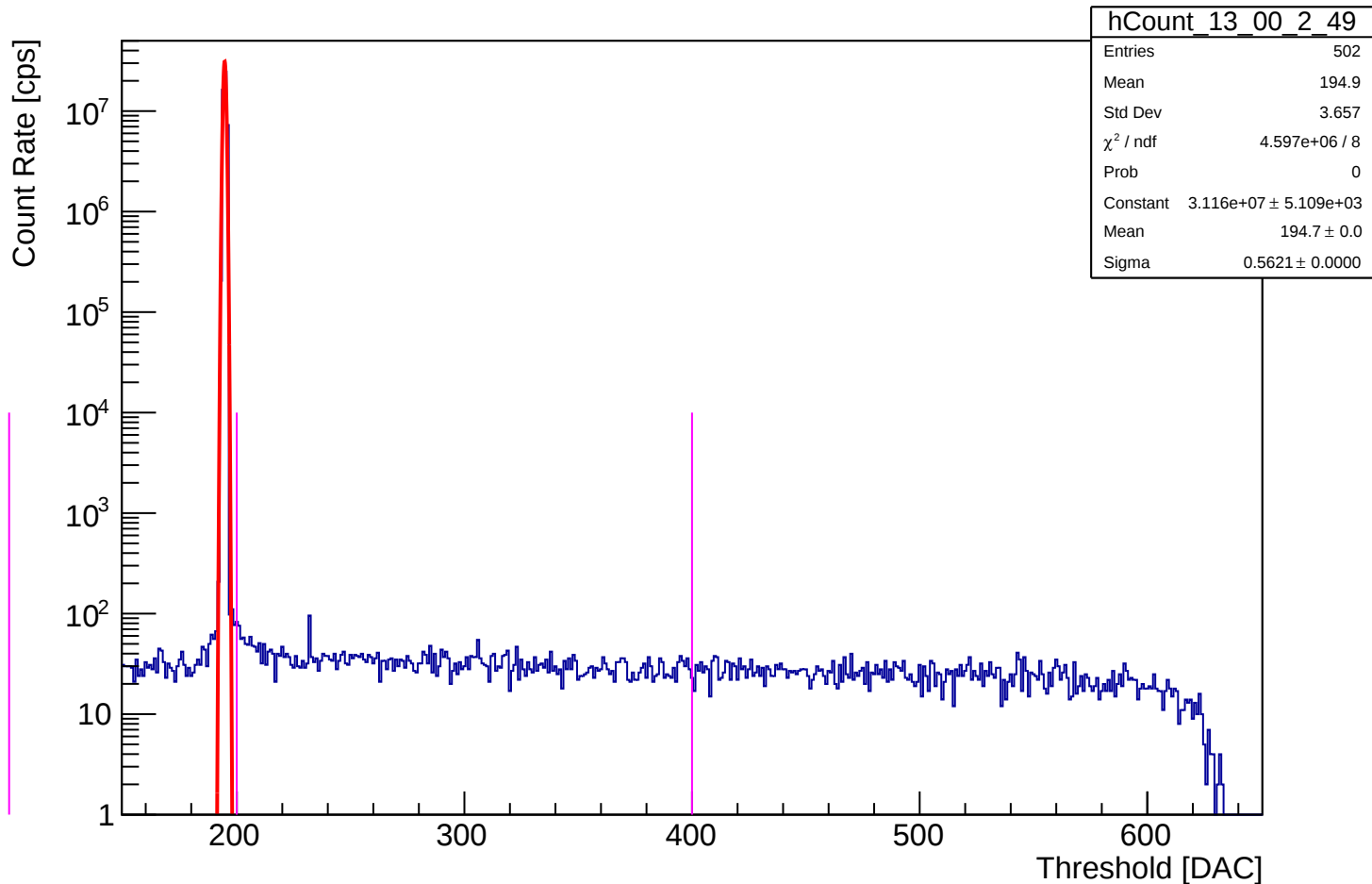
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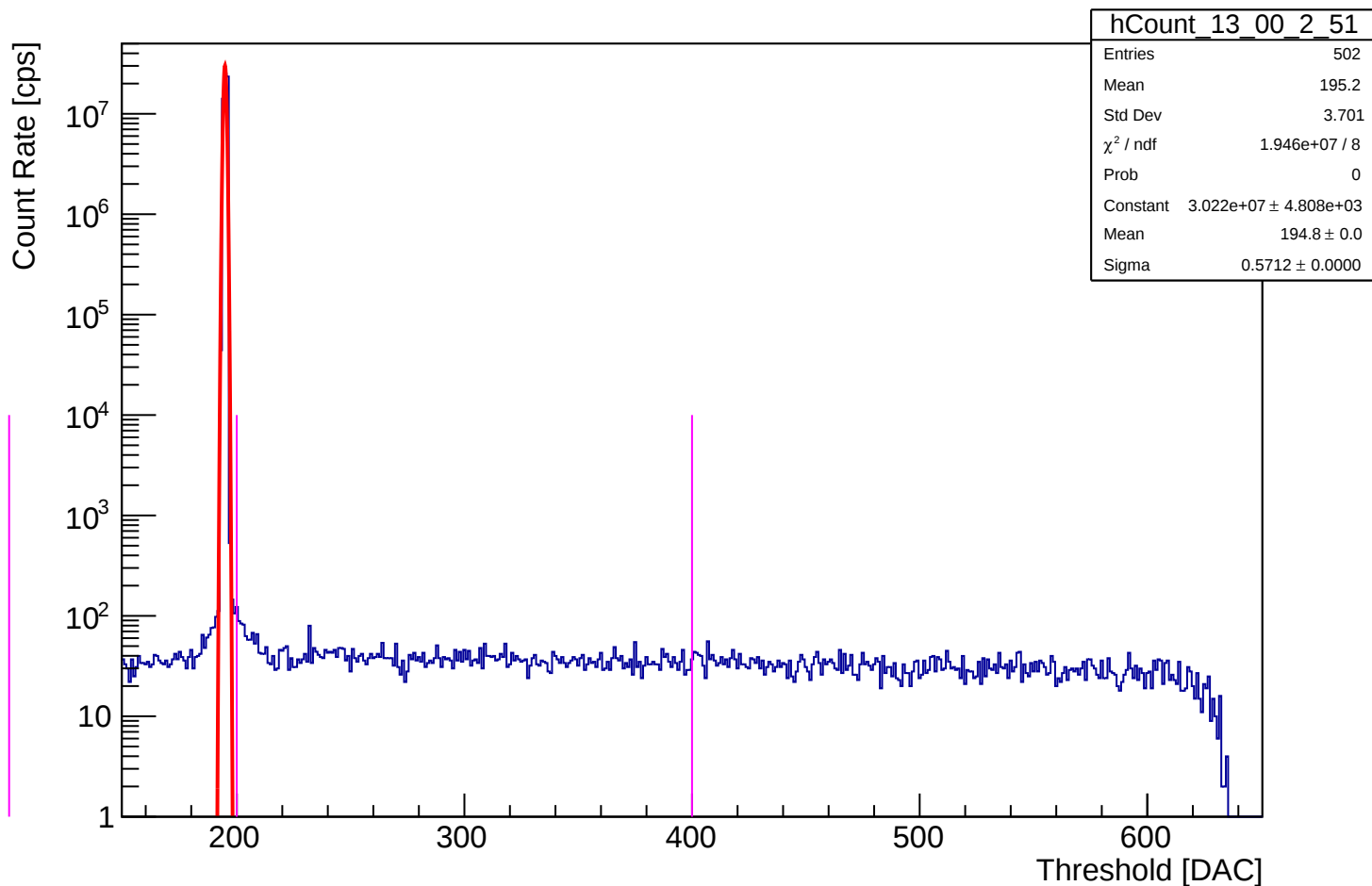
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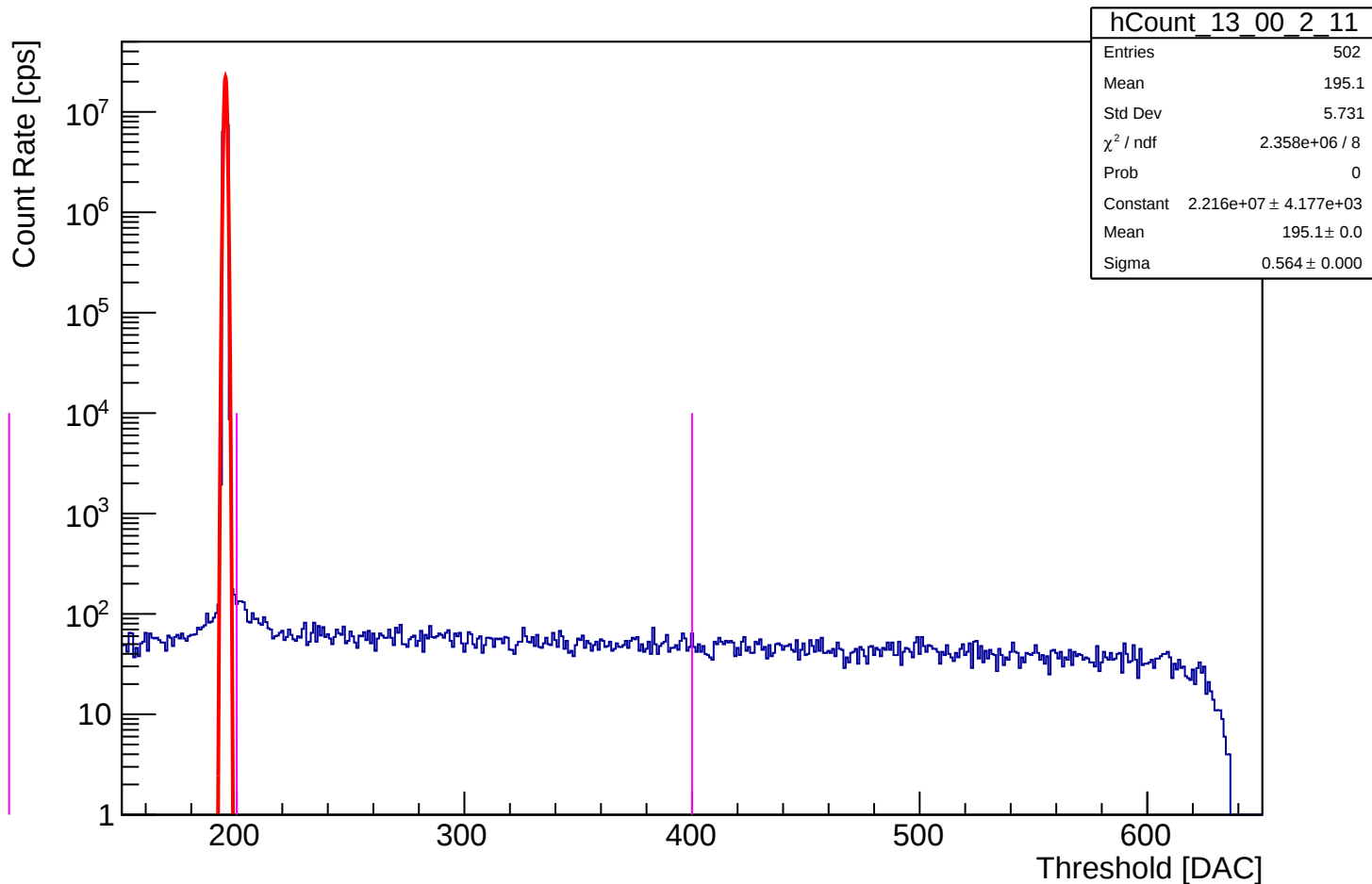
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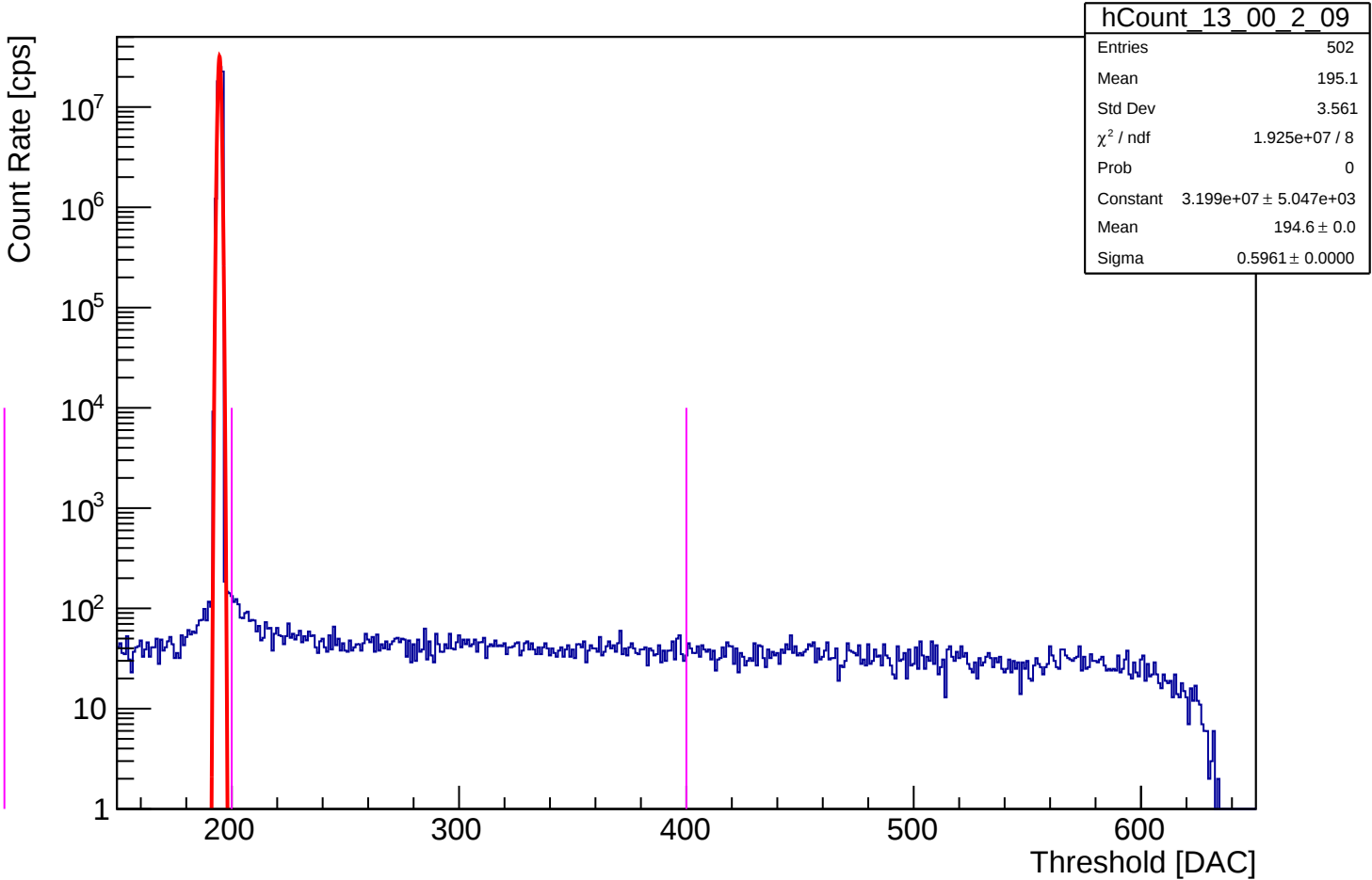


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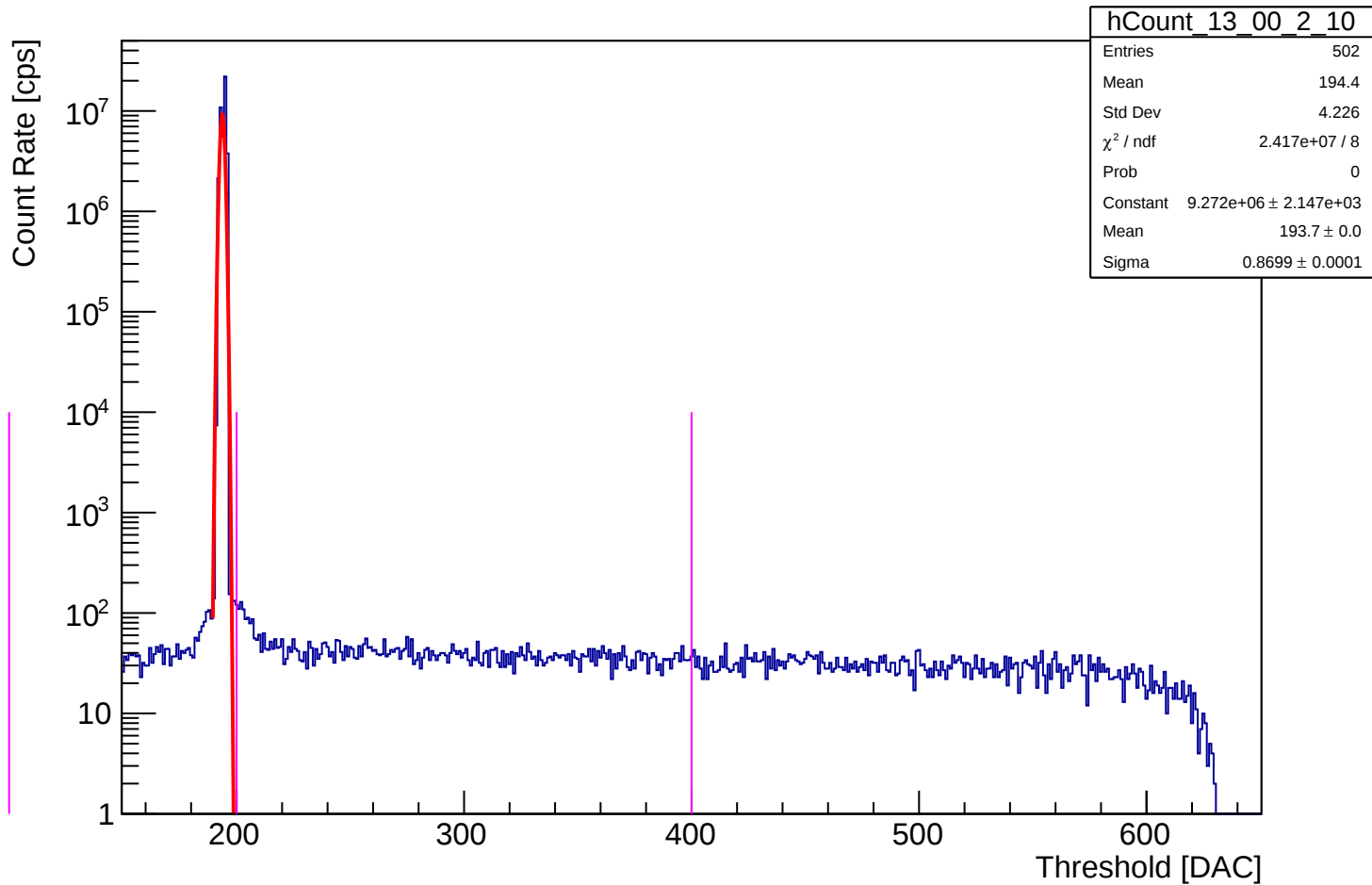


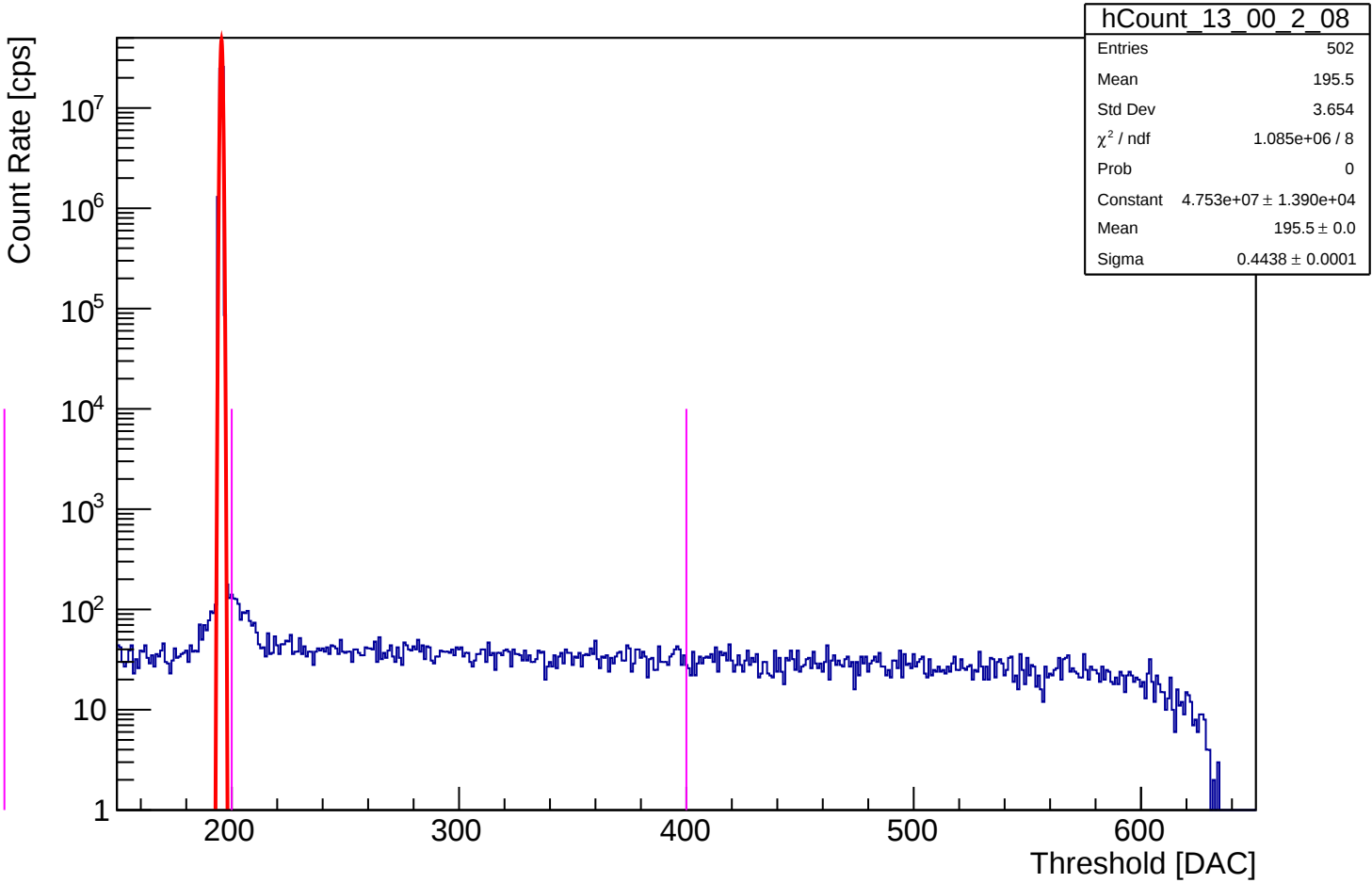
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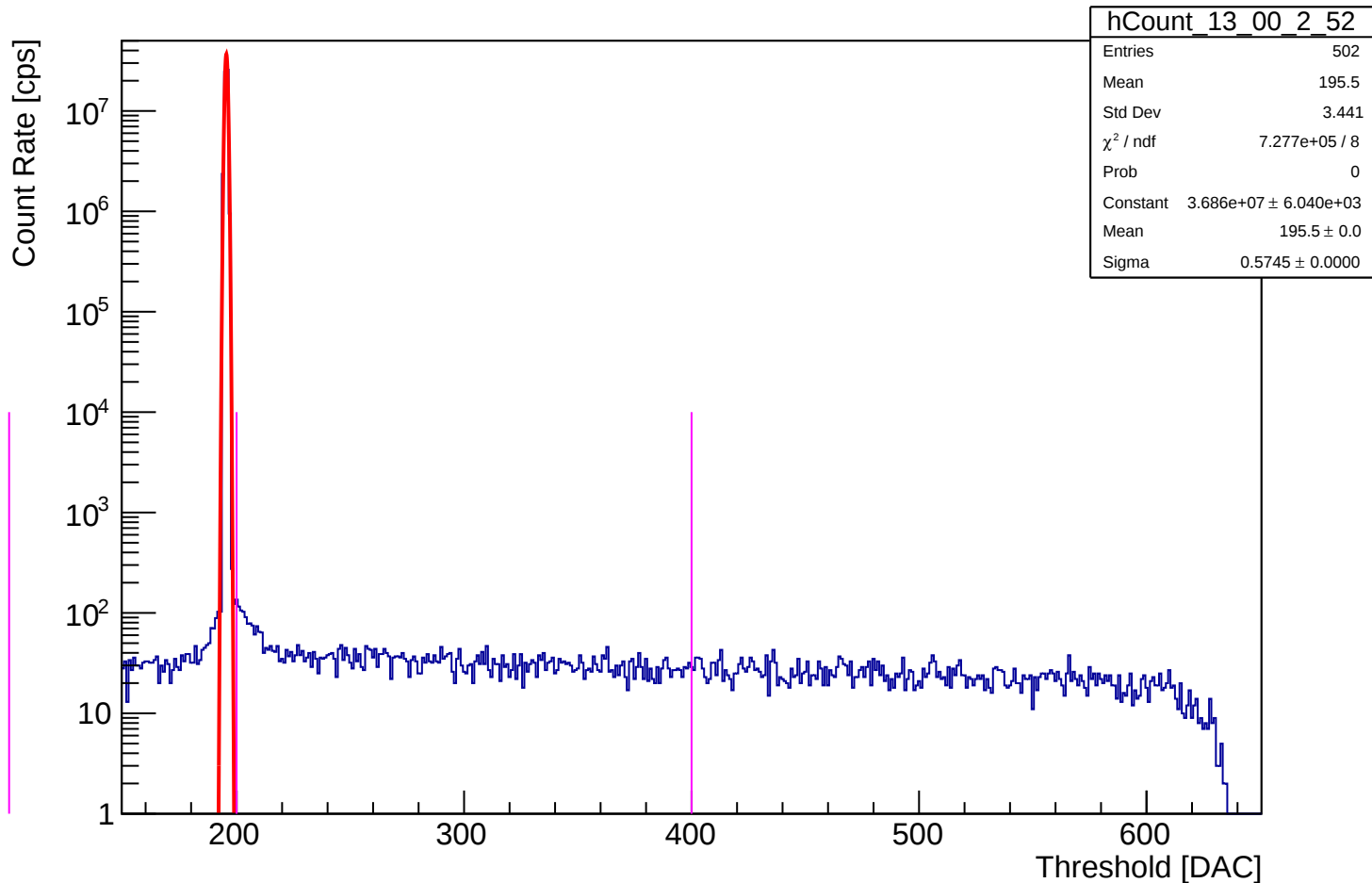


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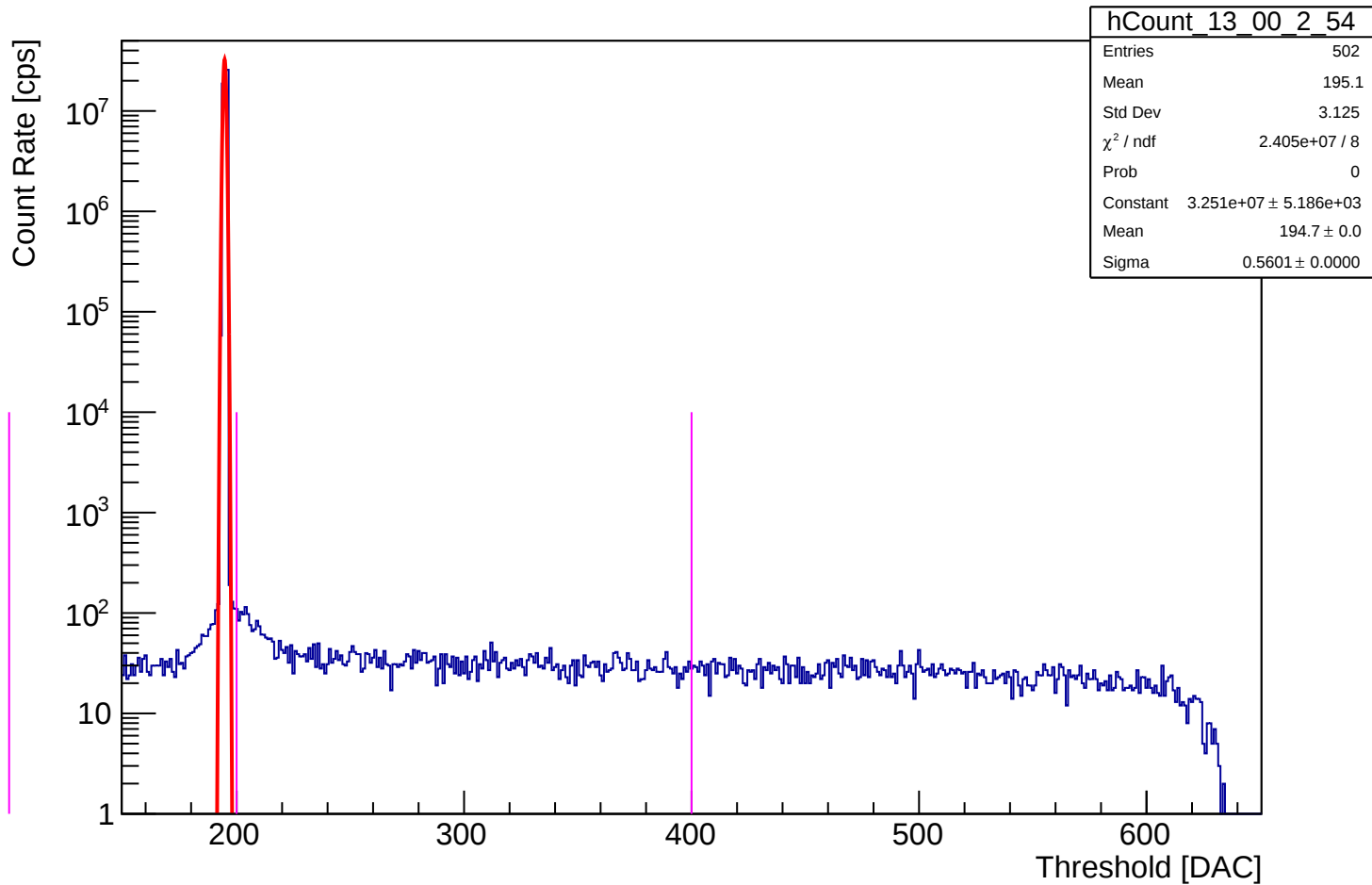




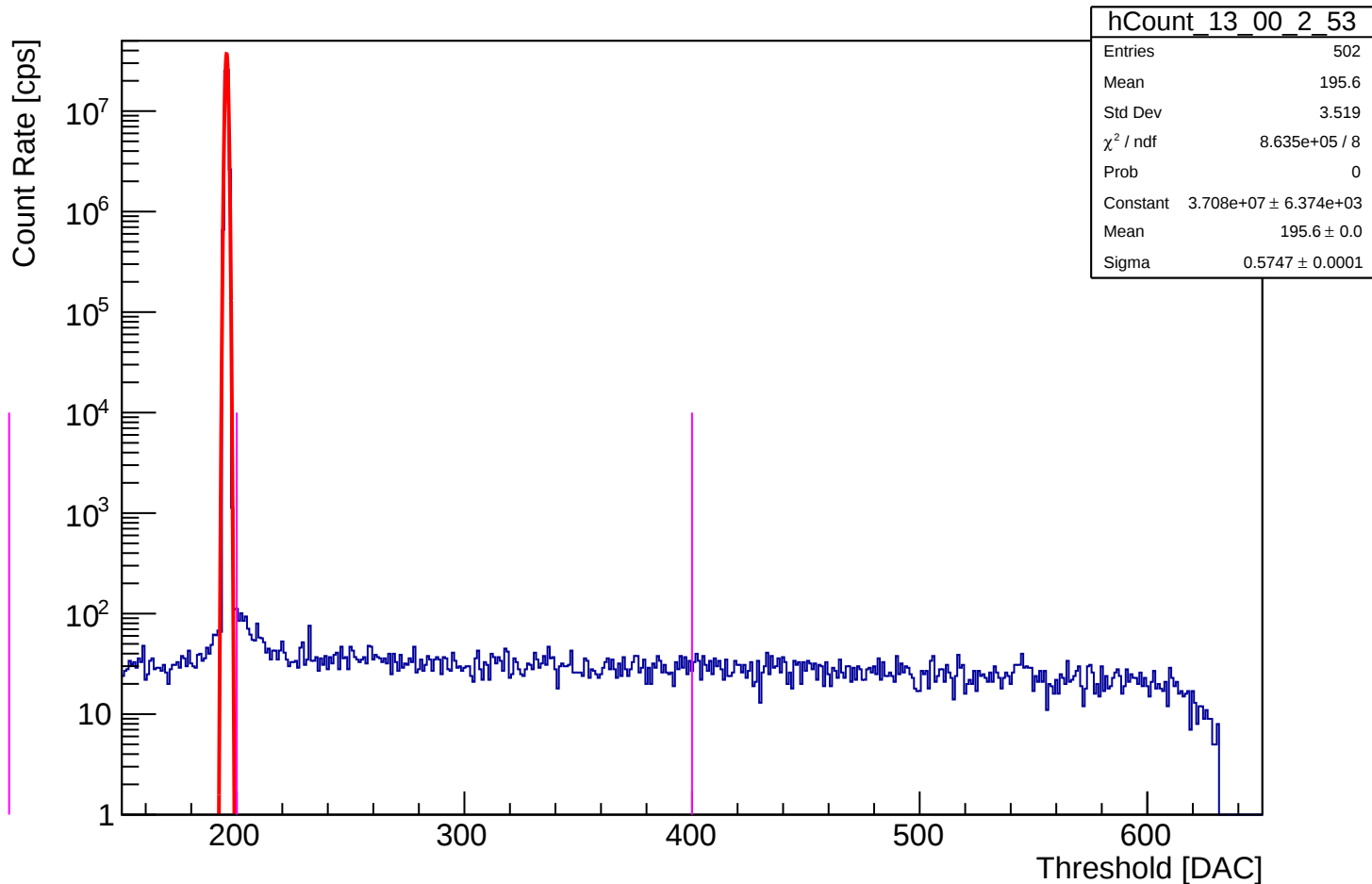
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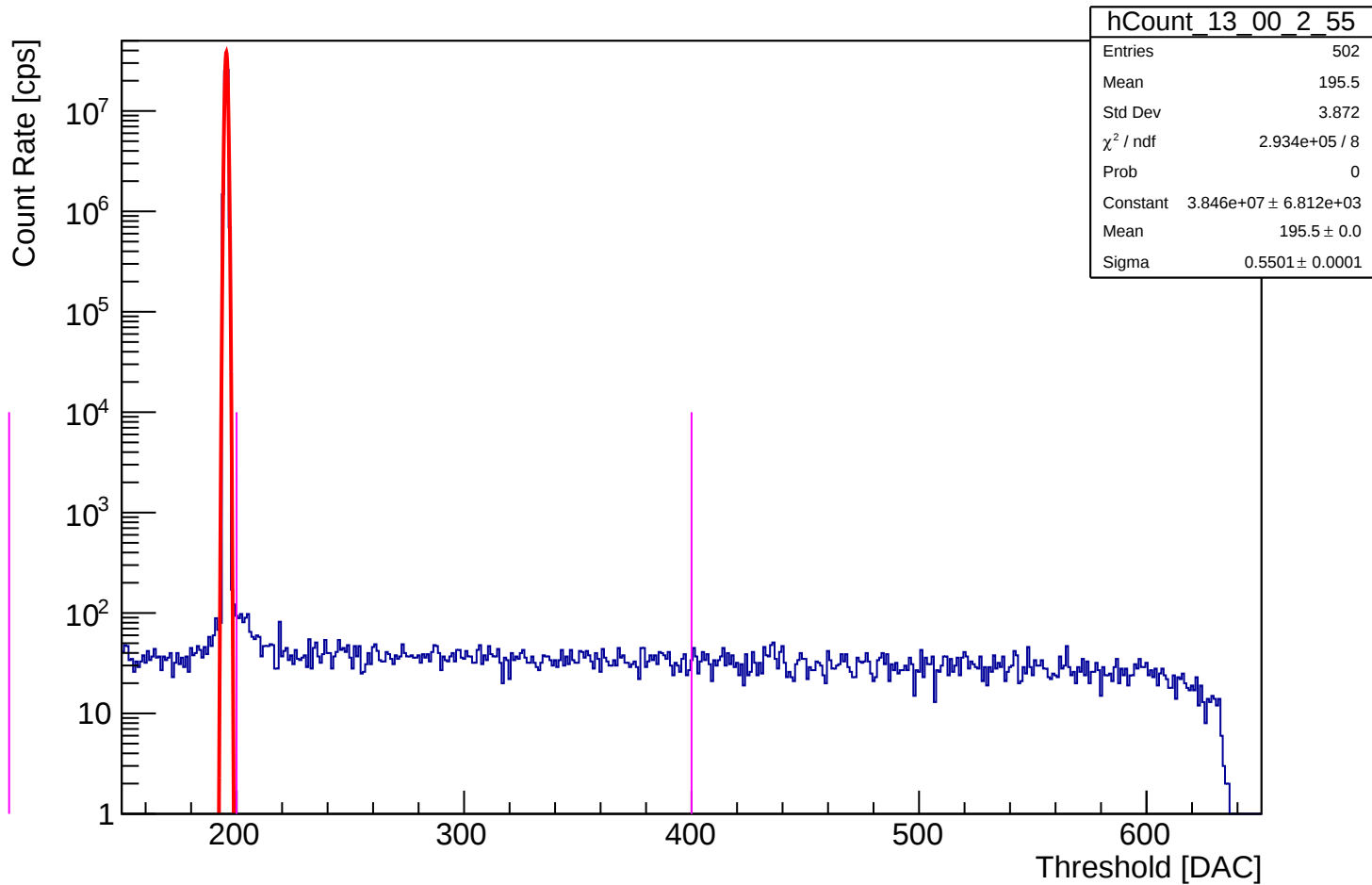
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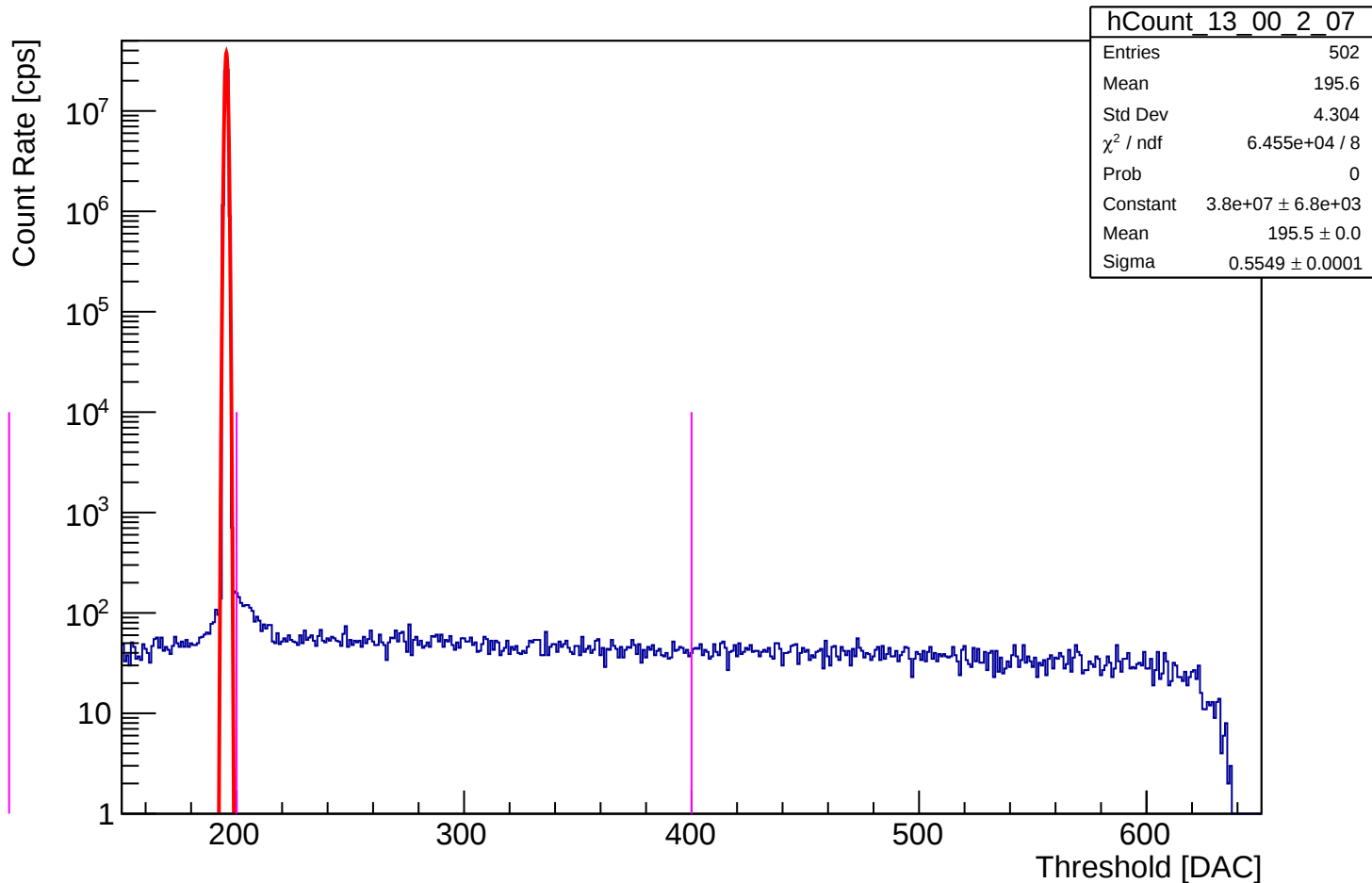
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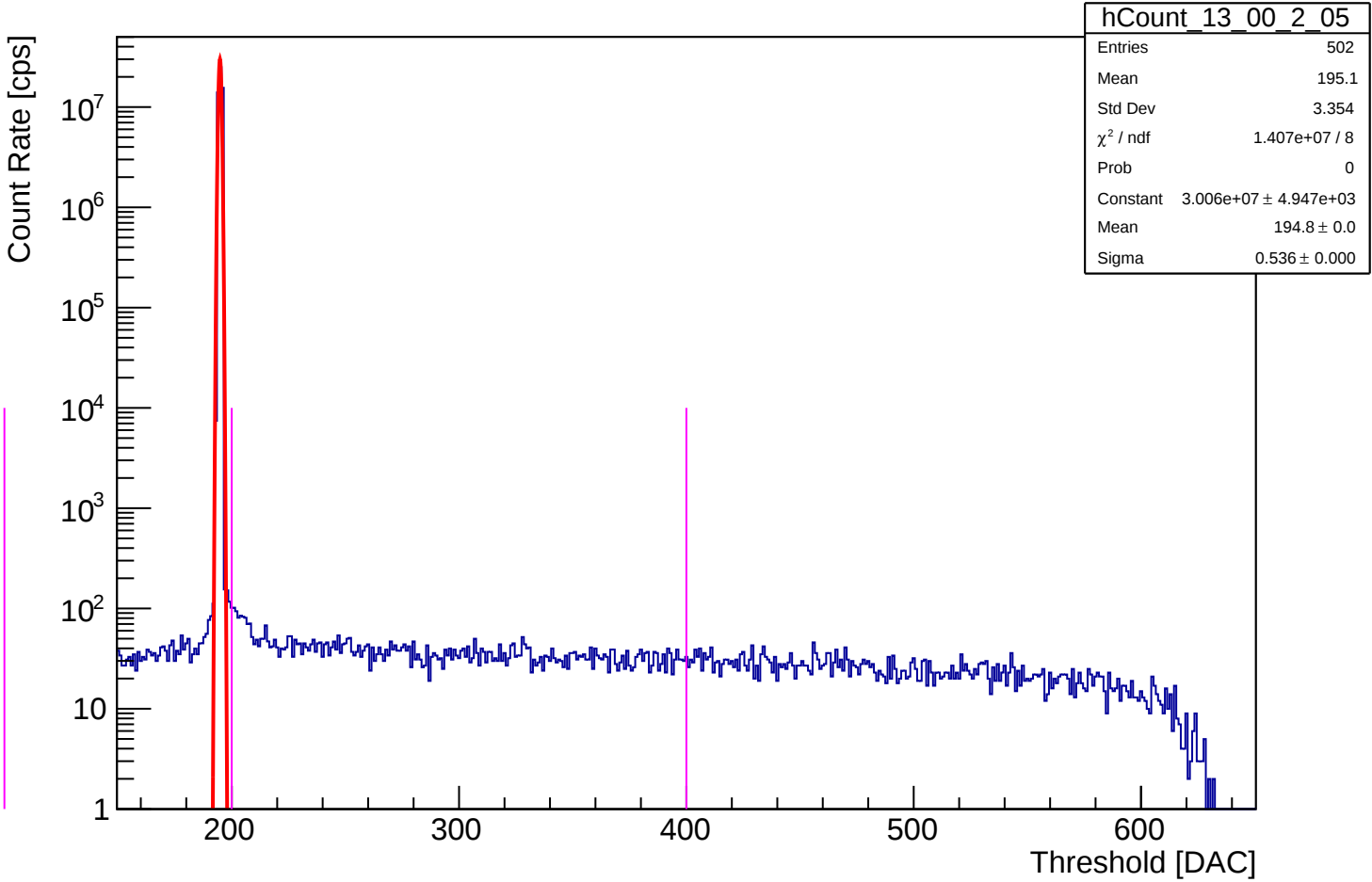


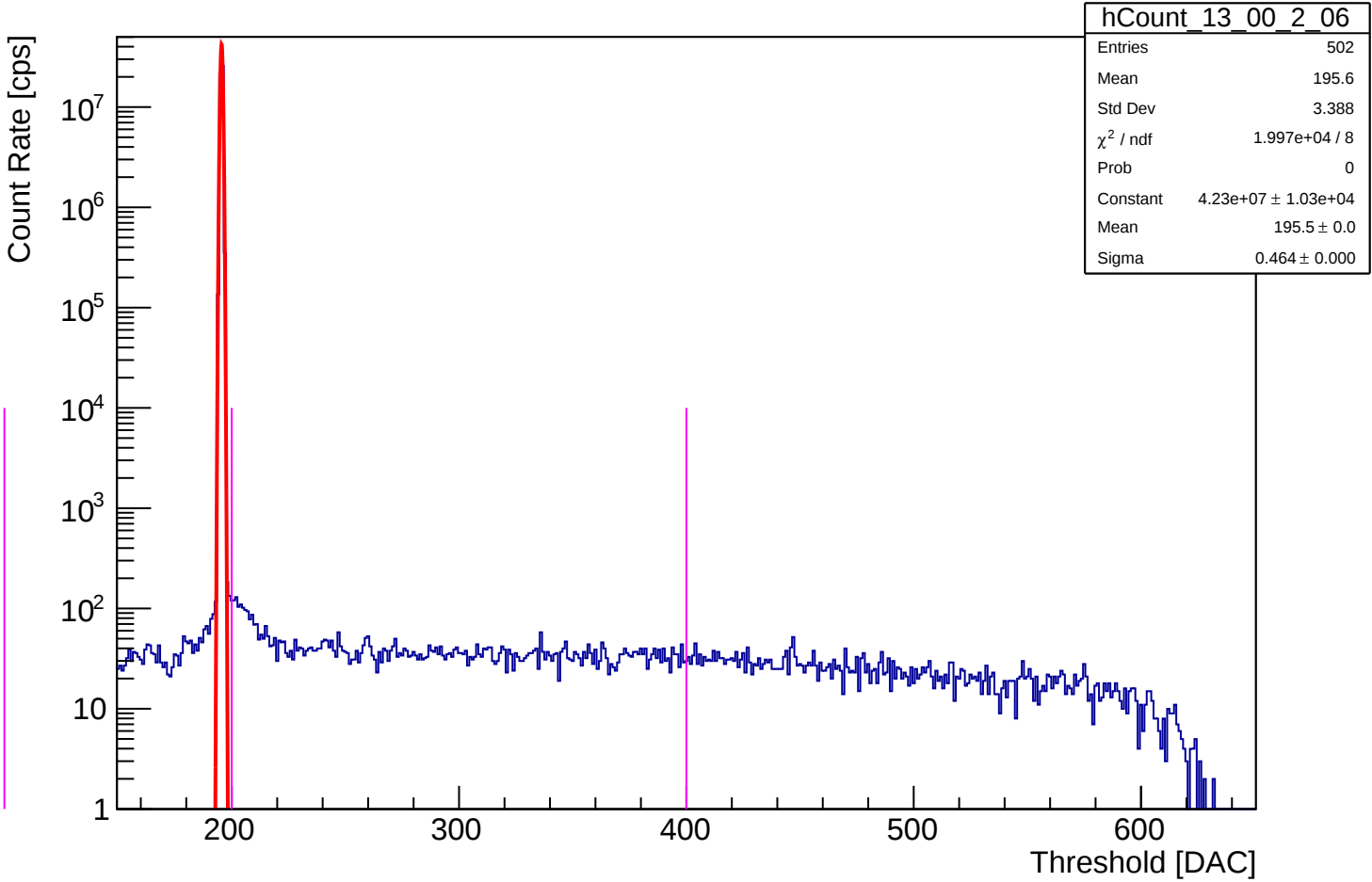
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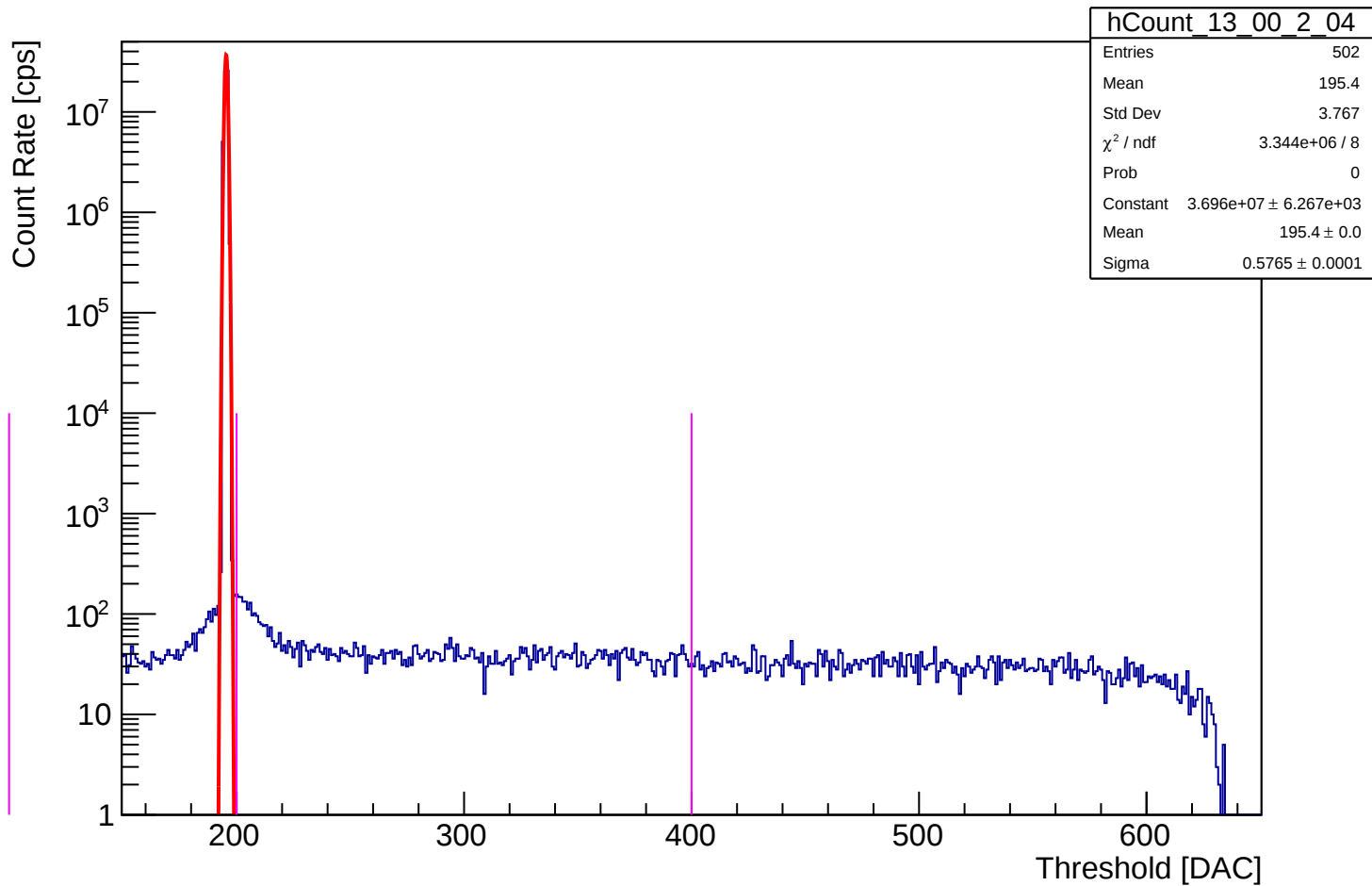
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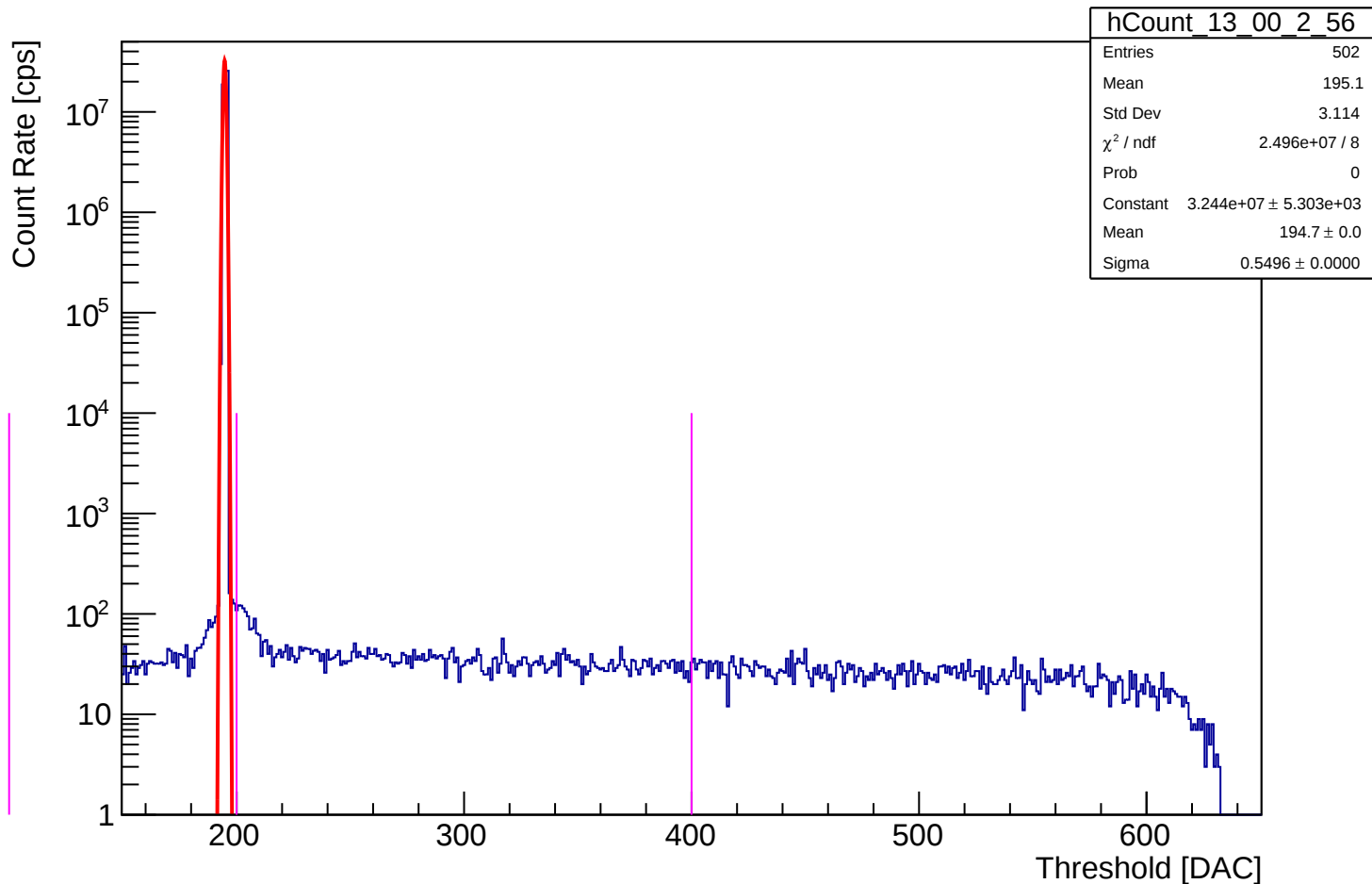




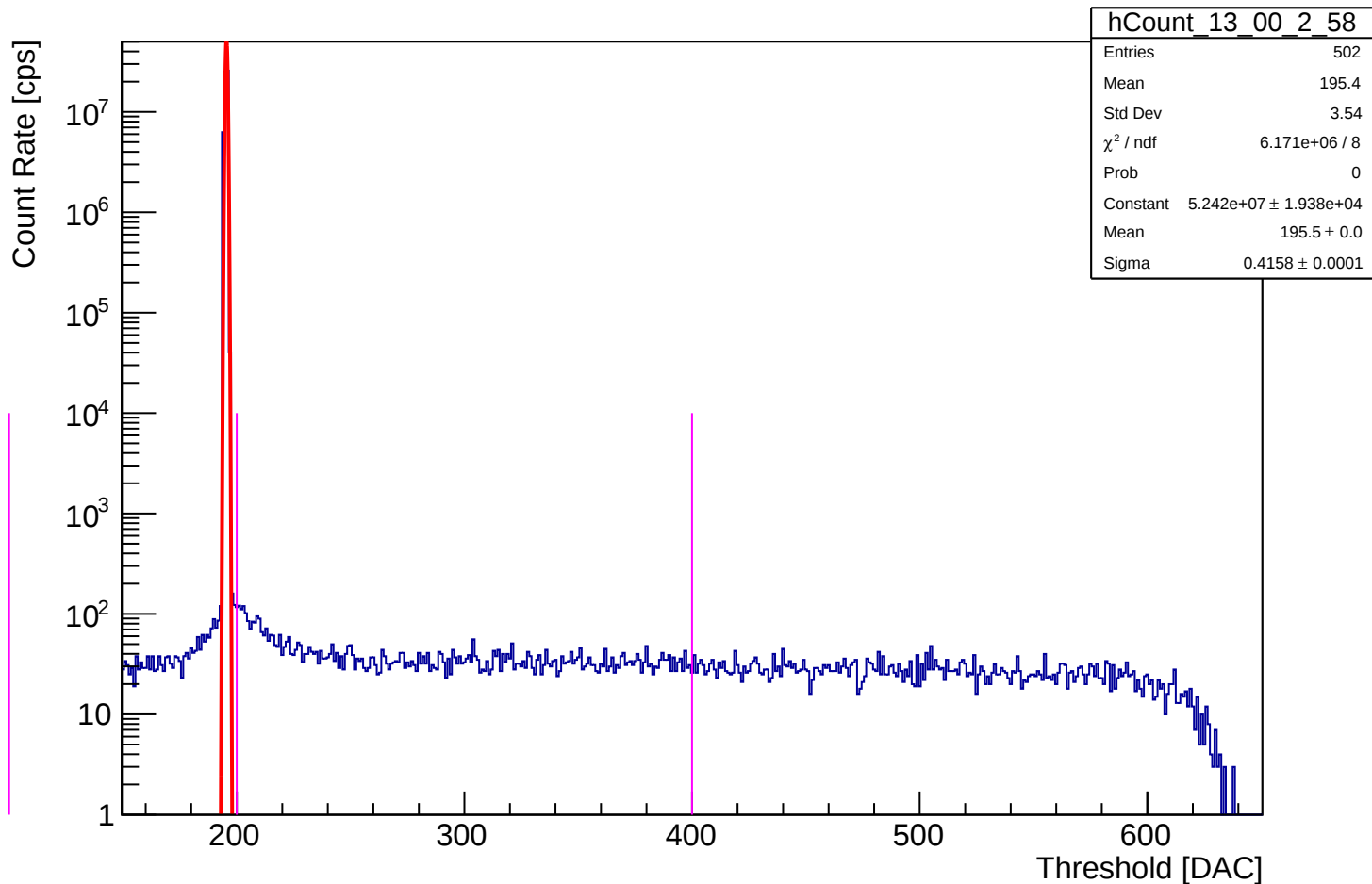
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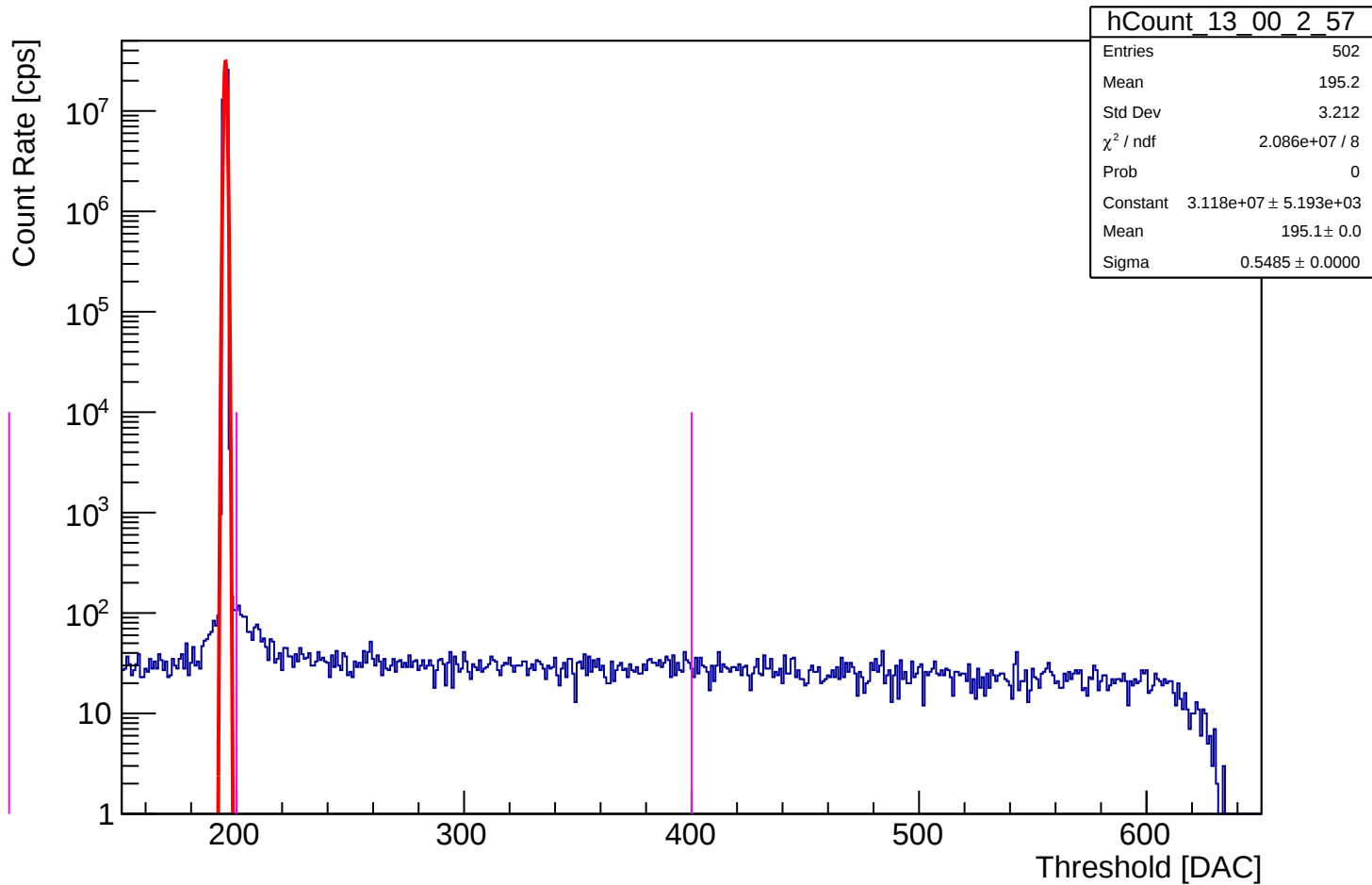
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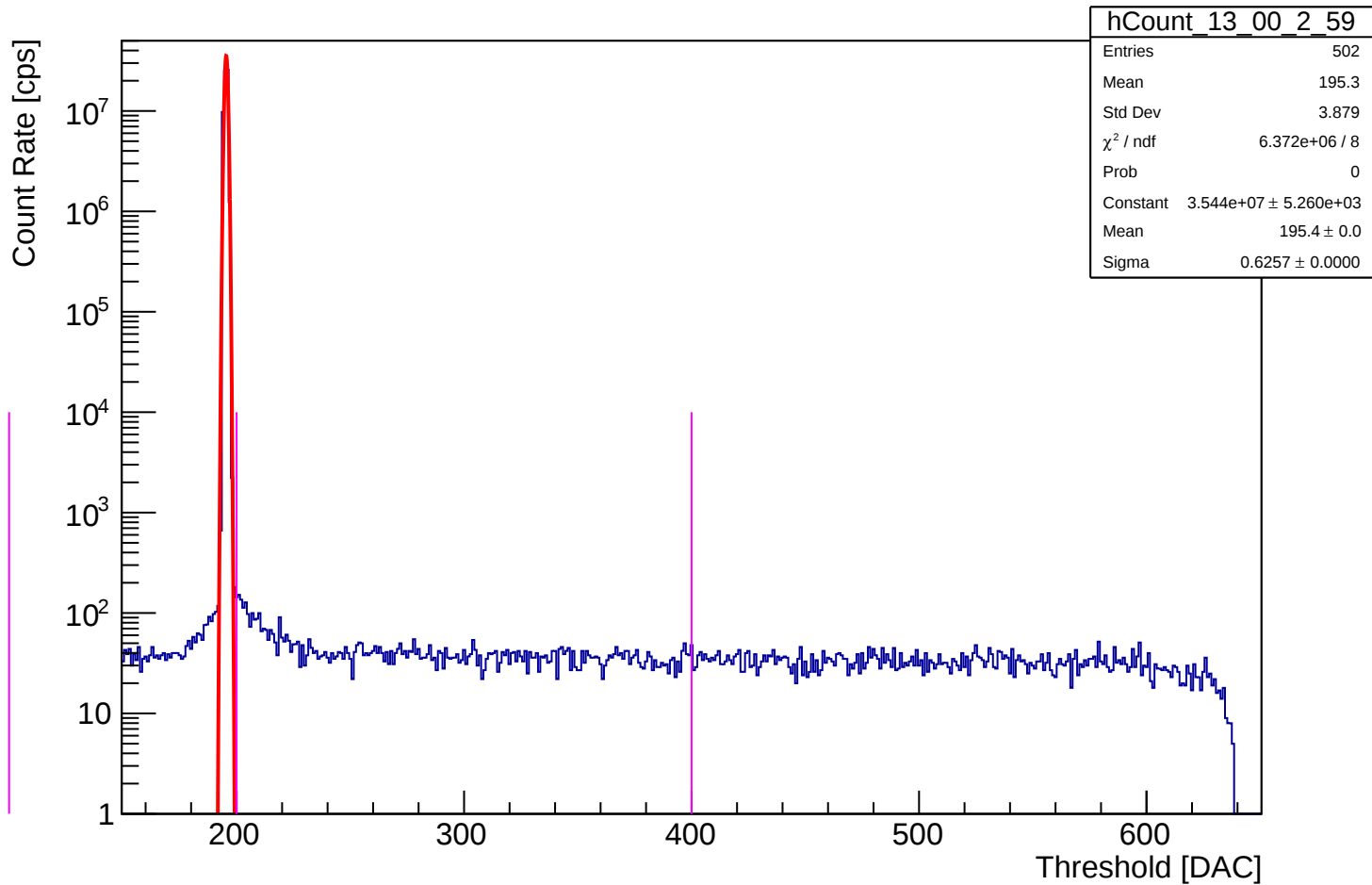
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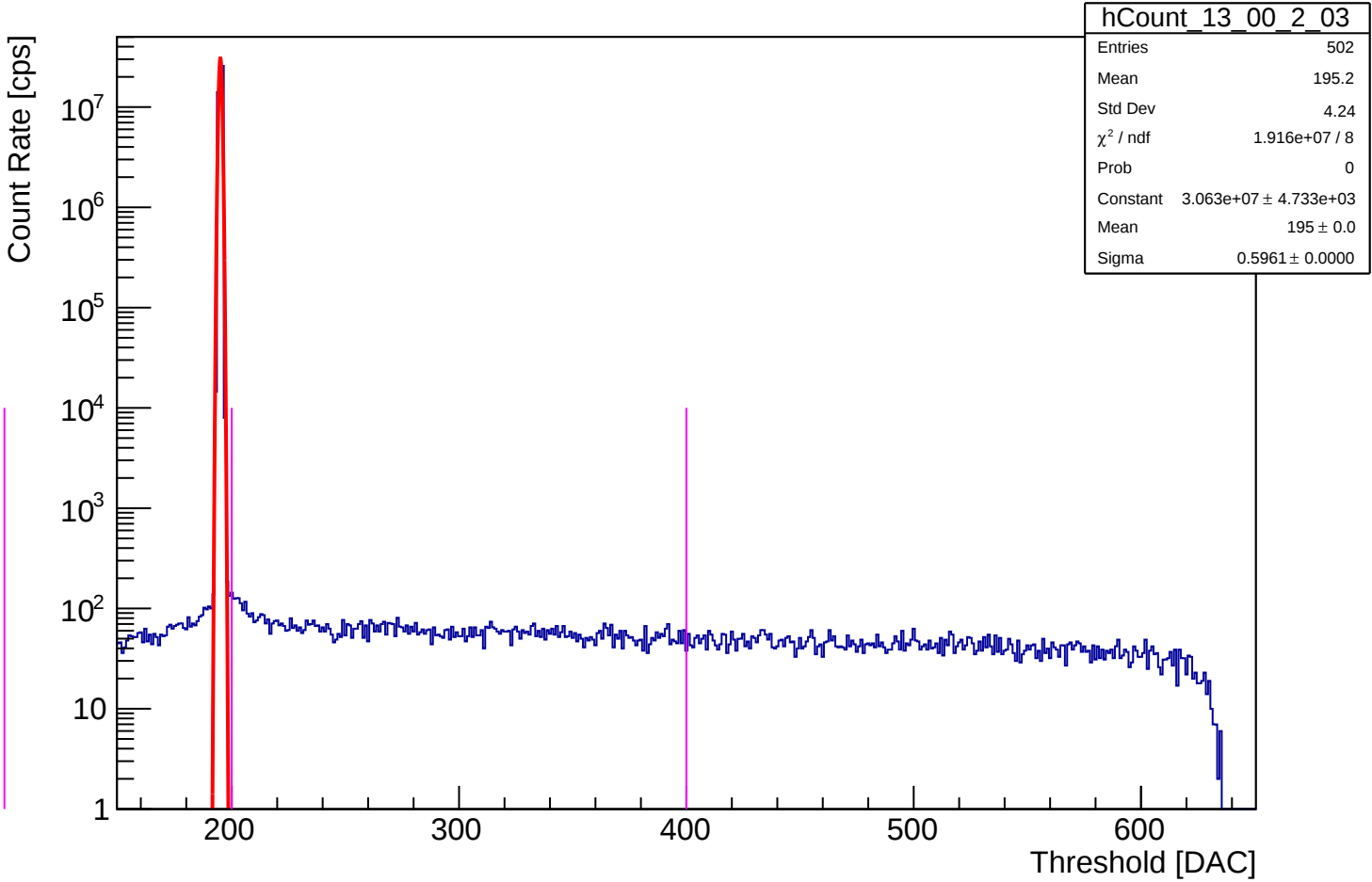


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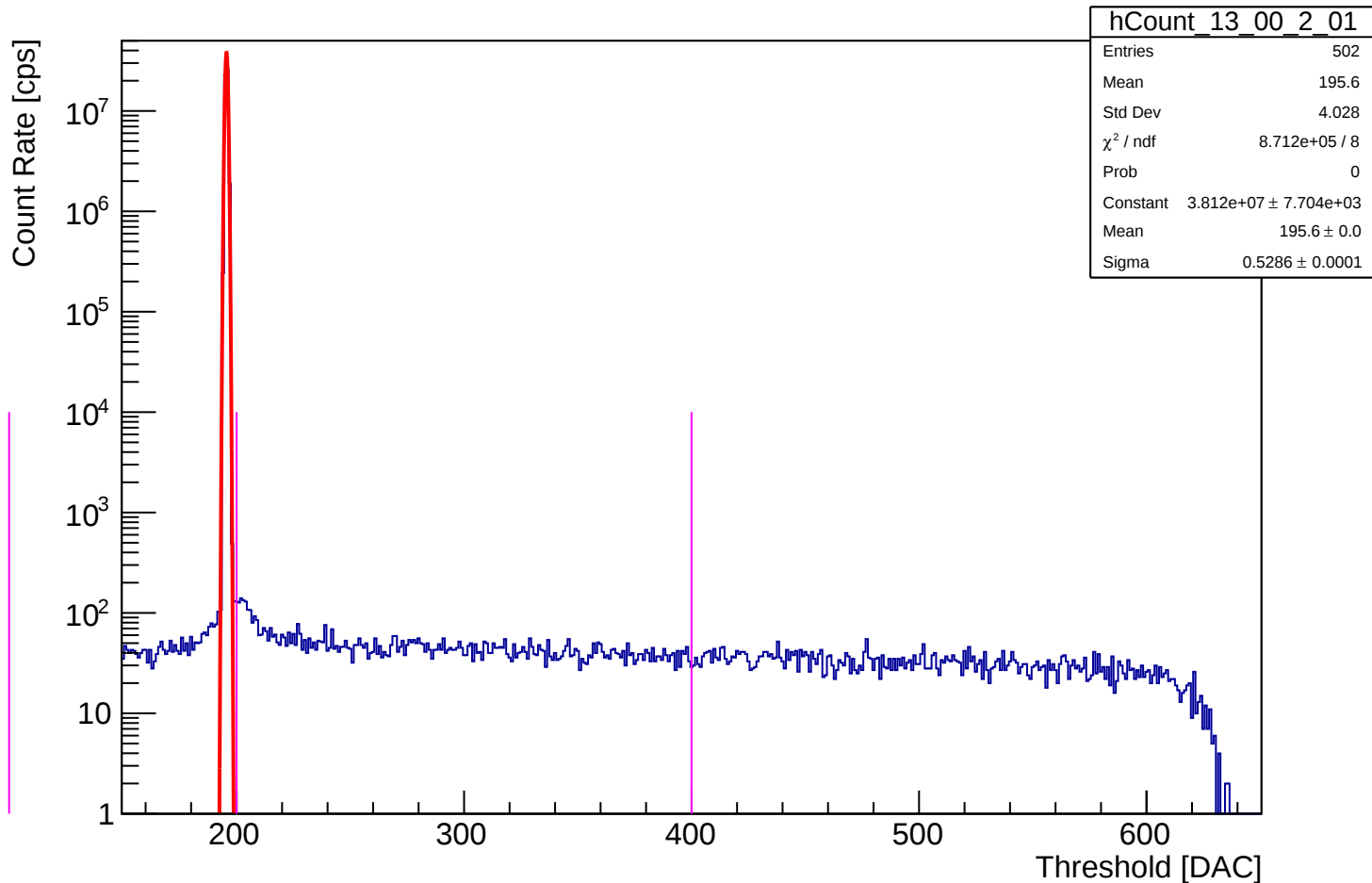


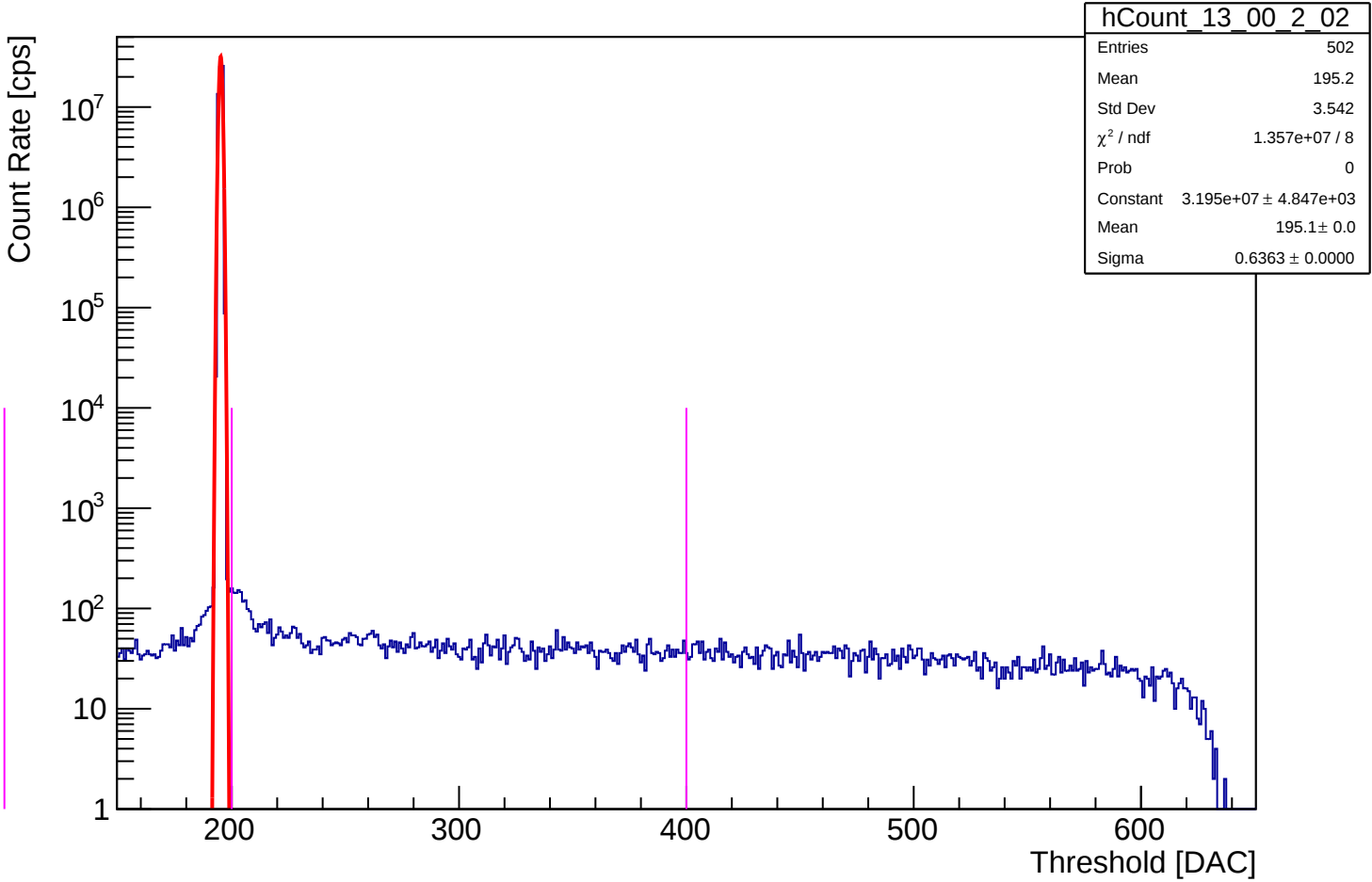
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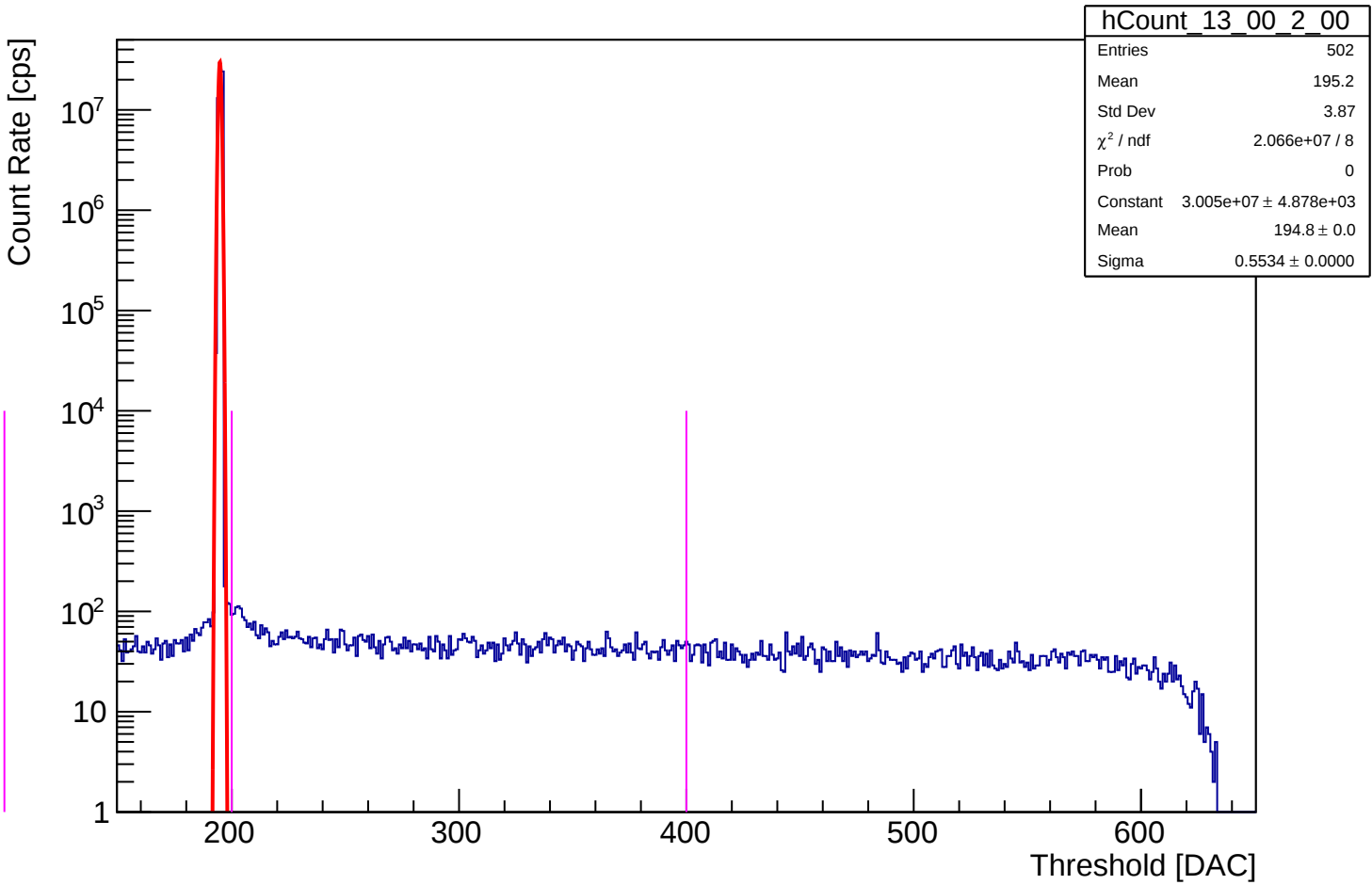




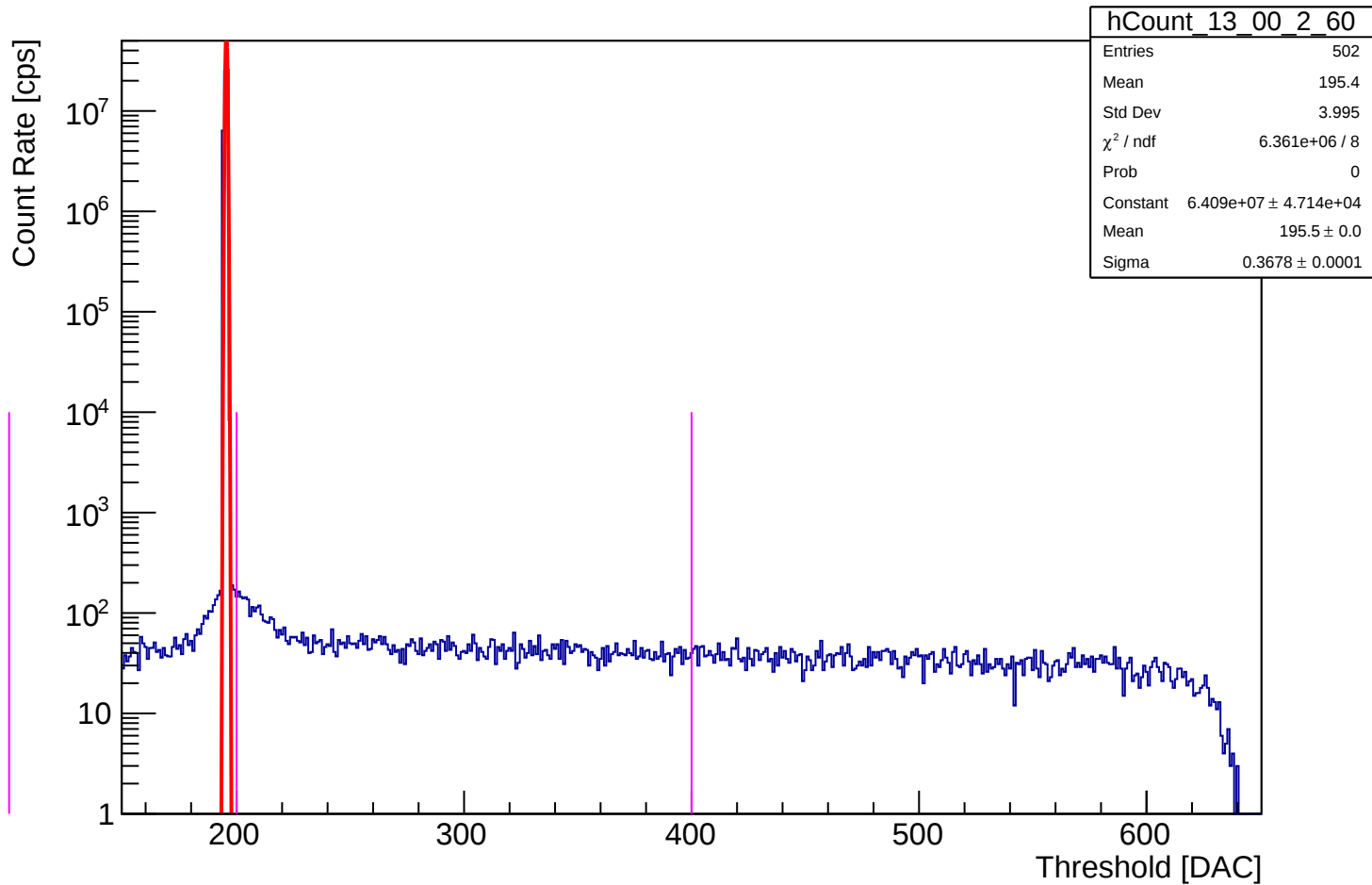
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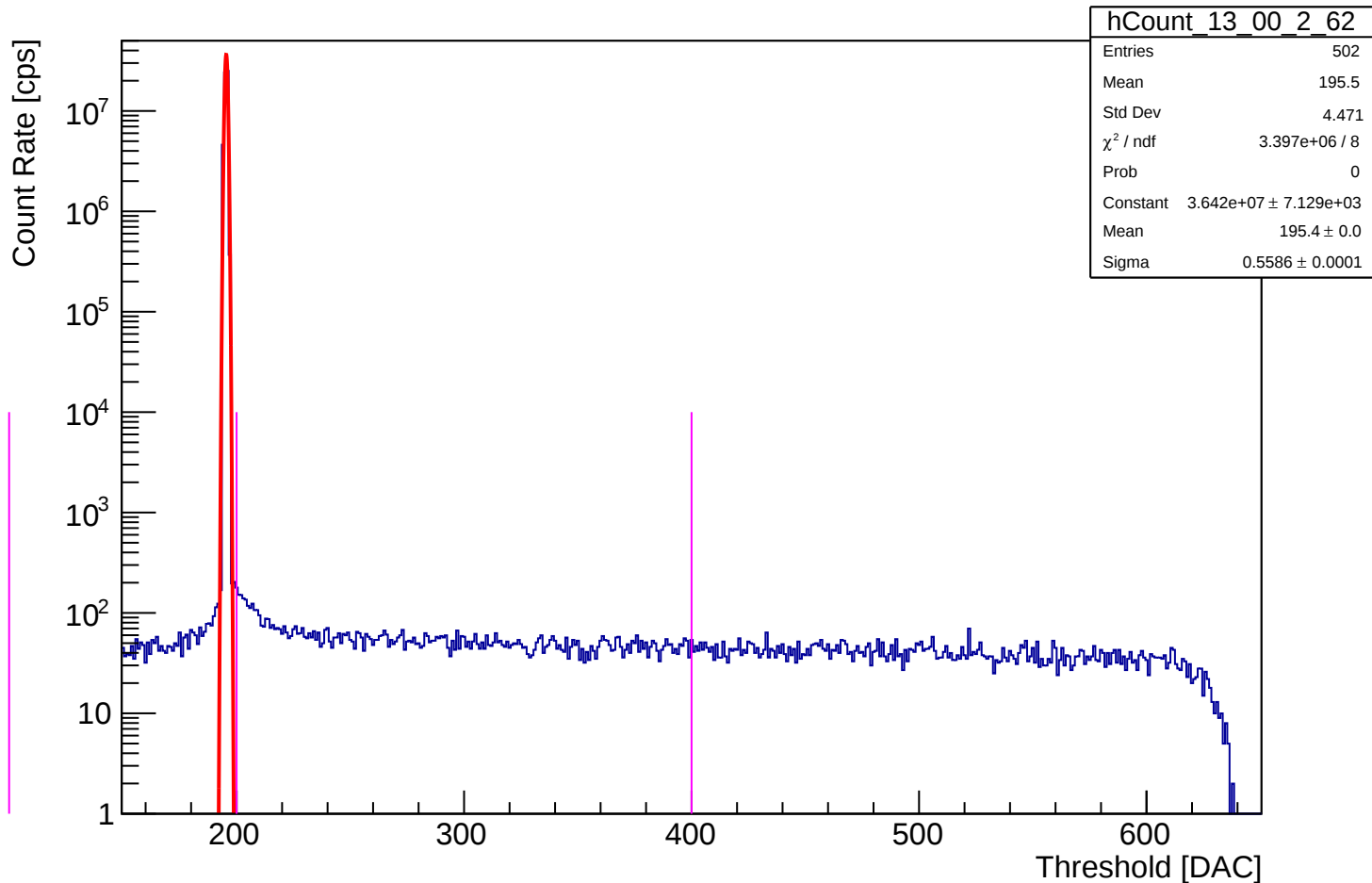




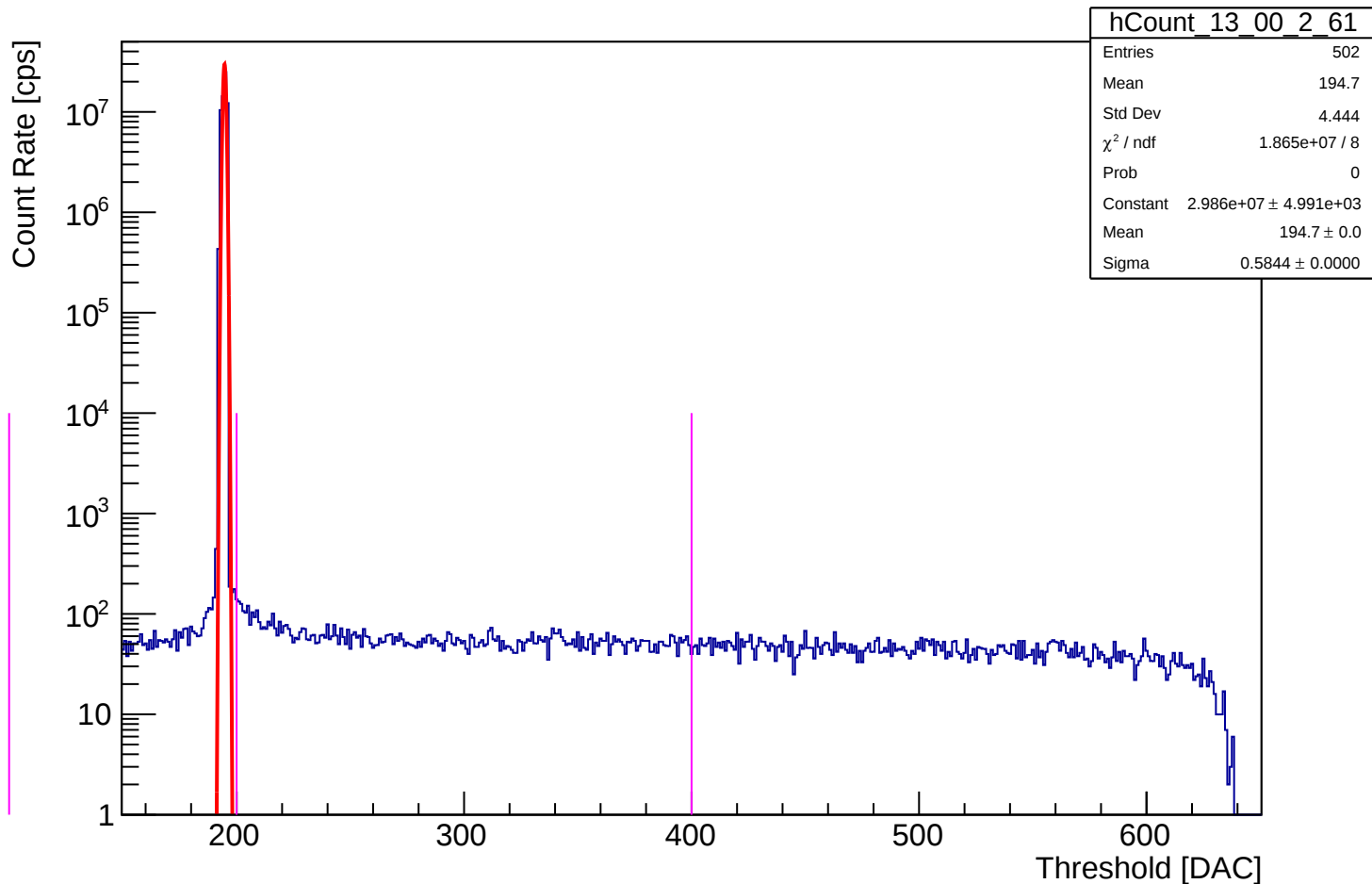
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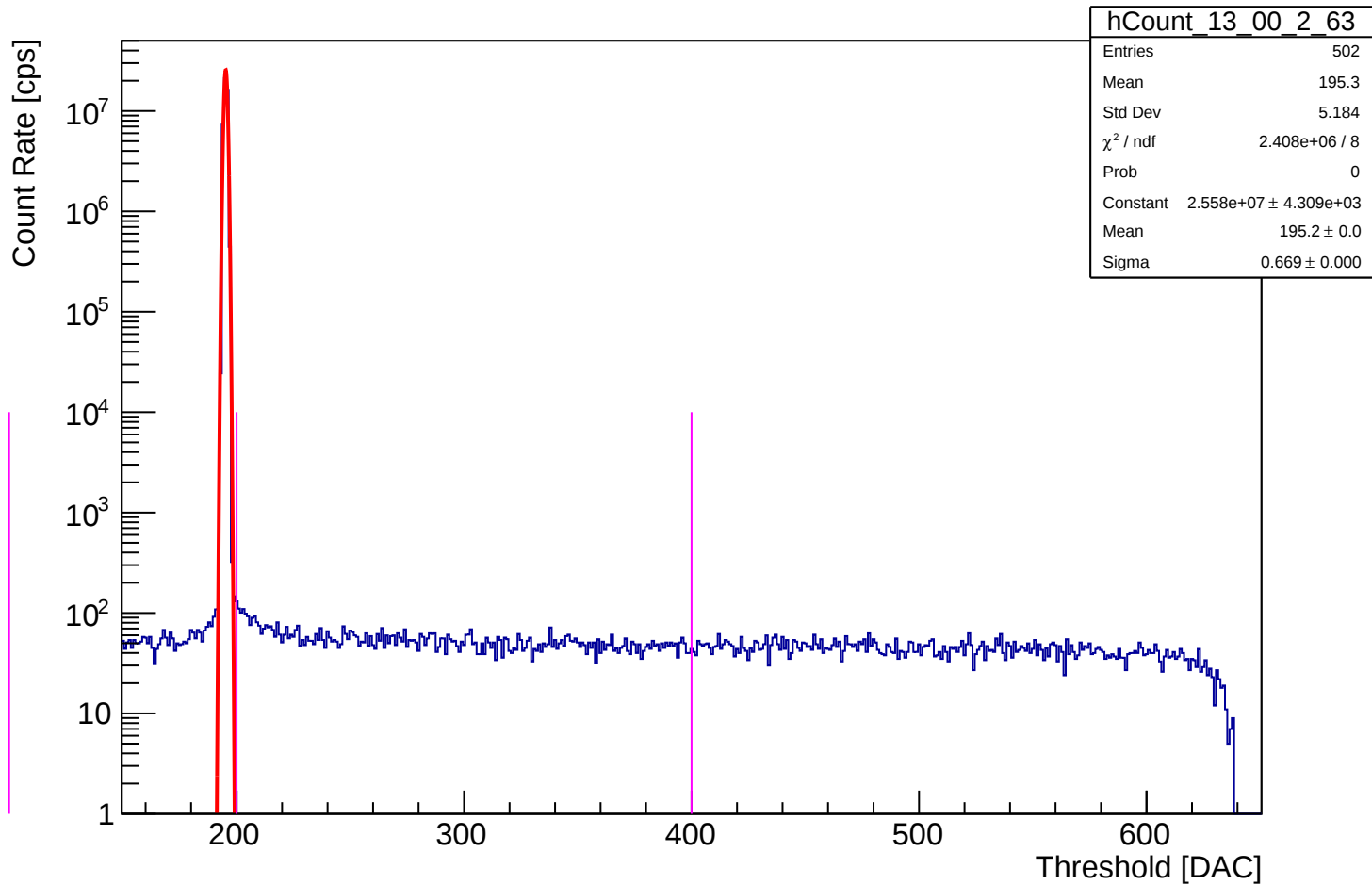
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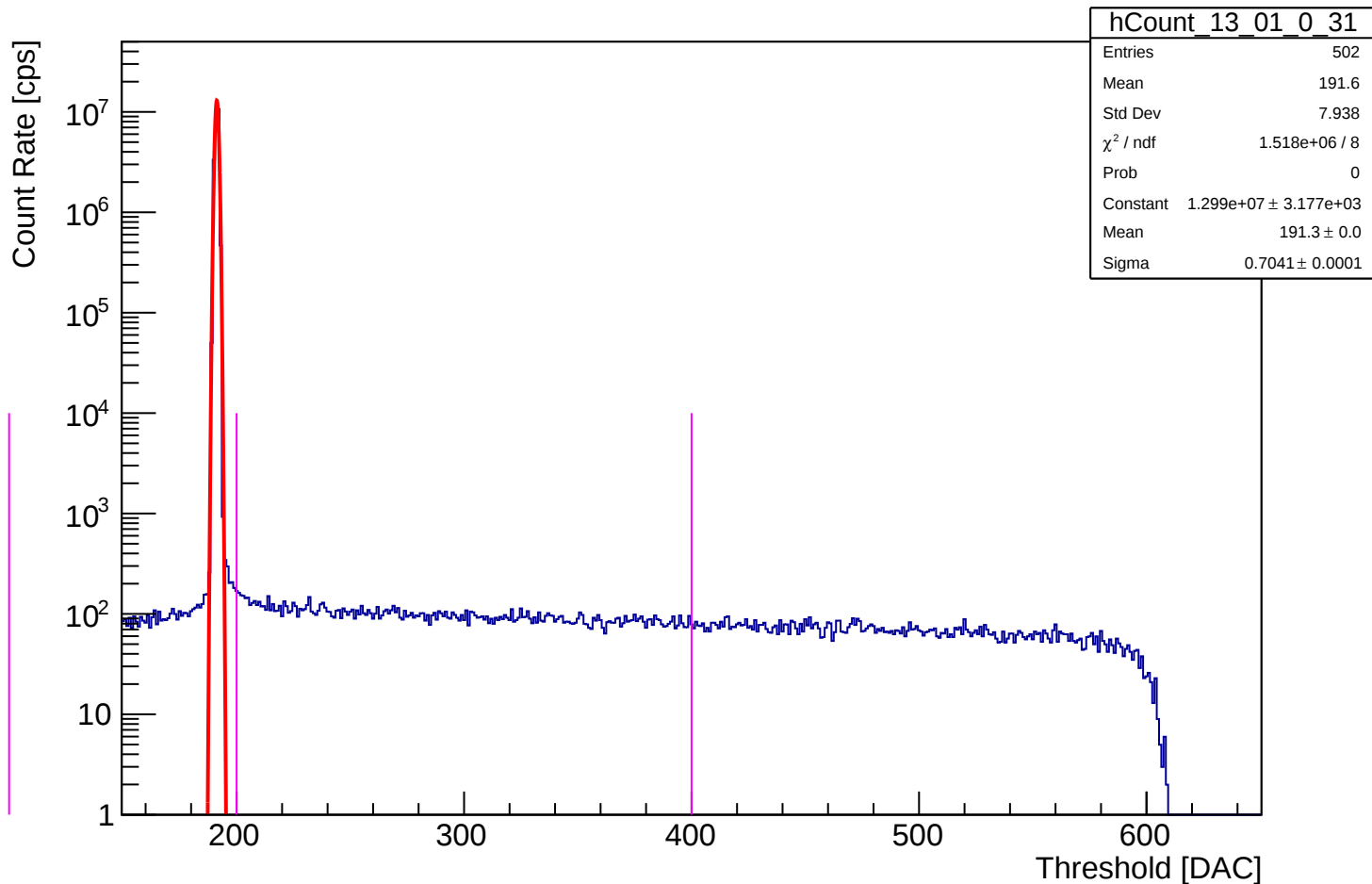
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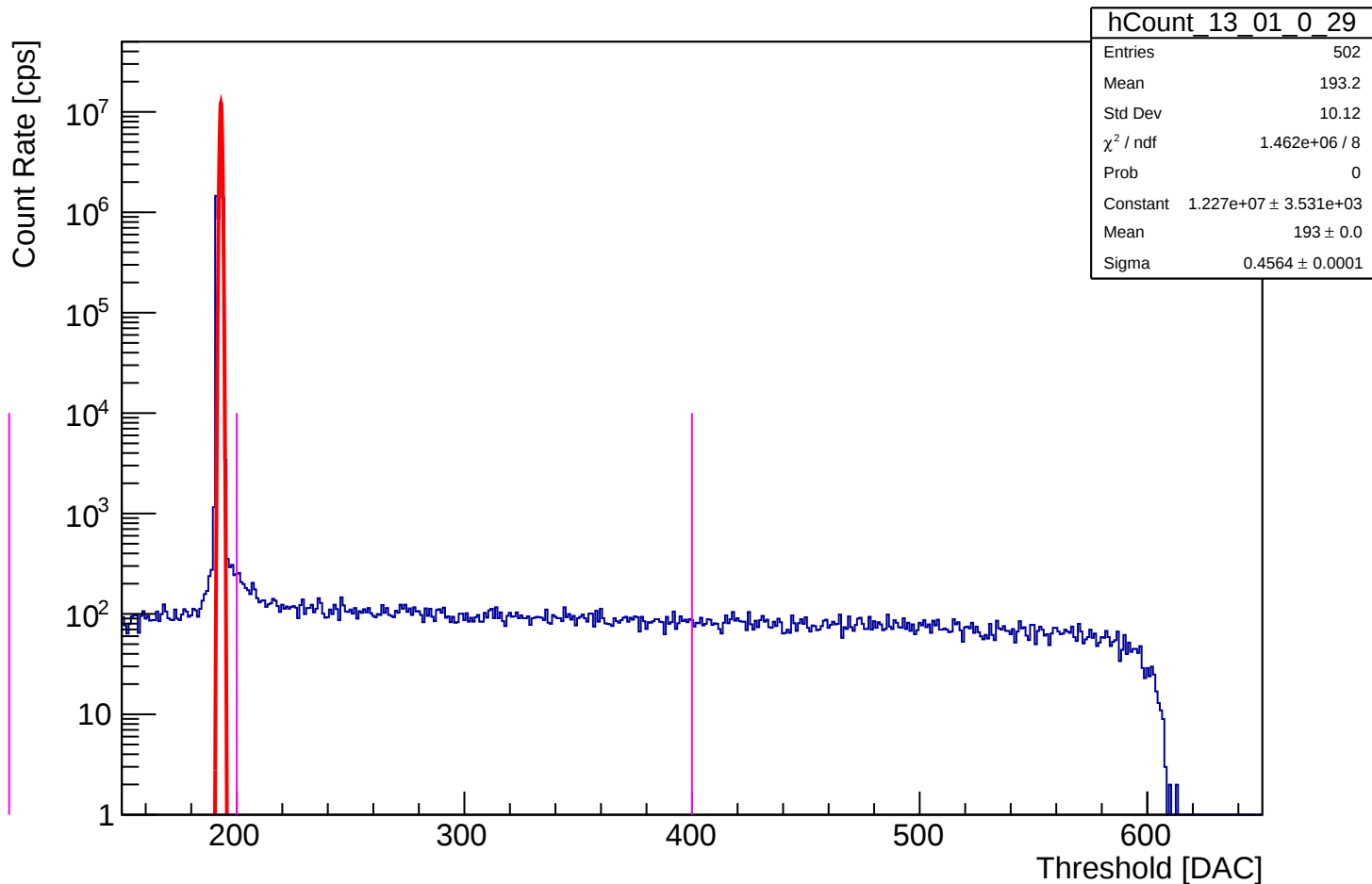
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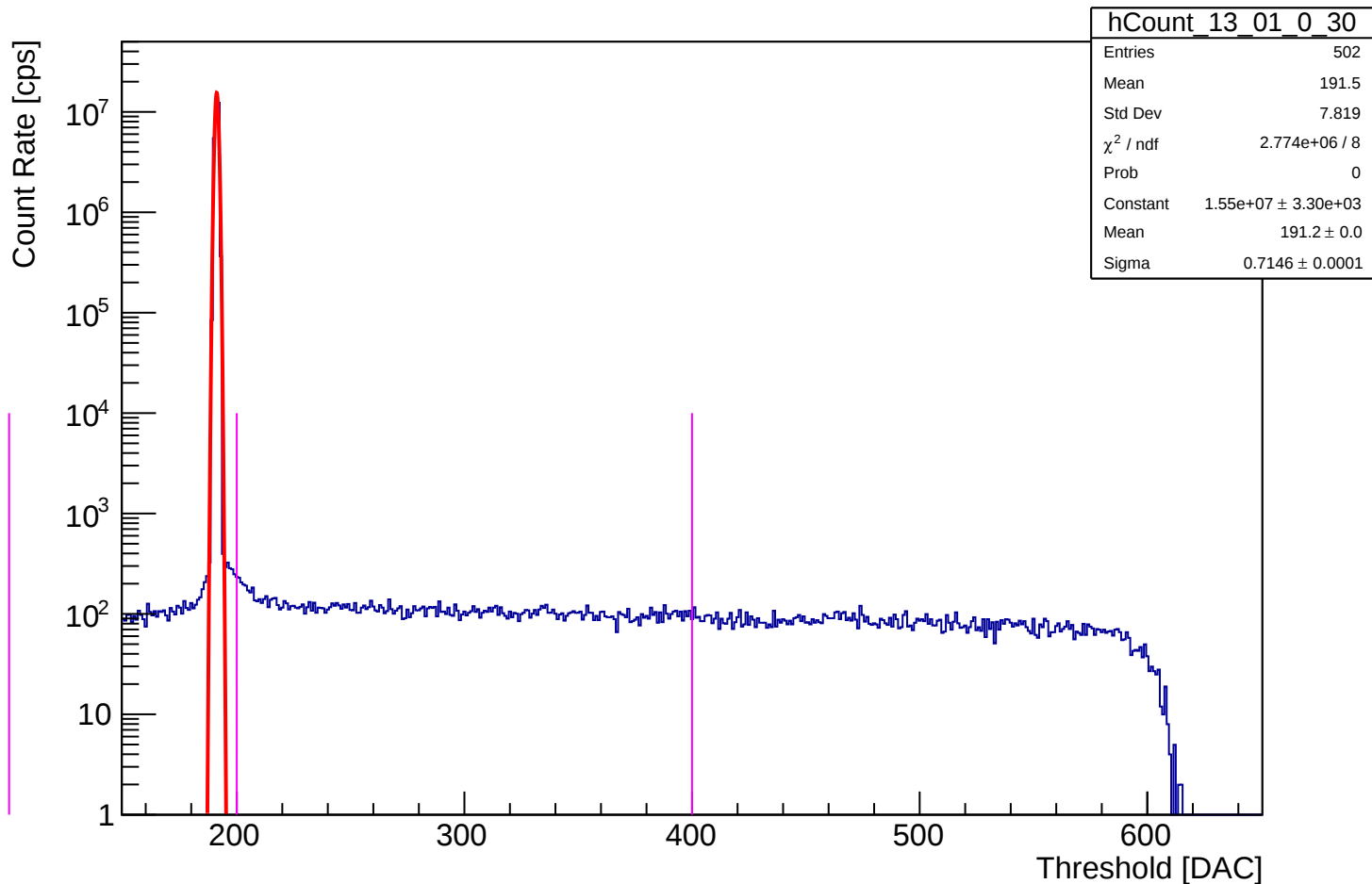
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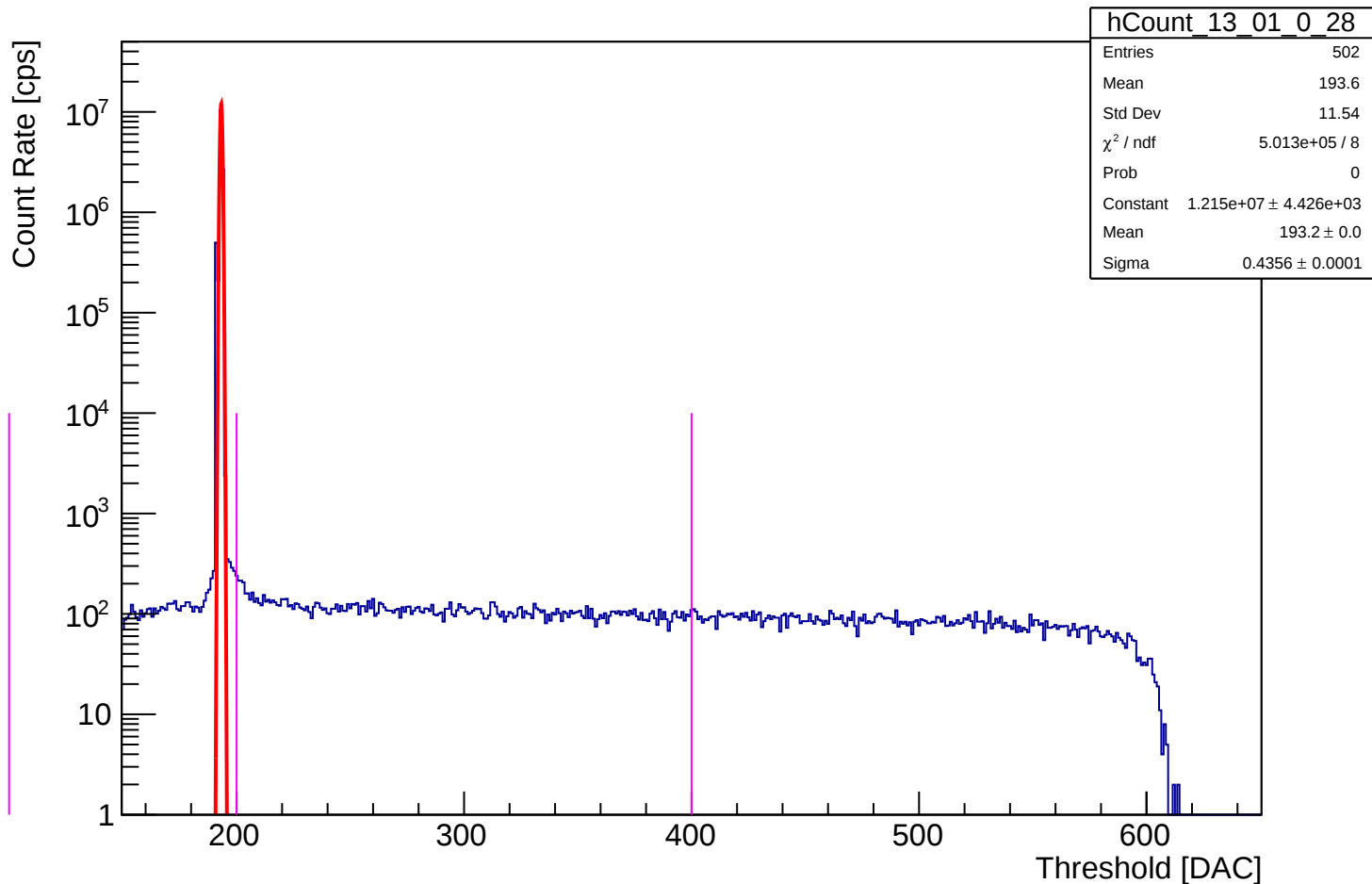
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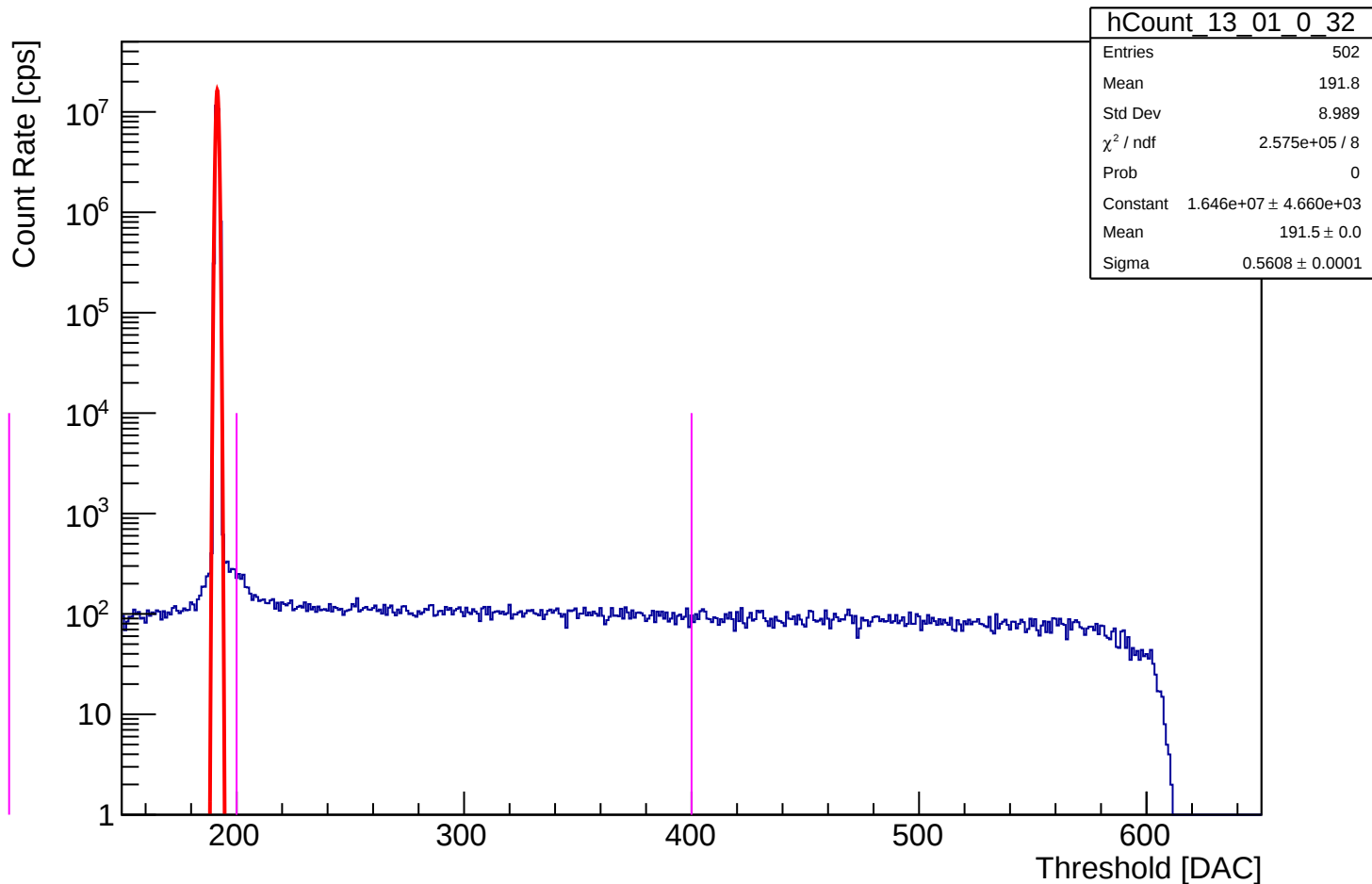
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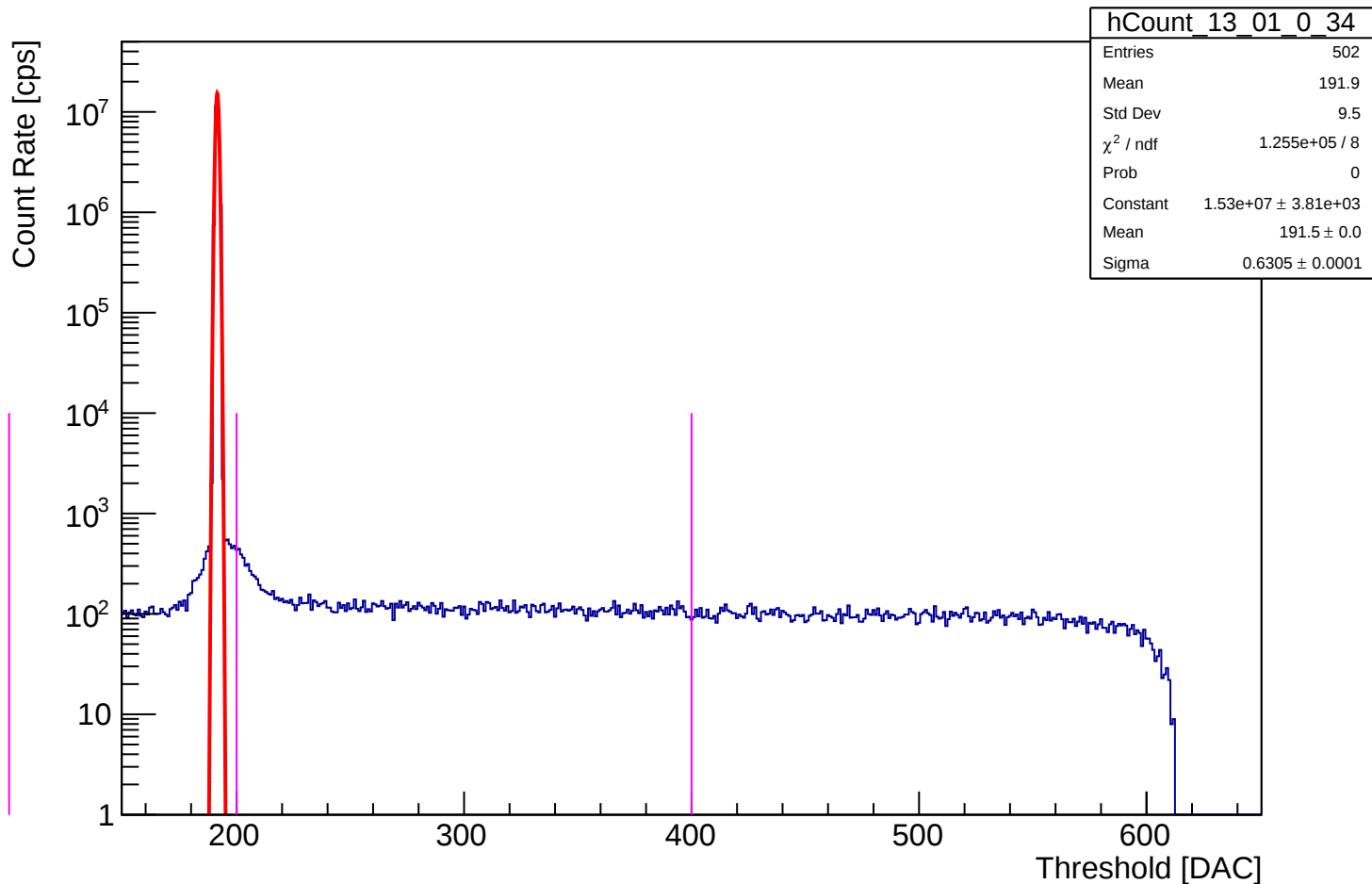
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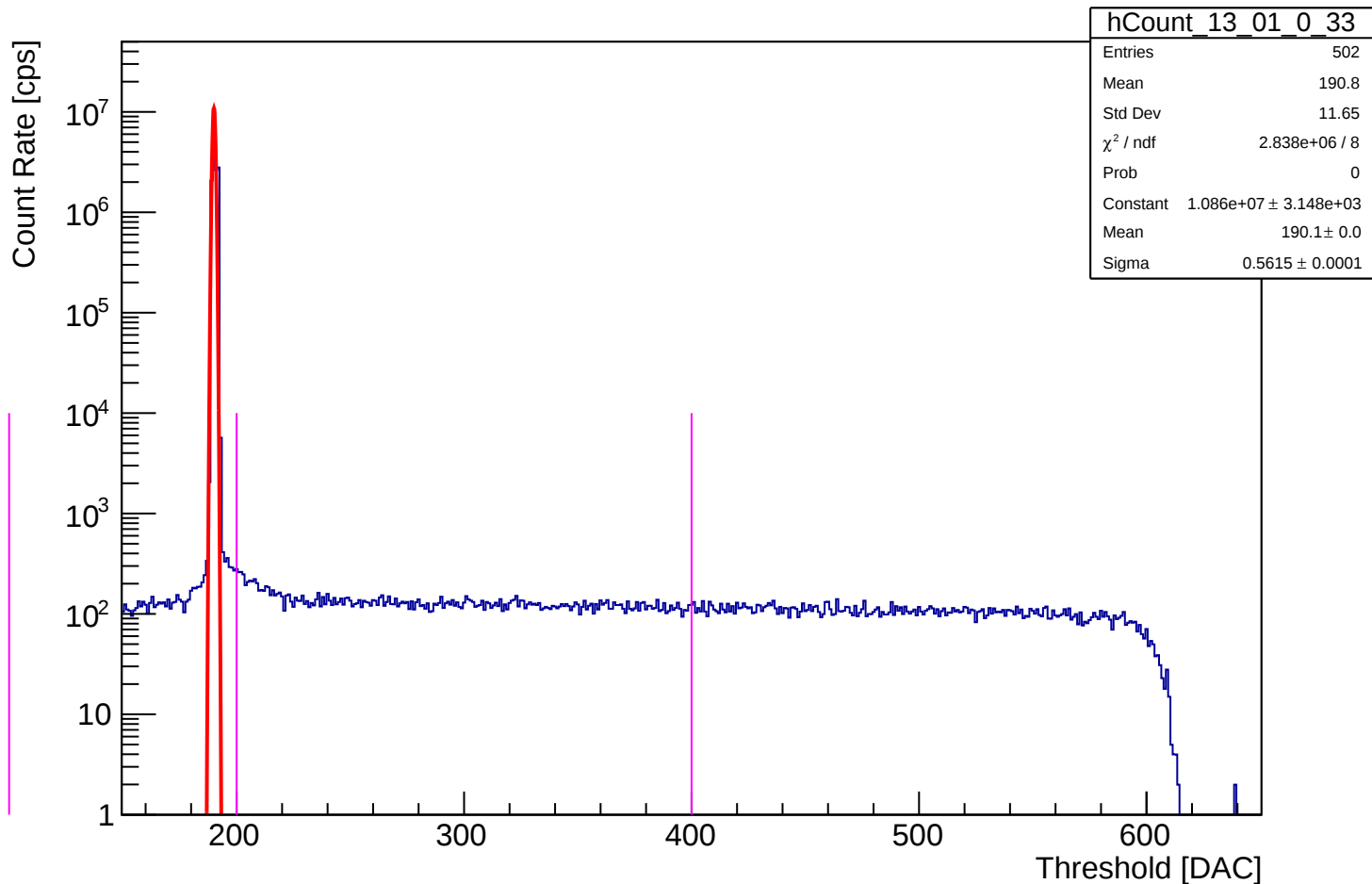
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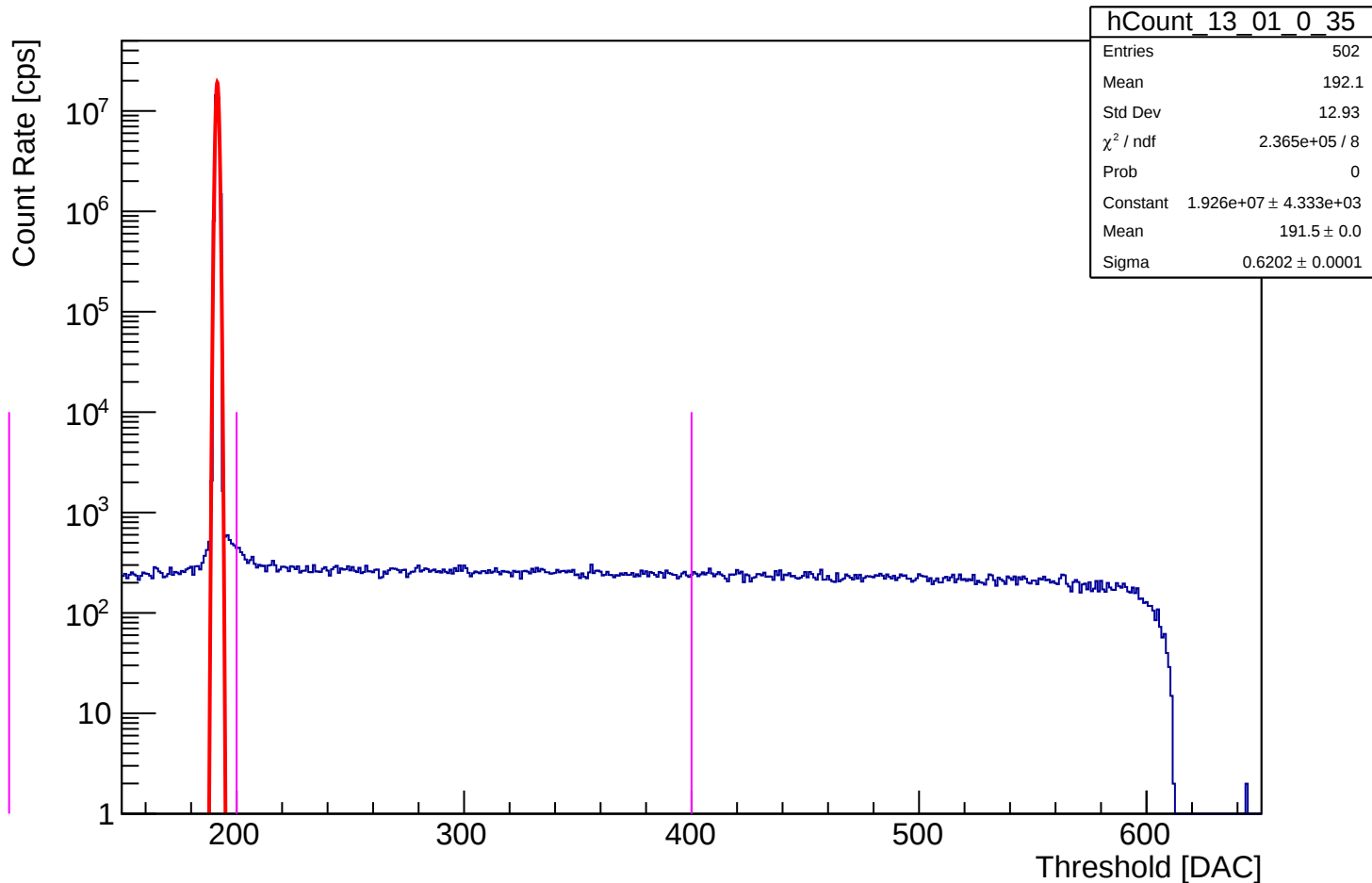
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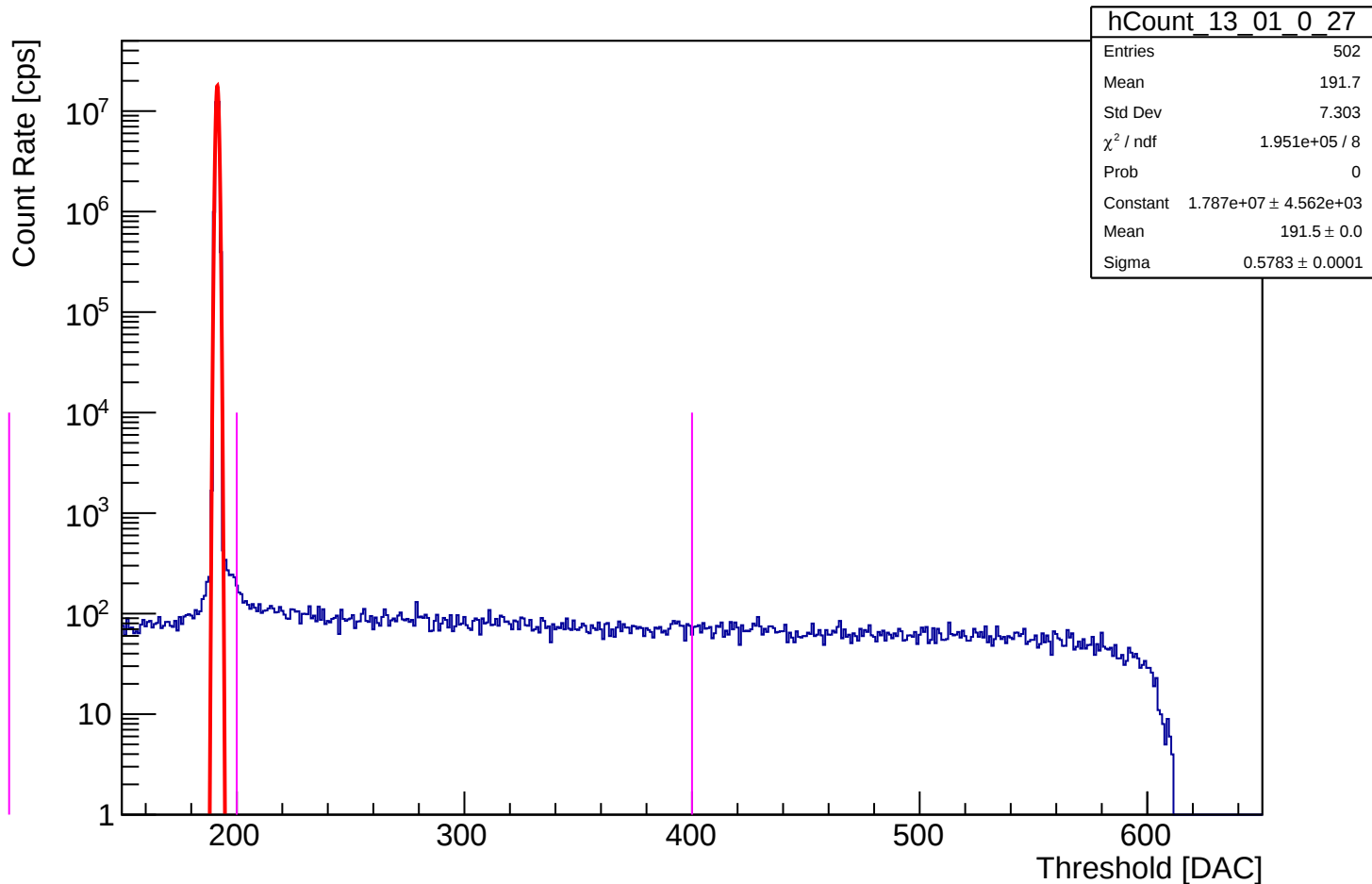
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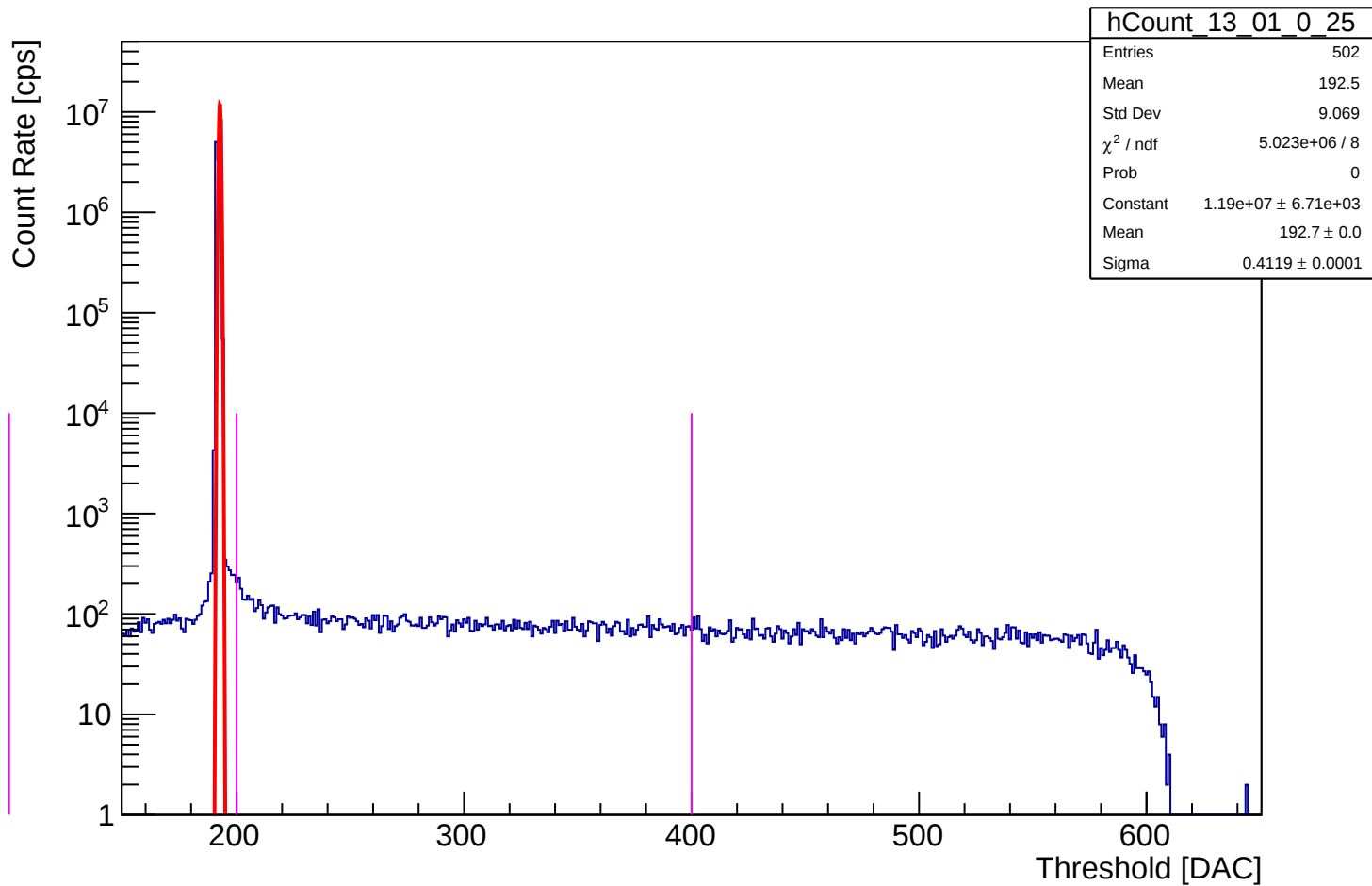
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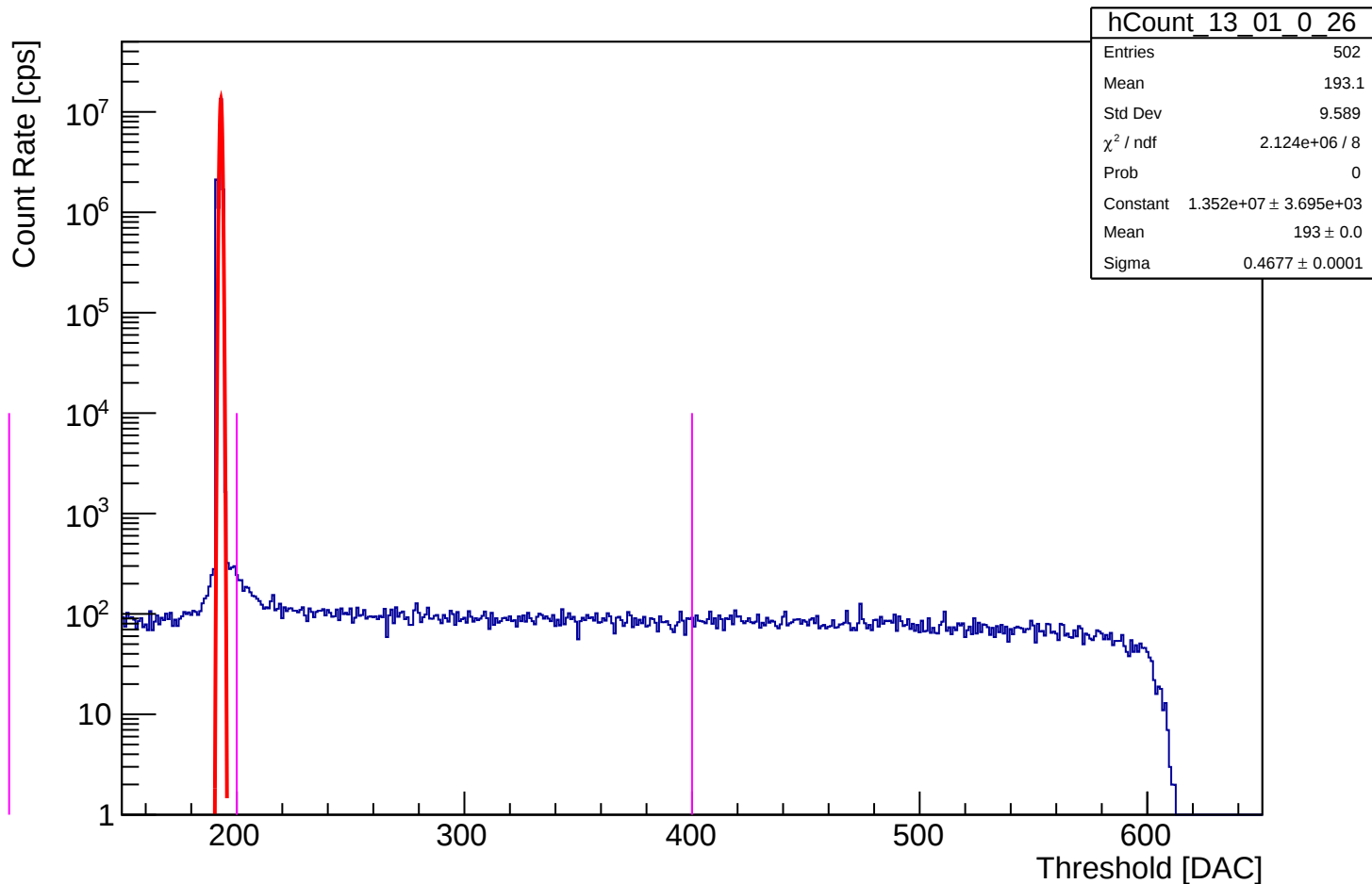
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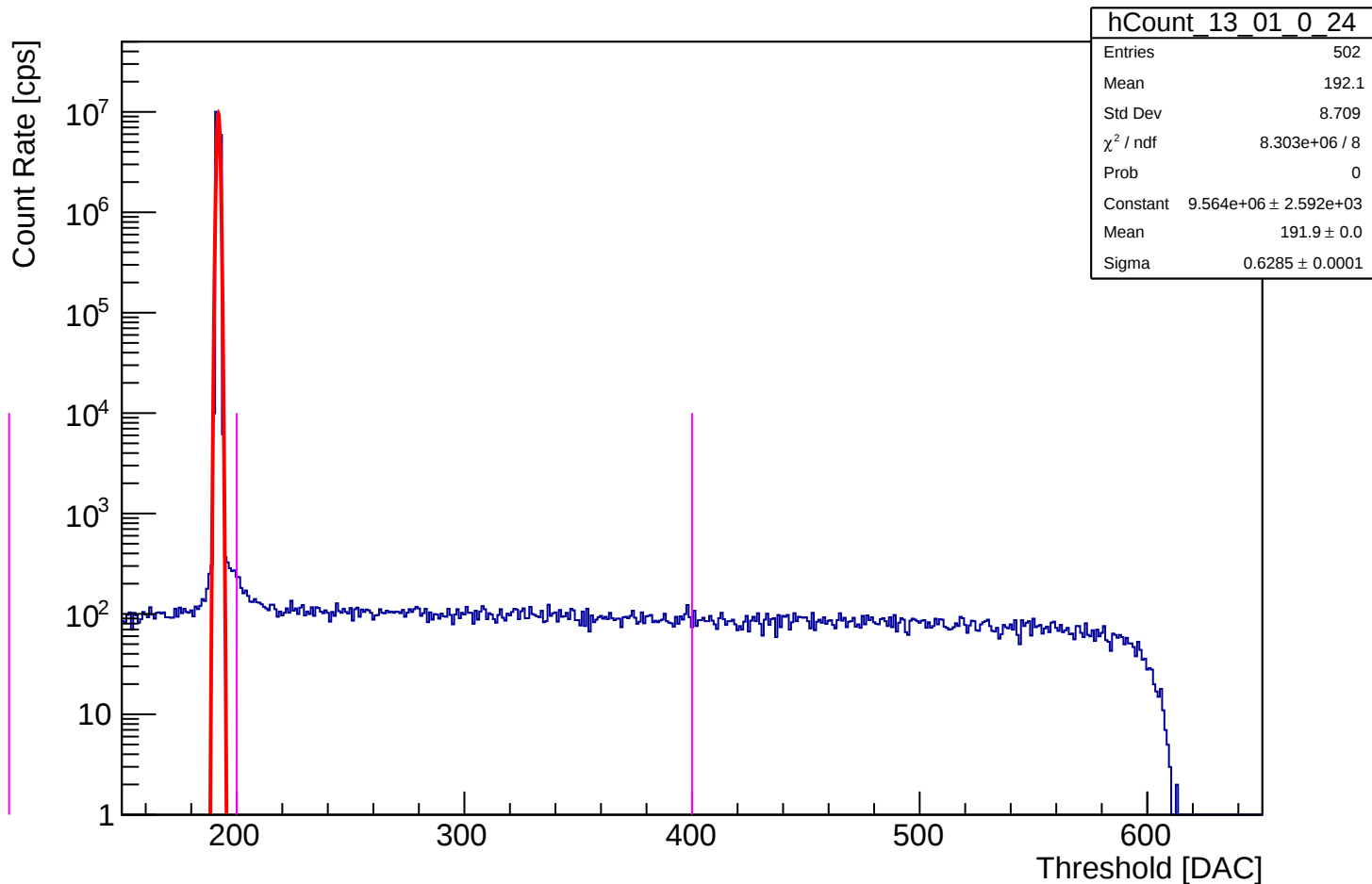
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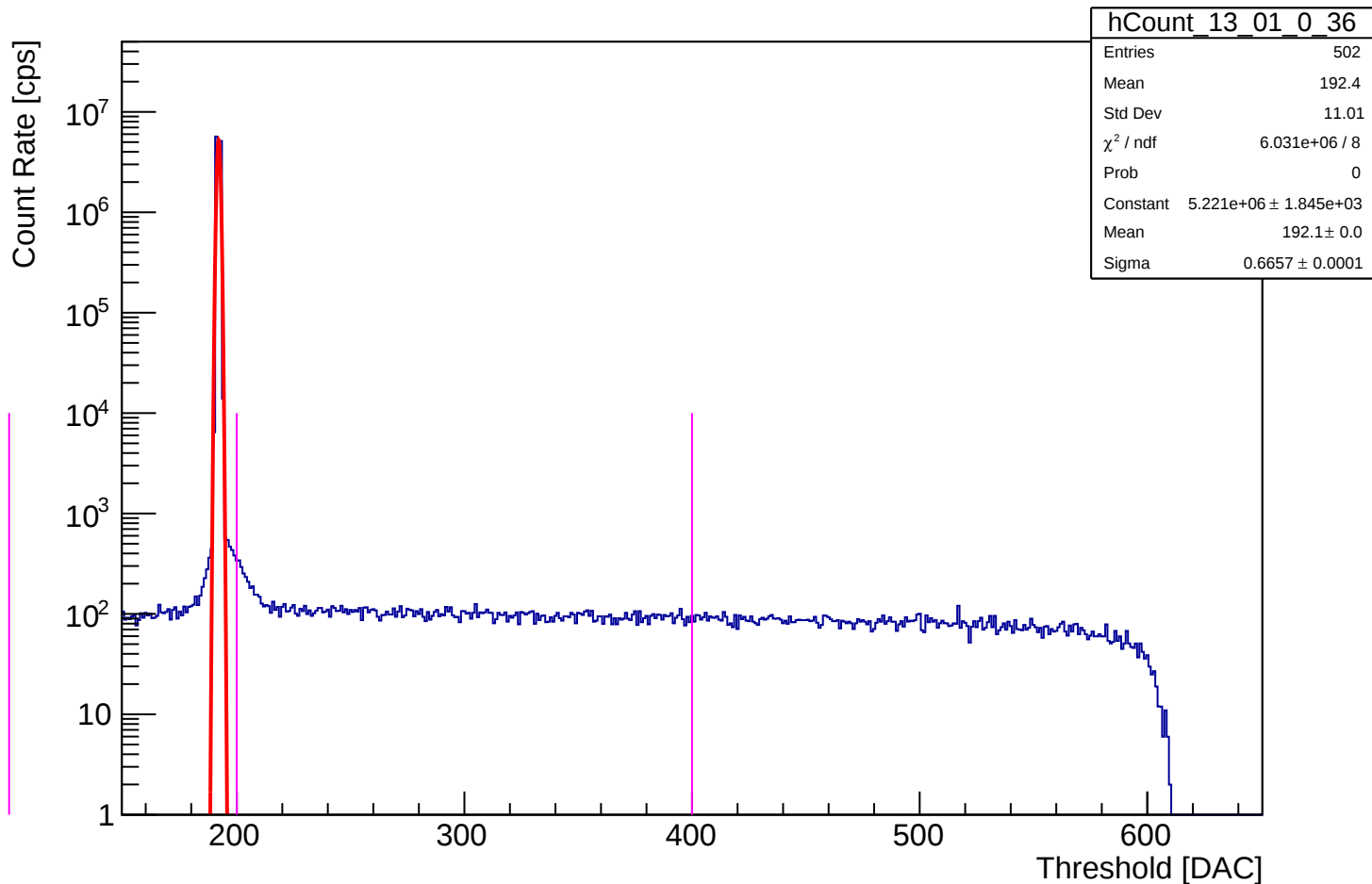
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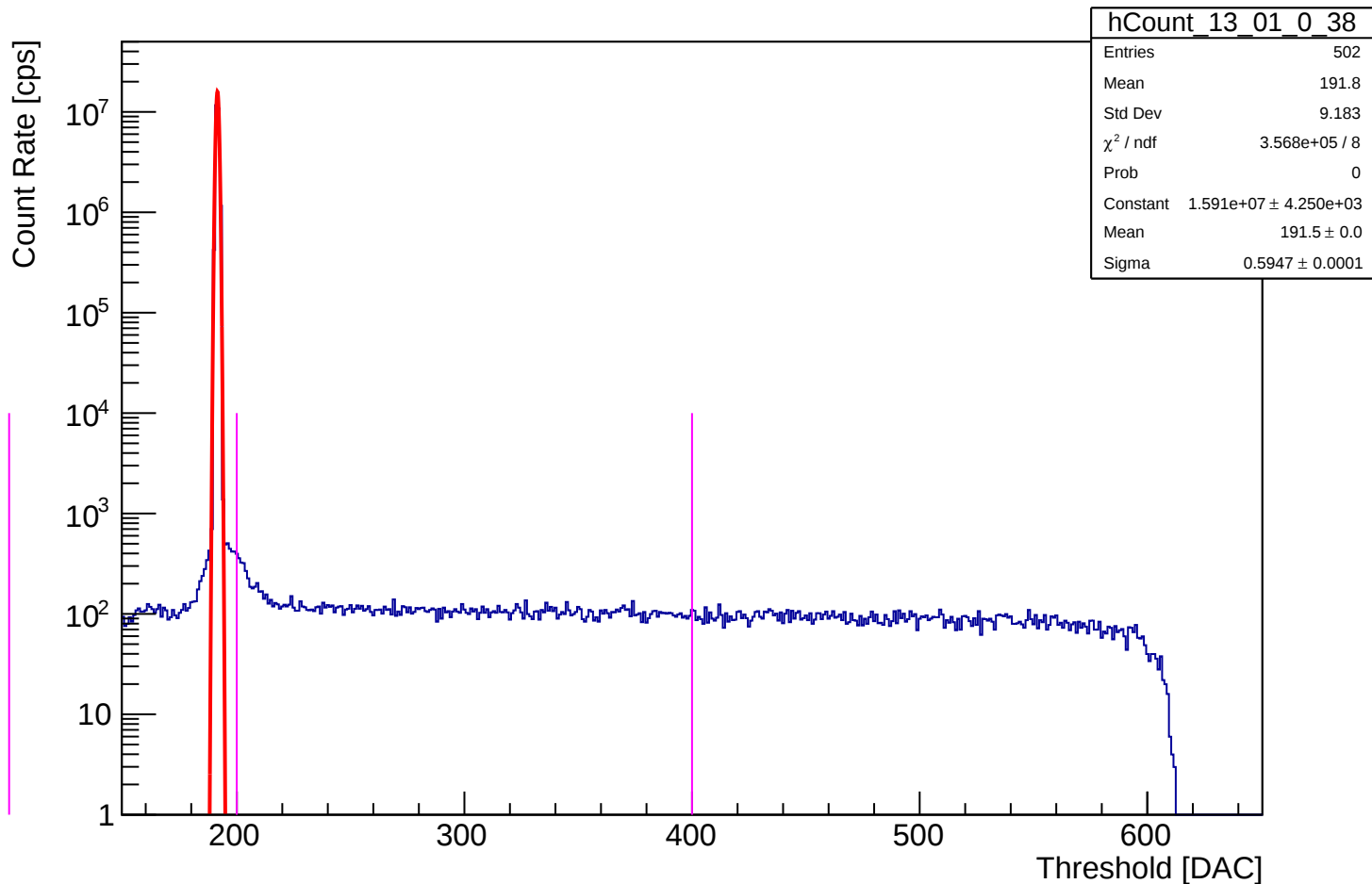
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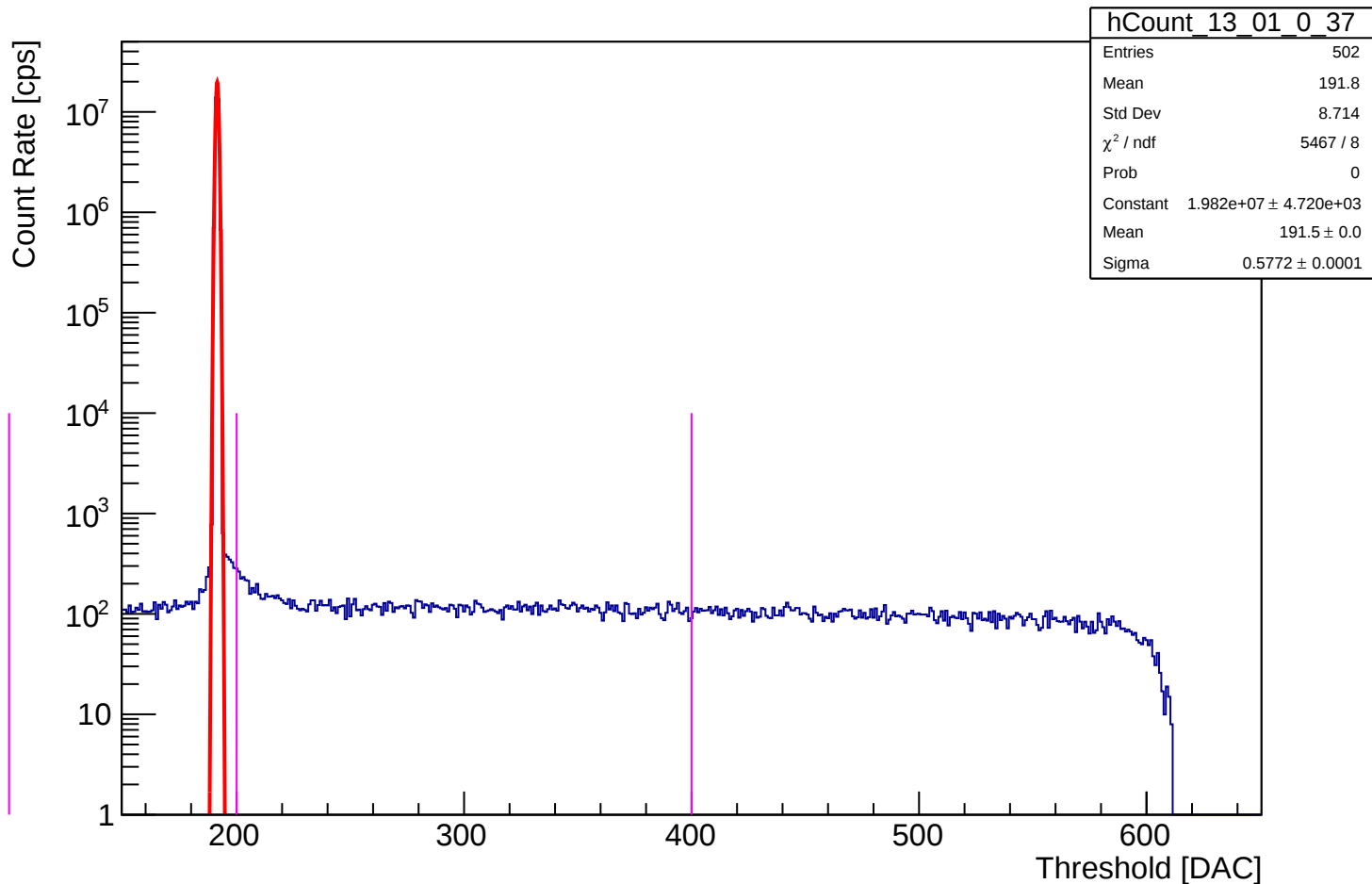
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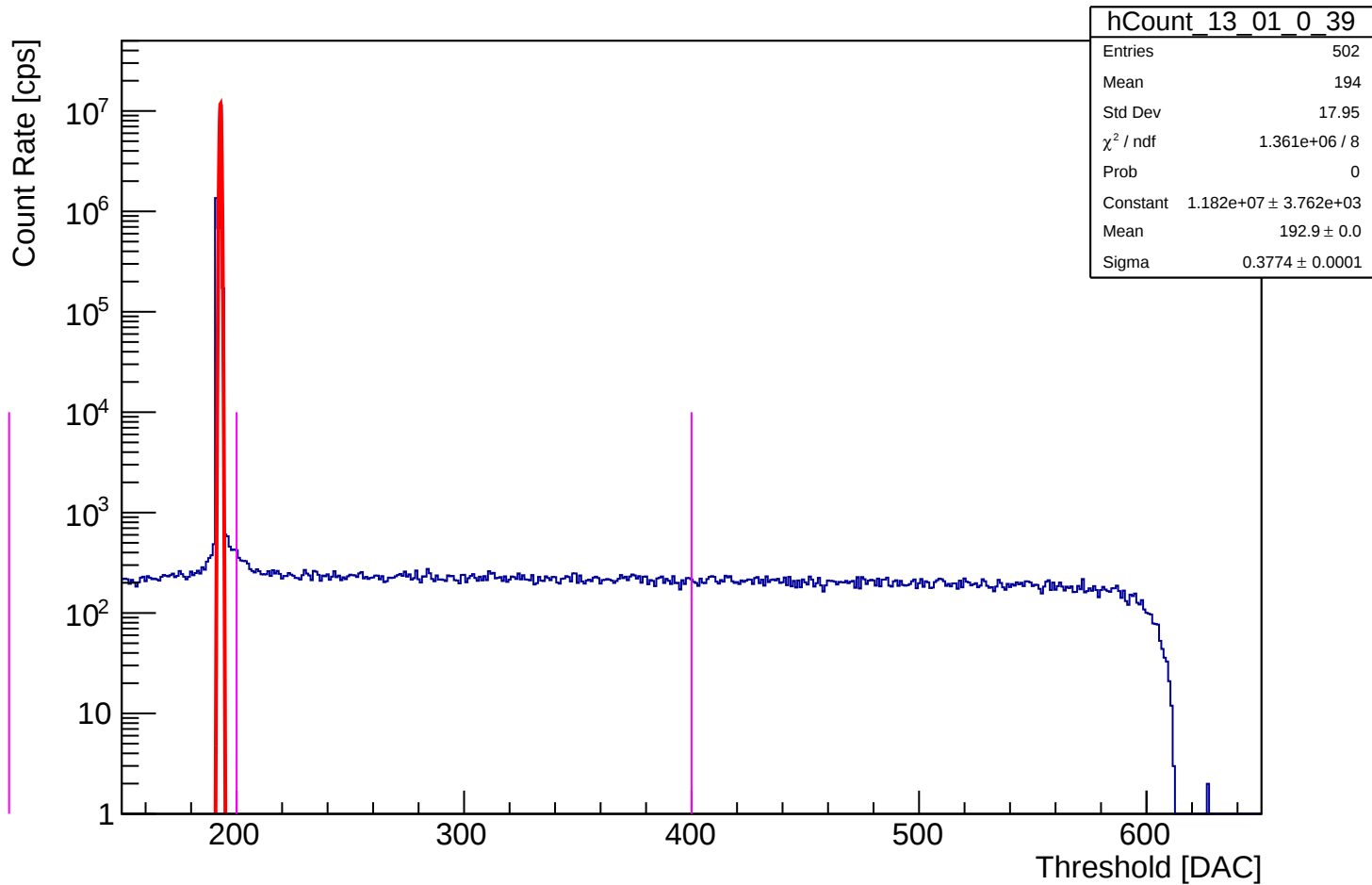
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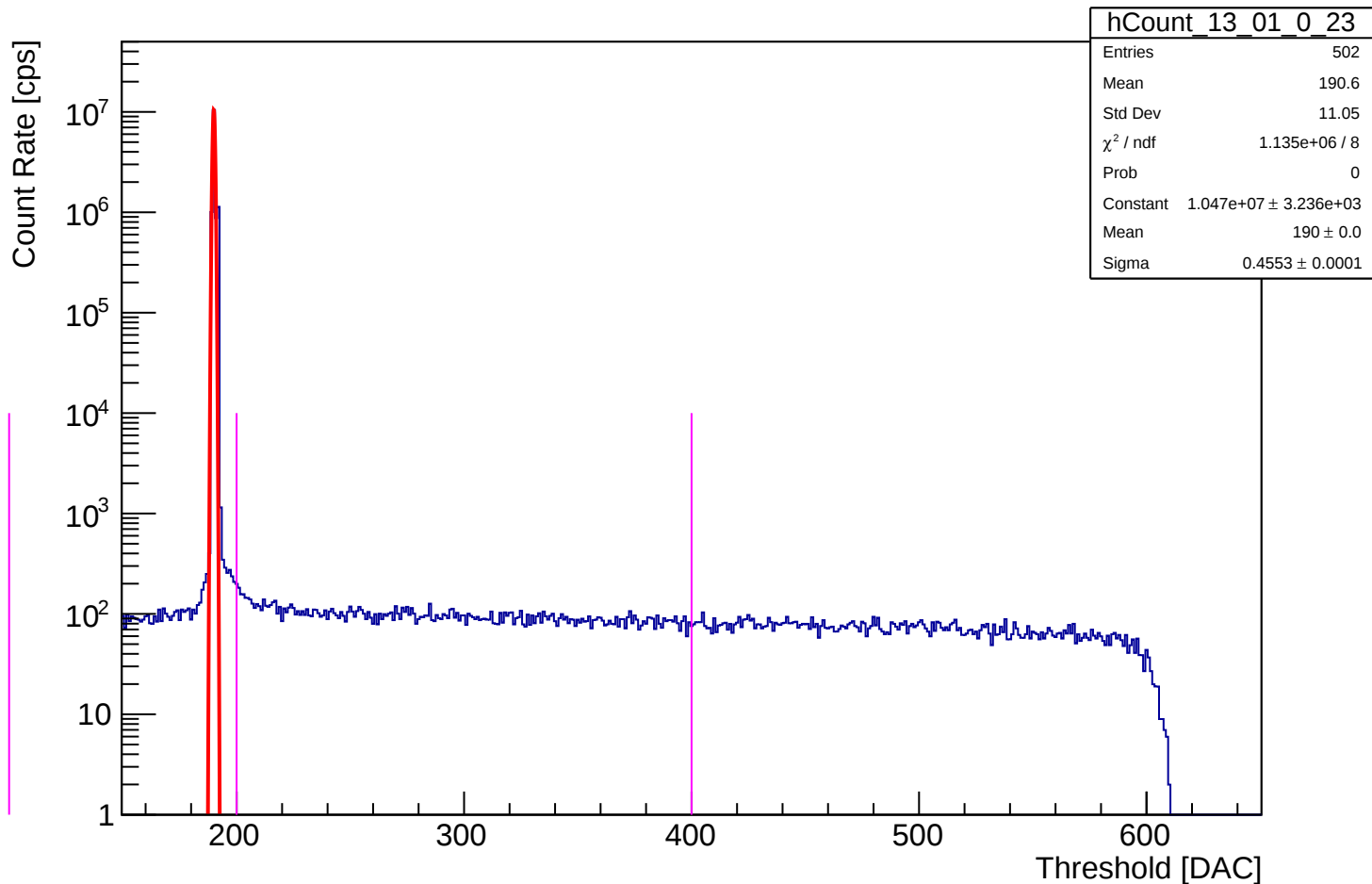
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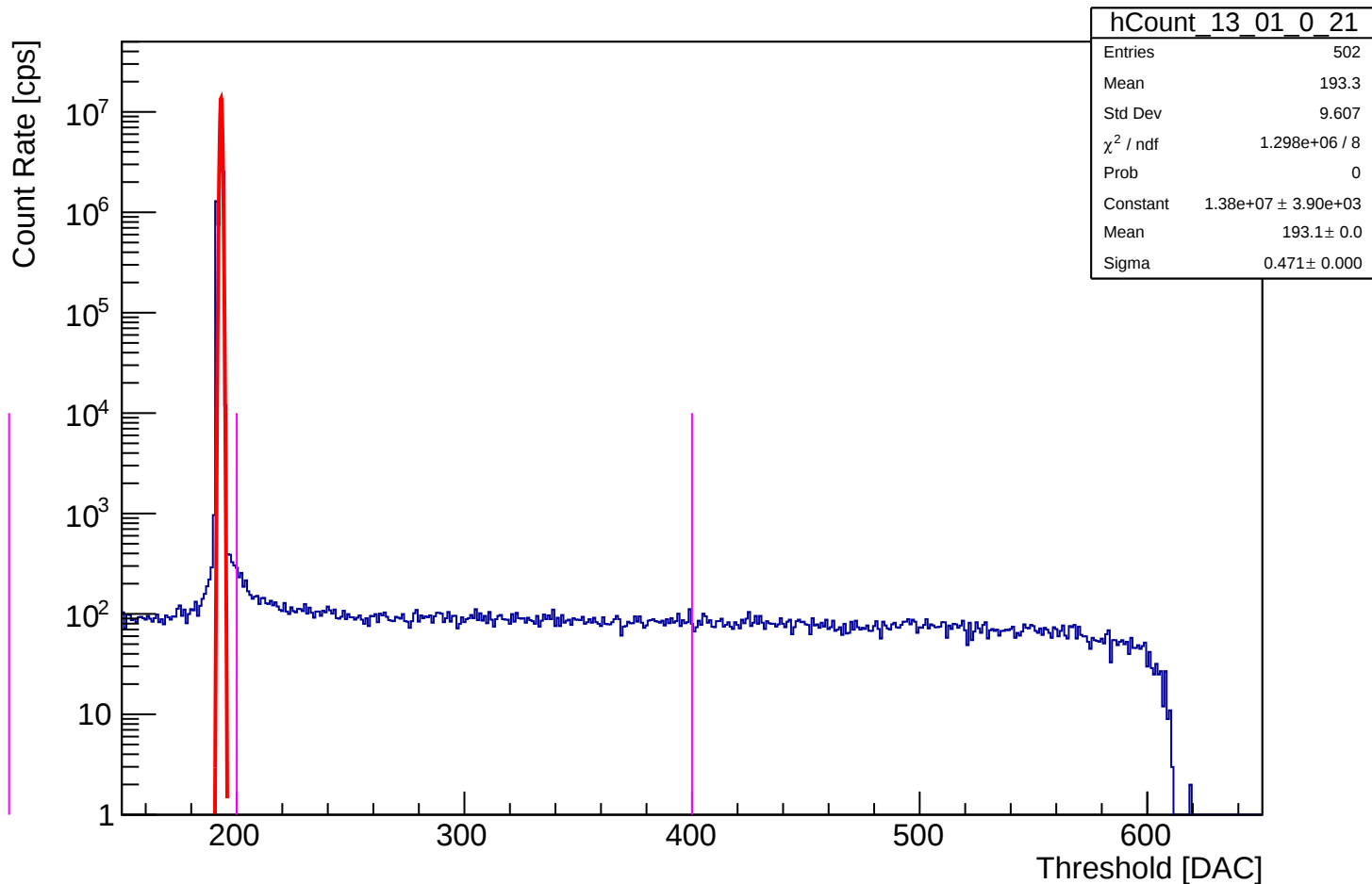
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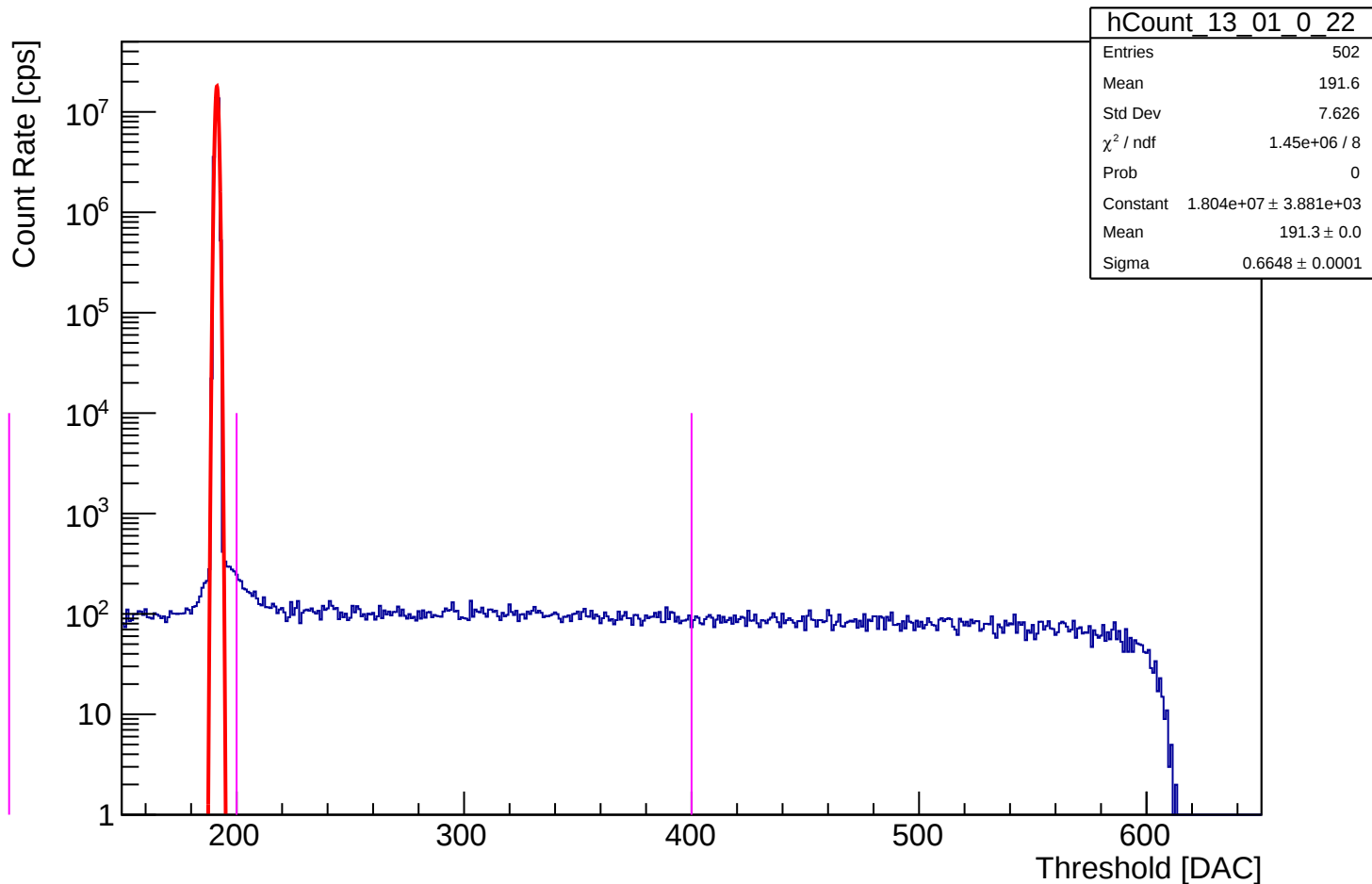
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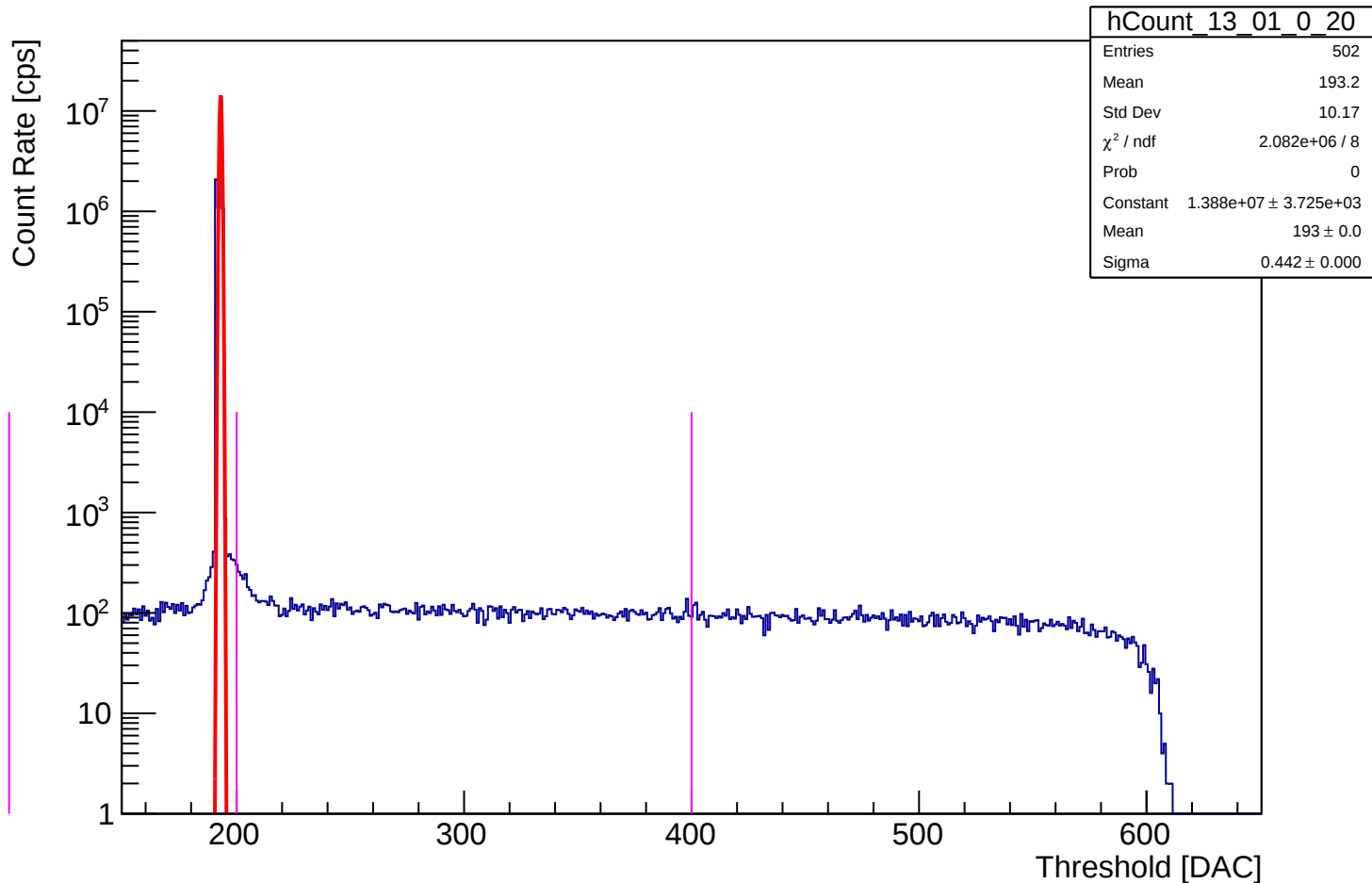
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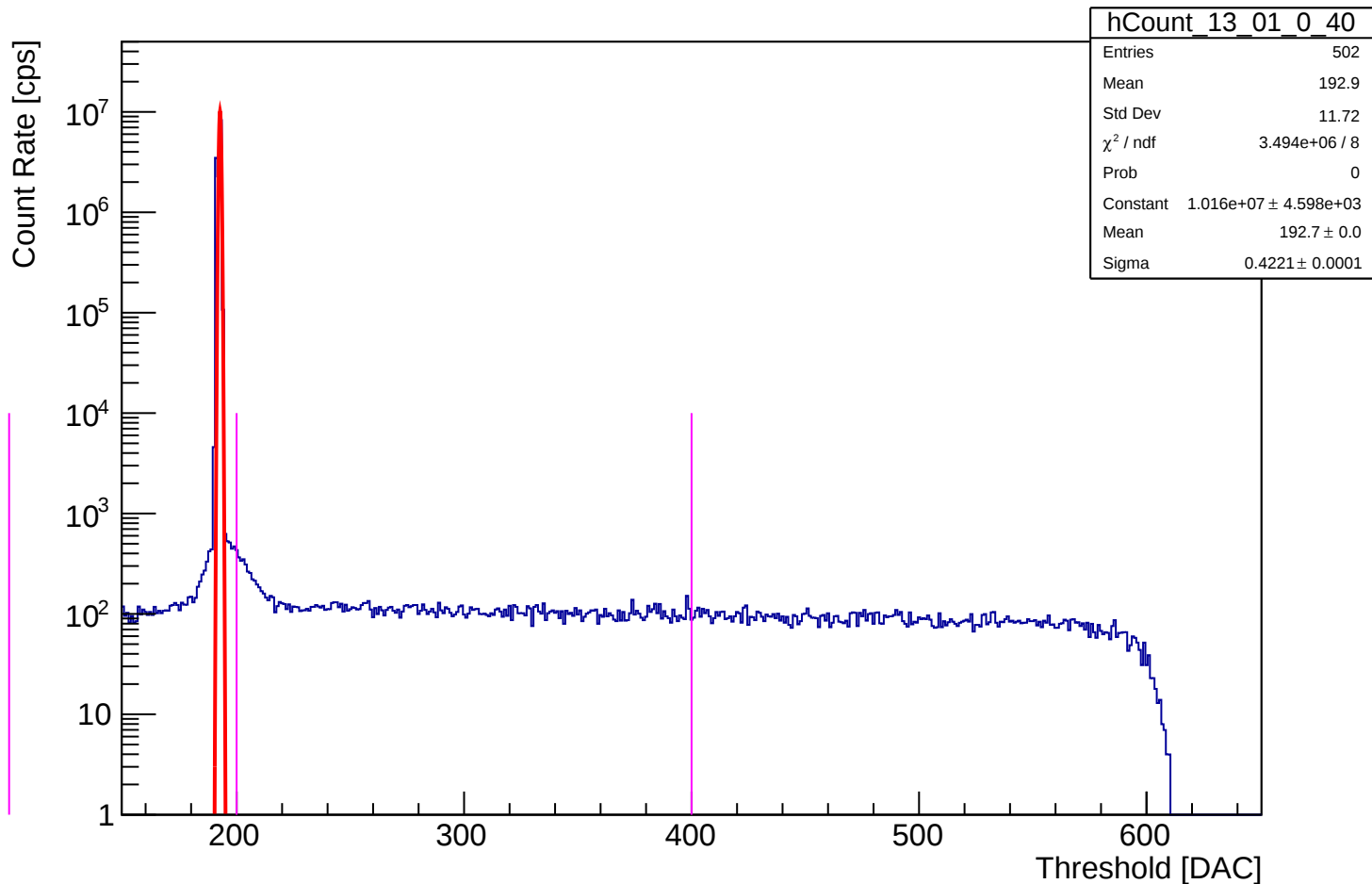
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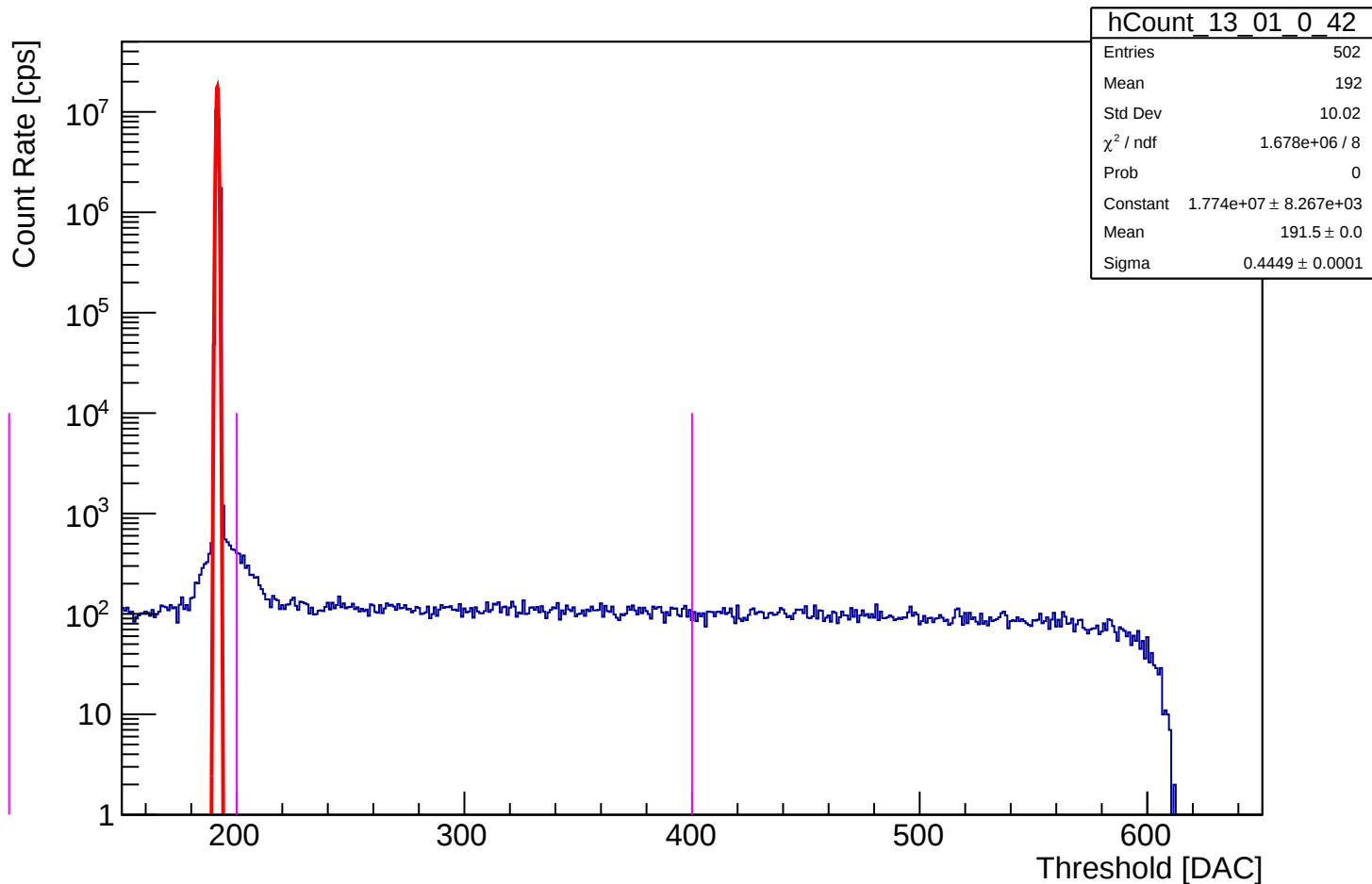
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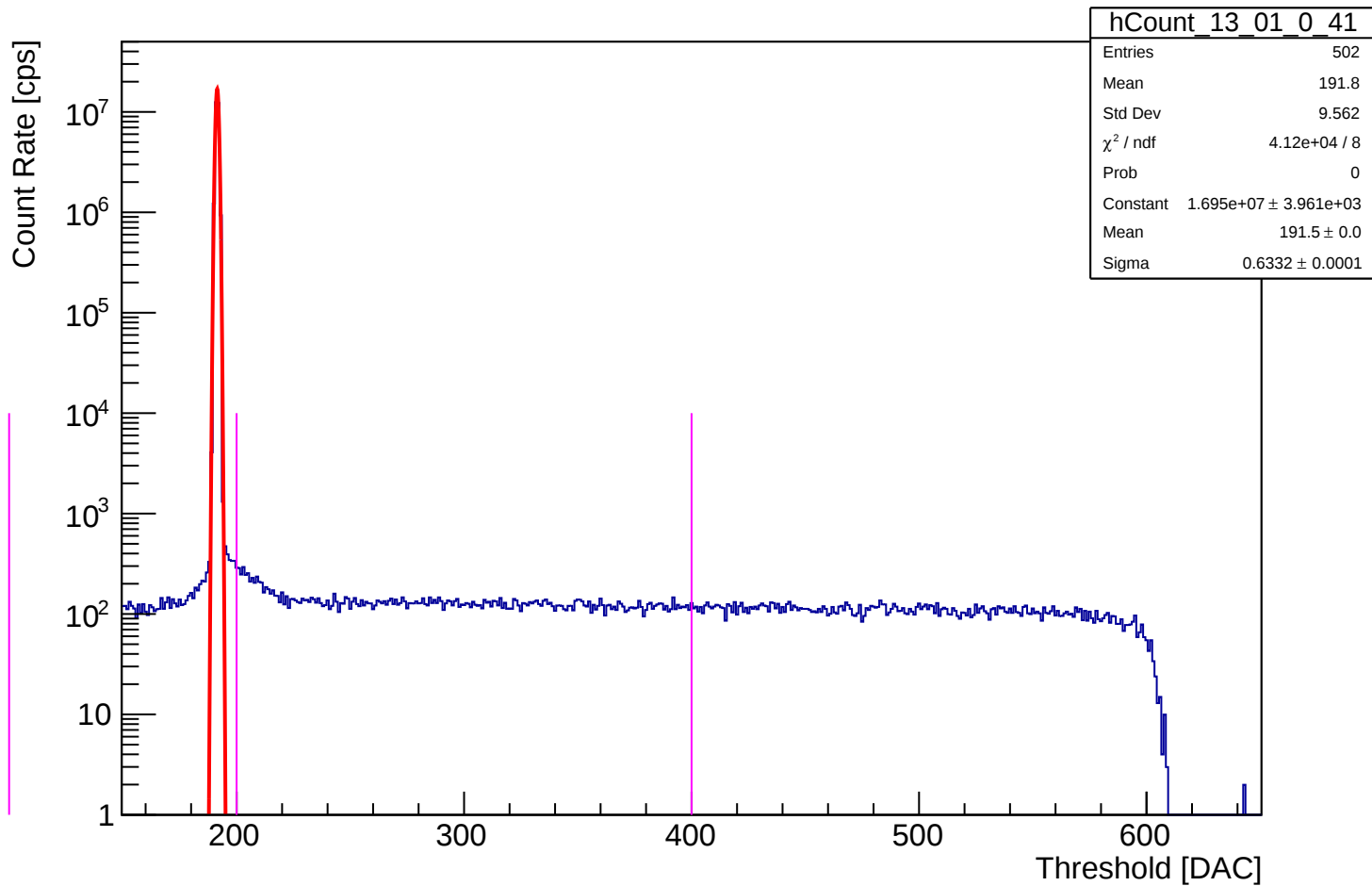
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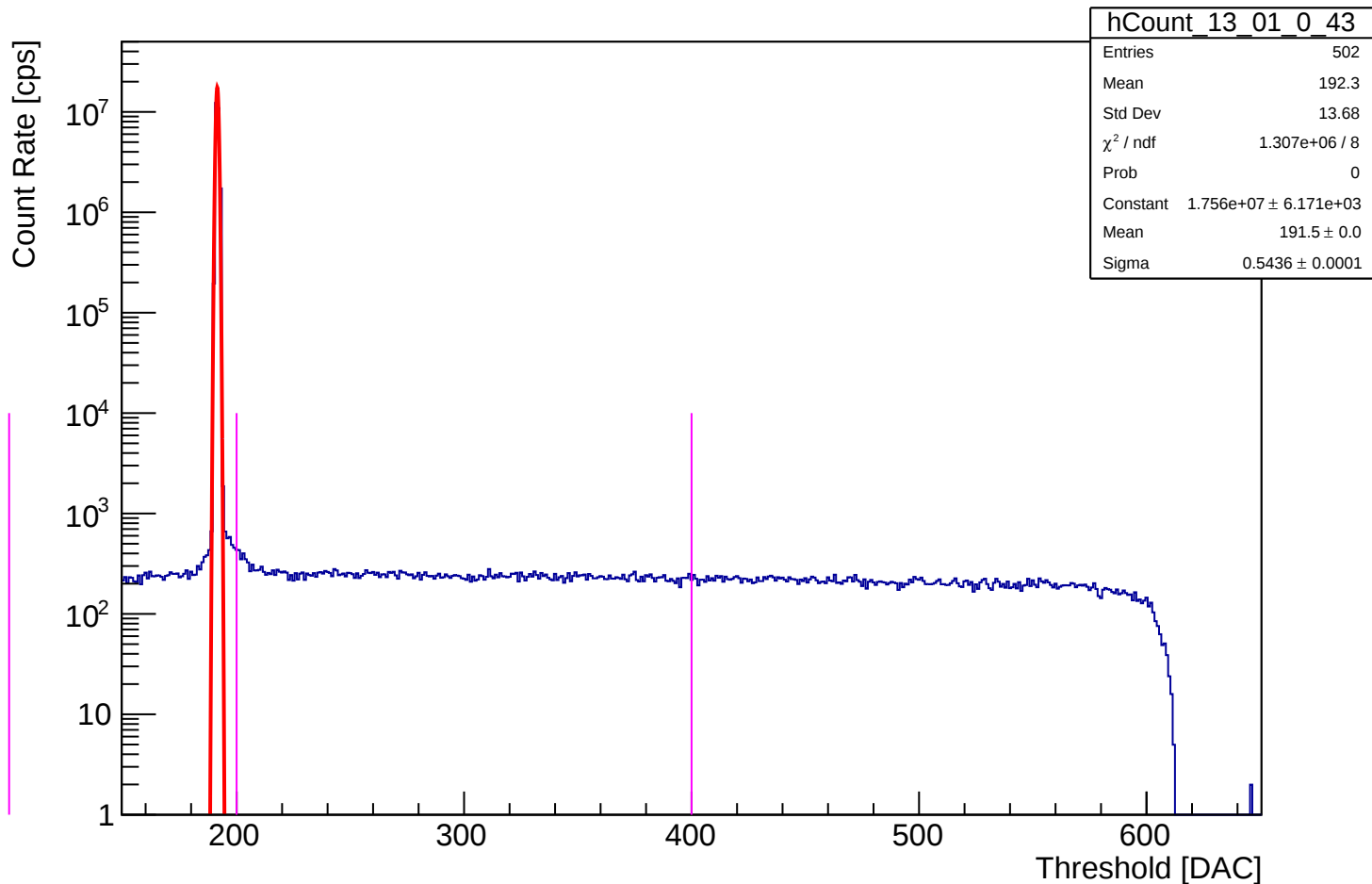
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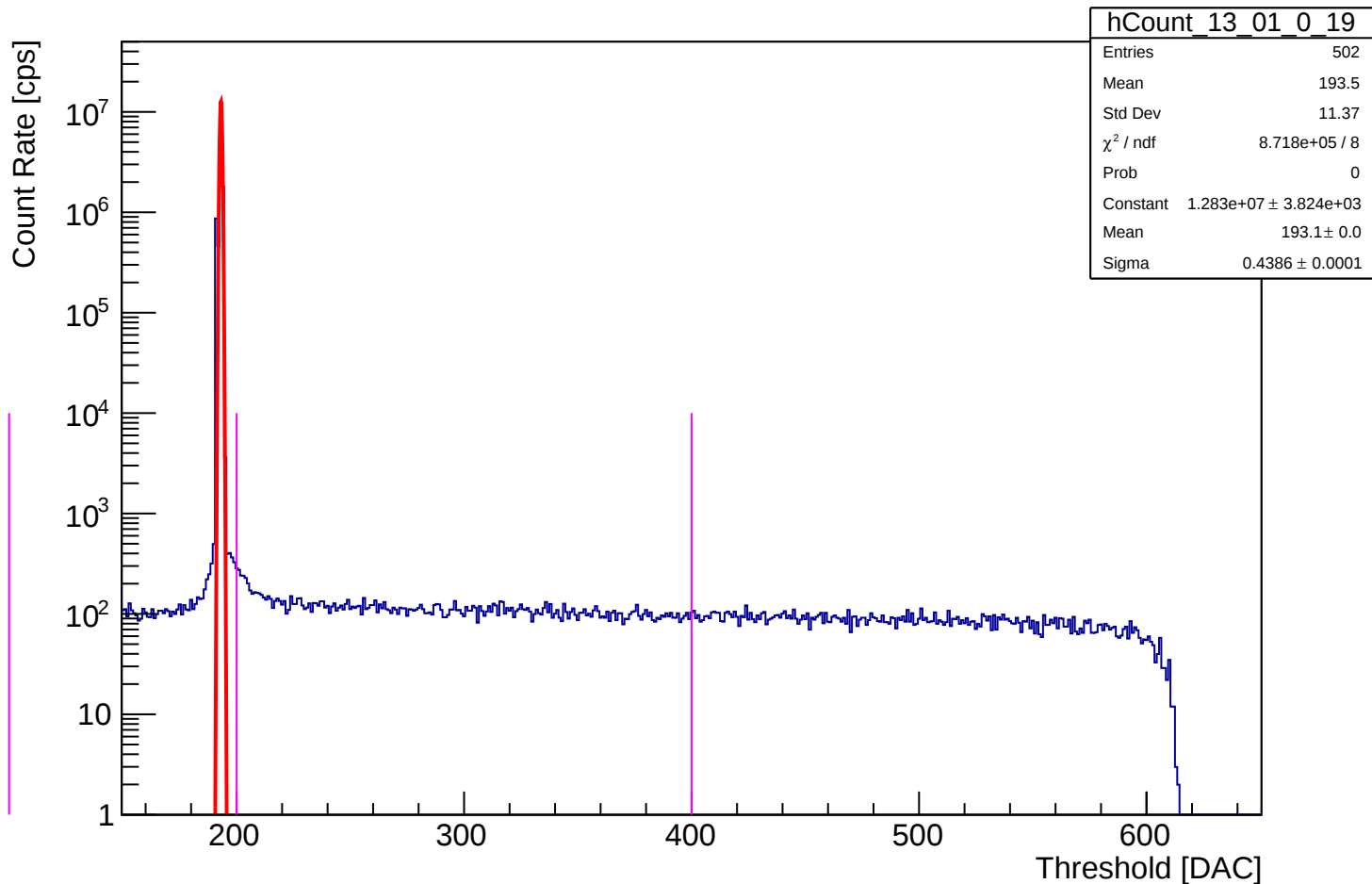
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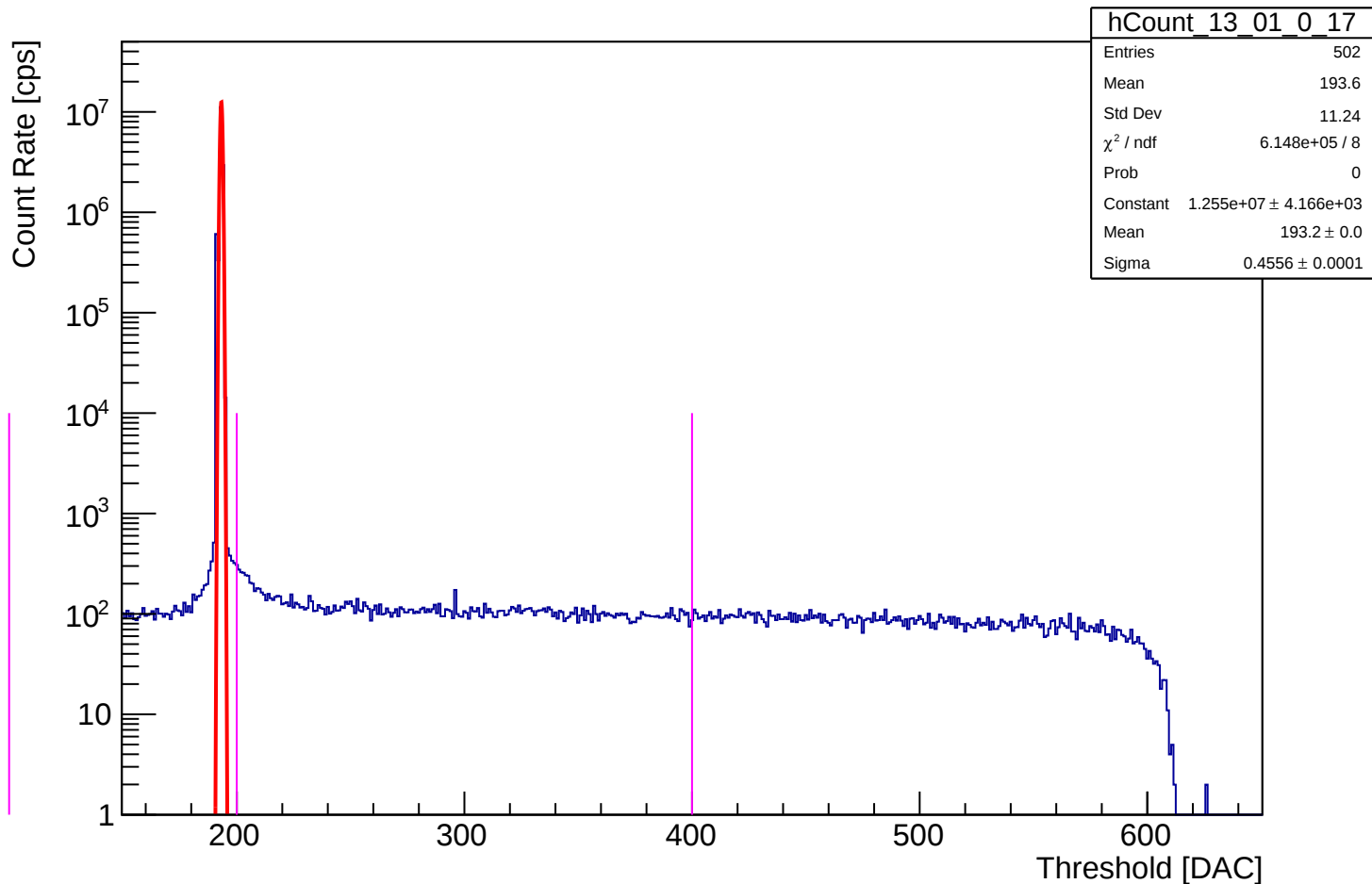
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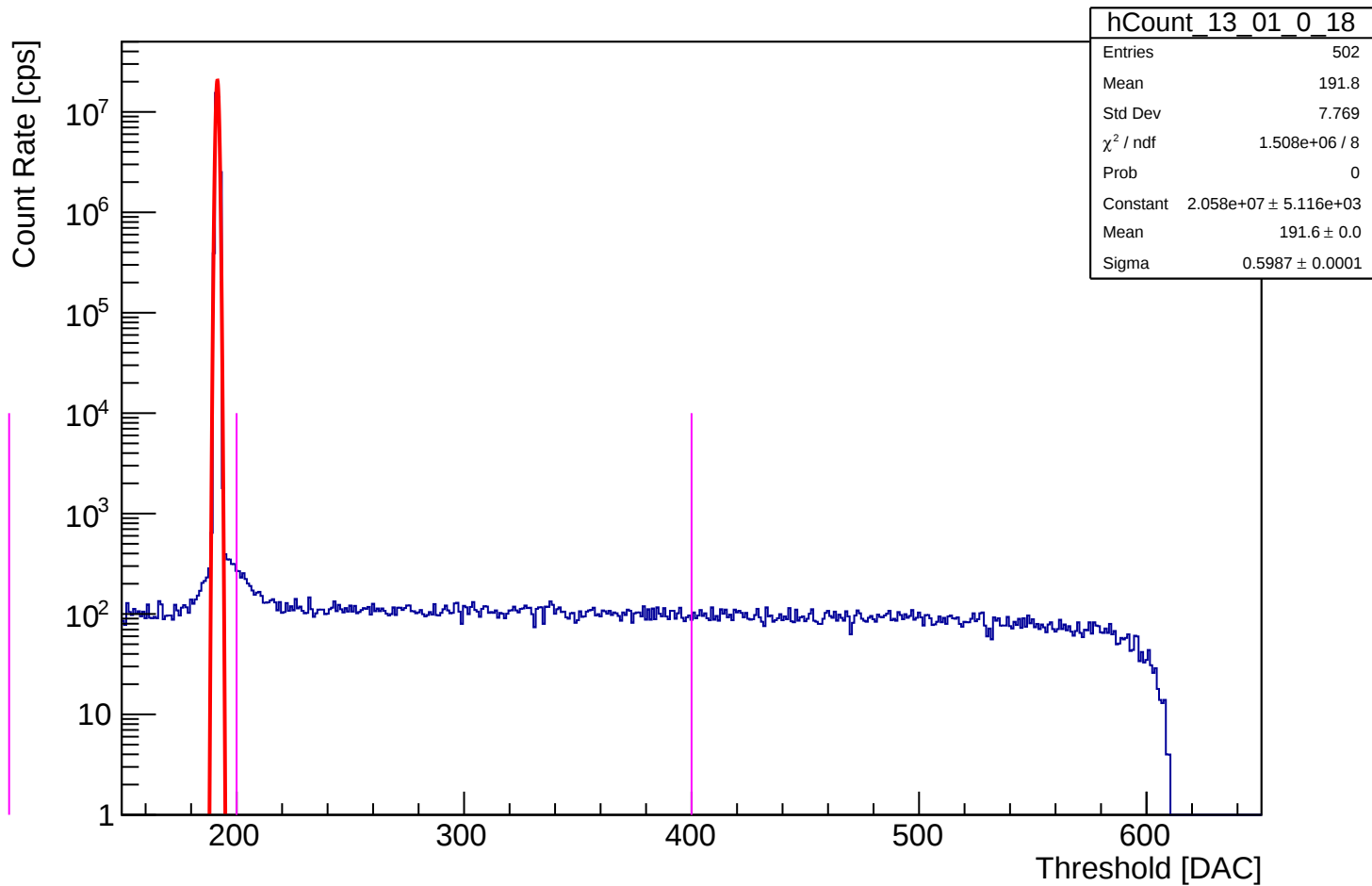
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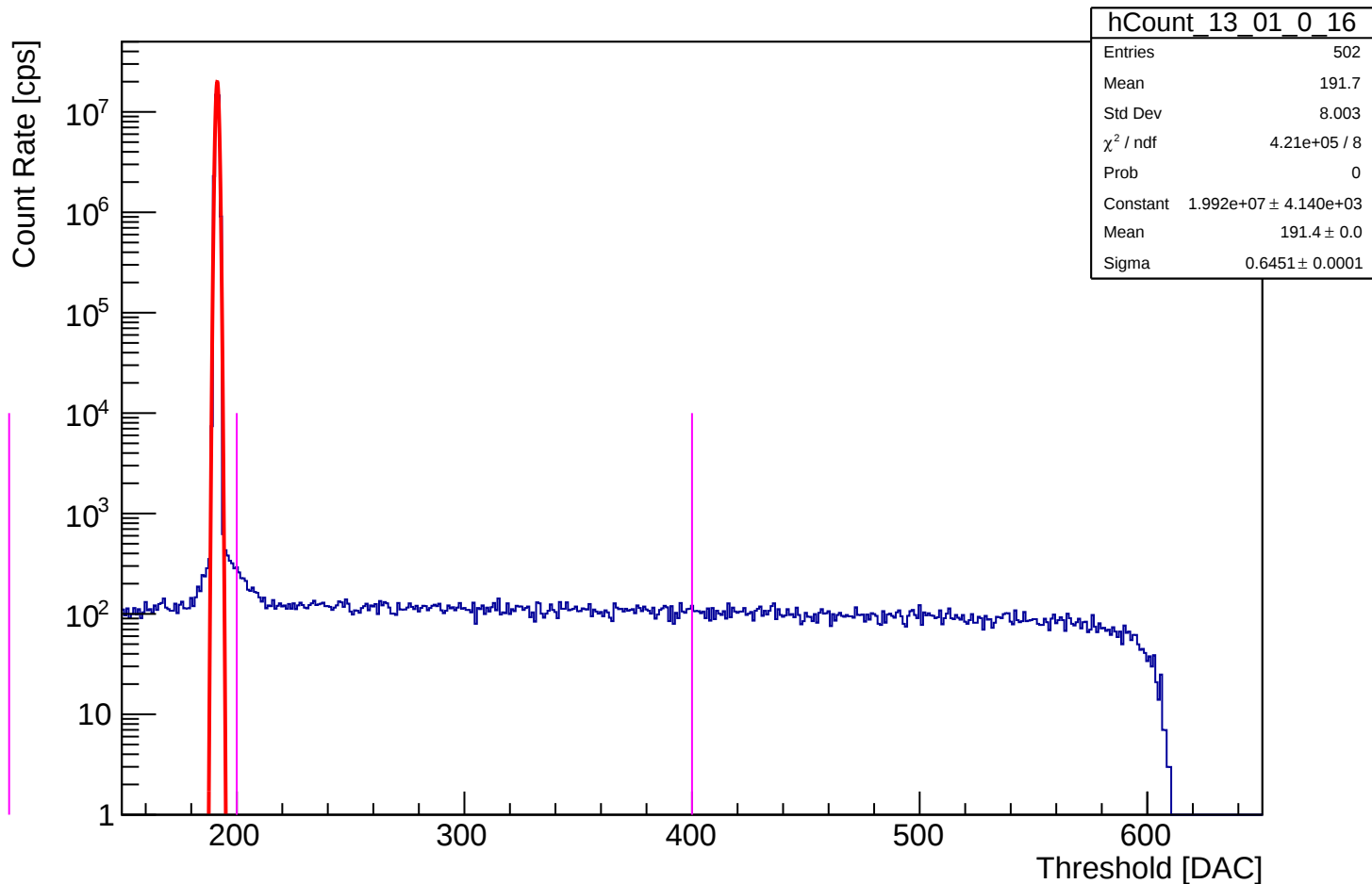
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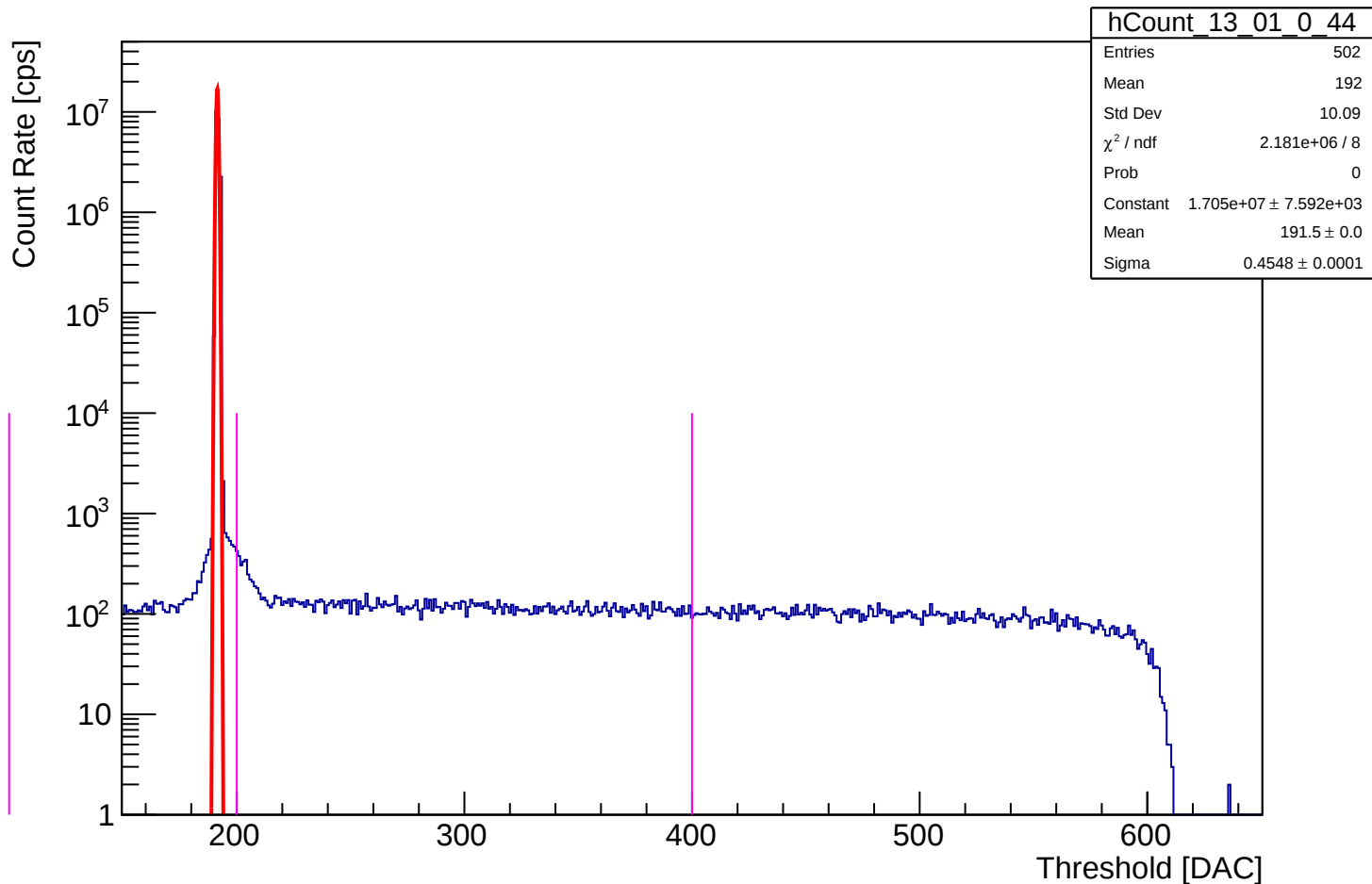
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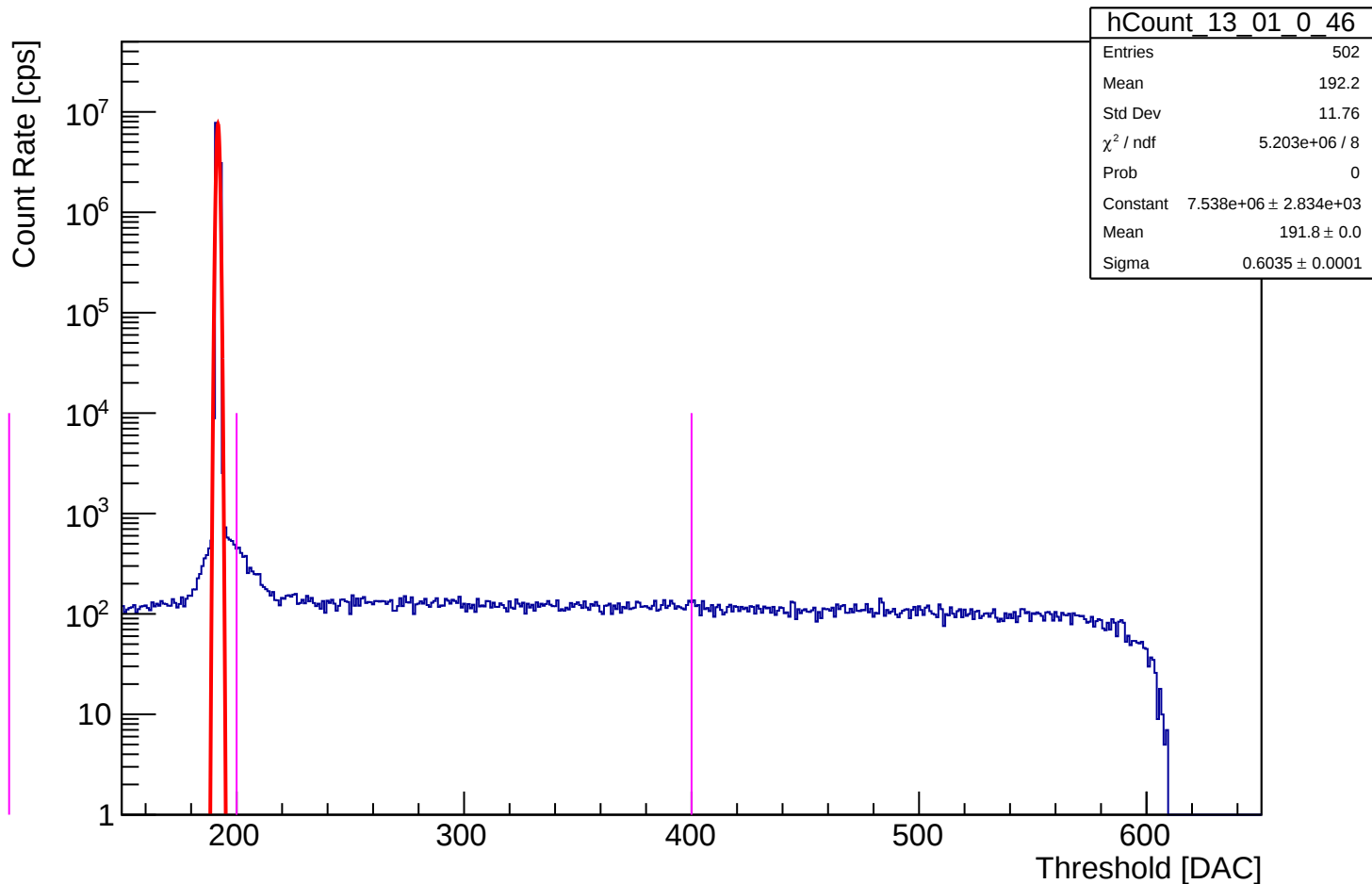
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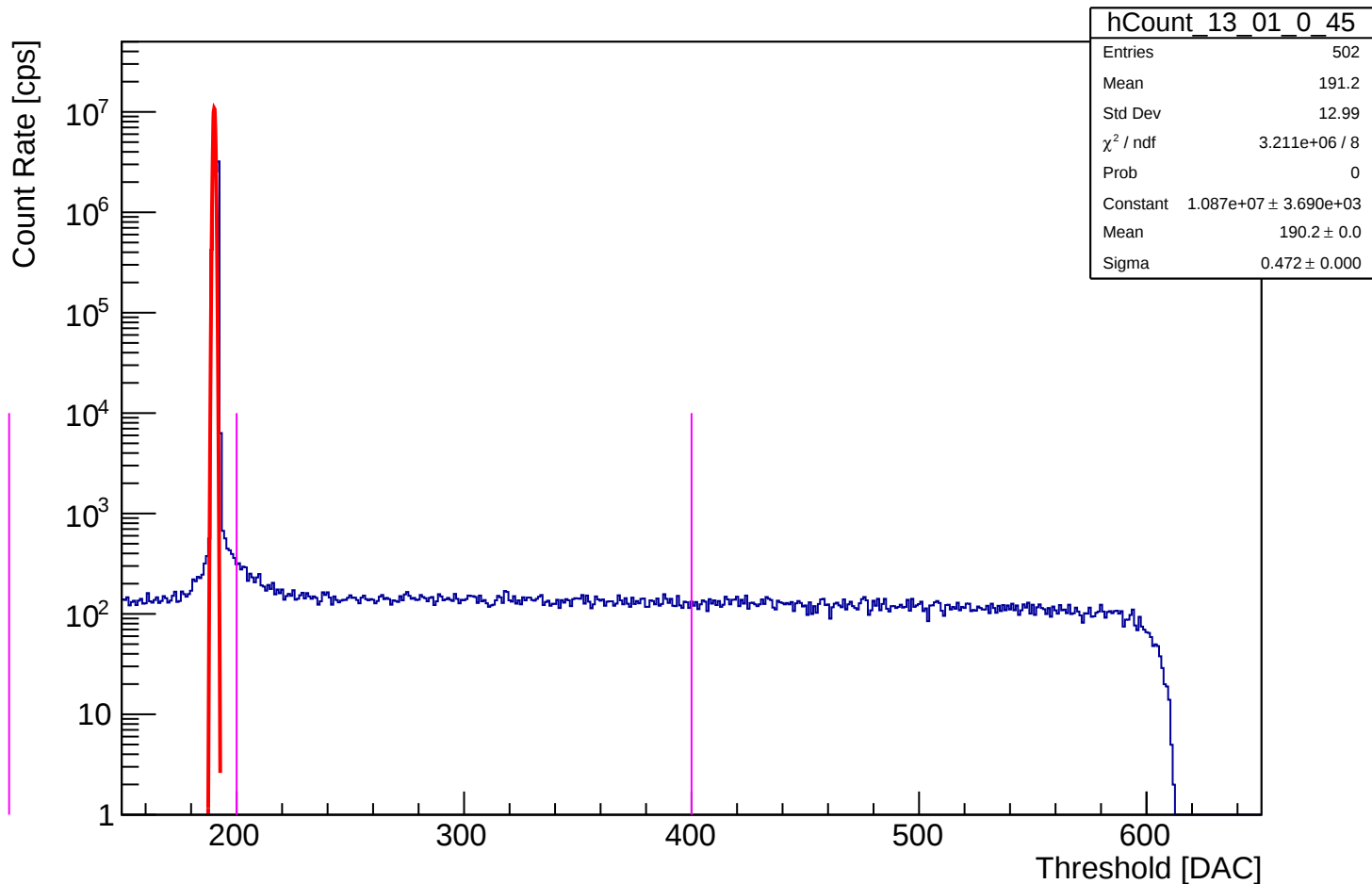
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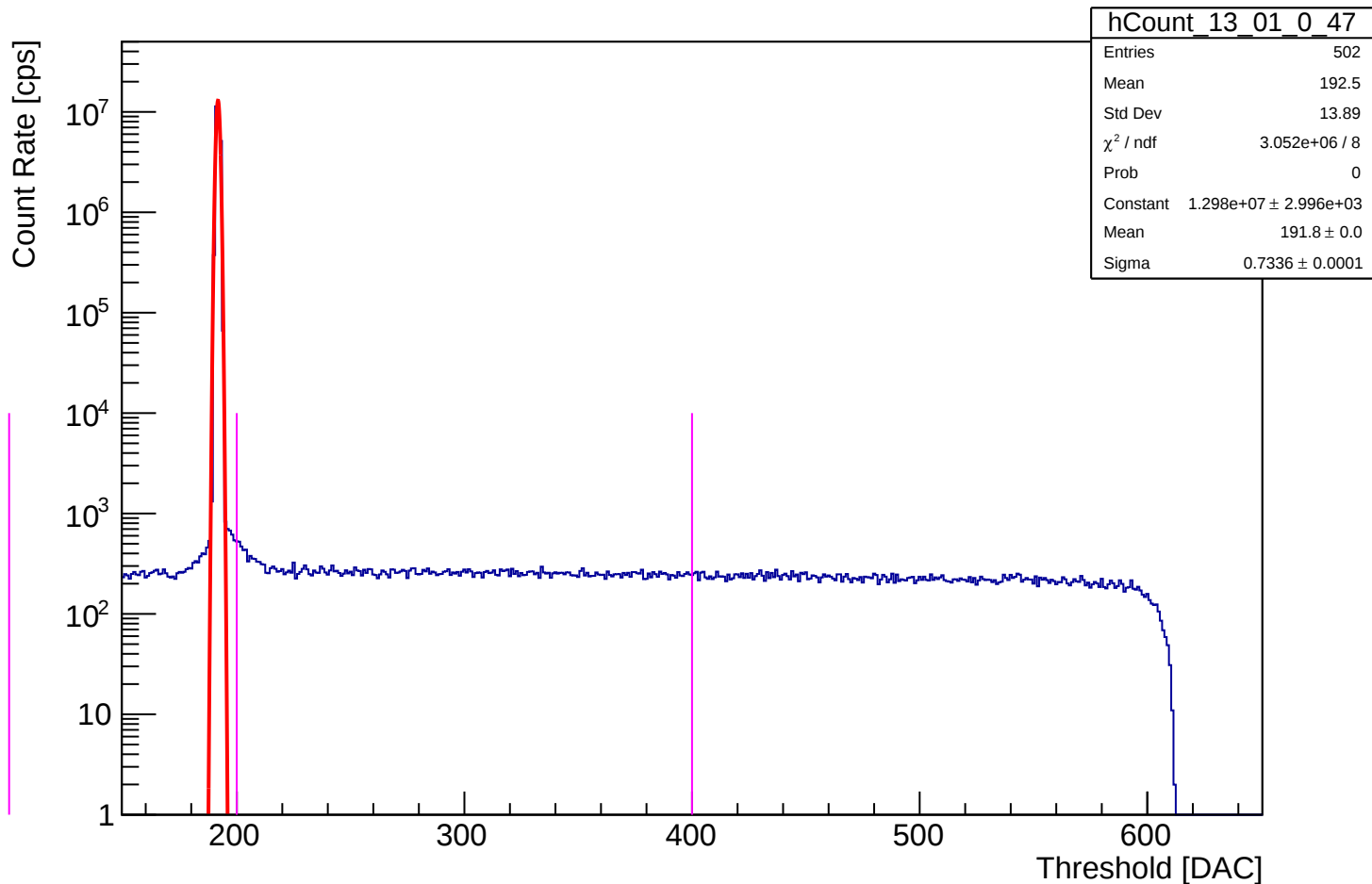
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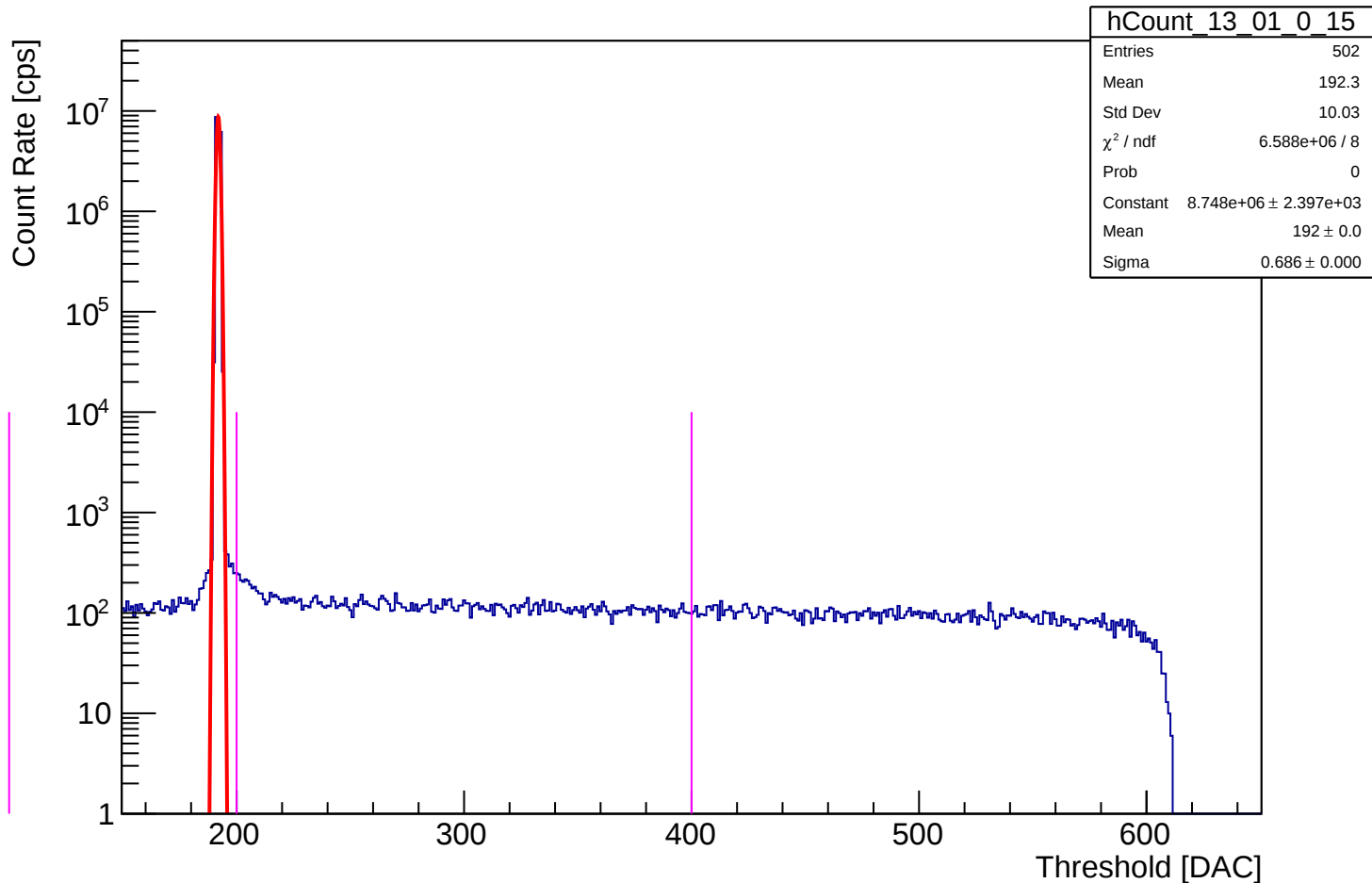
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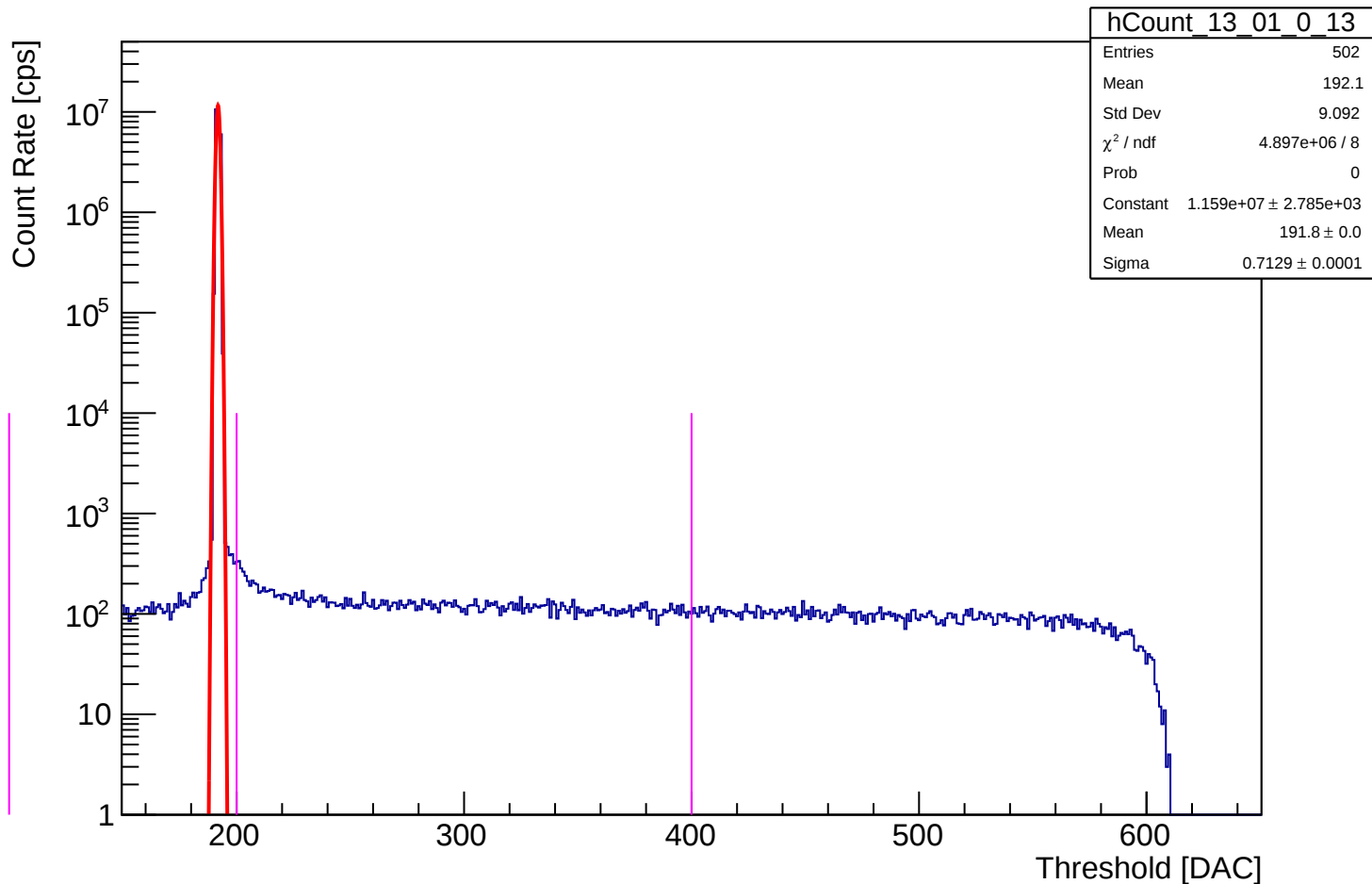
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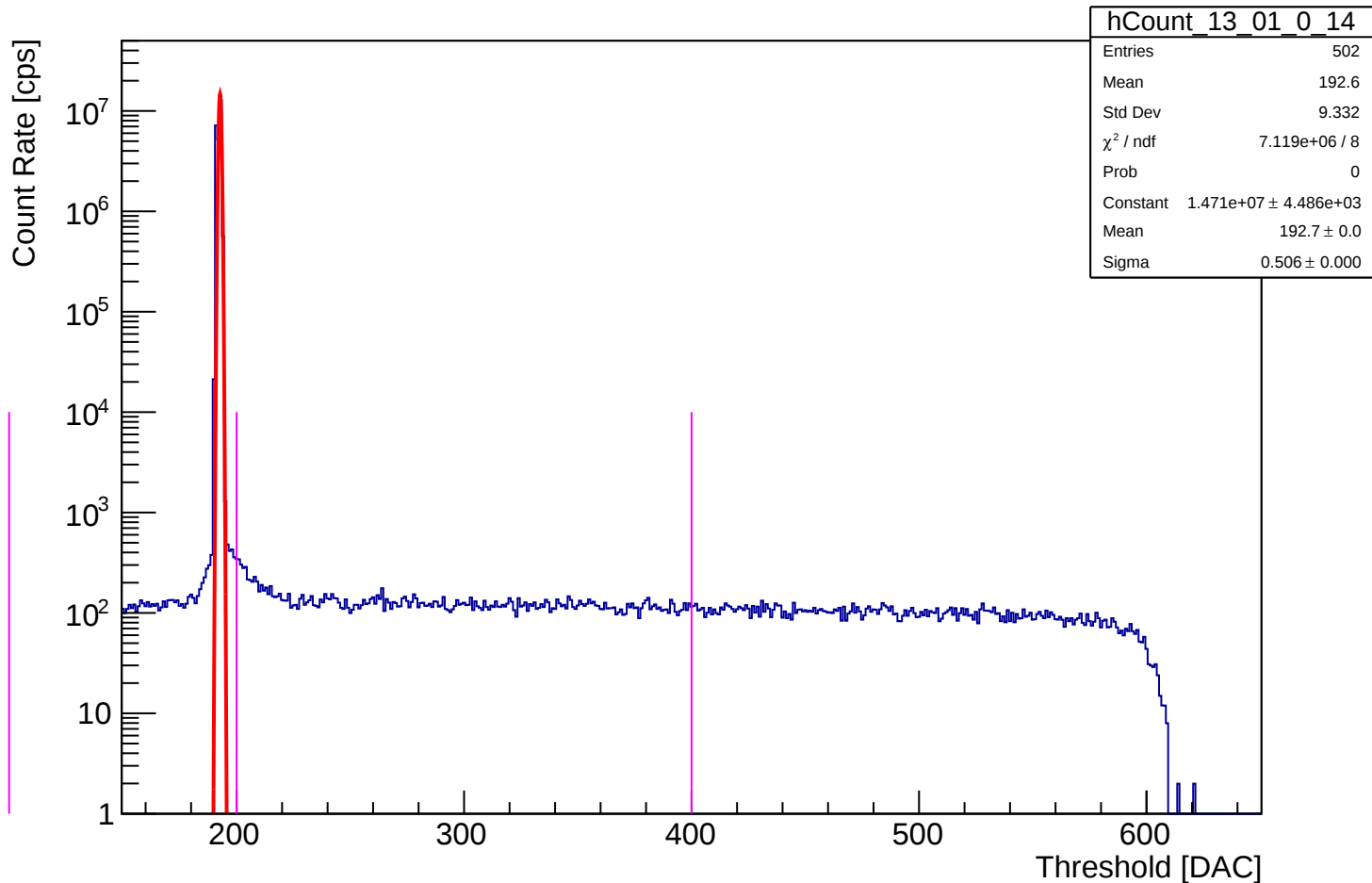
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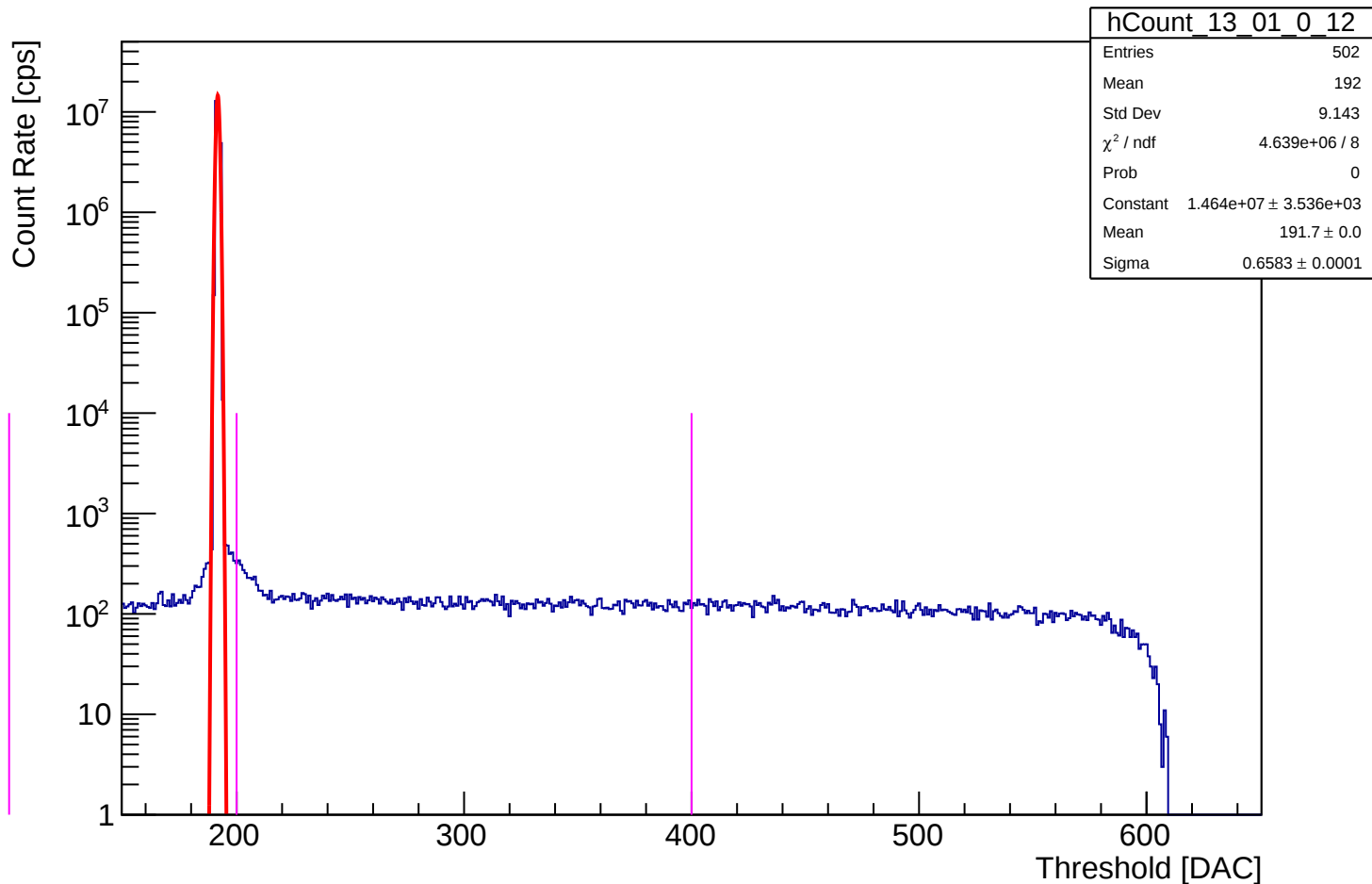
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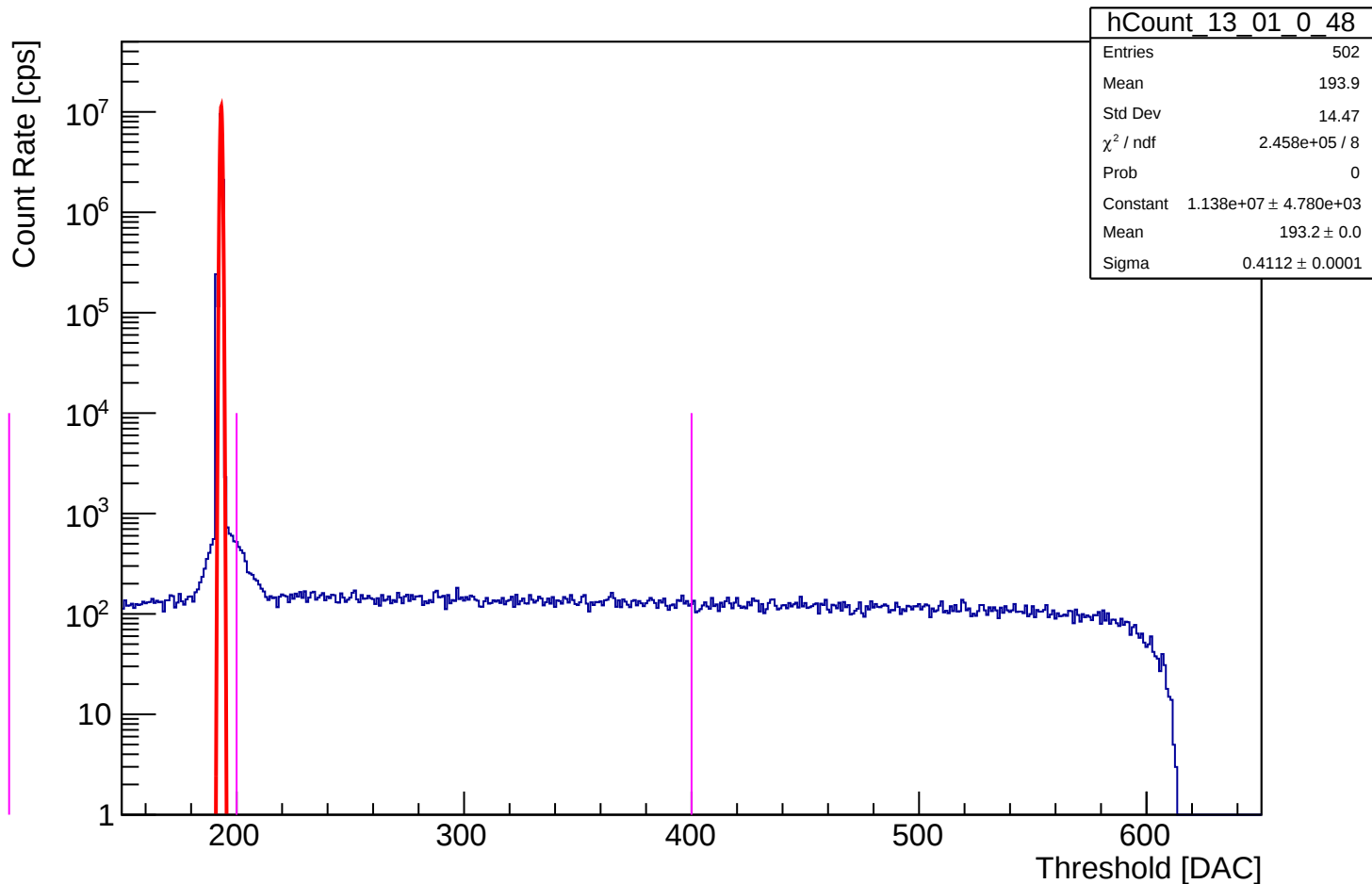
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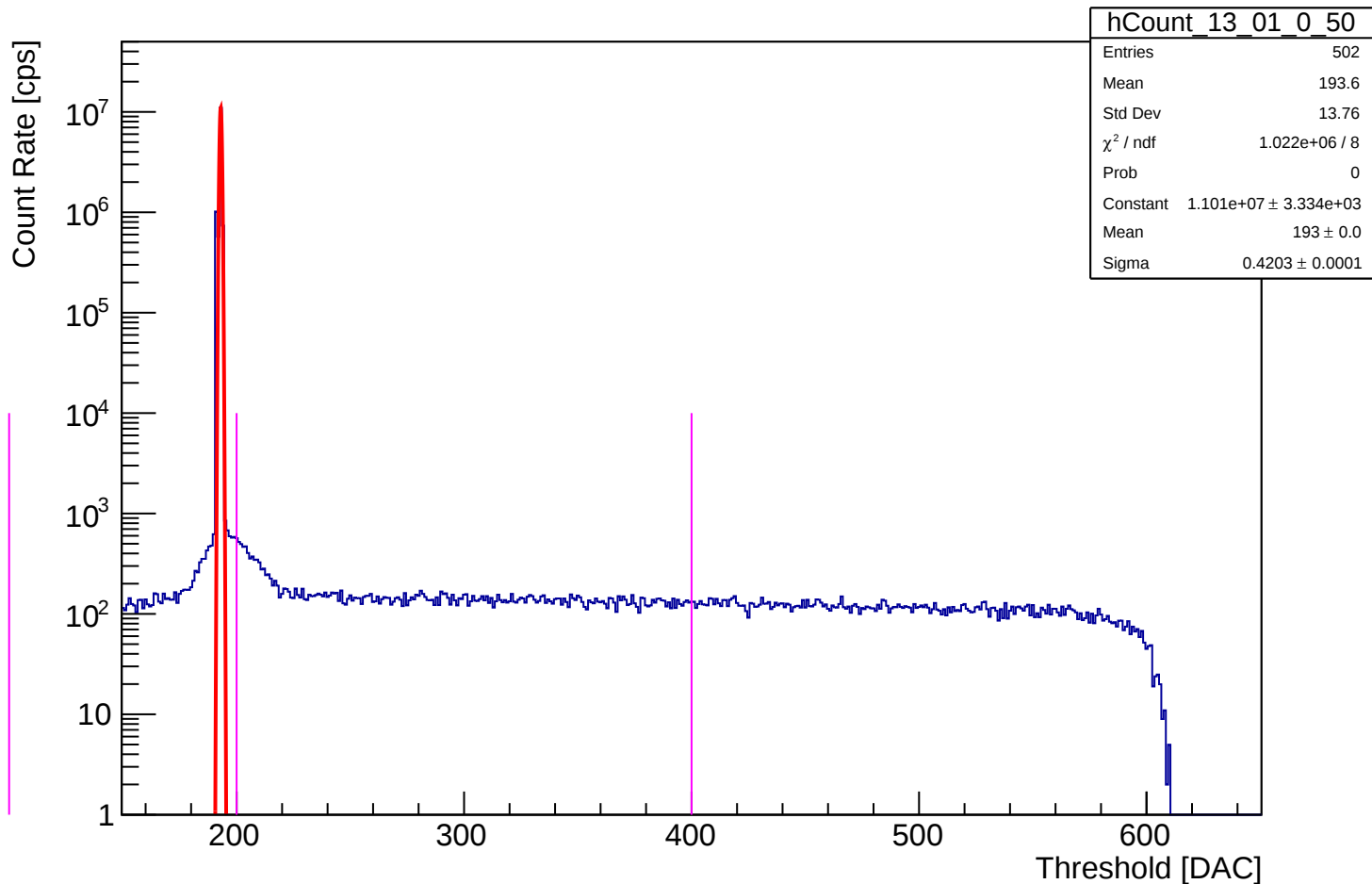
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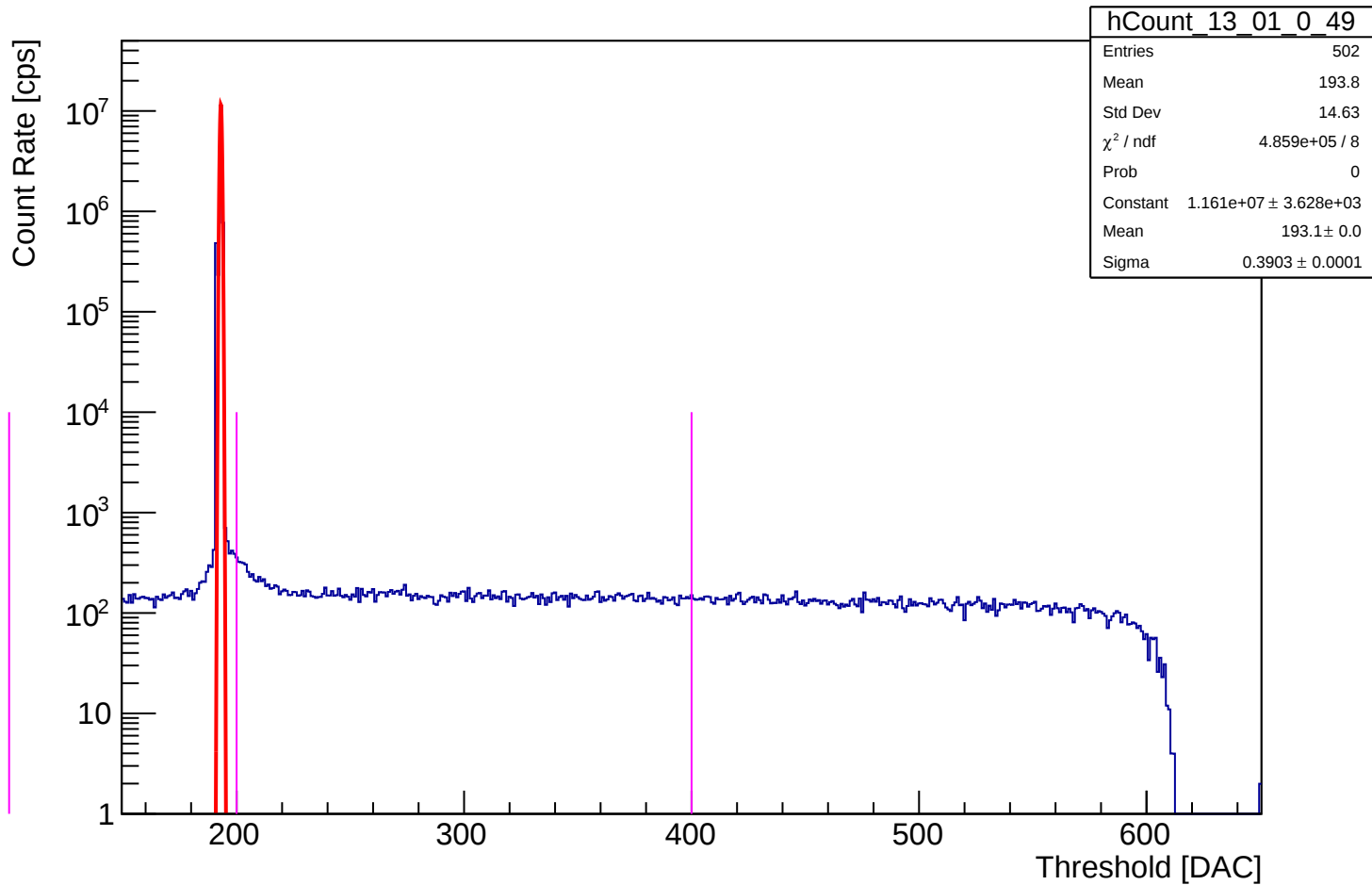
Row 1 Tile 1 Pmt 4 Pixel 37 Slot 13 Fiber 1 Asic 0 Channel 48



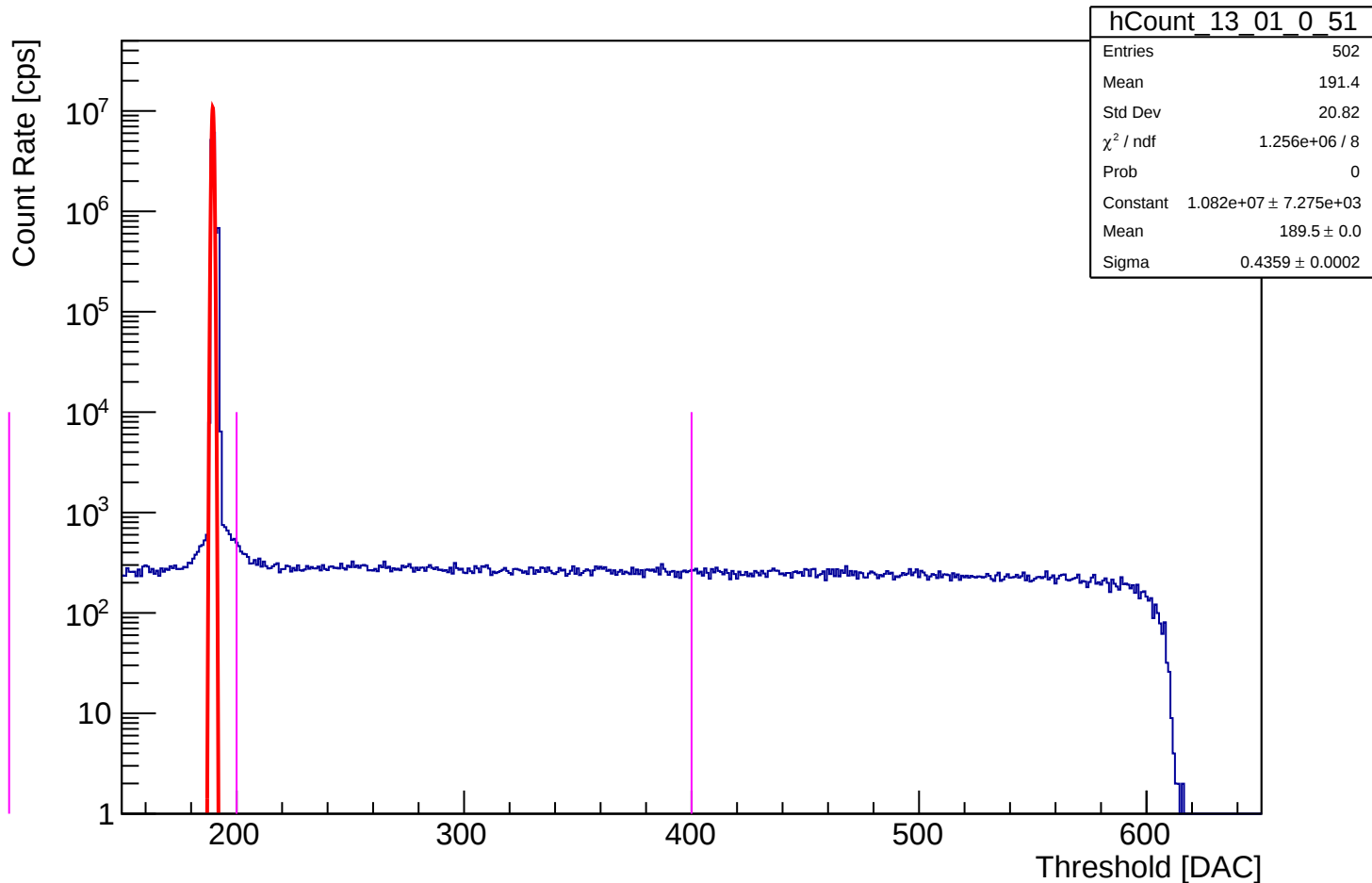
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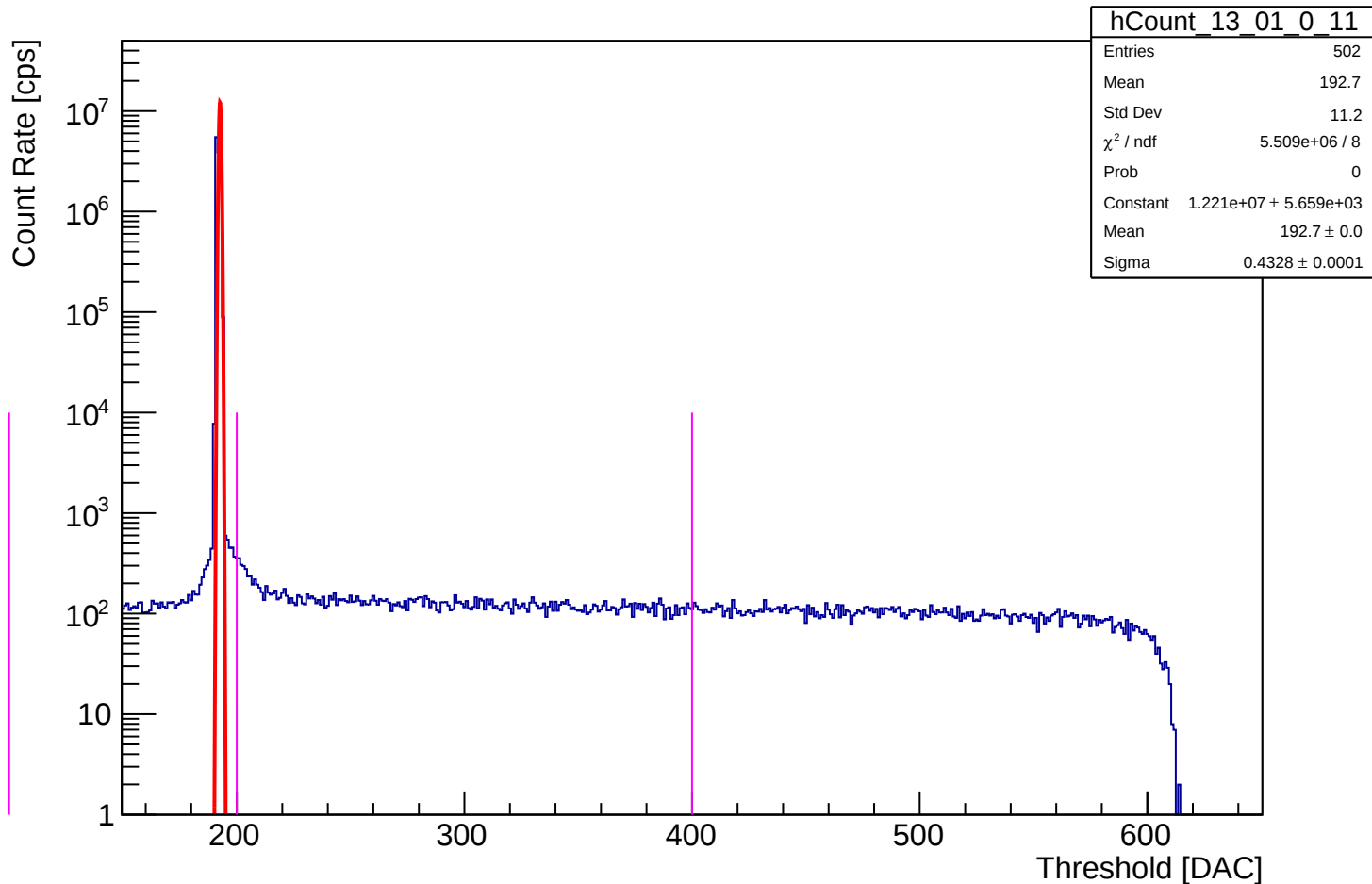
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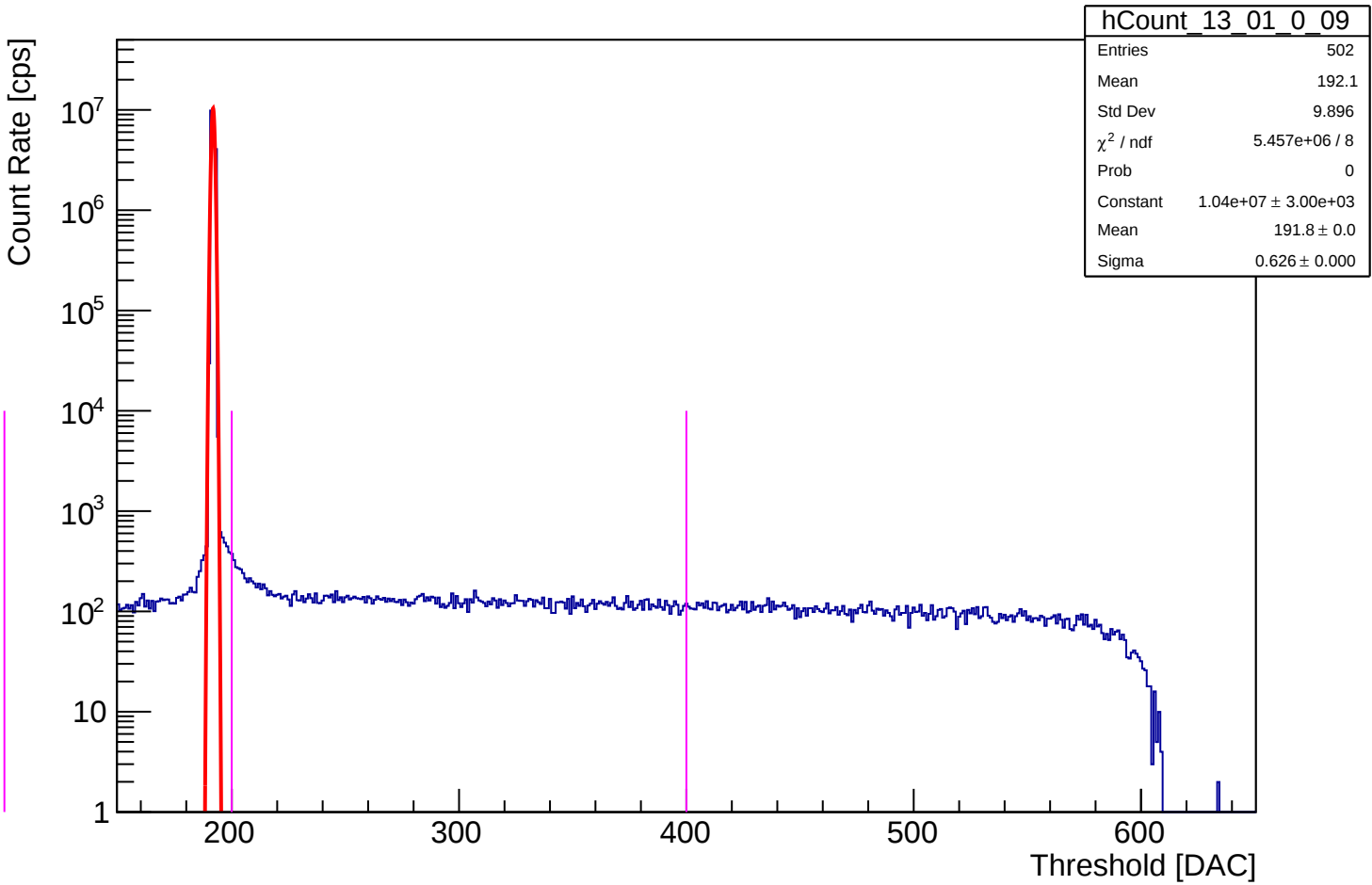


Row 1 Tile 1 Pmt 4 Pixel 40 Slot 13 Fiber 1 Asic 0 Channel 51

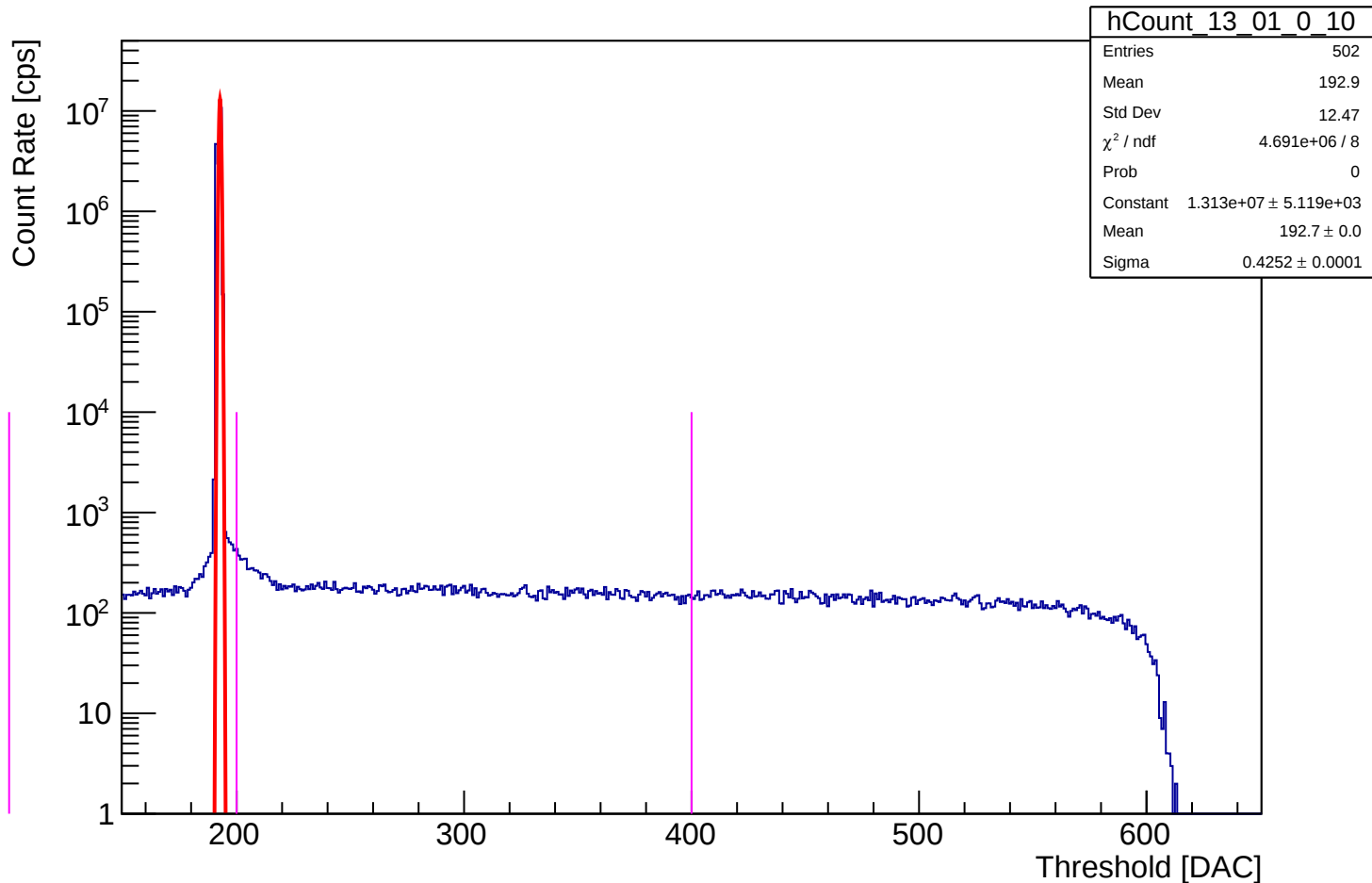


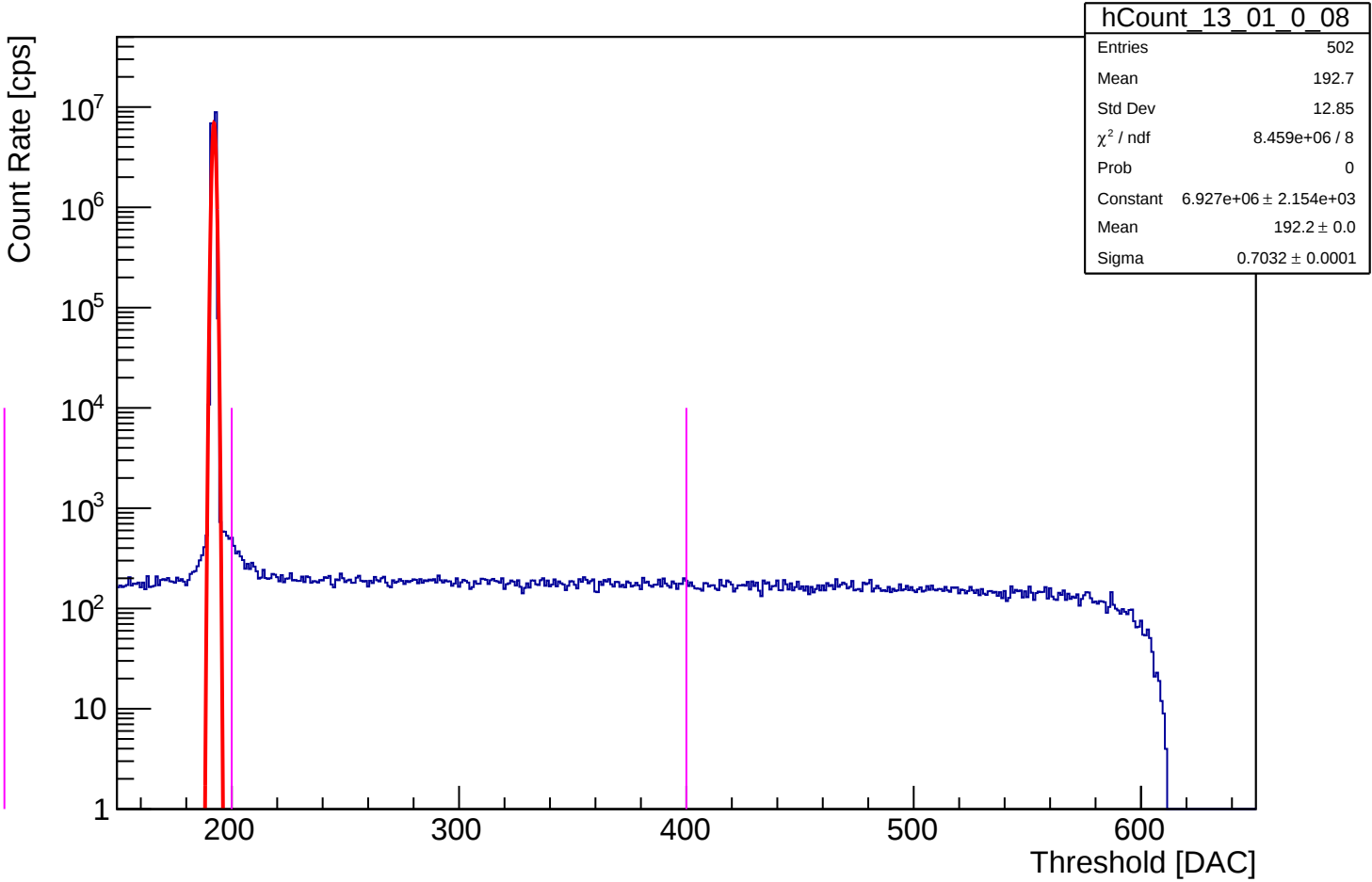
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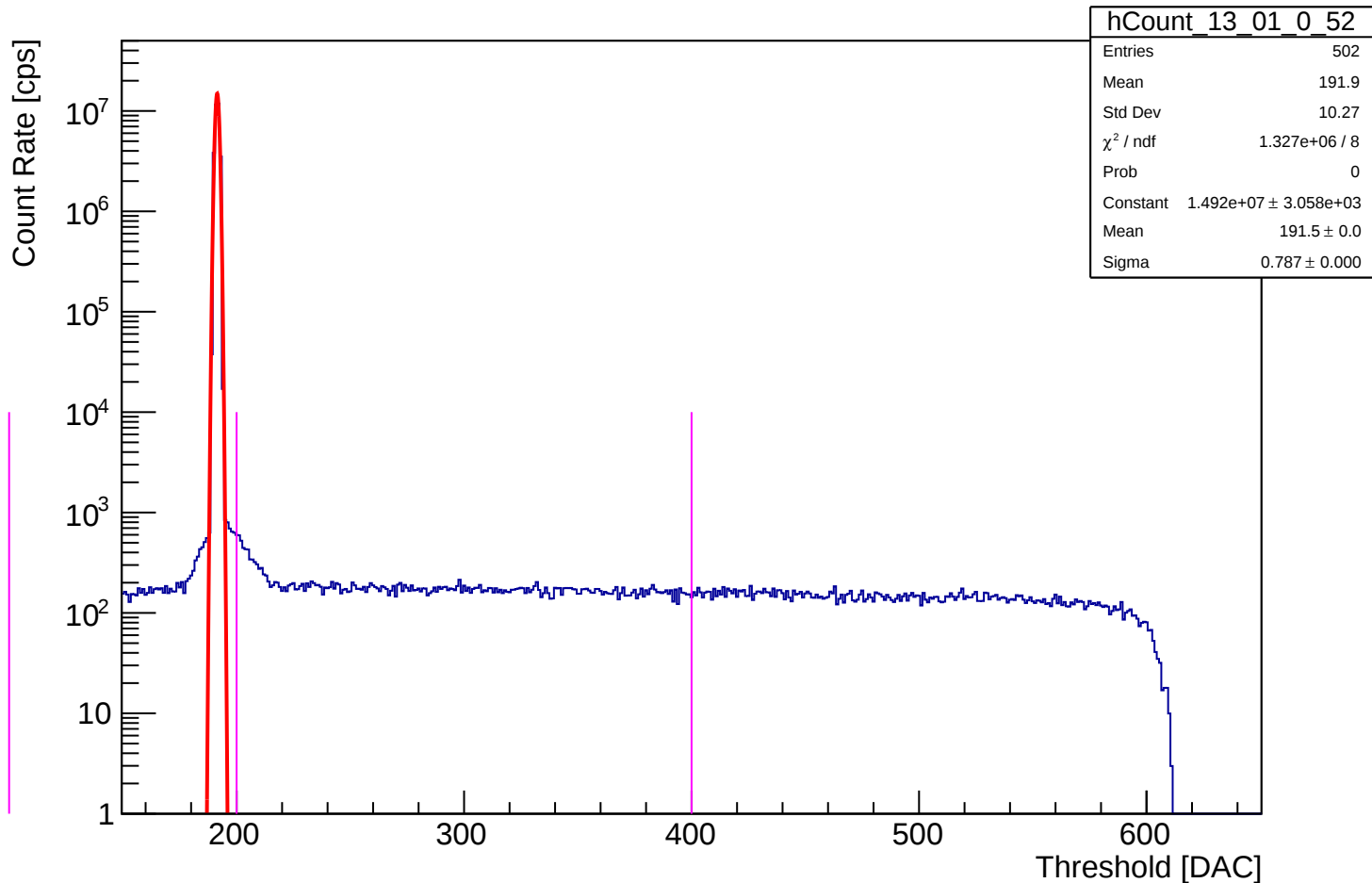


Row 1 Tile 1 Pmt 4 Pixel 43 Slot 13 Fiber 1 Asic 0 Channel 10

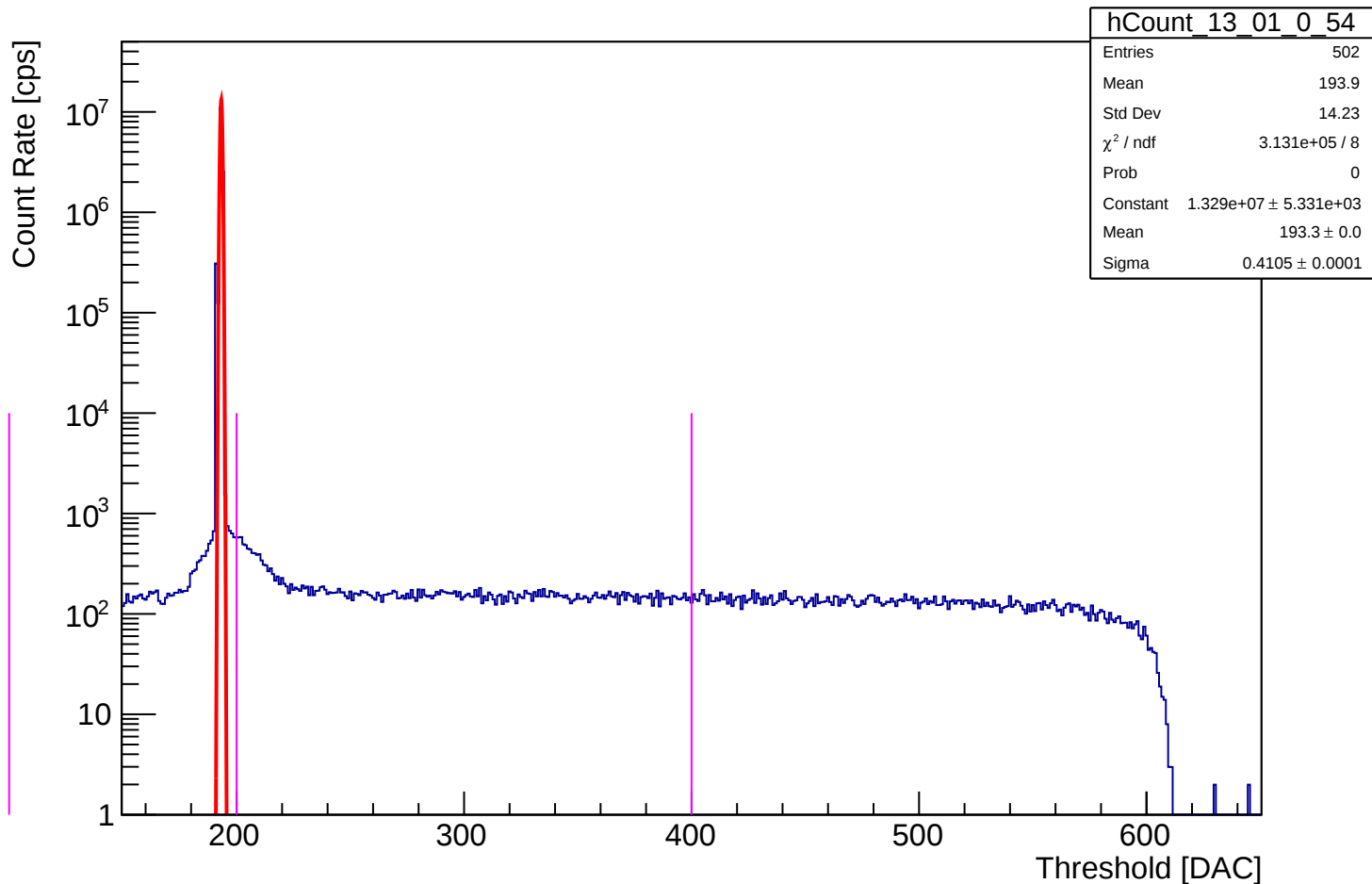




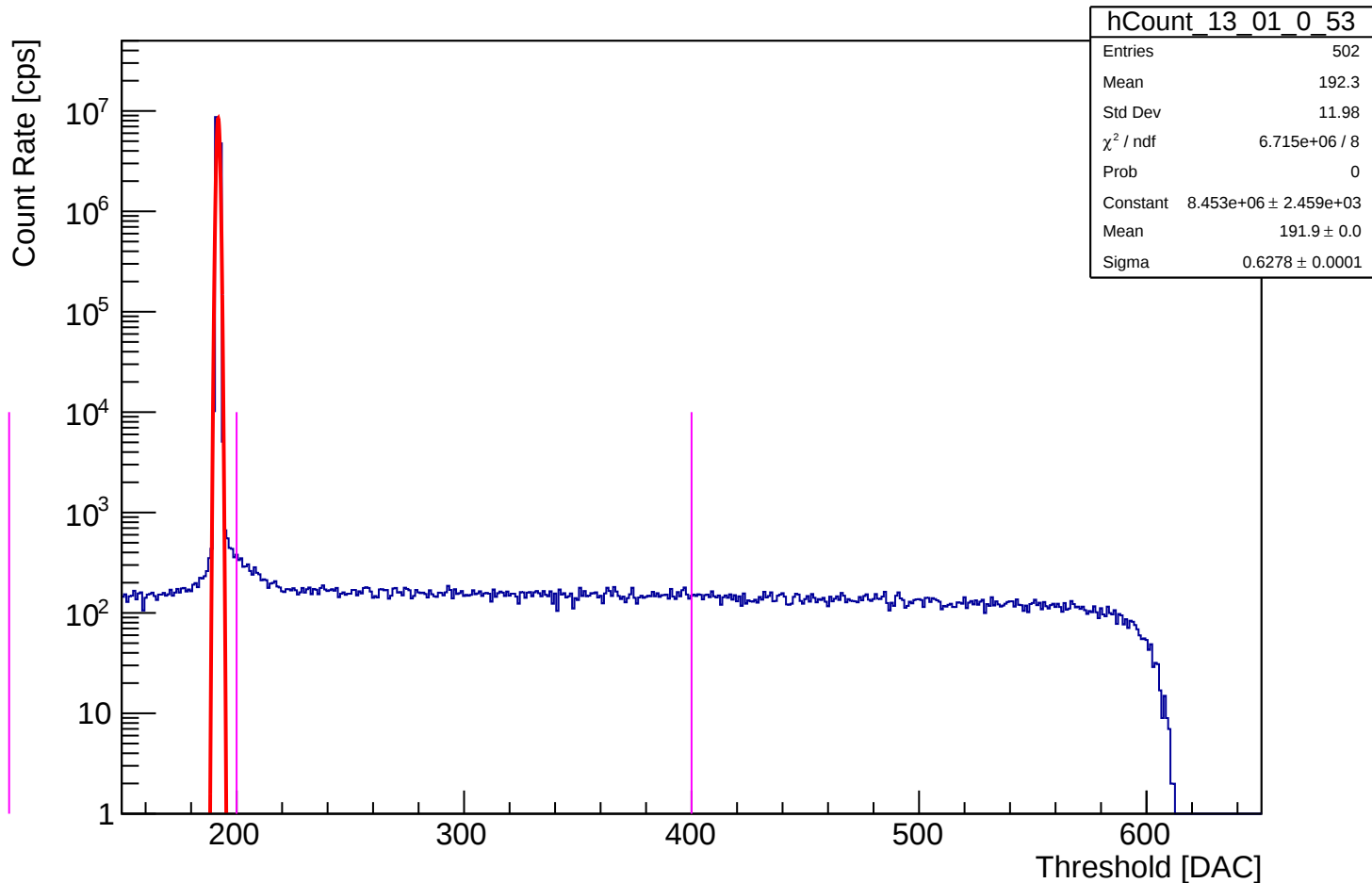
Row 1 Tile 1 Pmt 4 Pixel 45 Slot 13 Fiber 1 Asic 0 Channel 52



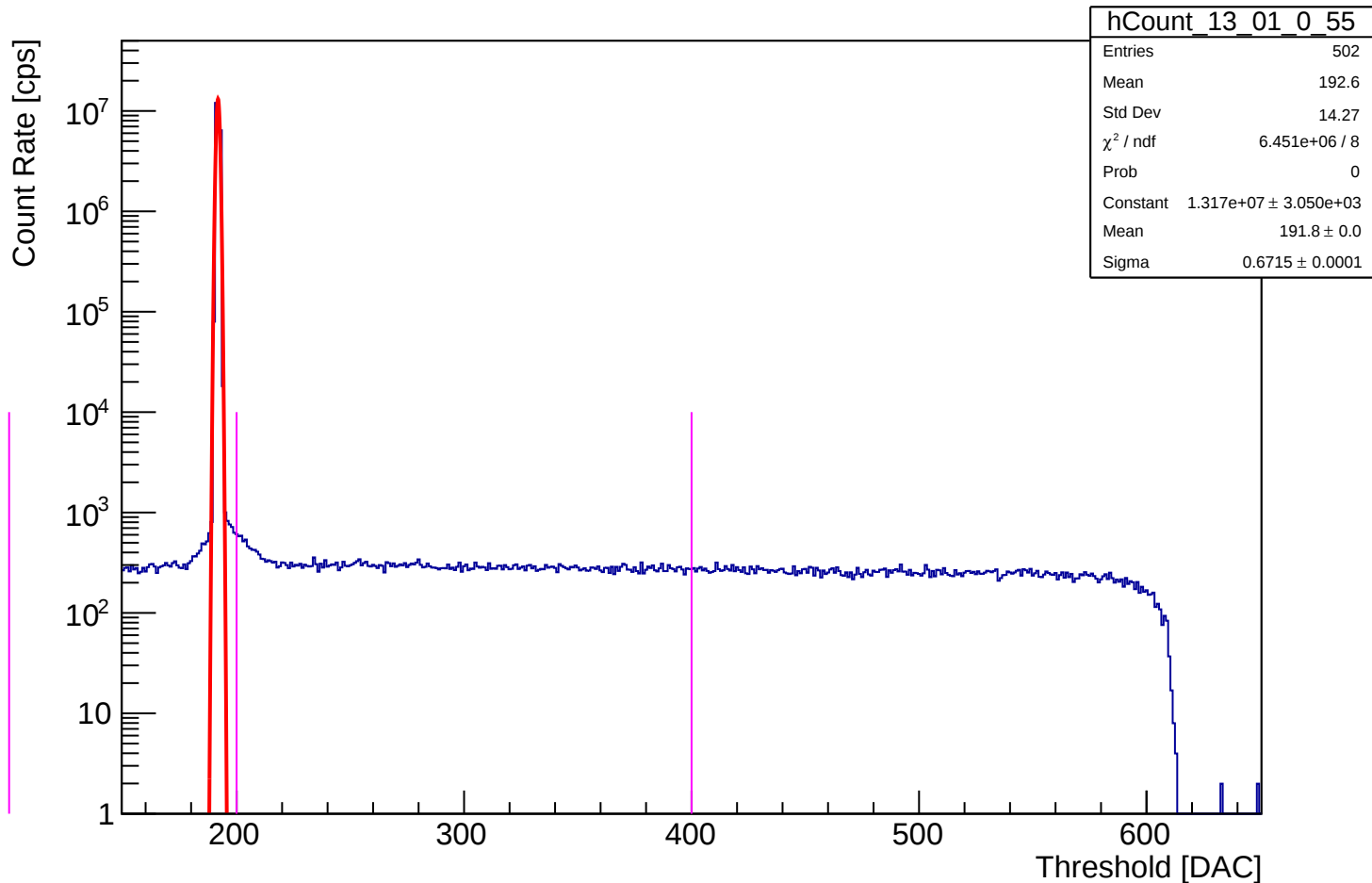
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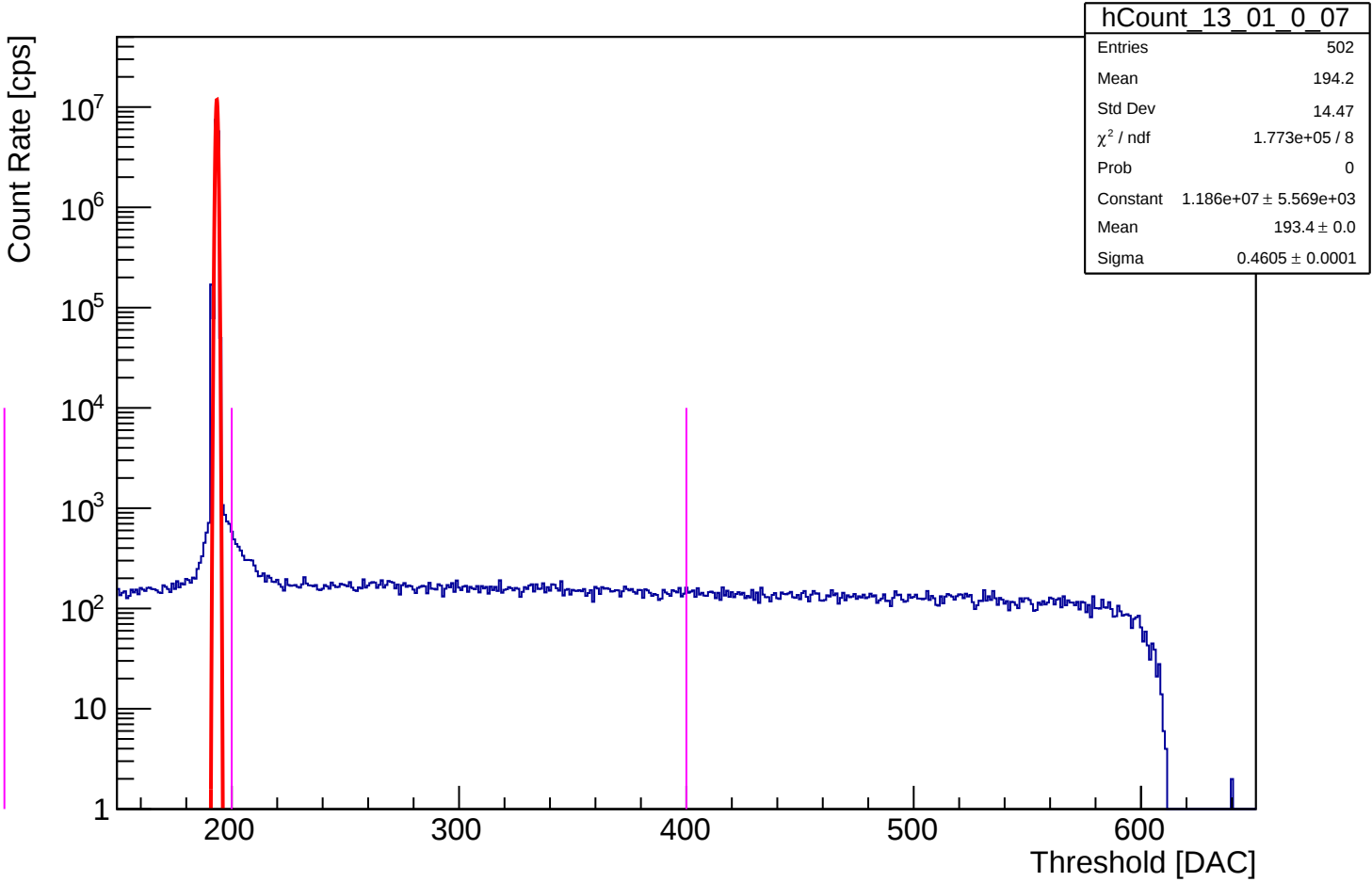


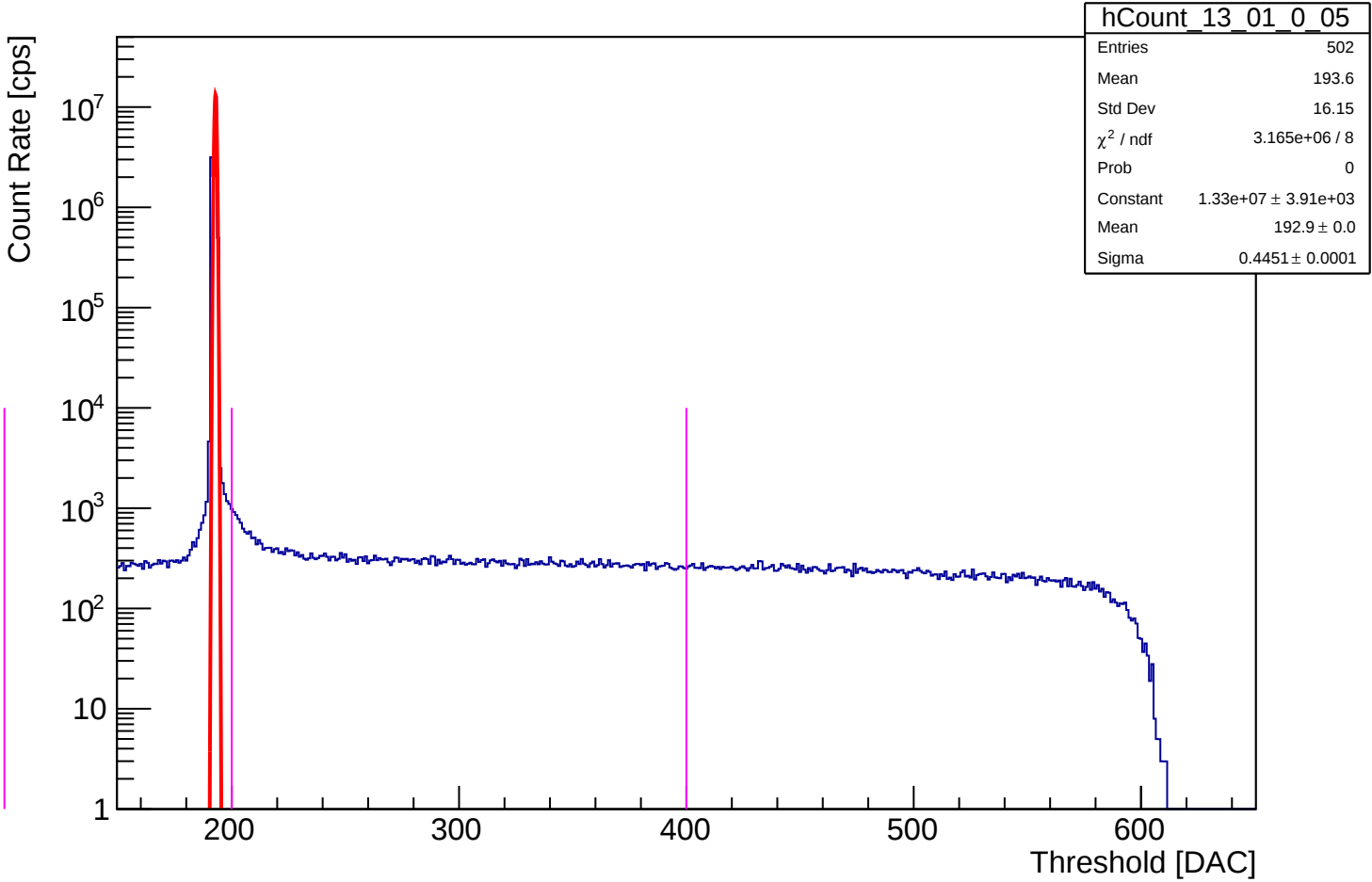
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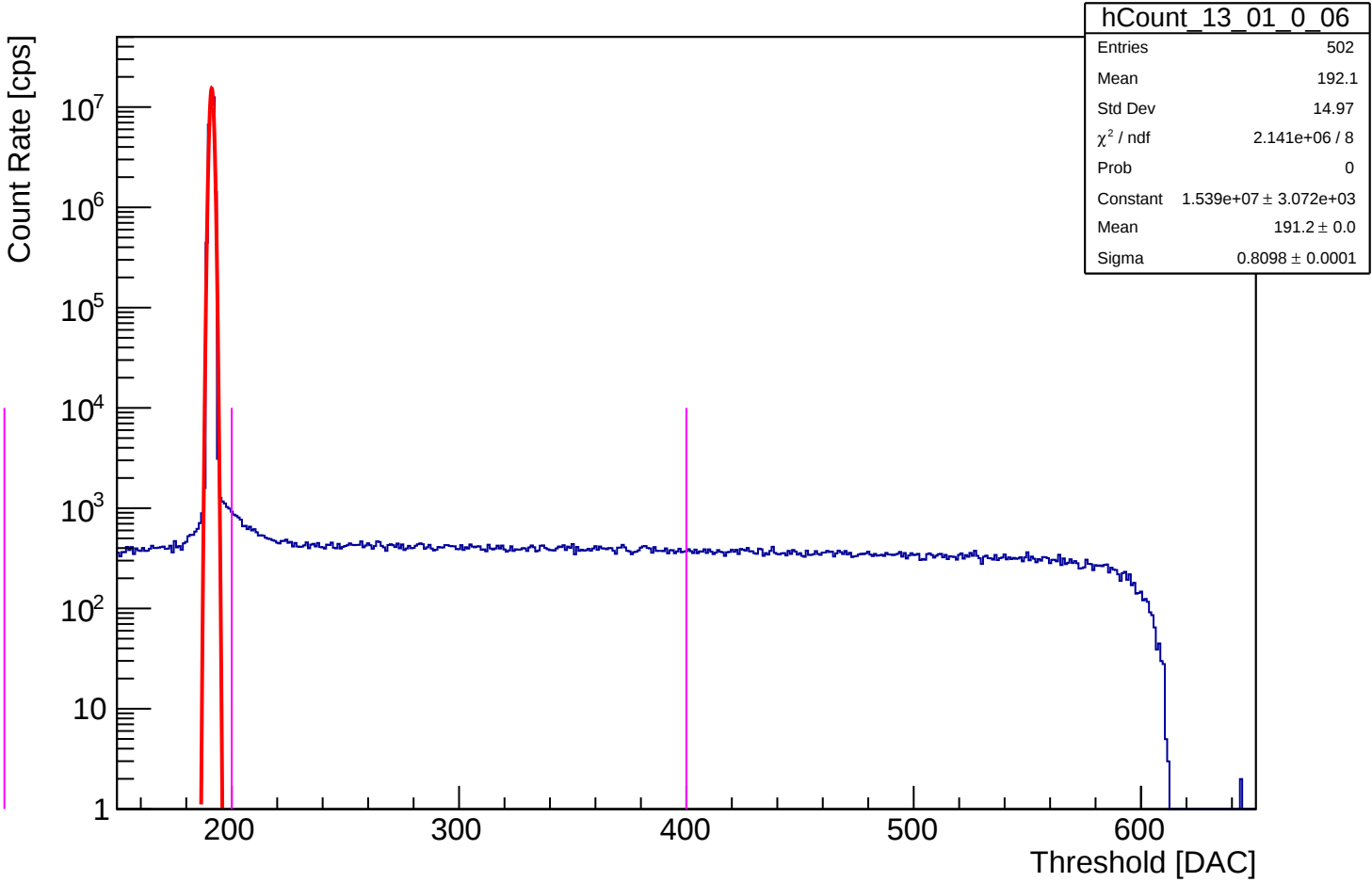


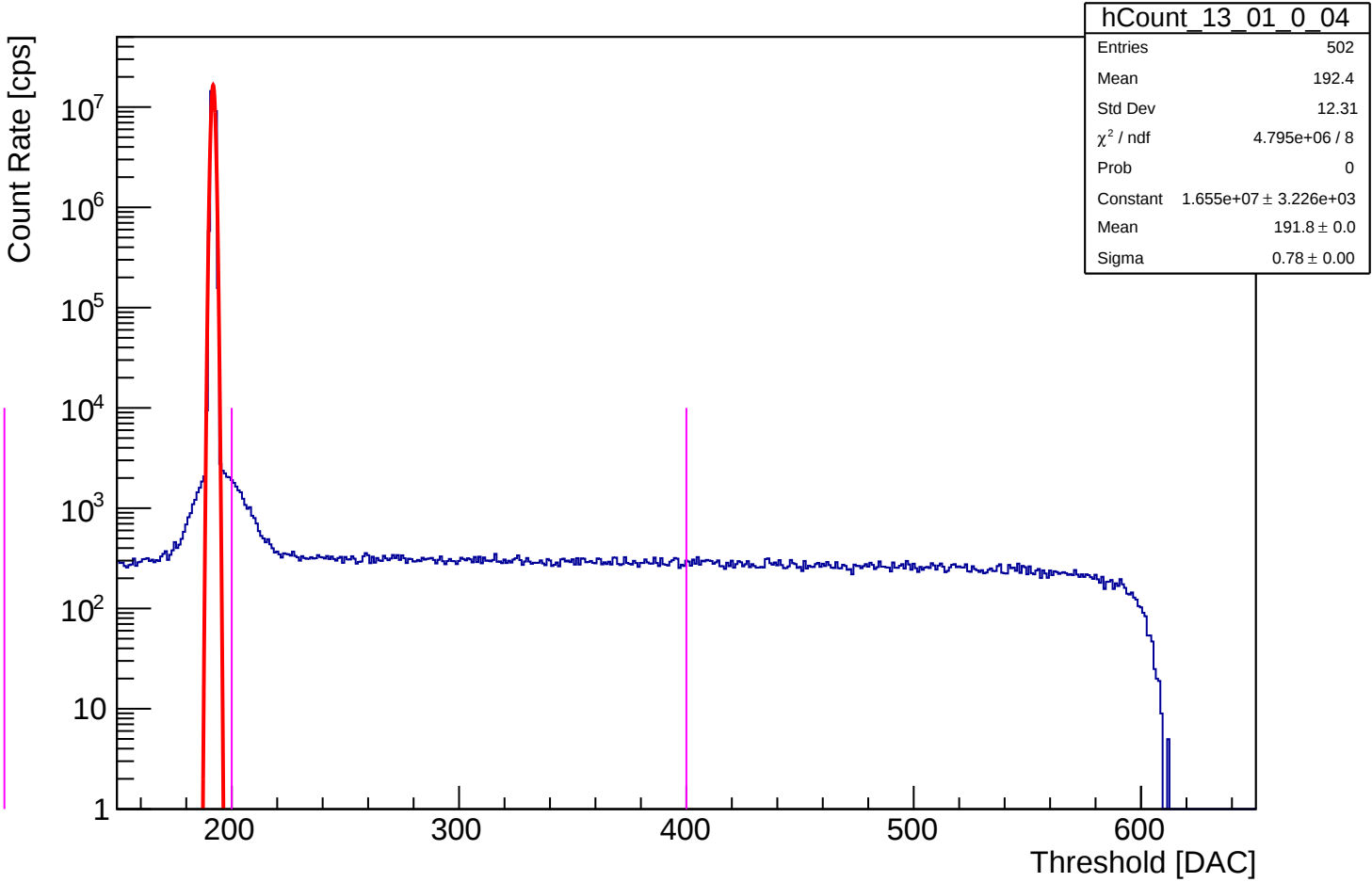
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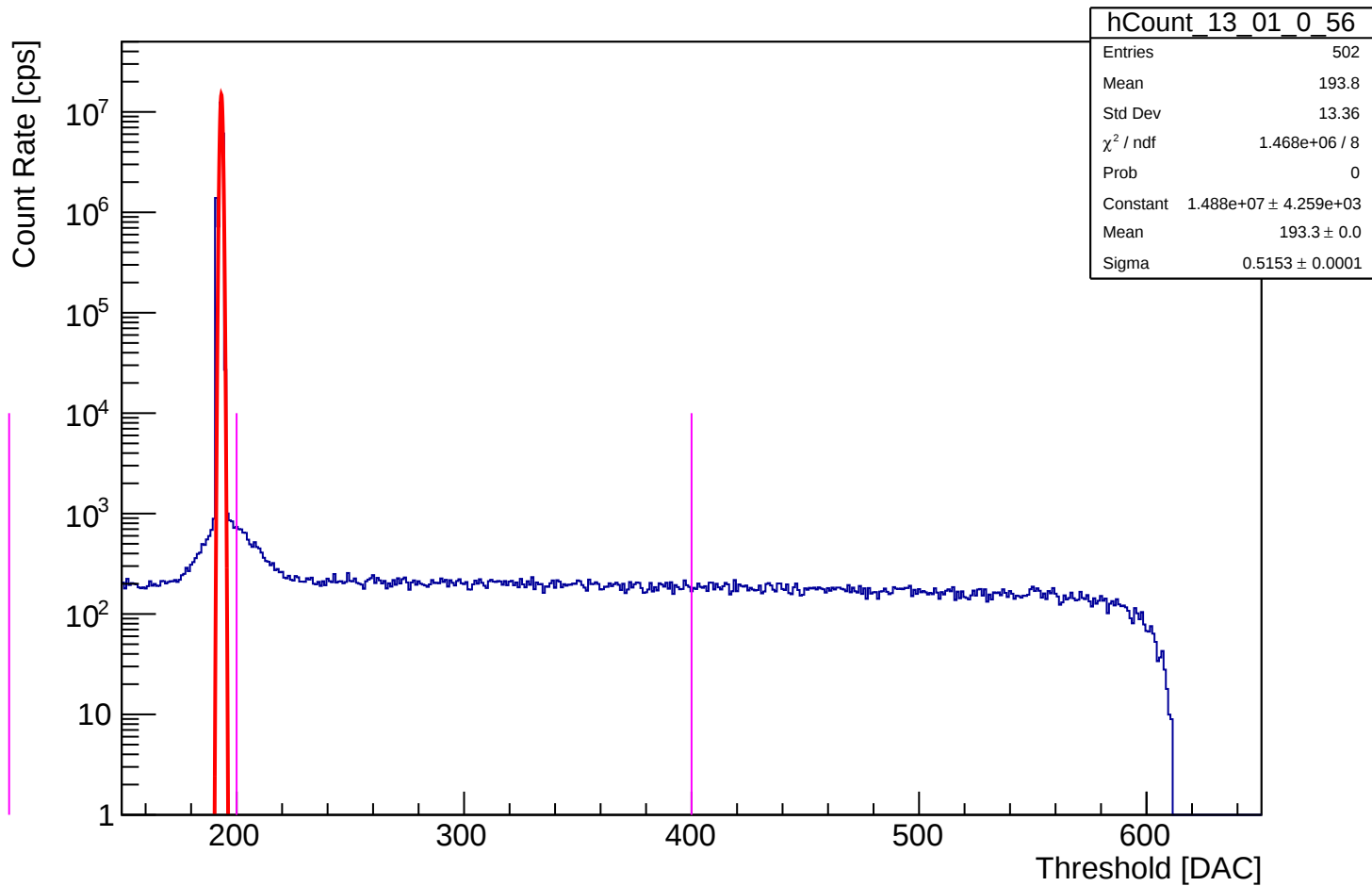




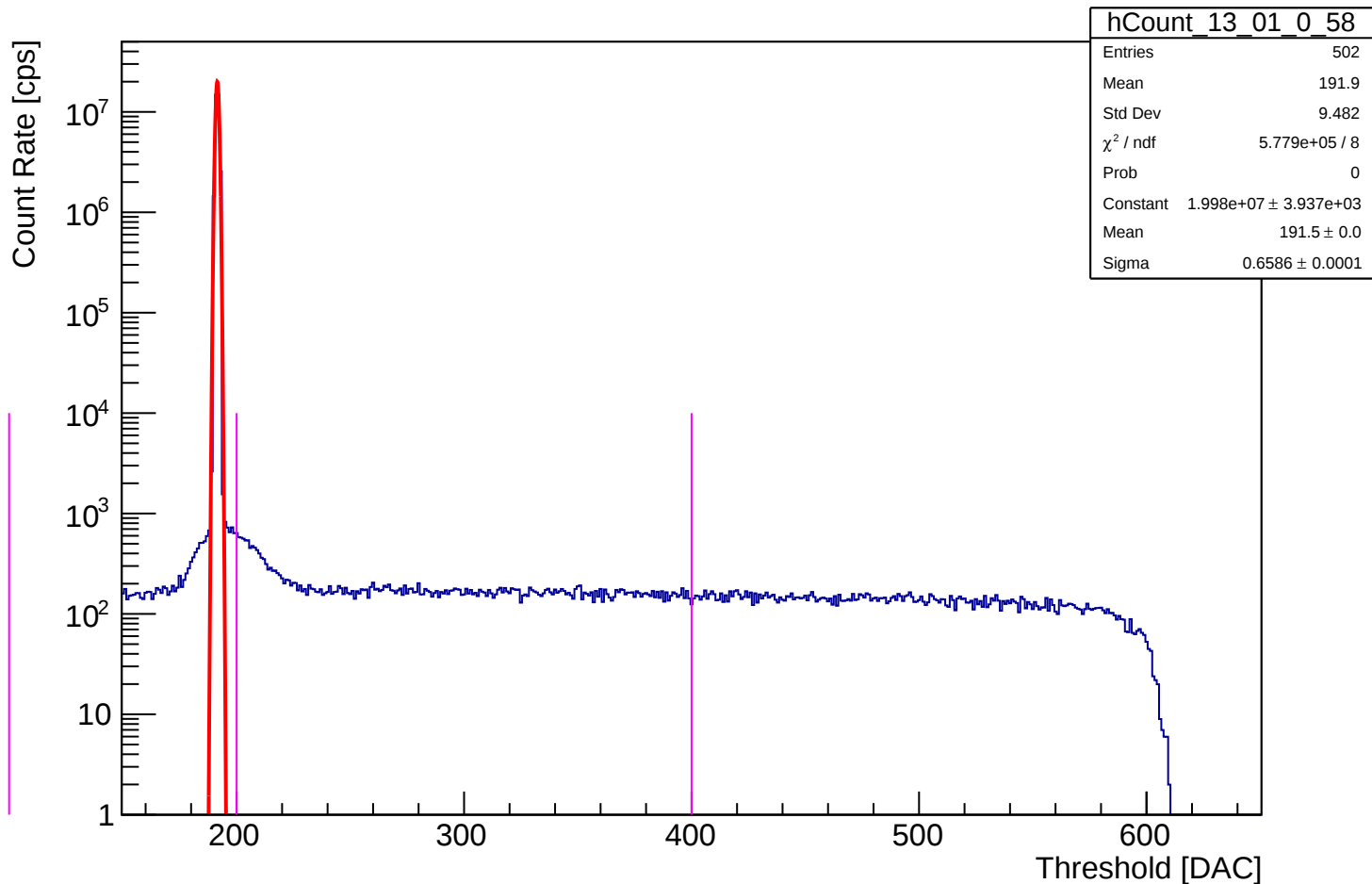




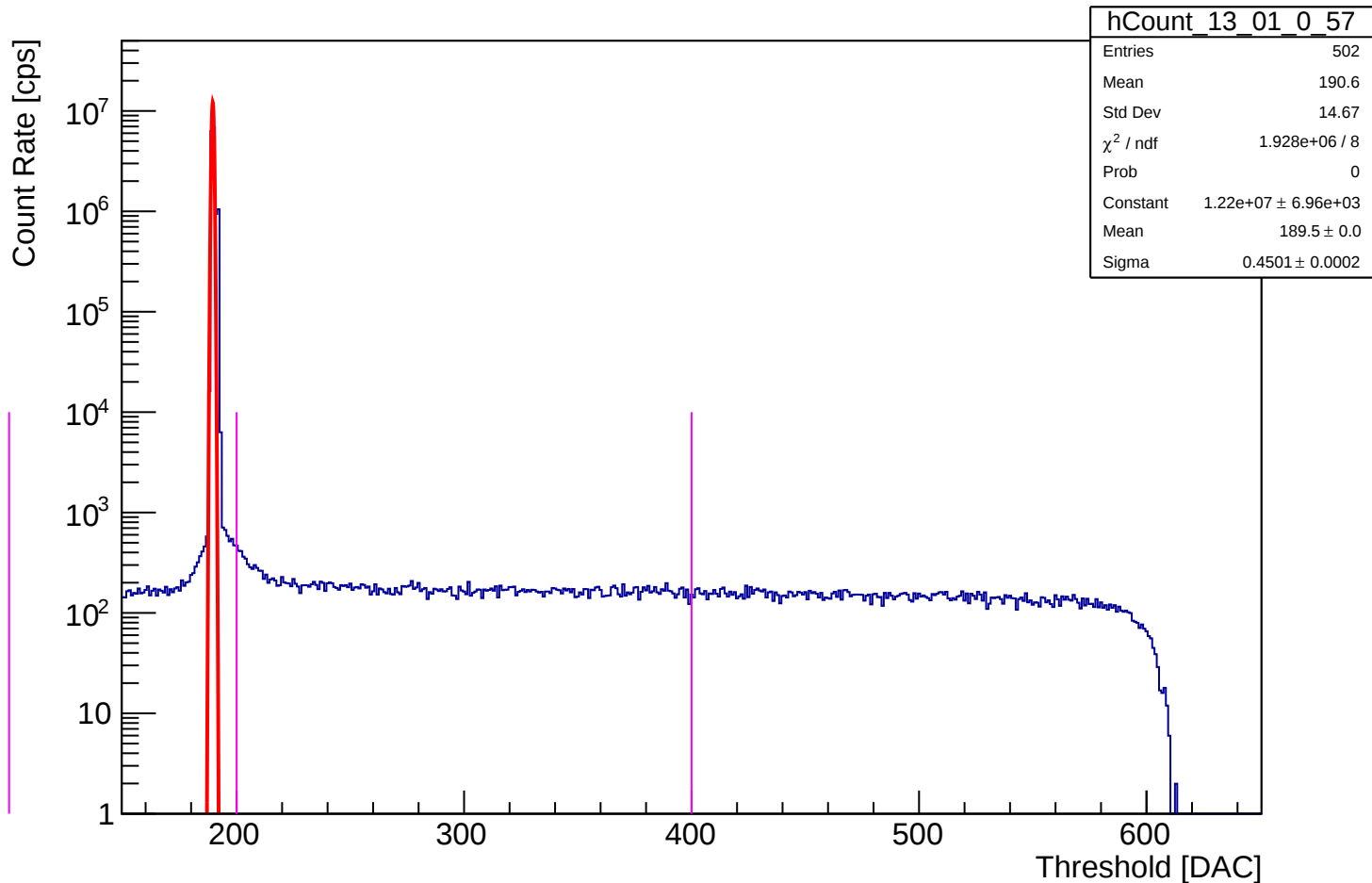
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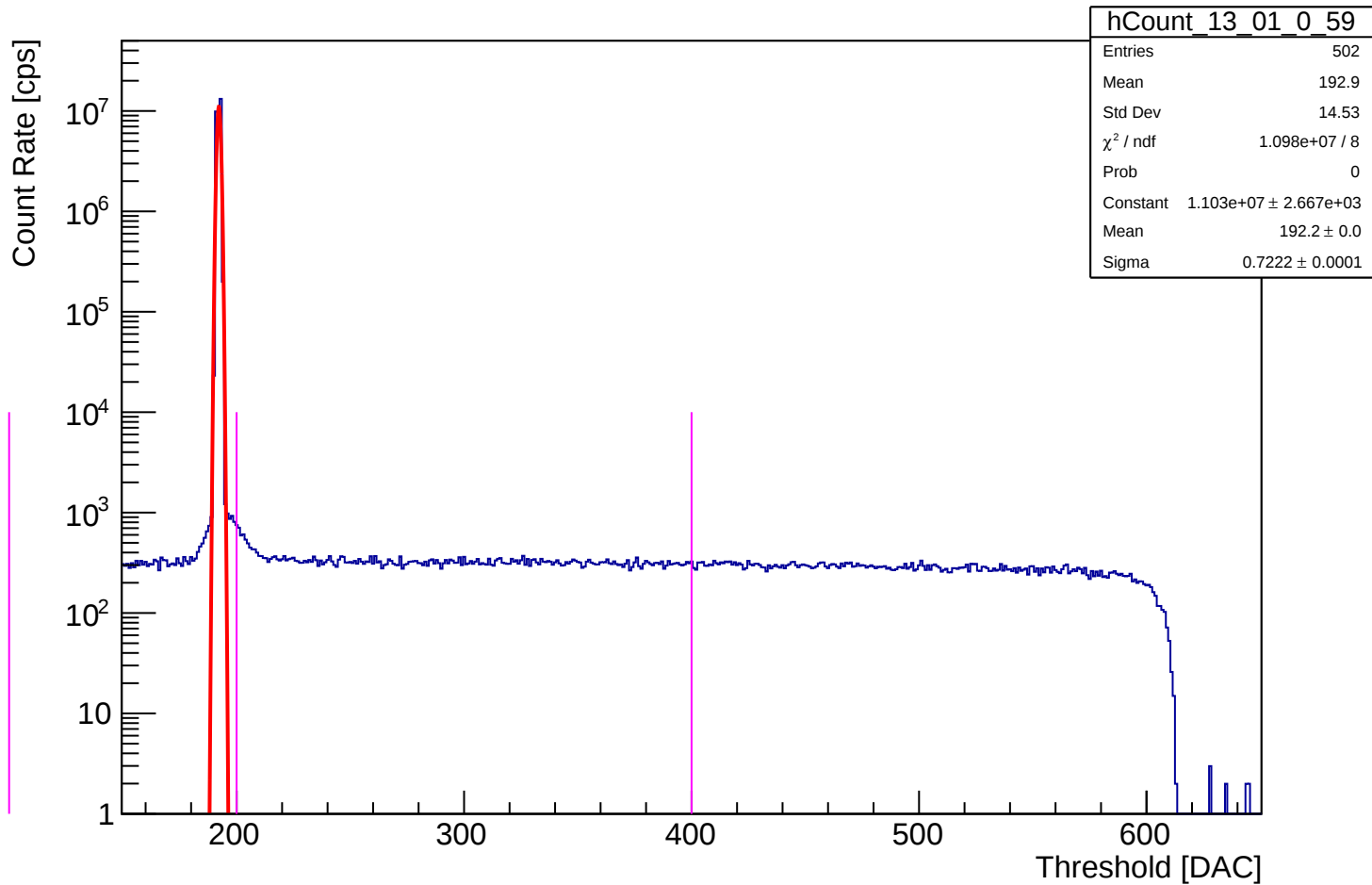
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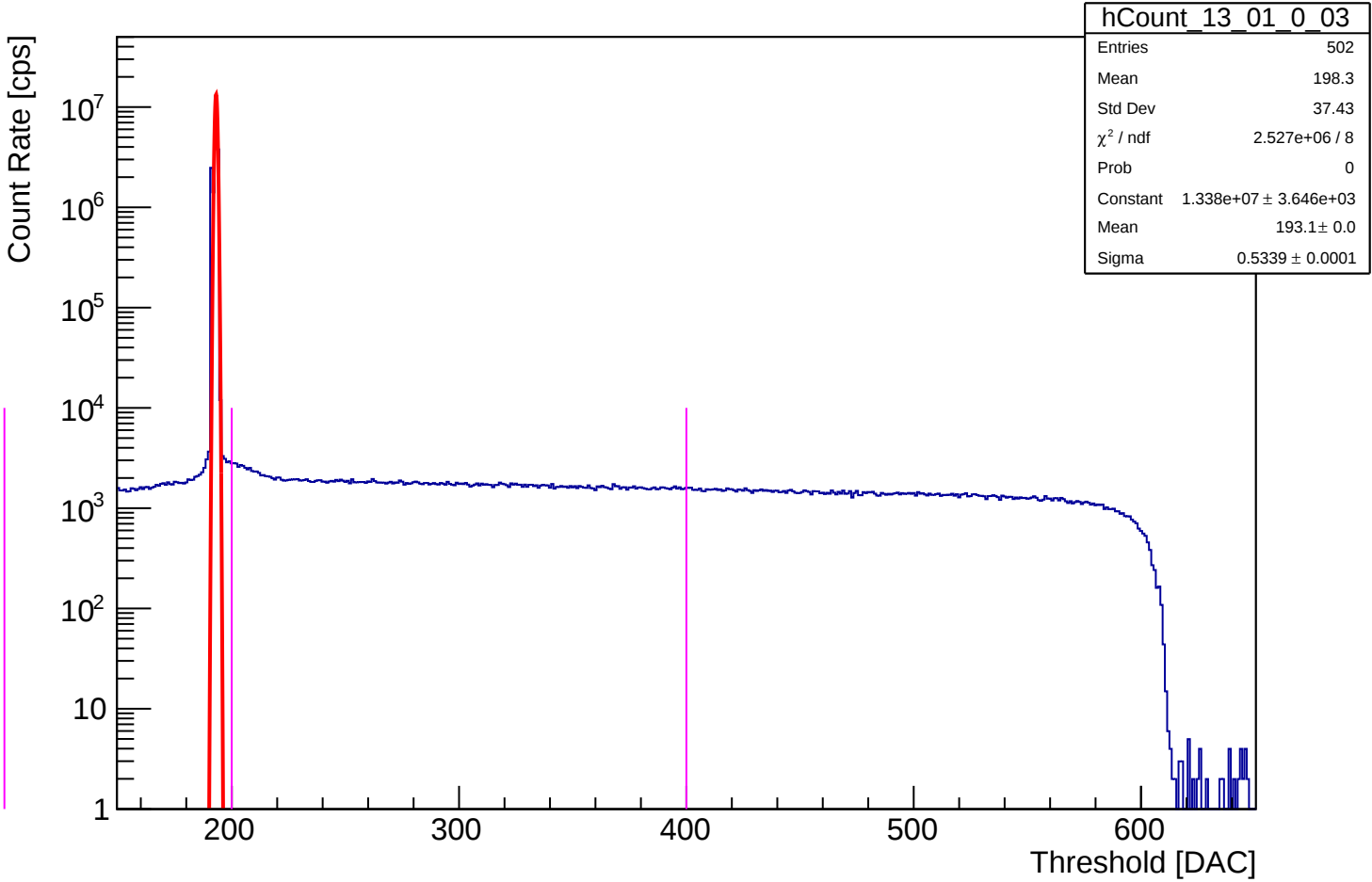


Row 1 Tile 1 Pmt 4 Pixel 55 Slot 13 Fiber 1 Asic 0 Channel 57

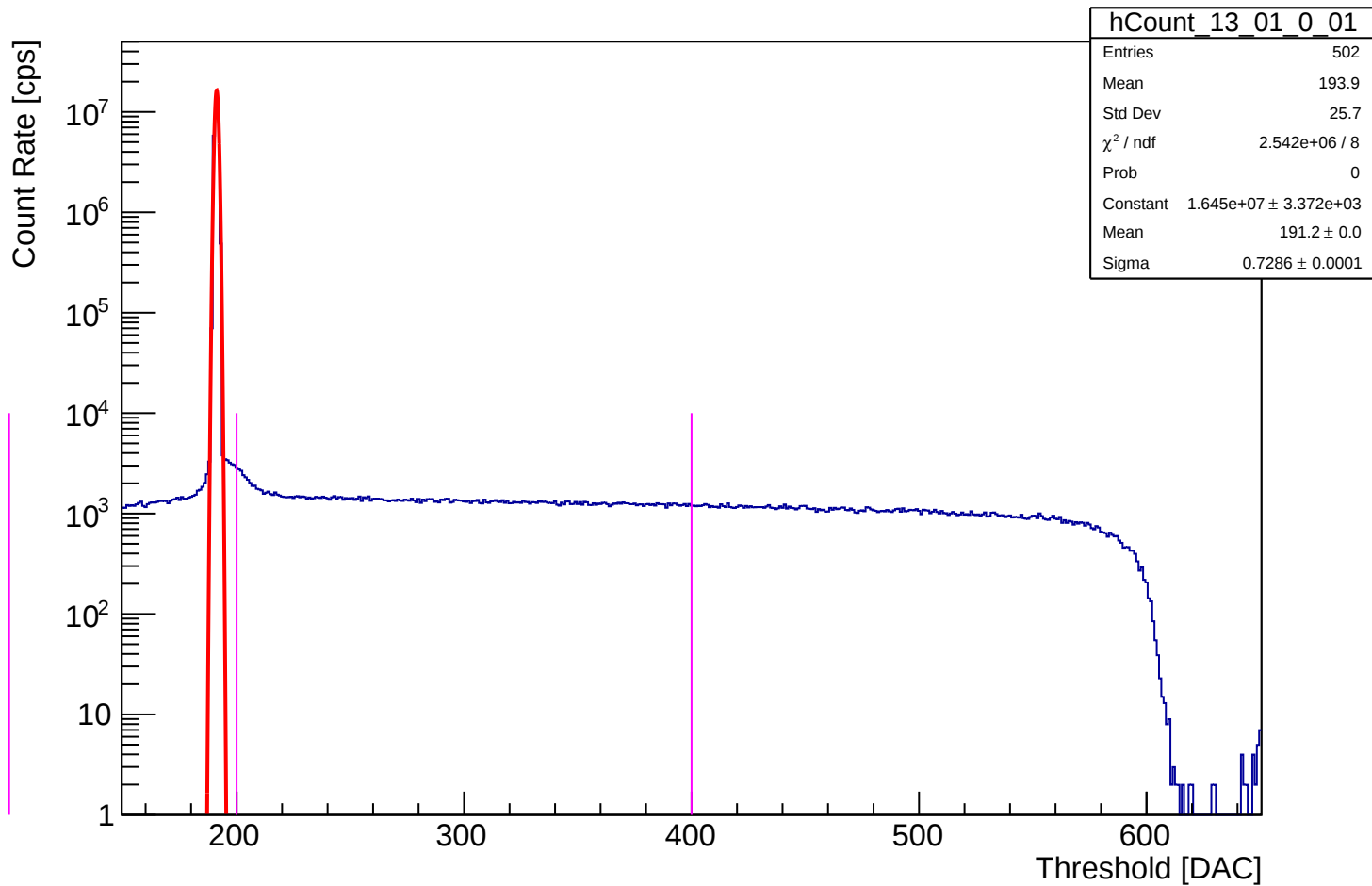


Row 1 Tile 1 Pmt 4 Pixel 56 Slot 13 Fiber 1 Asic 0 Channel 59

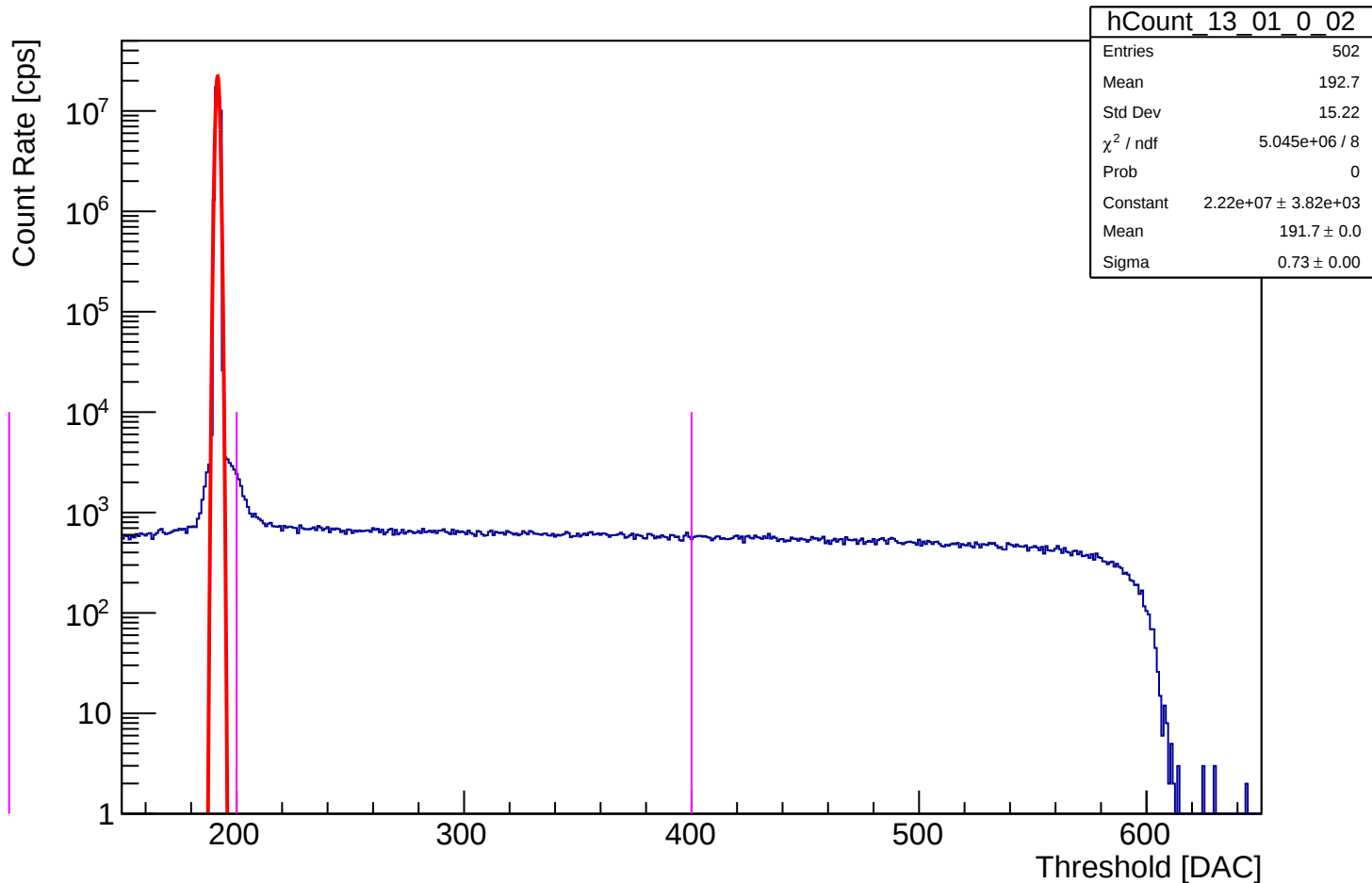


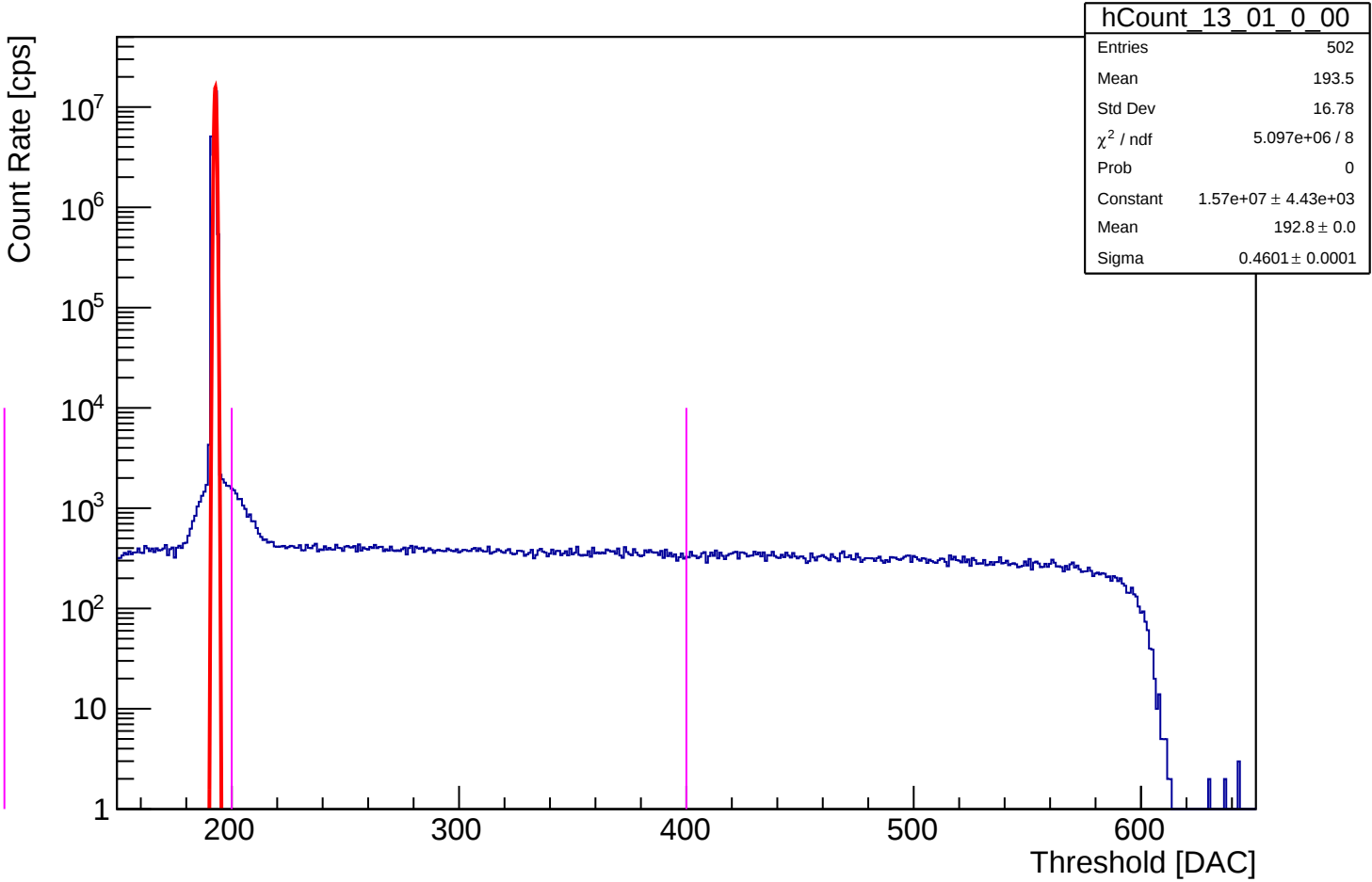


Row 1 Tile 1 Pmt 4 Pixel 58 Slot 13 Fiber 1 Asic 0 Channel 1

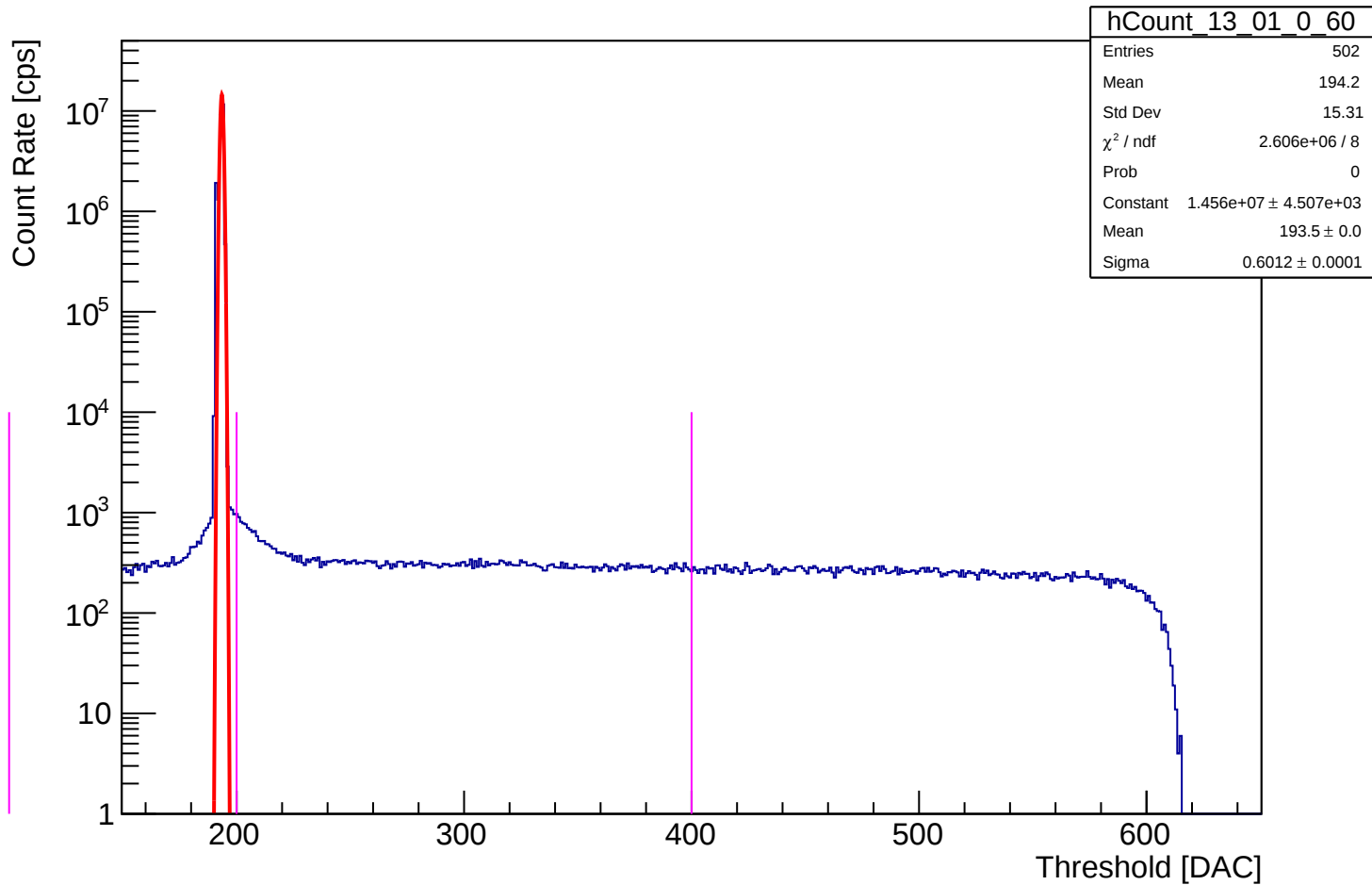


Row 1 Tile 1 Pmt 4 Pixel 59 Slot 13 Fiber 1 Asic 0 Channel 2

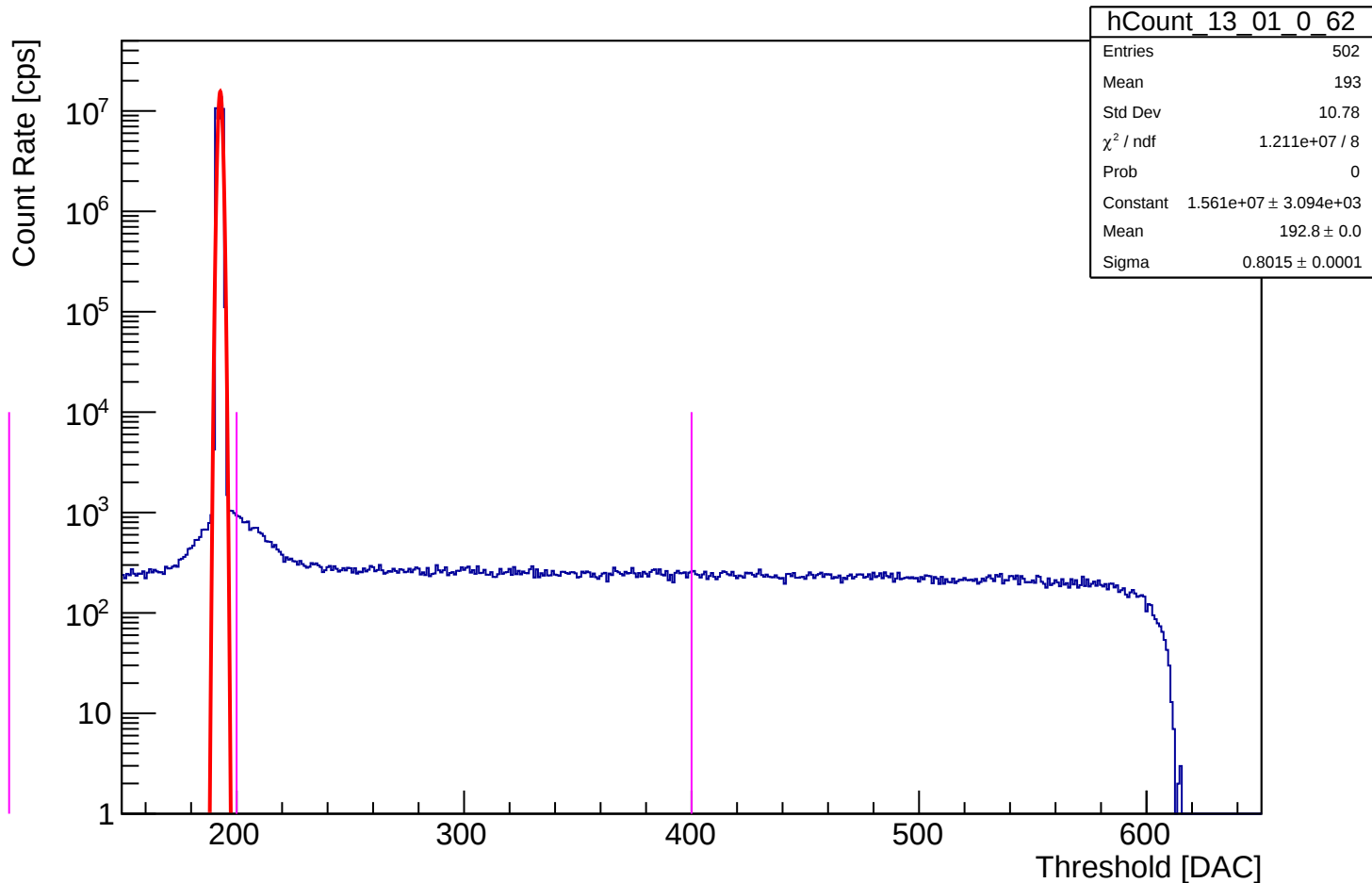


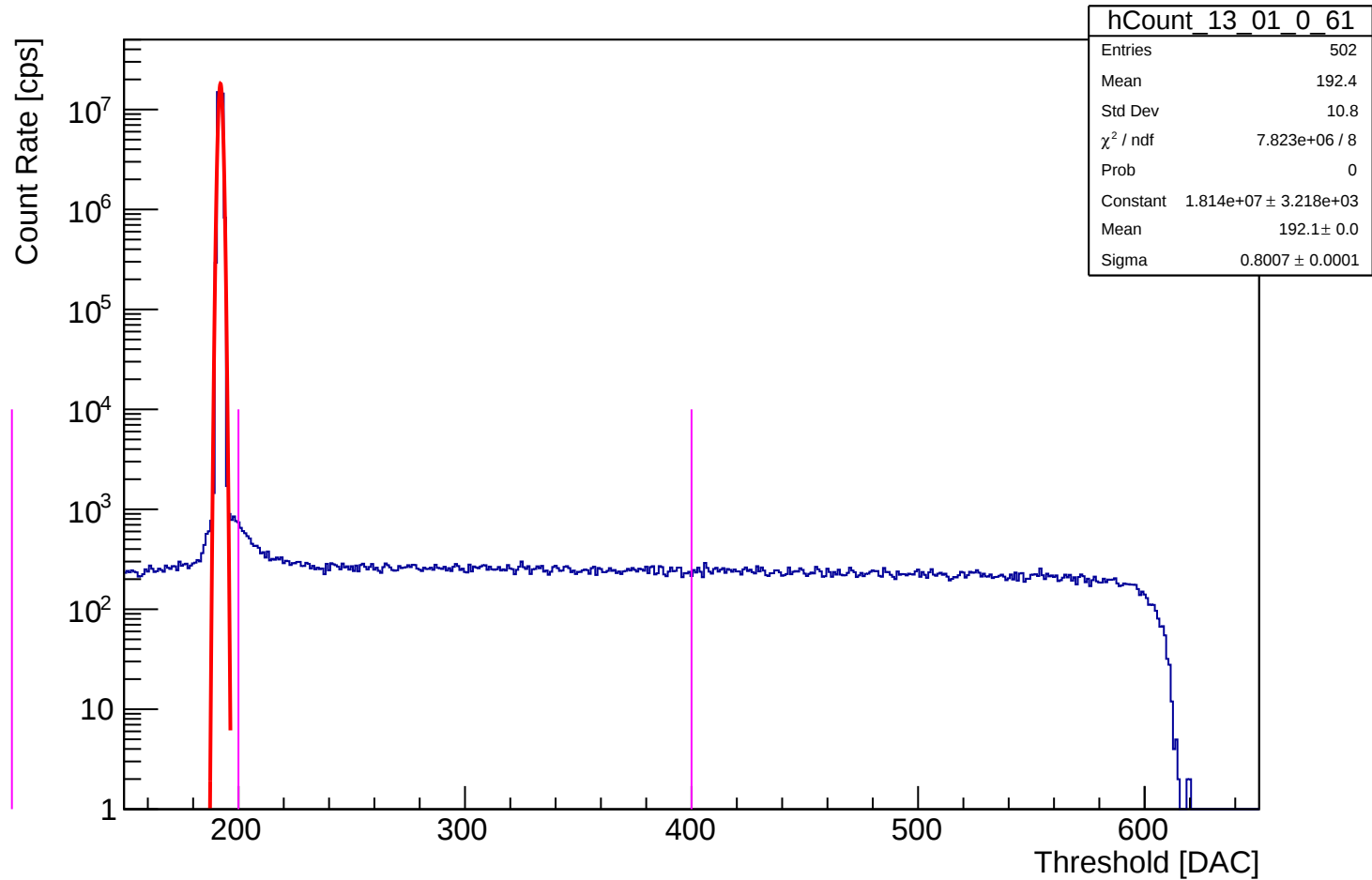


Row 1 Tile 1 Pmt 4 Pixel 61 Slot 13 Fiber 1 Asic 0 Channel 60

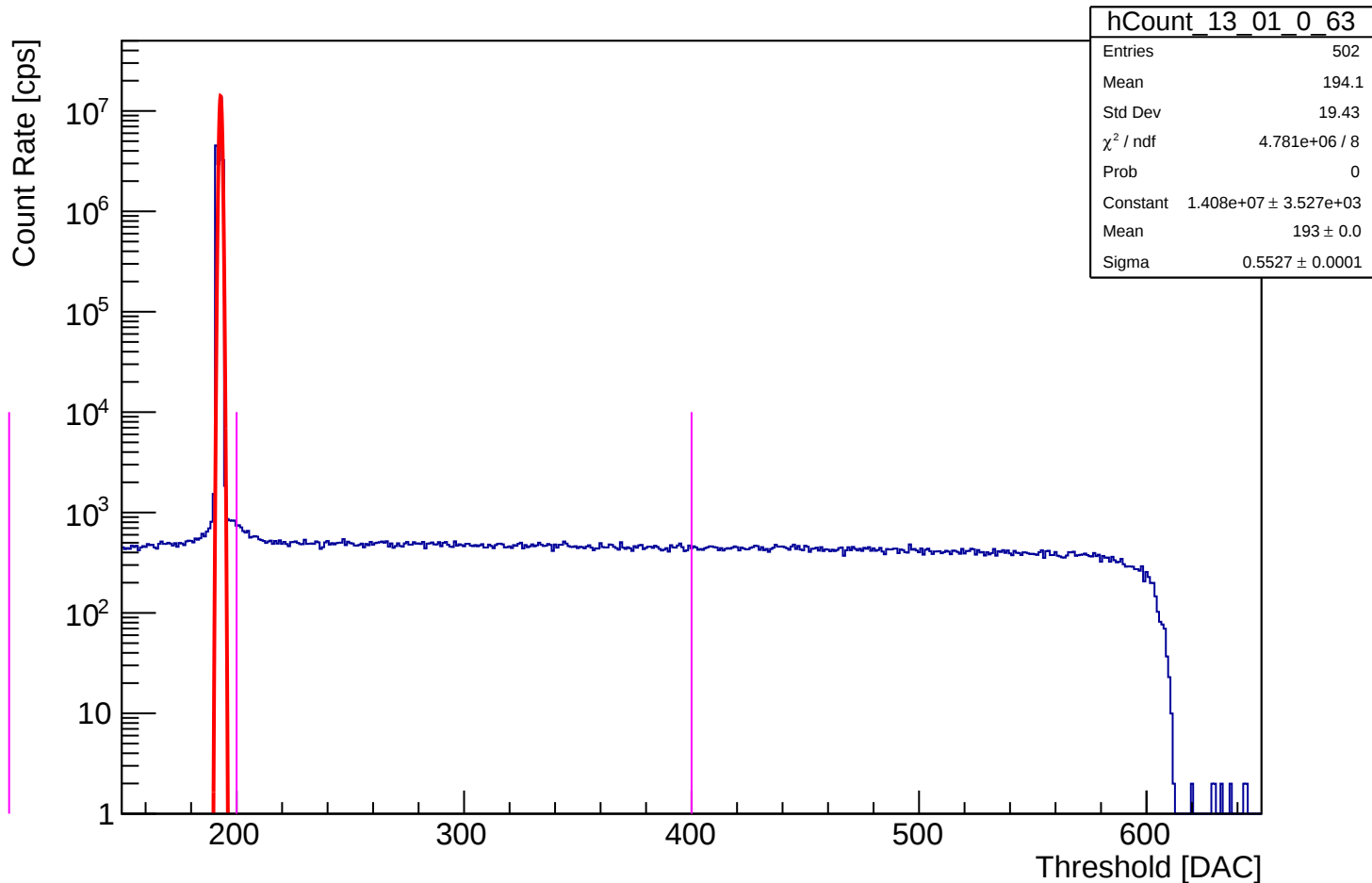


Row 1 Tile 1 Pmt 4 Pixel 62 Slot 13 Fiber 1 Asic 0 Channel 62

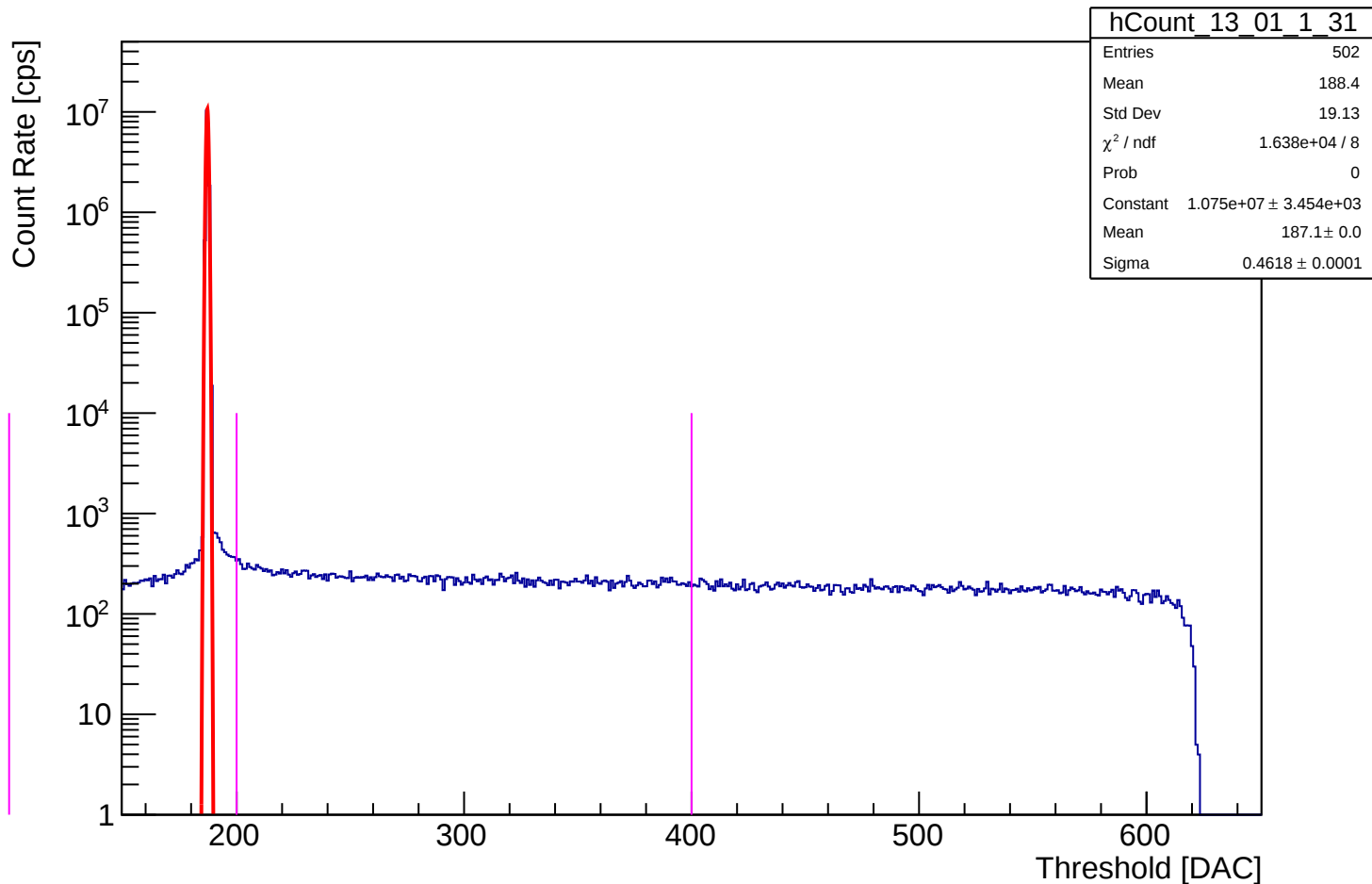




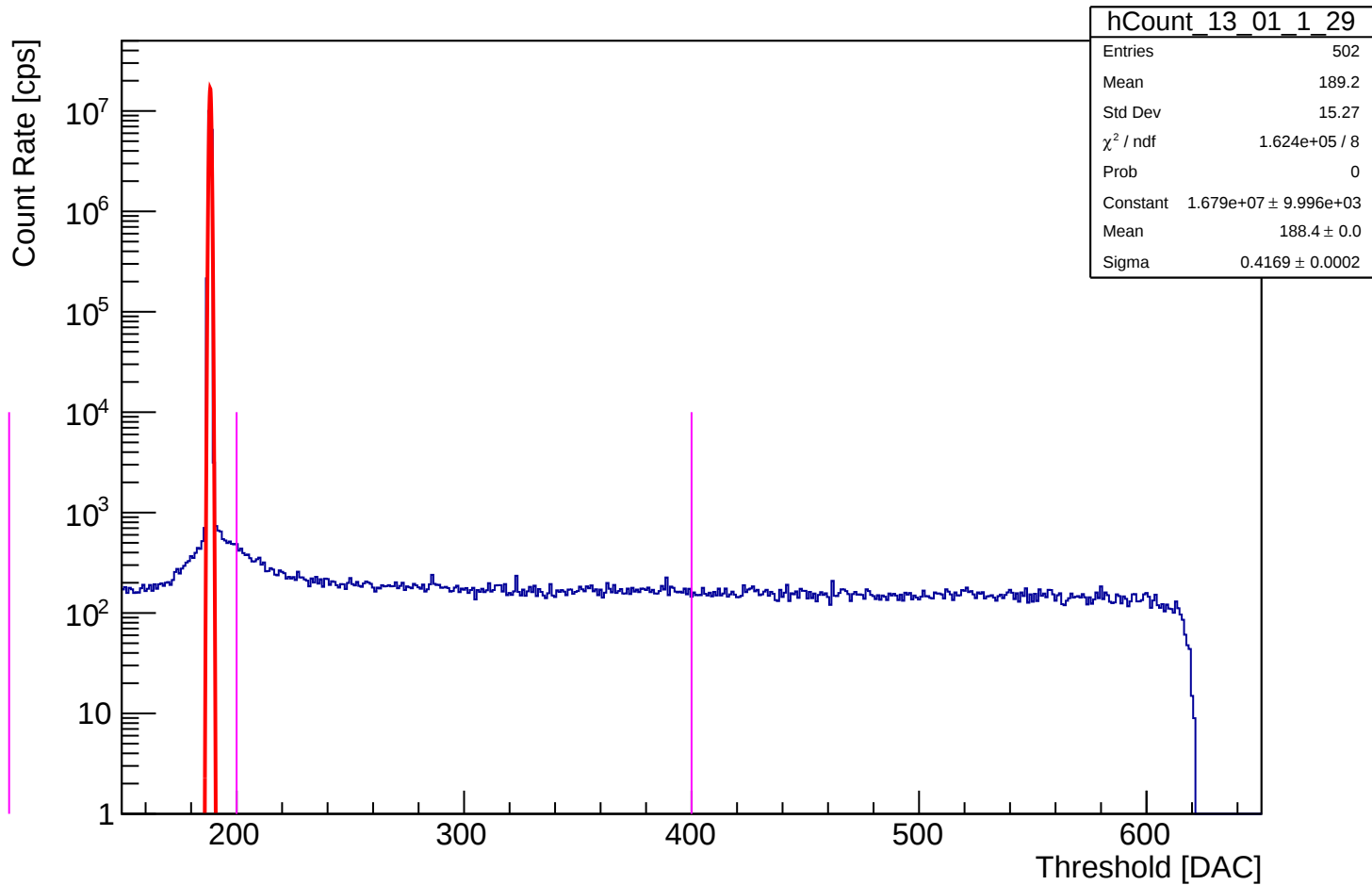
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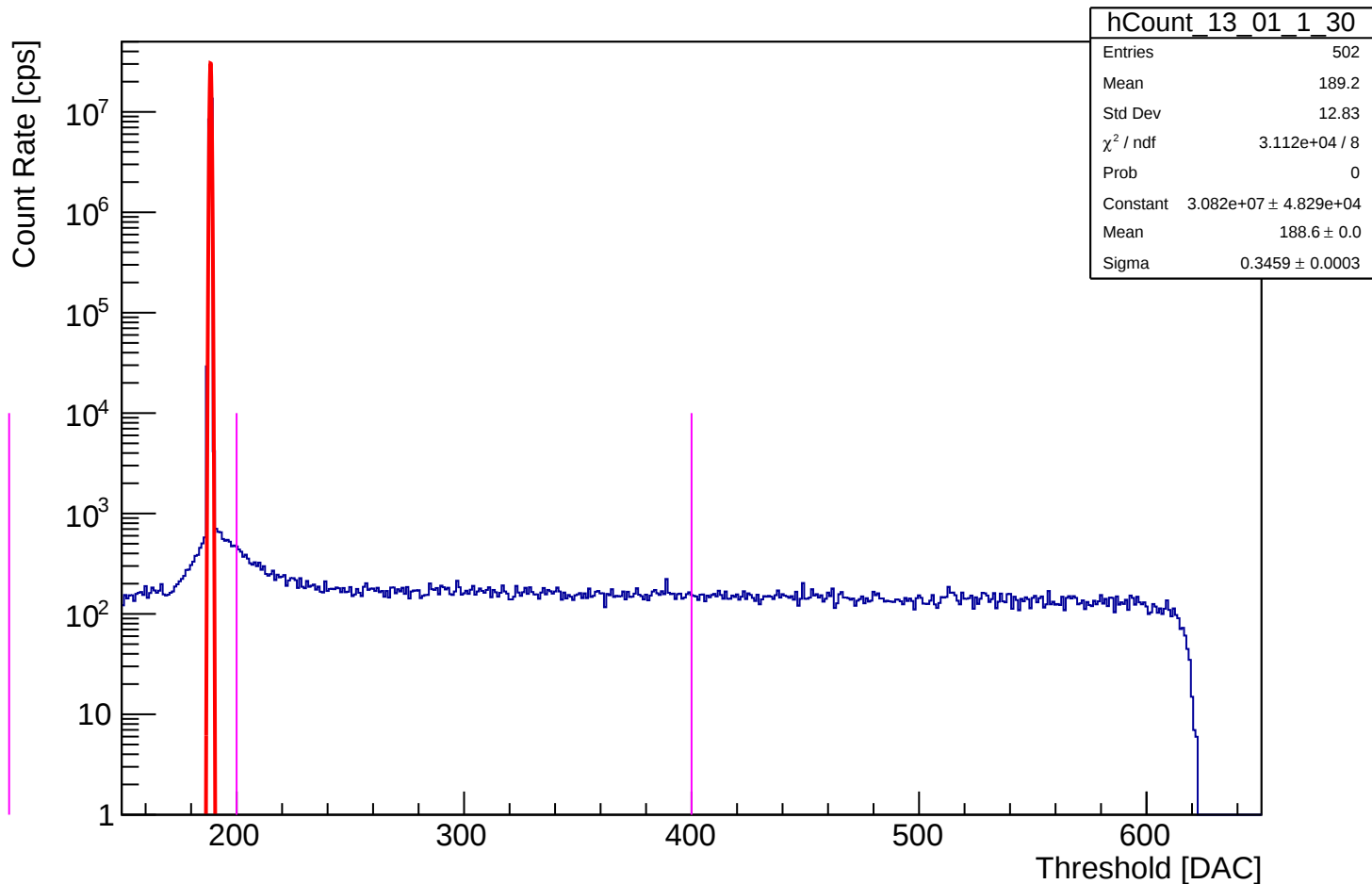
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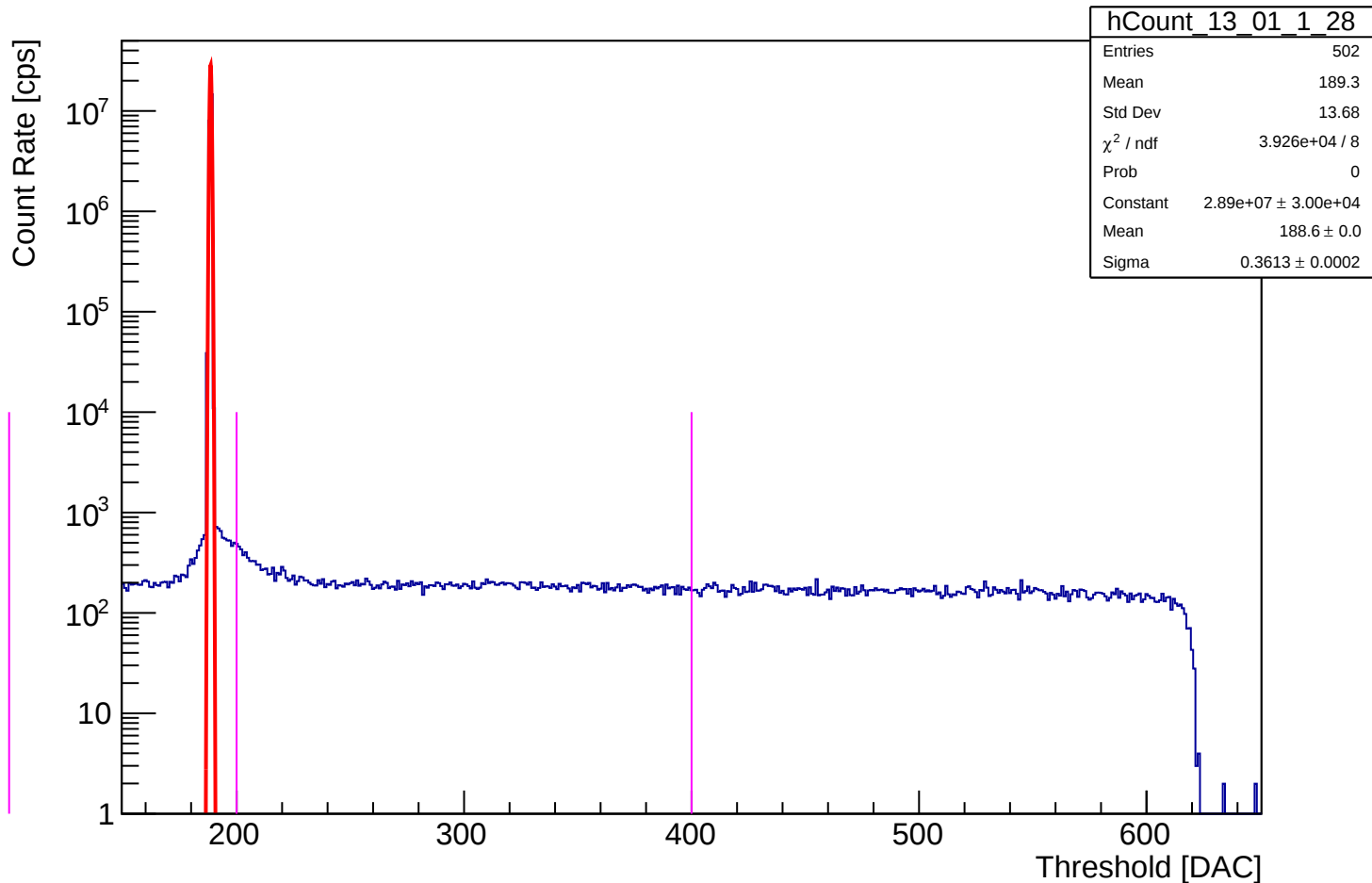
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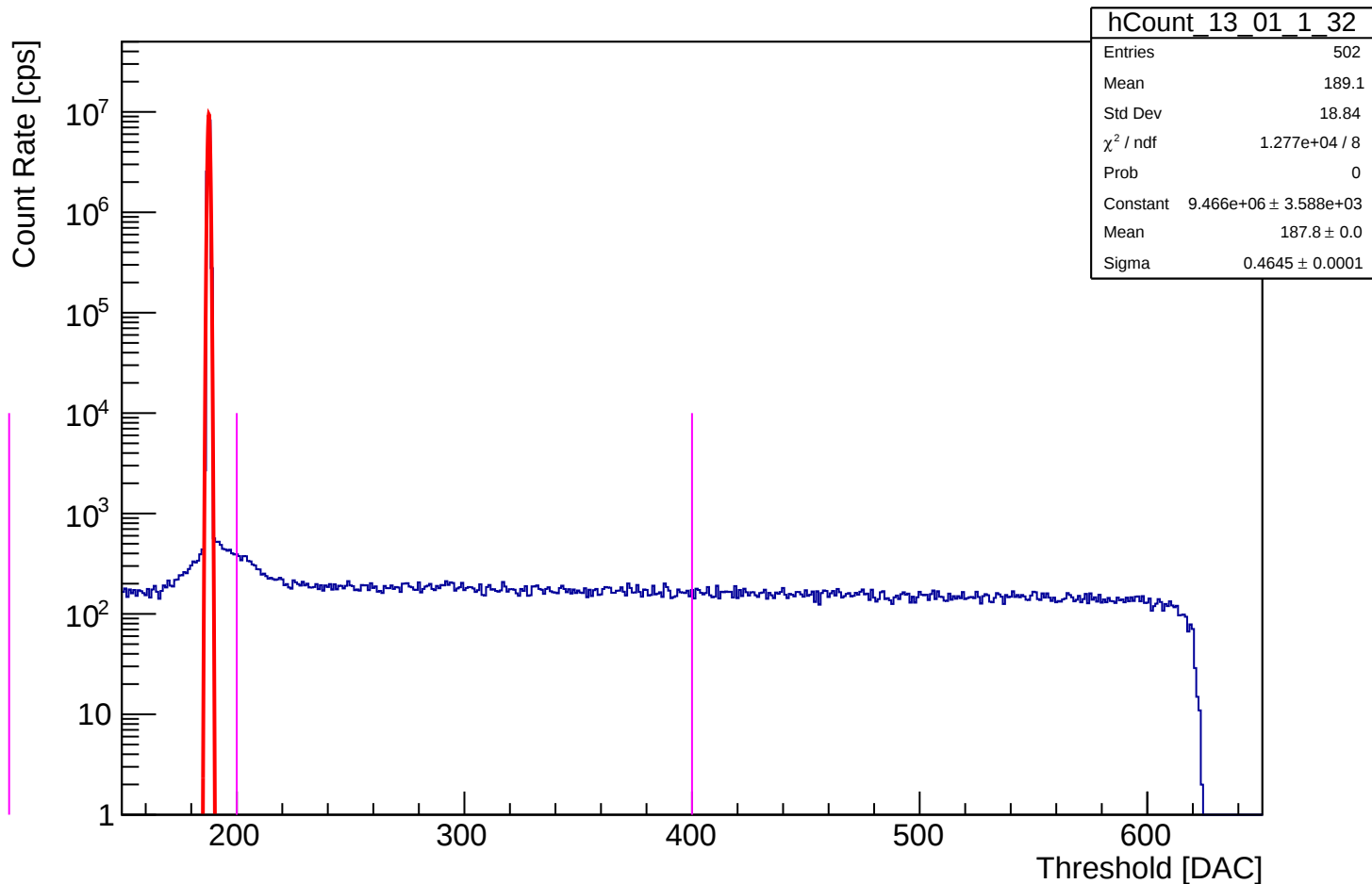
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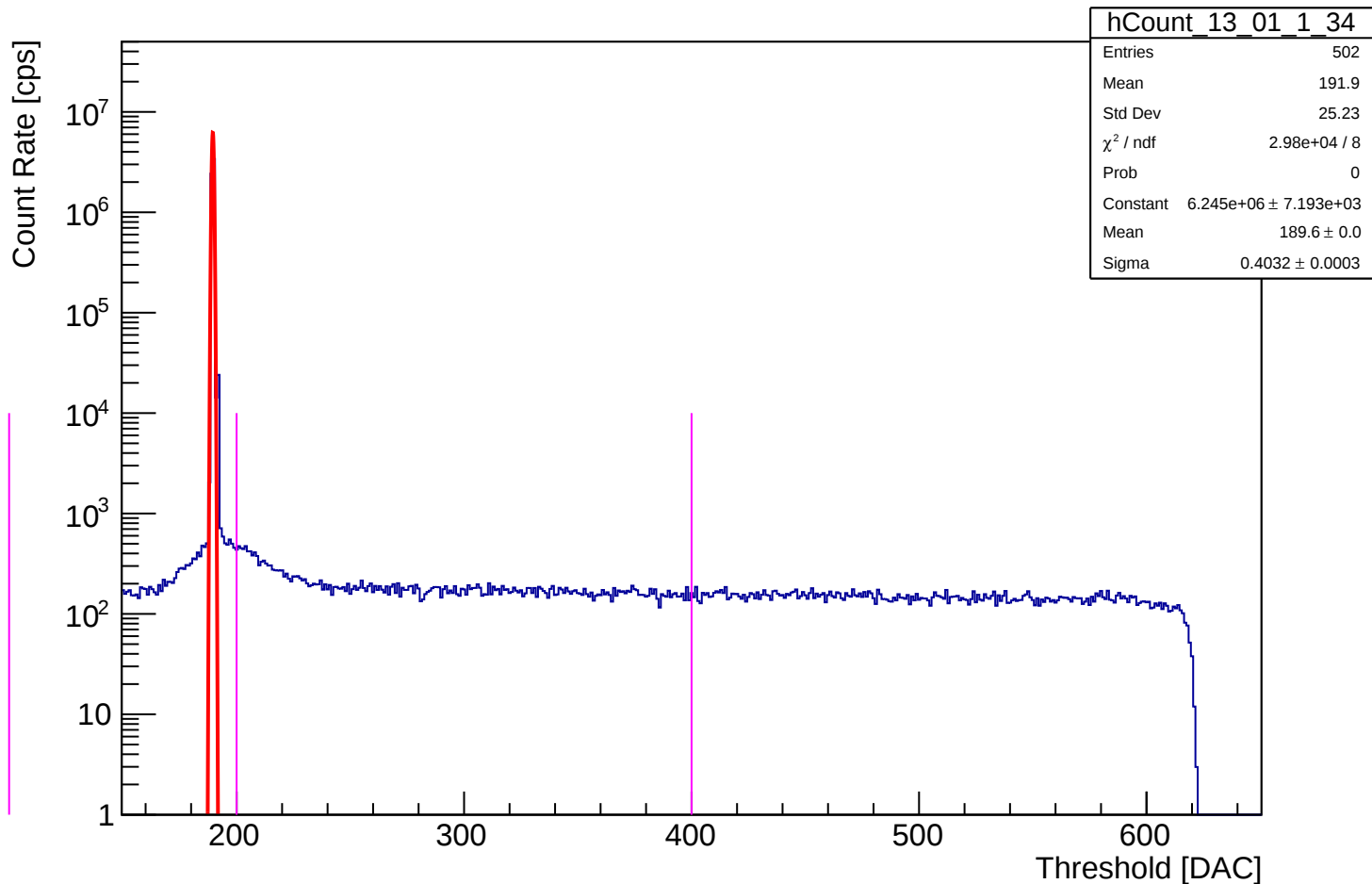
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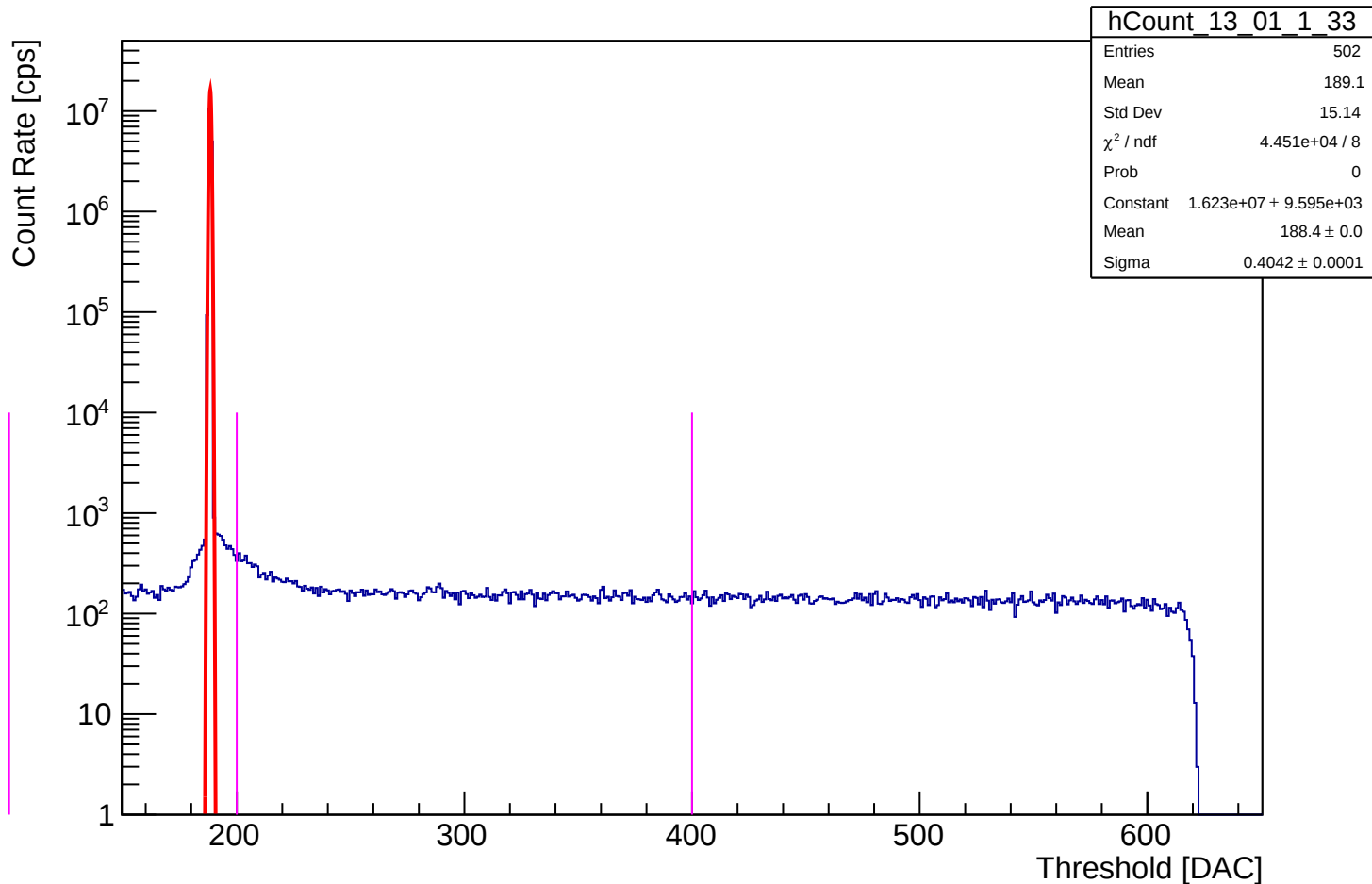
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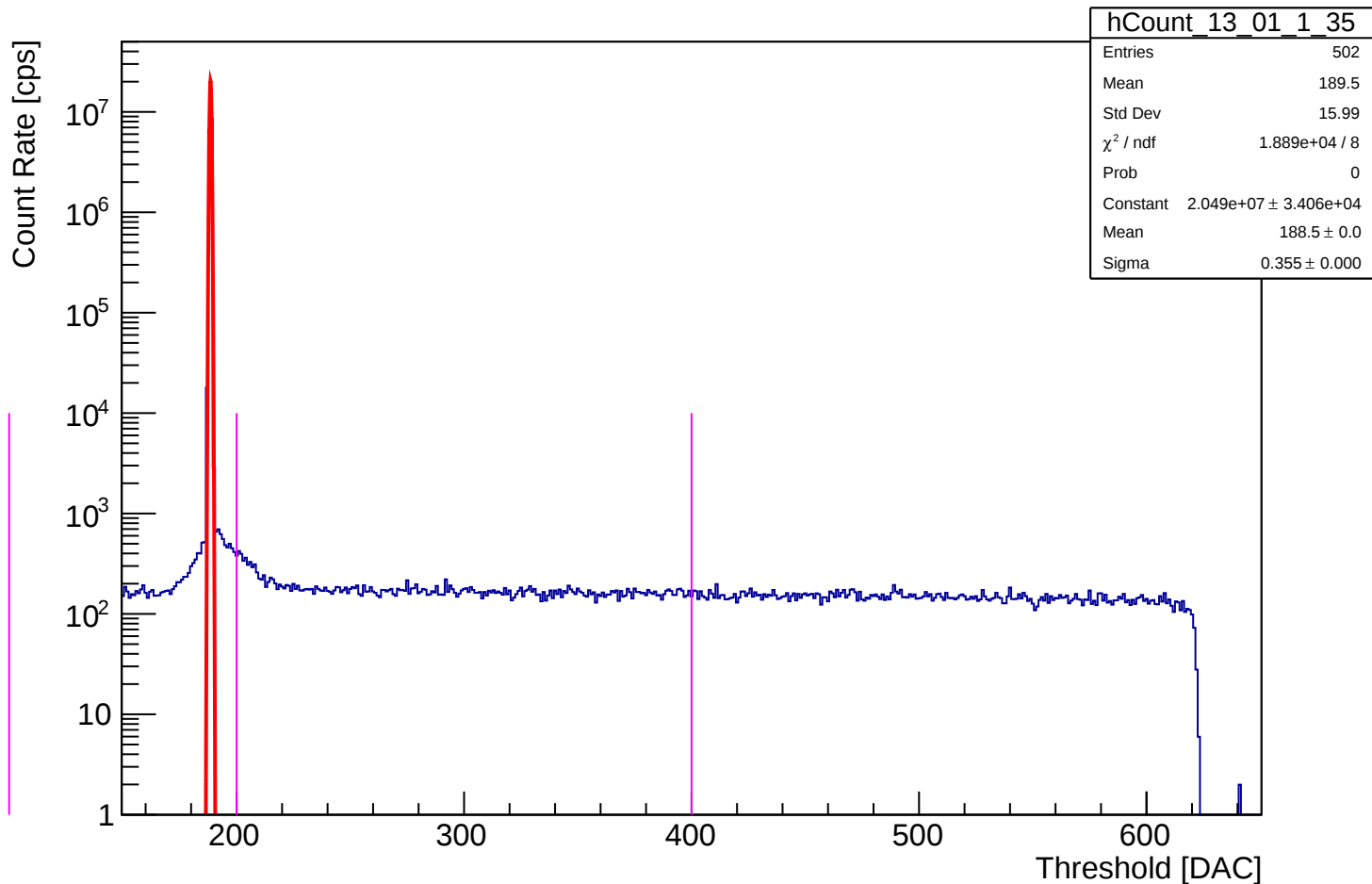
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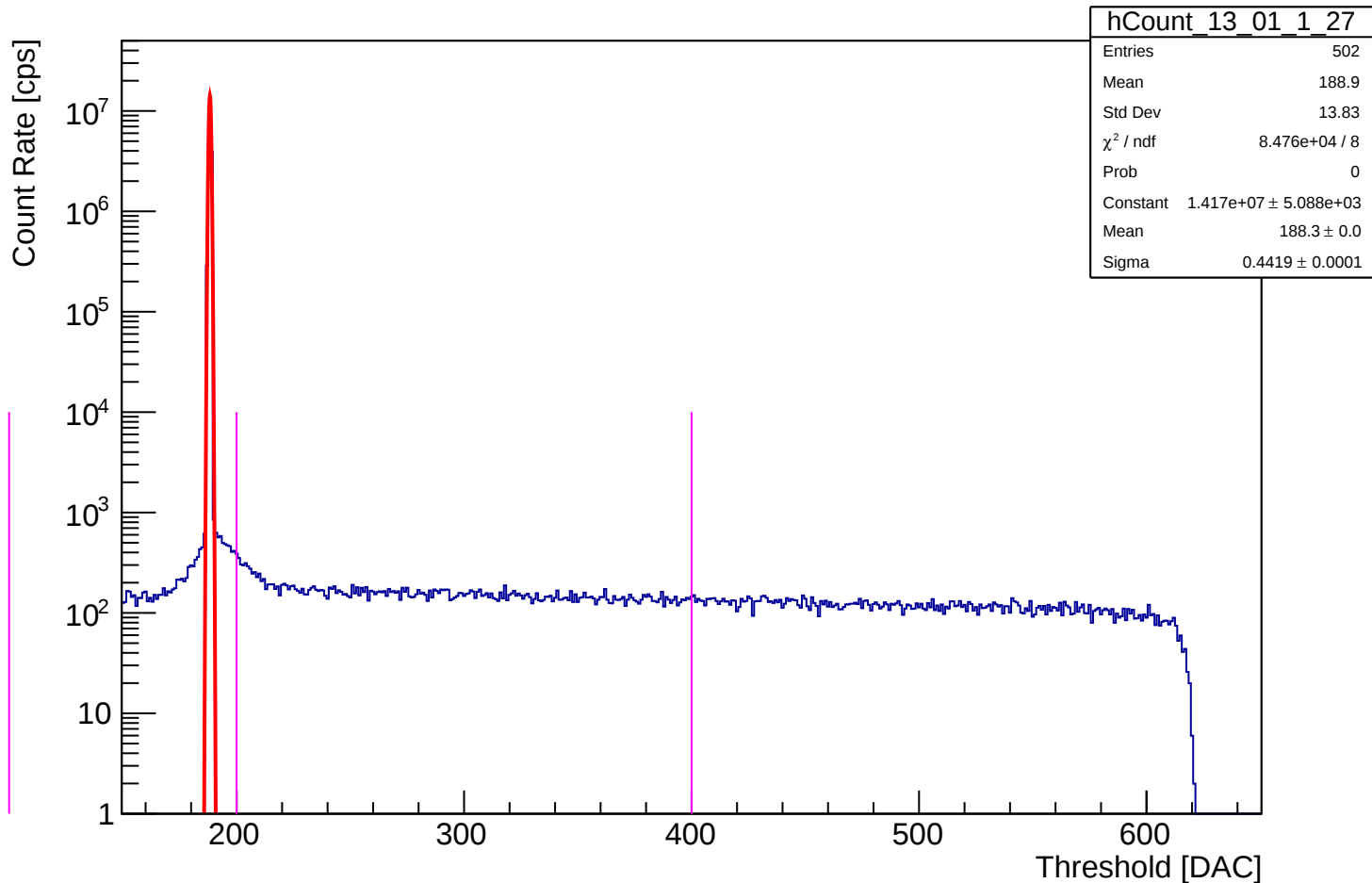
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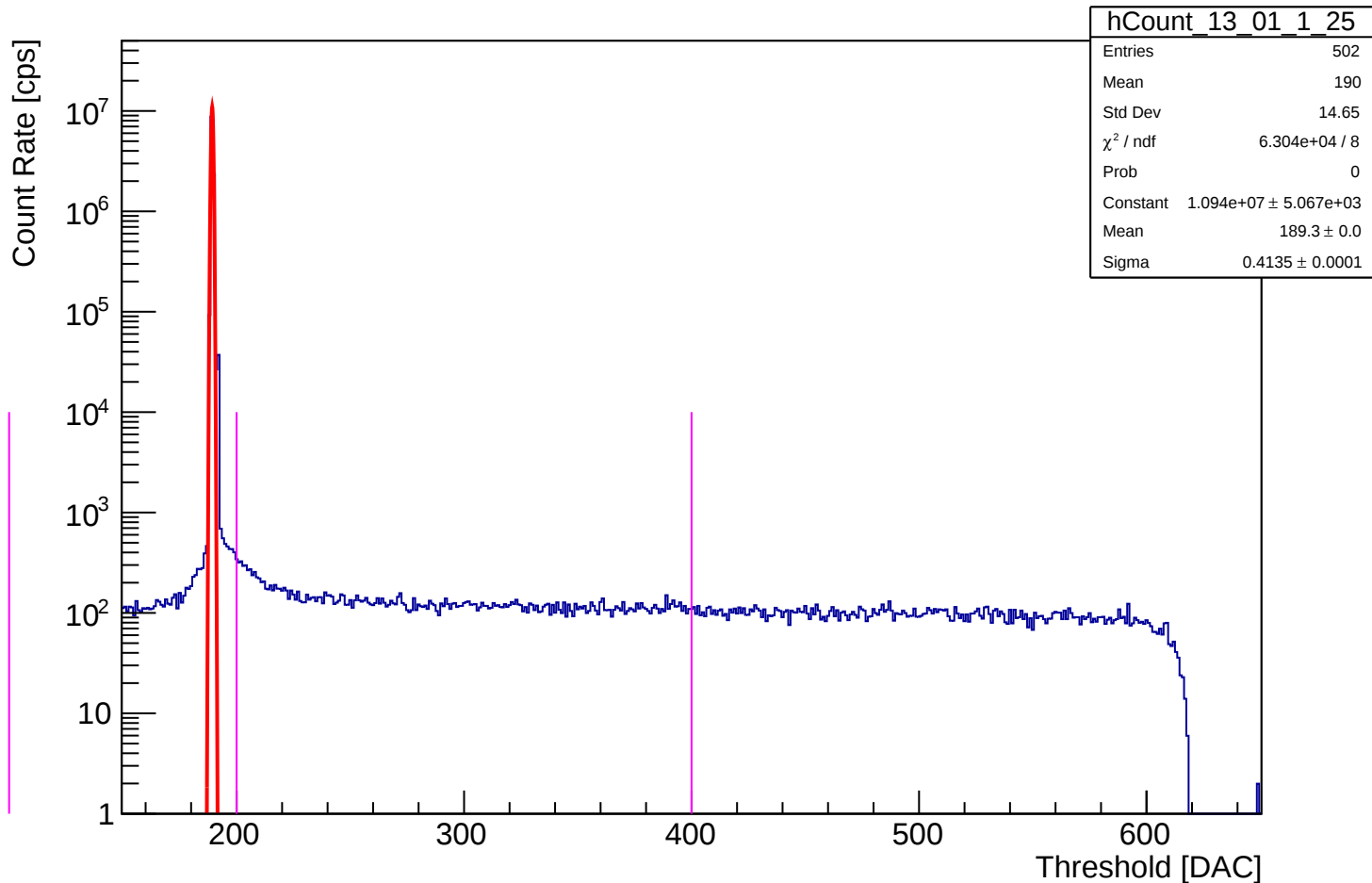
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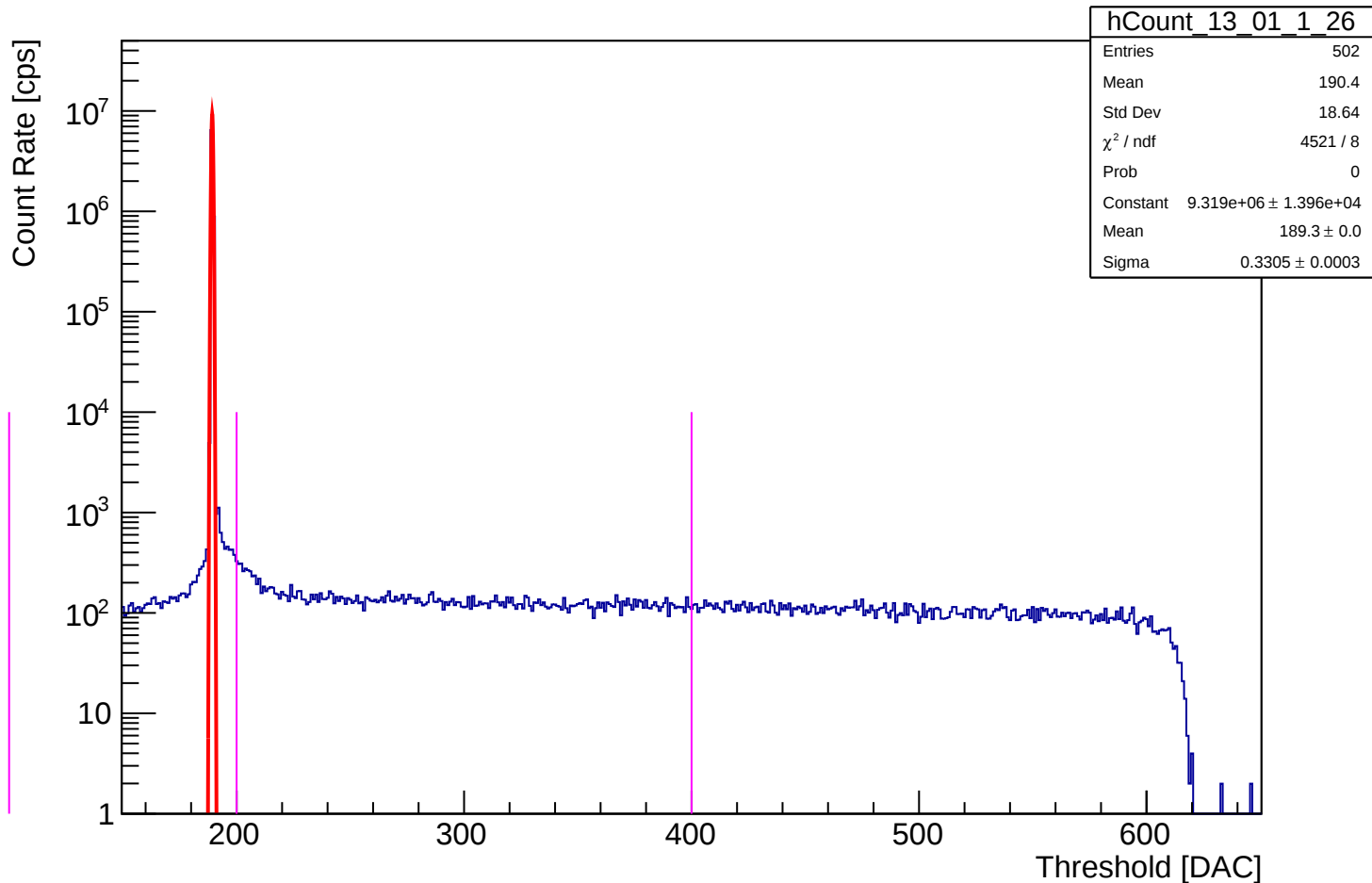
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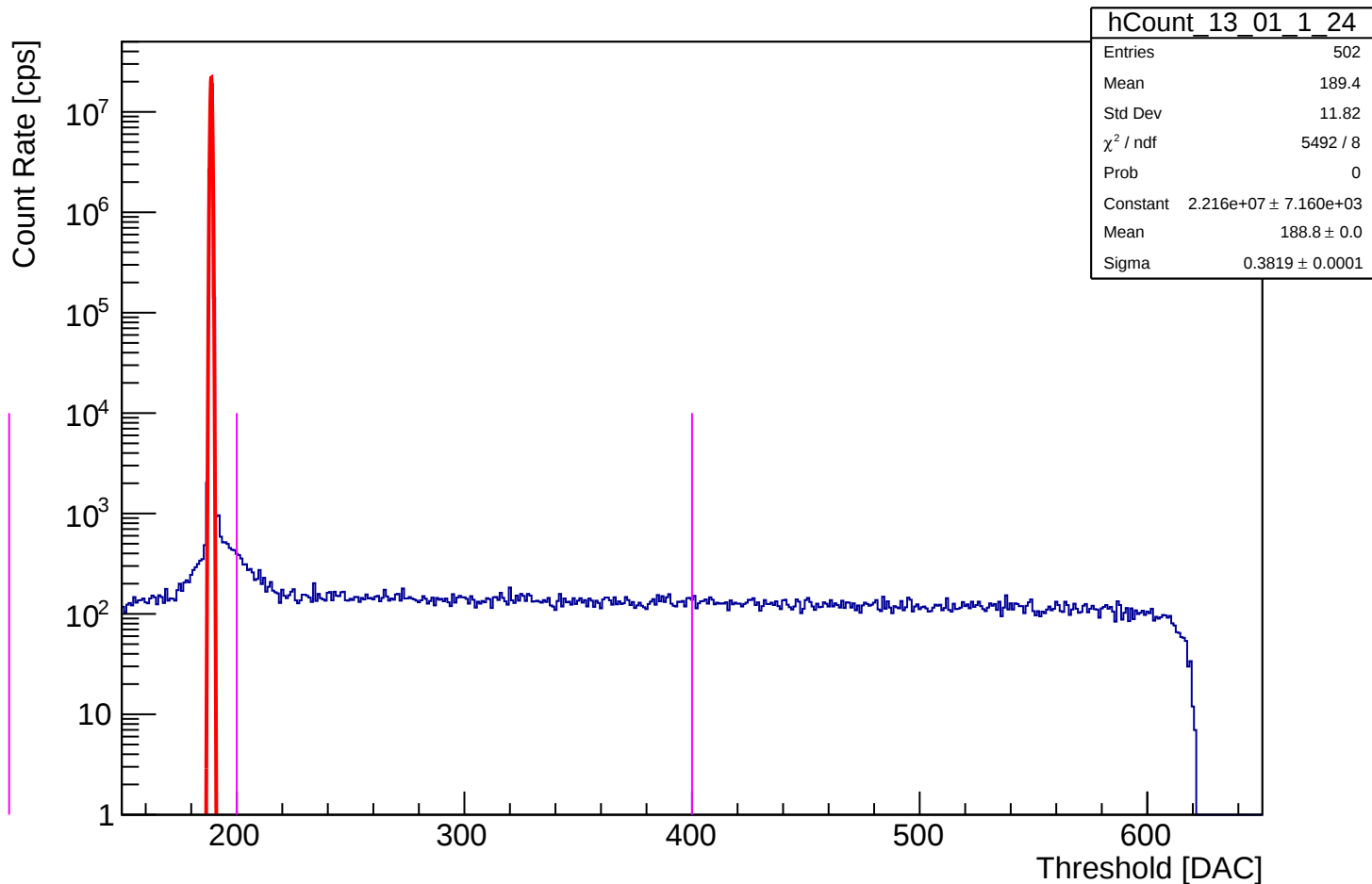
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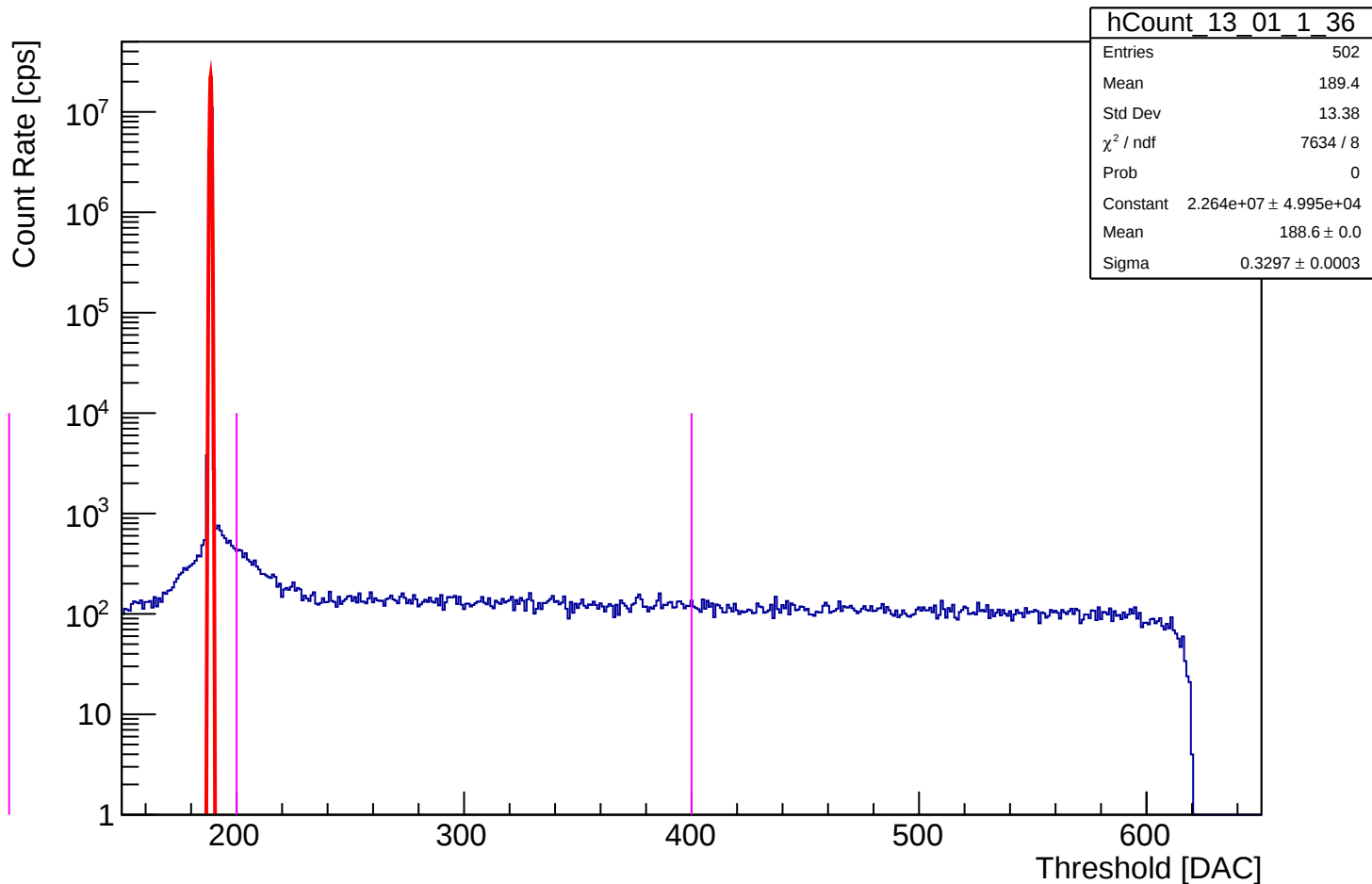
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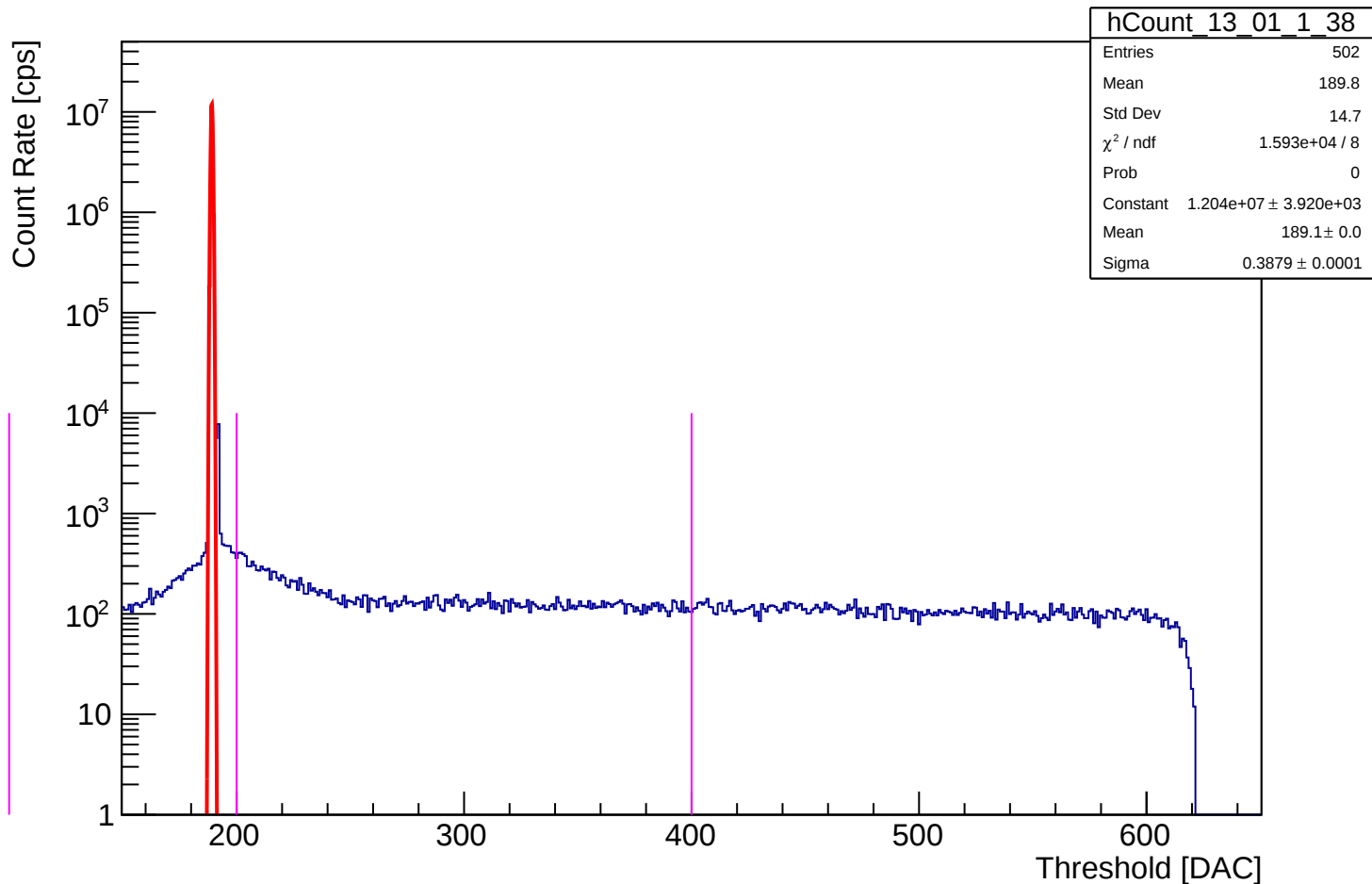
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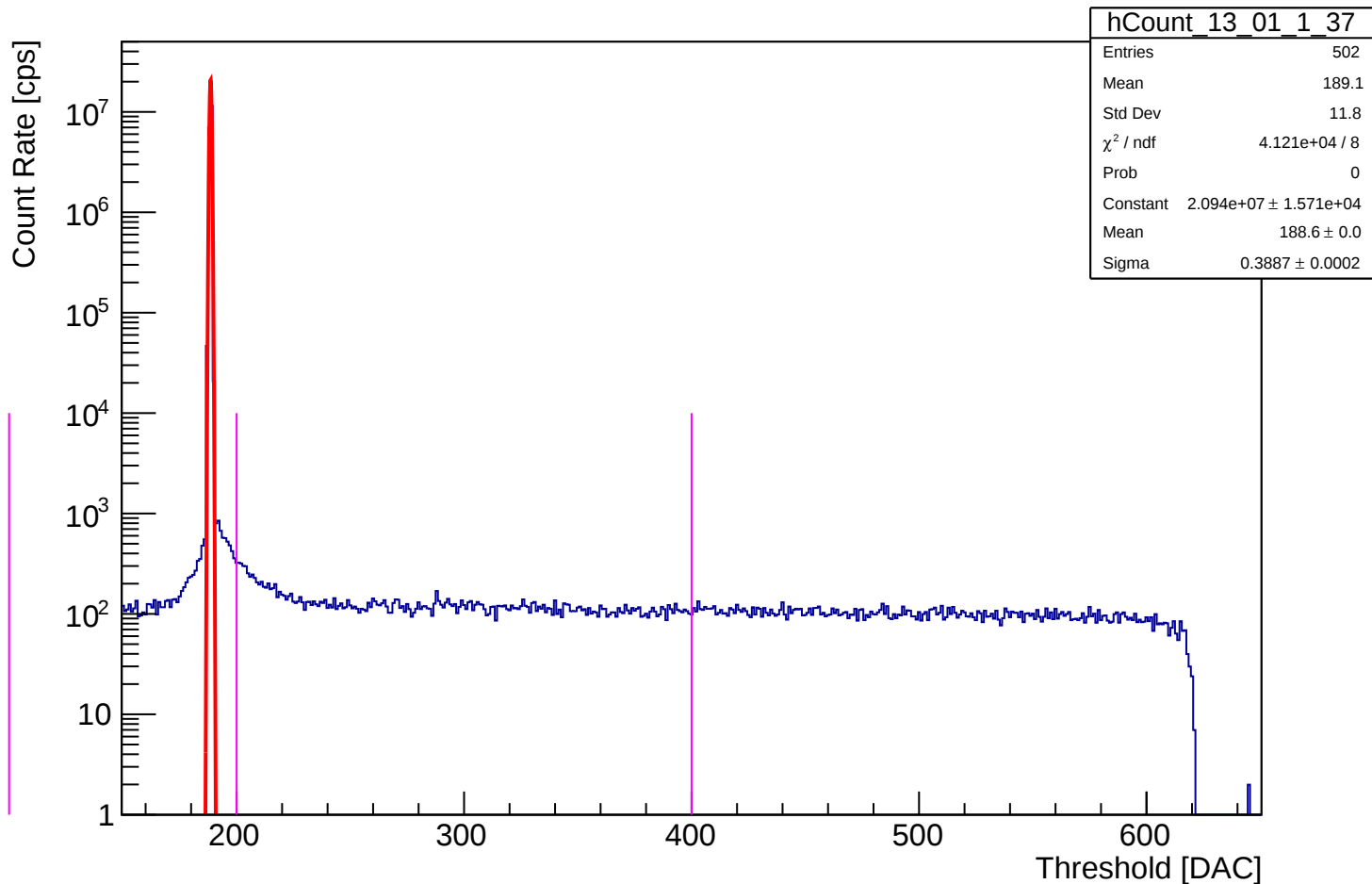
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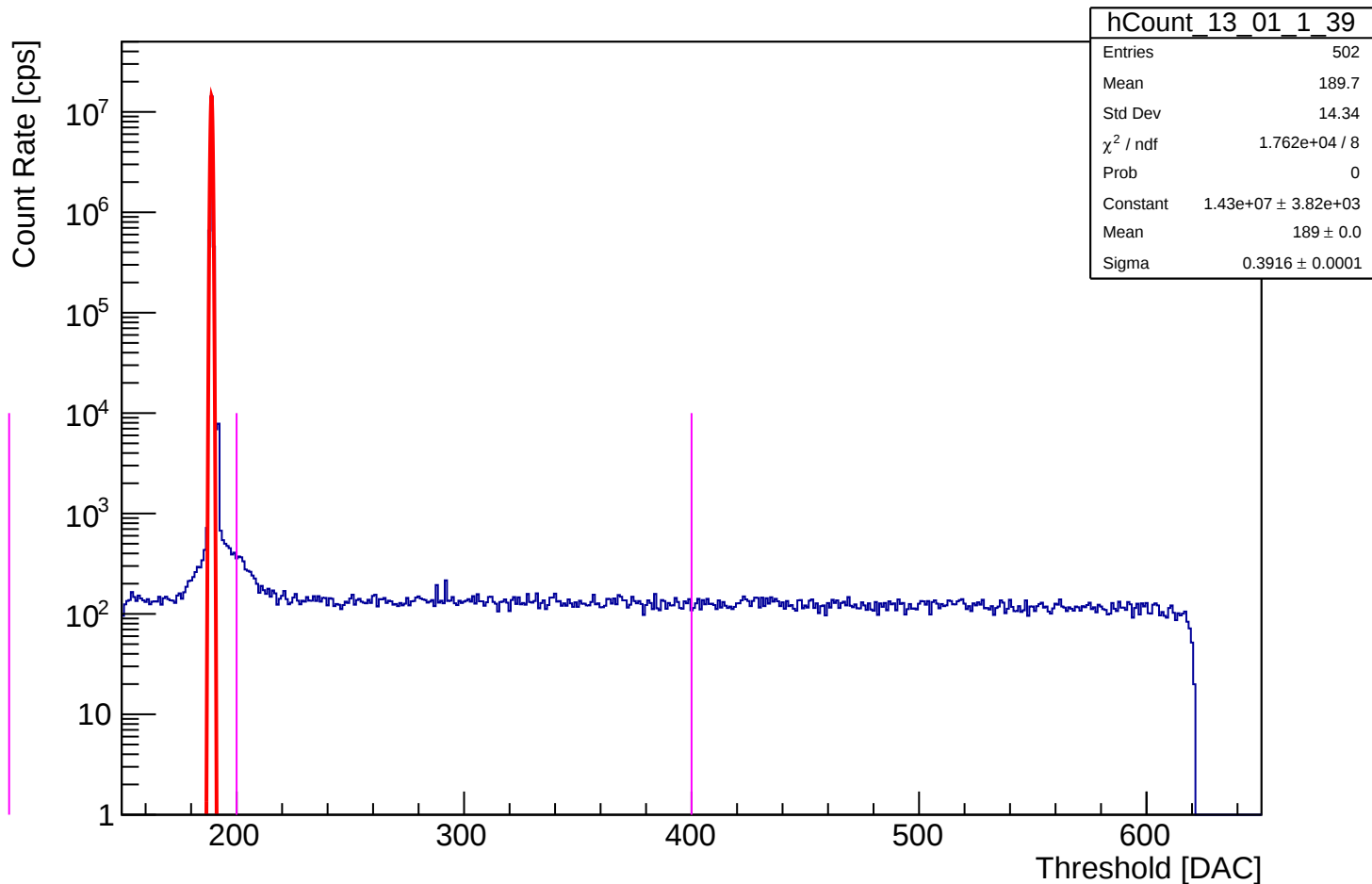
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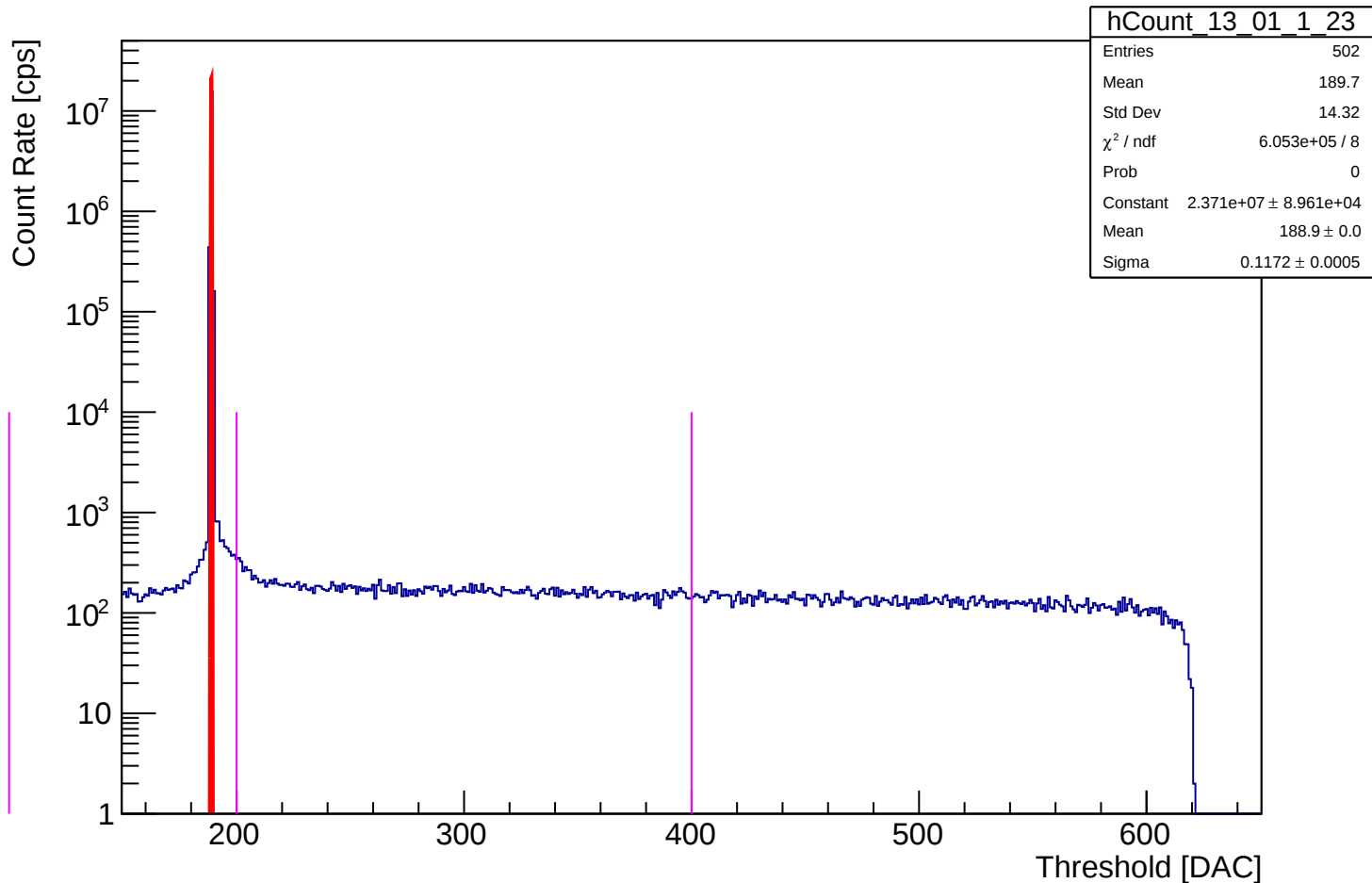
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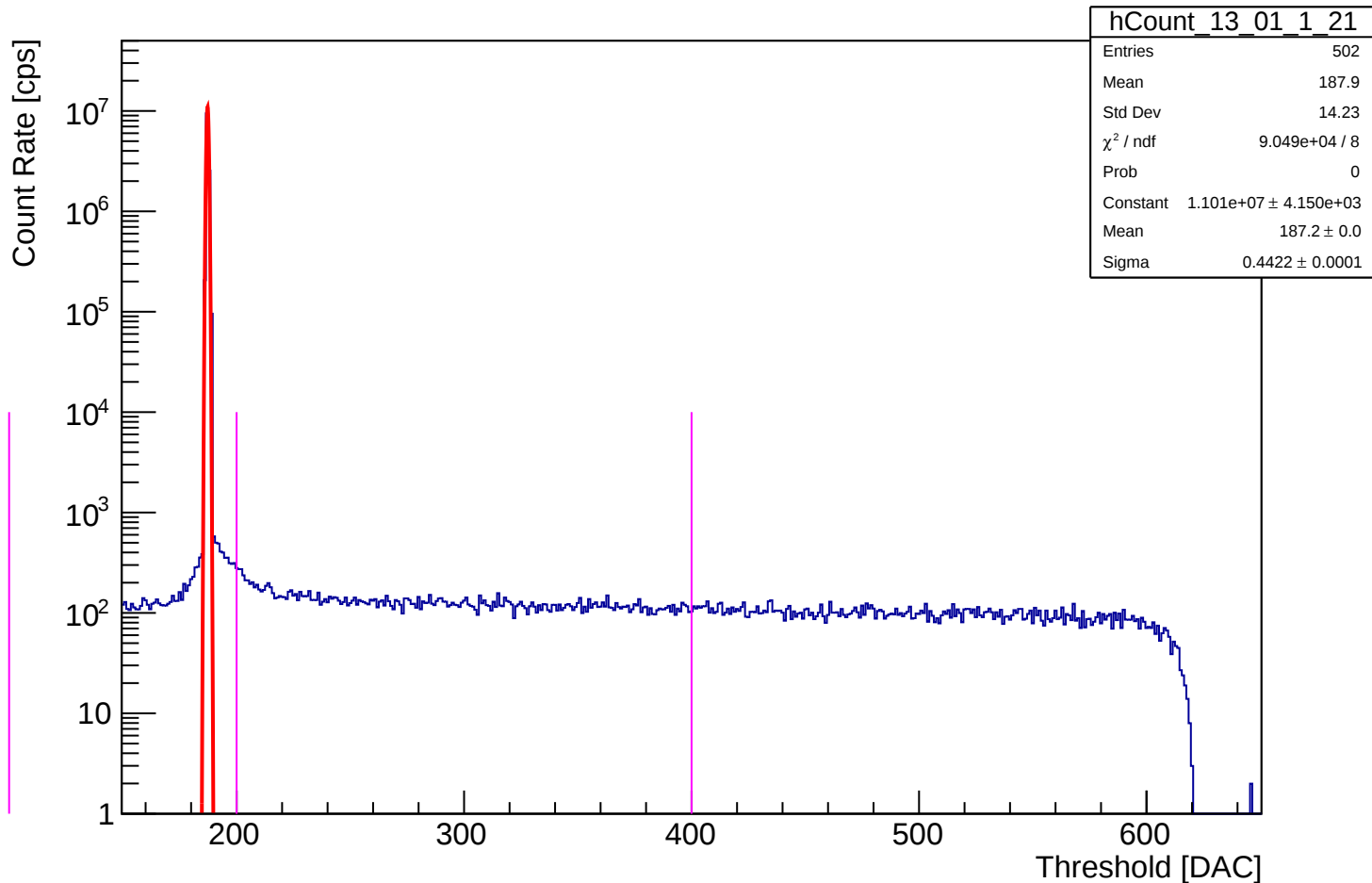
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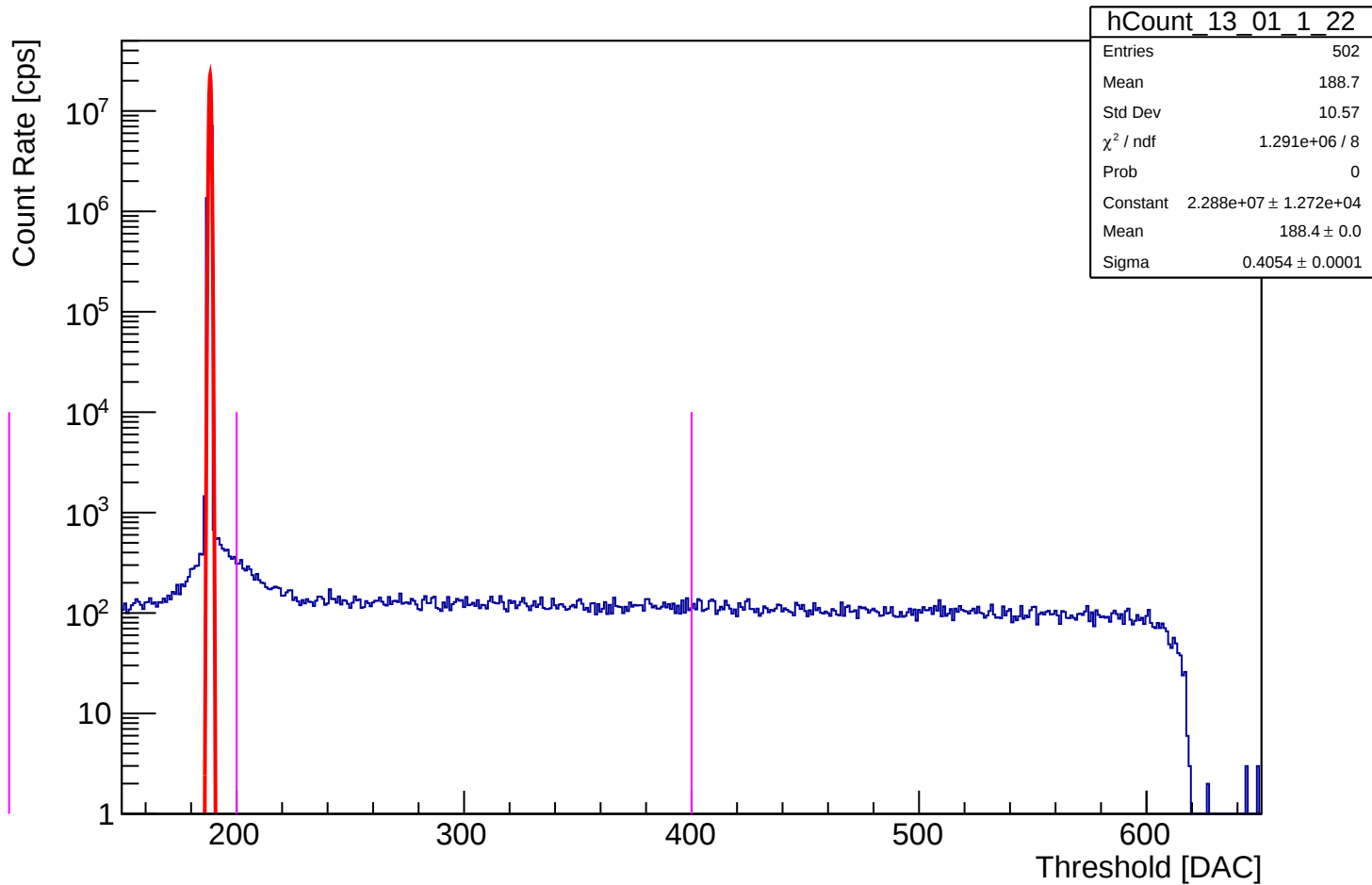
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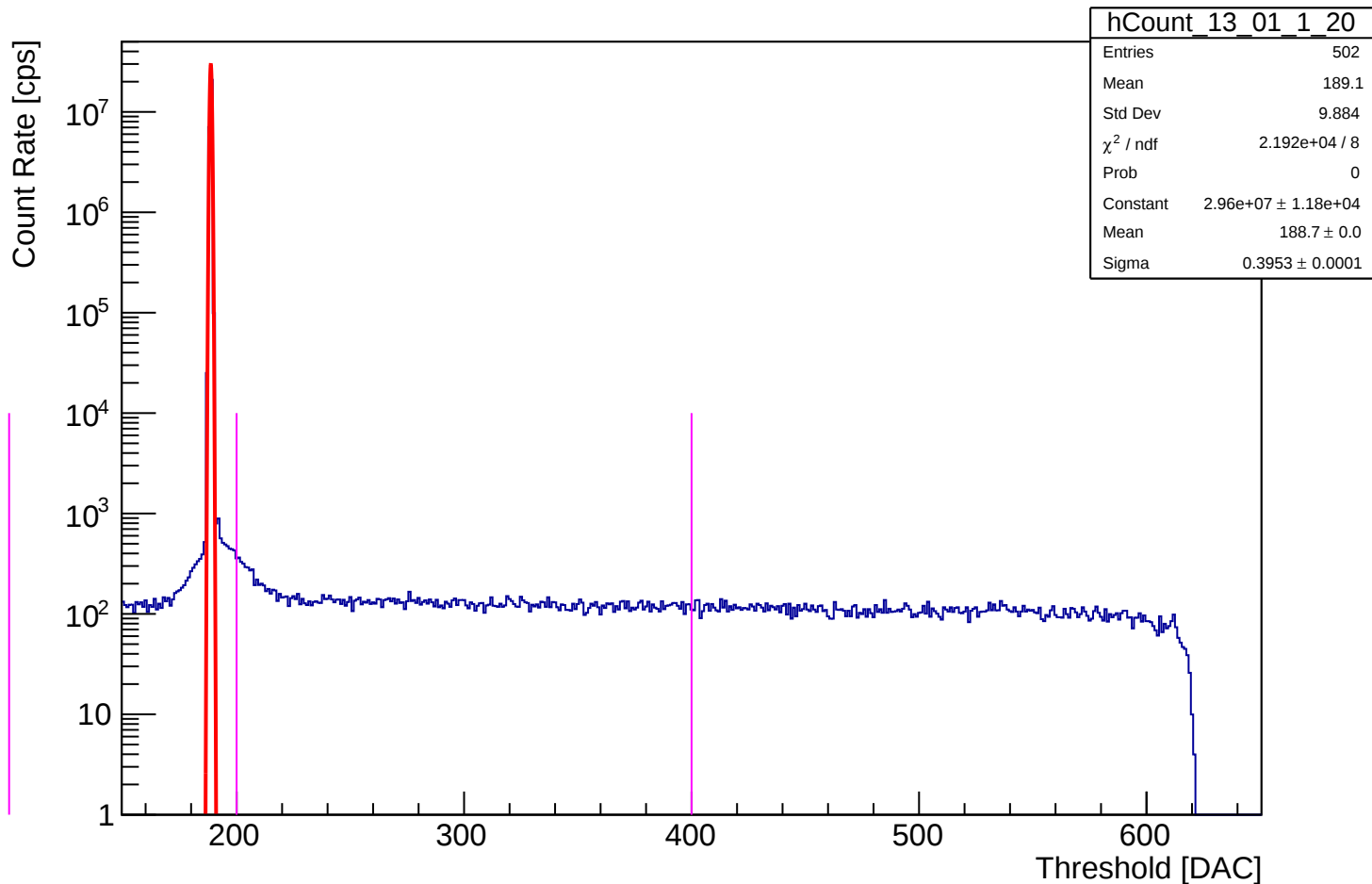
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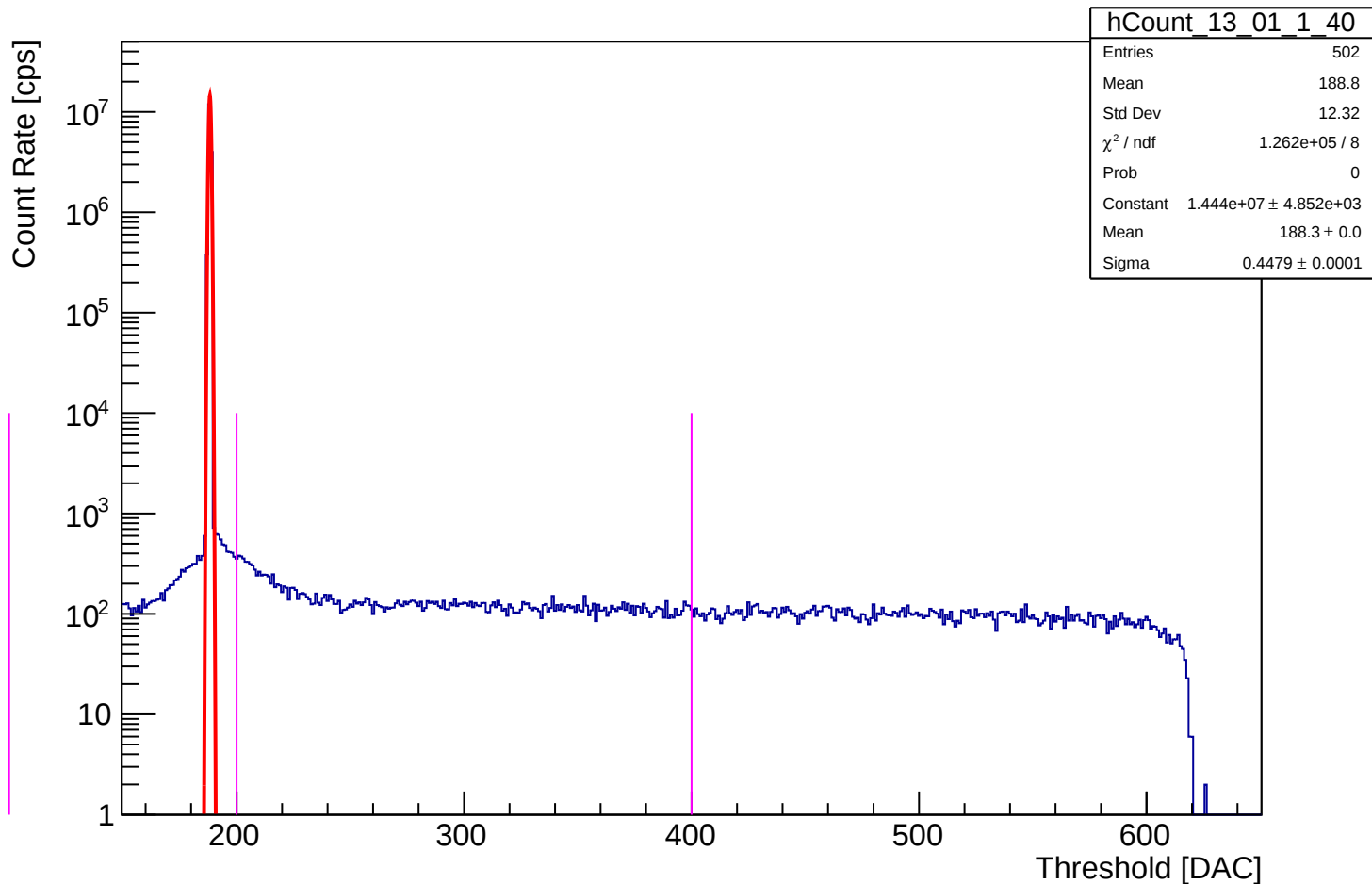
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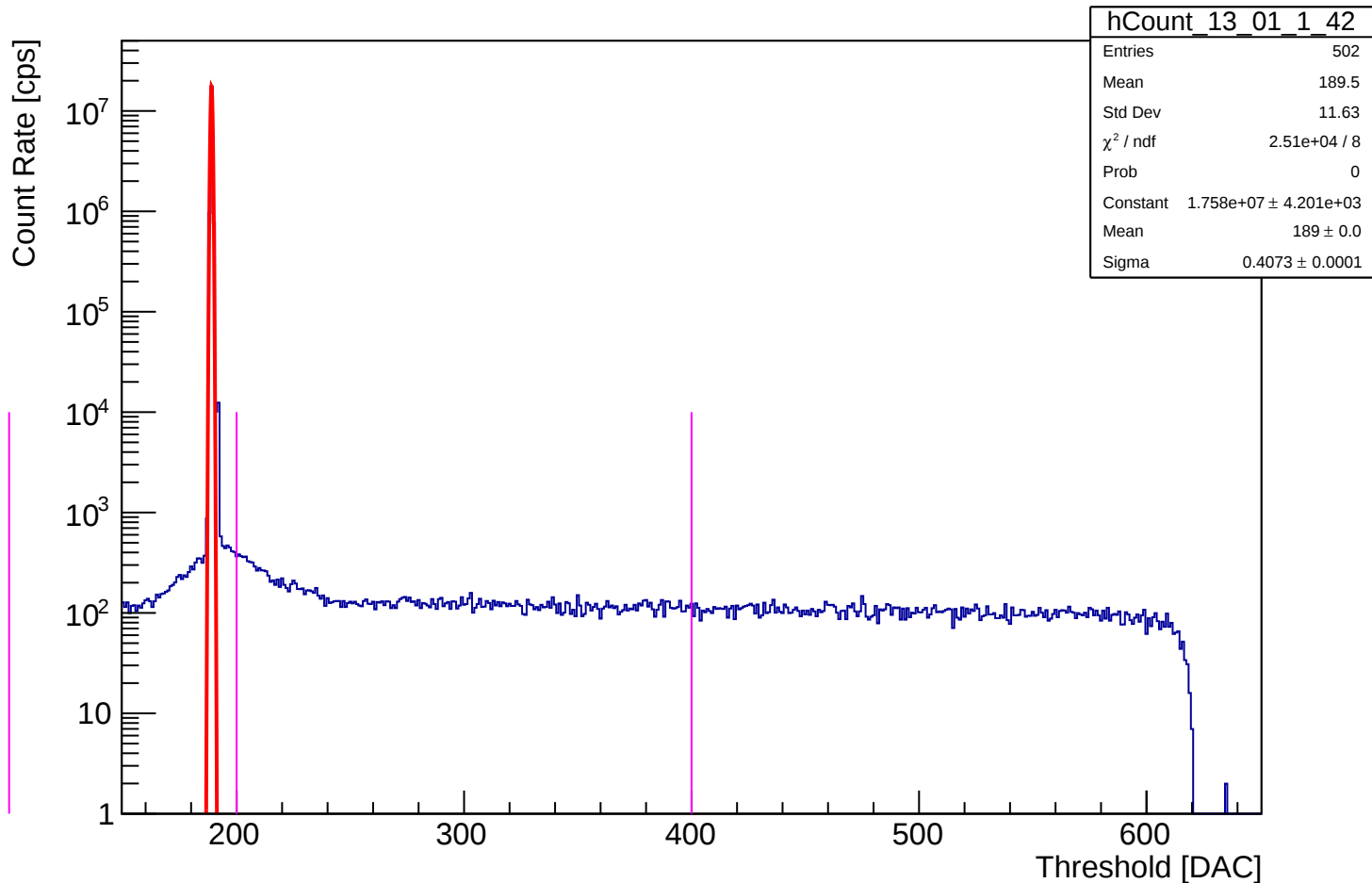
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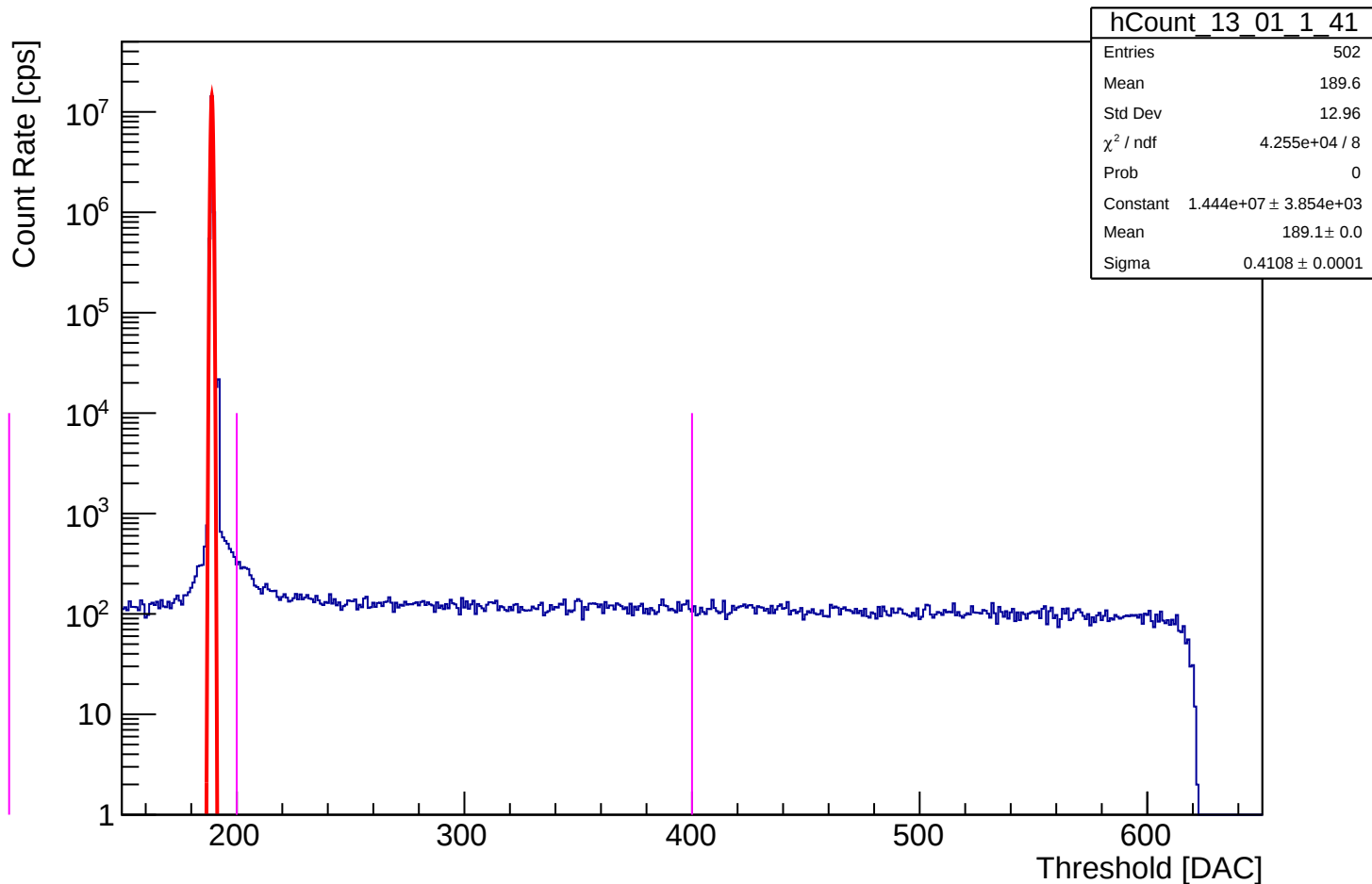
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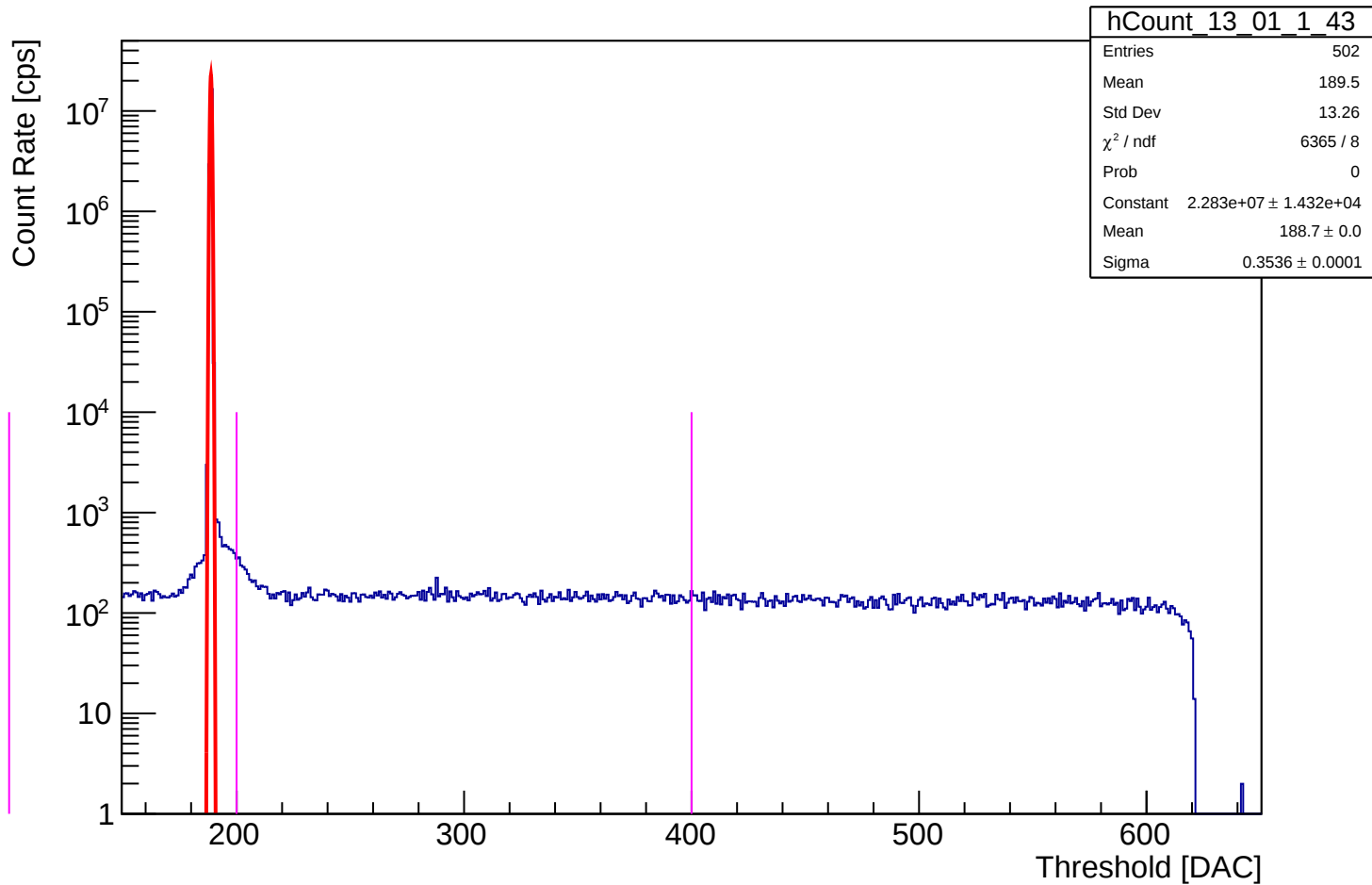
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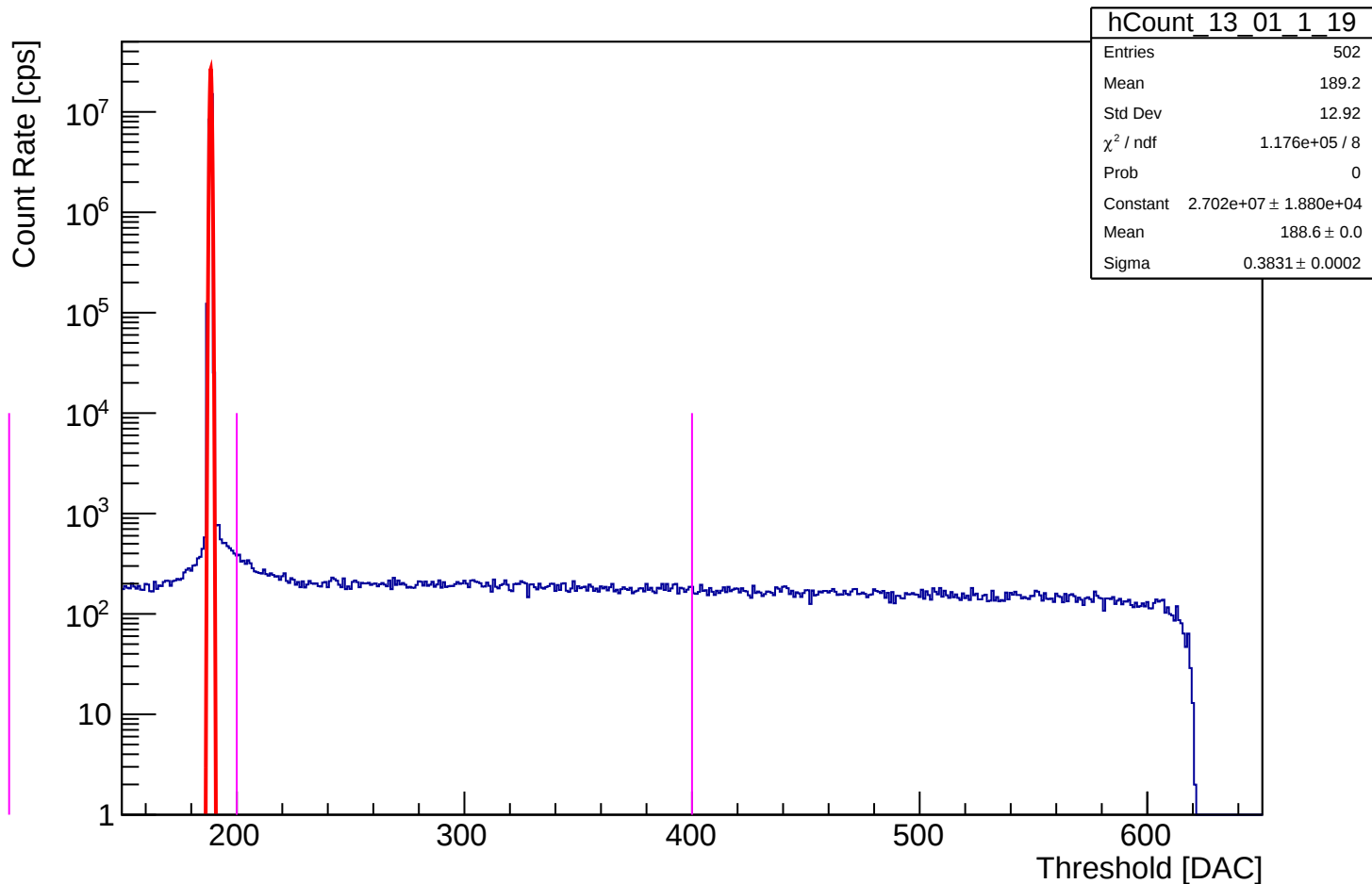


Row 1 Tile 1 Pmt 5 Pixel 23 Slot 13 Fiber 1 Asic 1 Channel 41

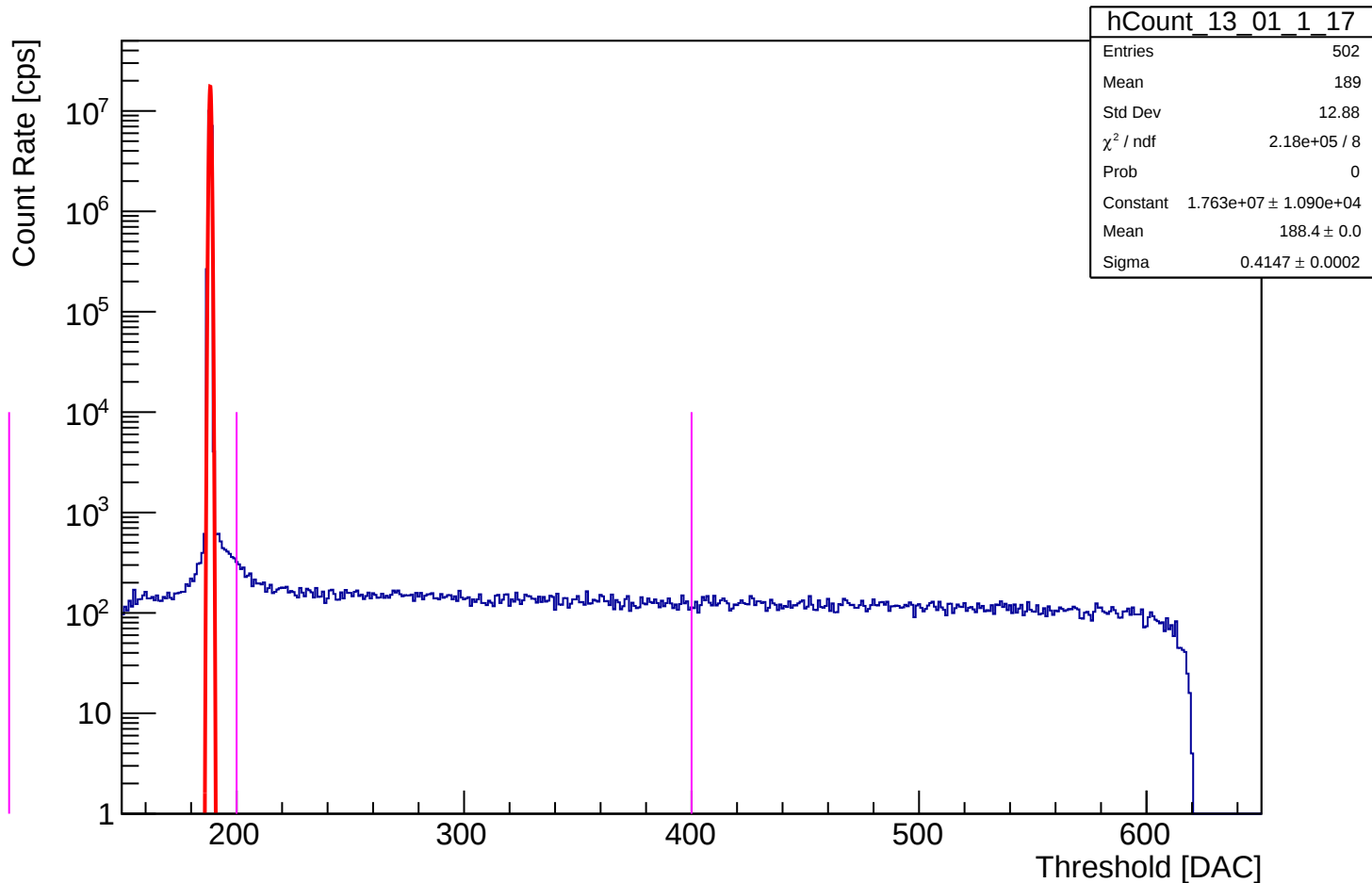


Row 1 Tile 1 Pmt 5 Pixel 24 Slot 13 Fiber 1 Asic 1 Channel 43

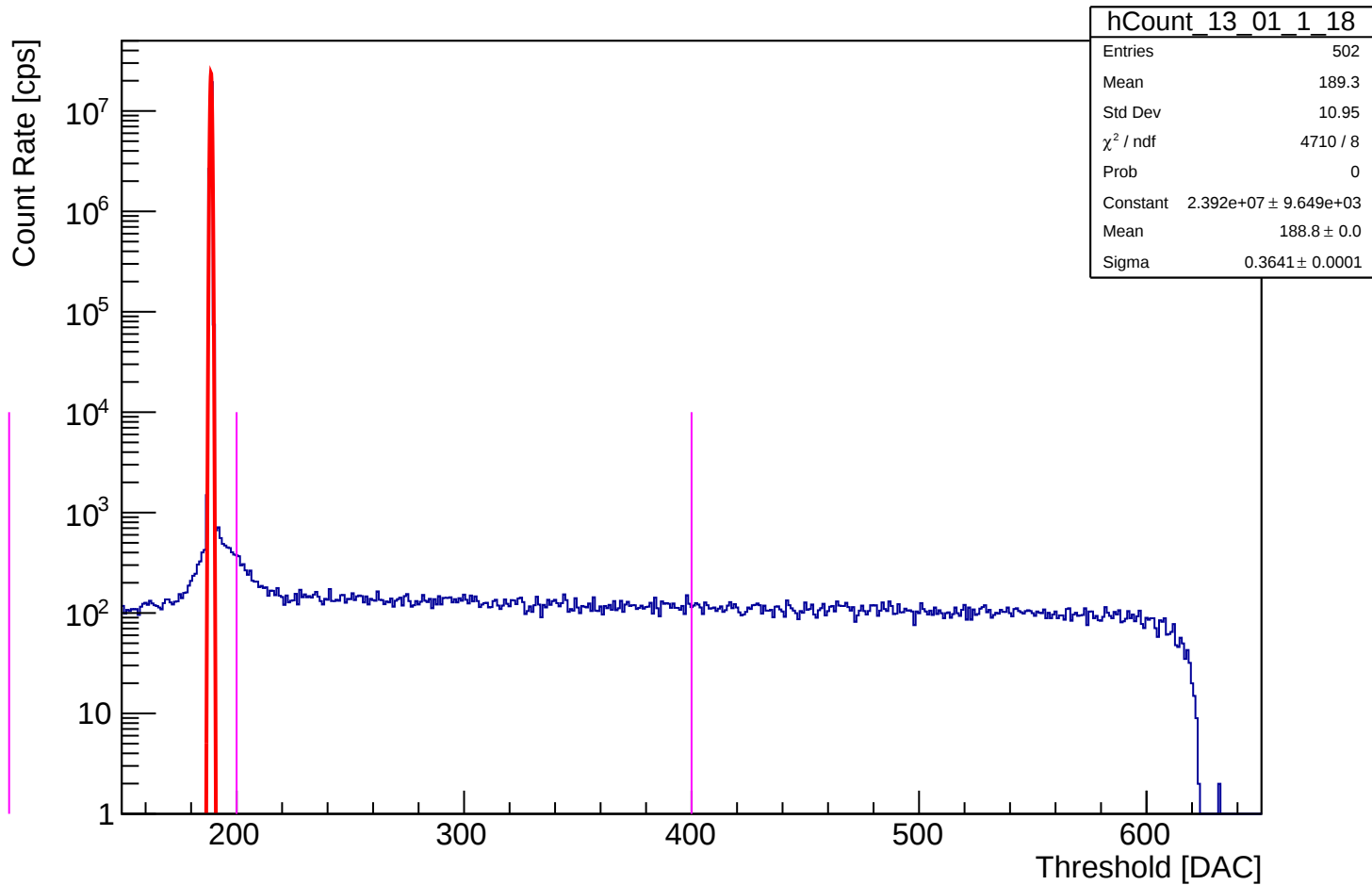




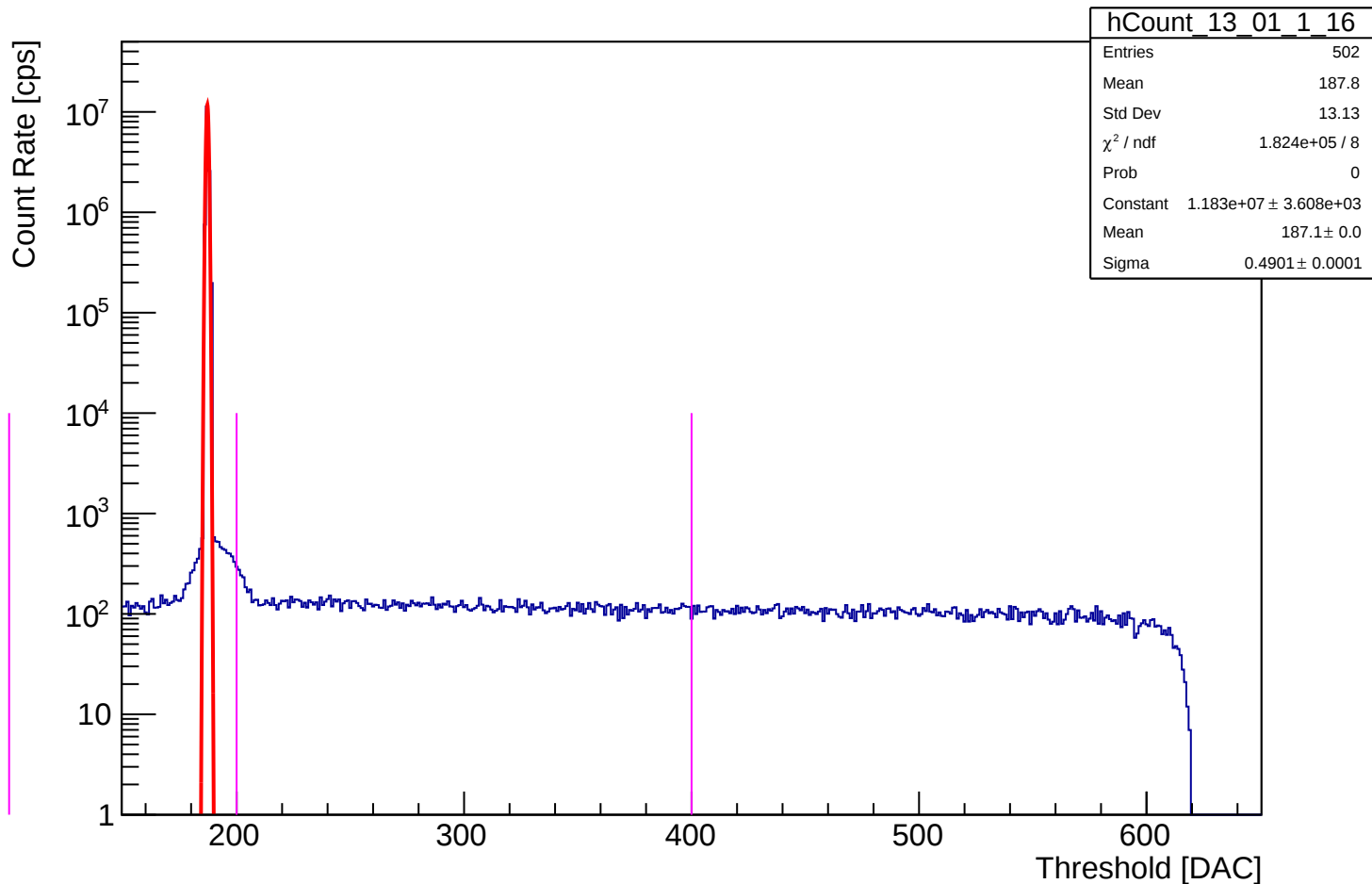
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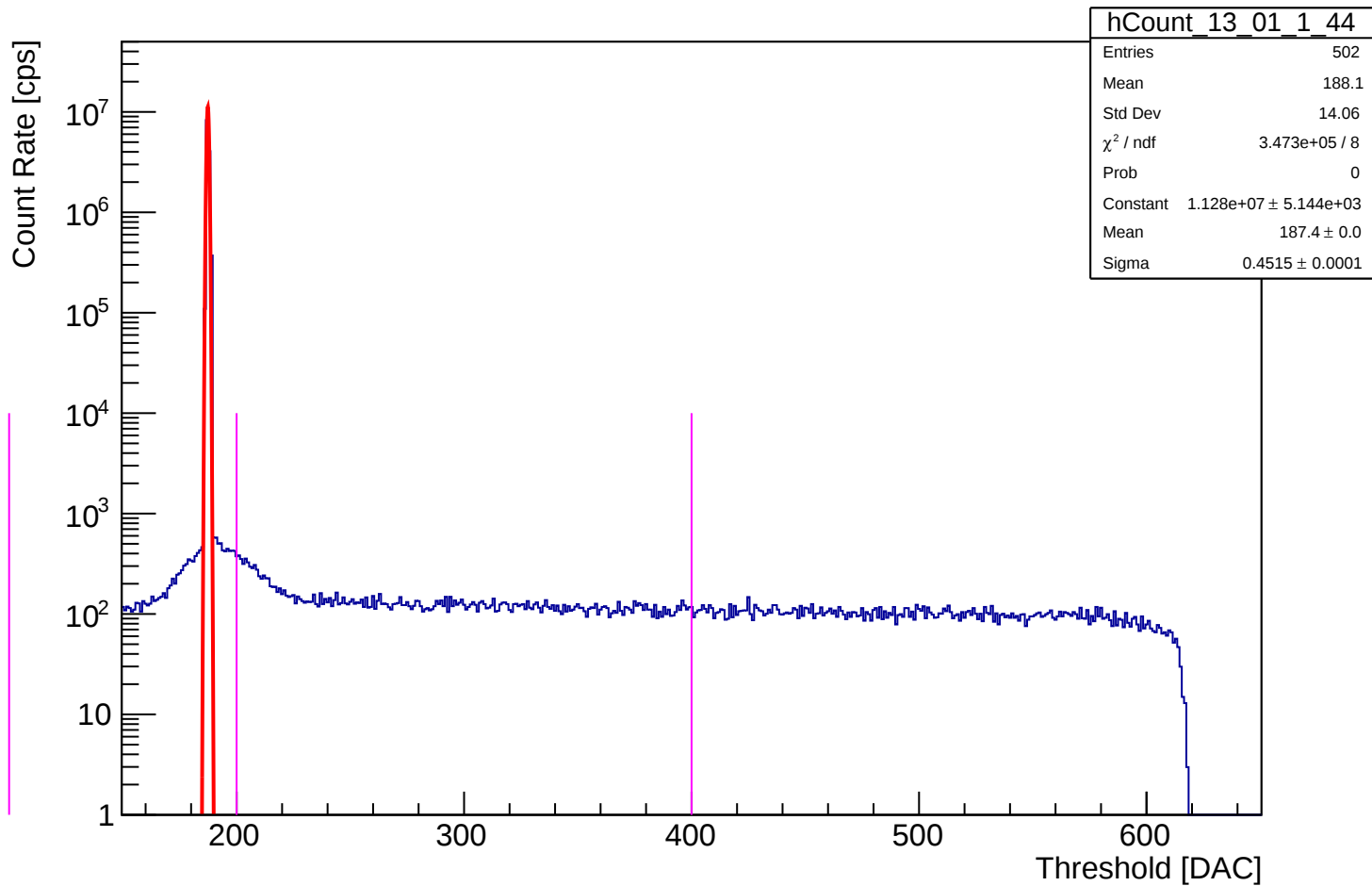
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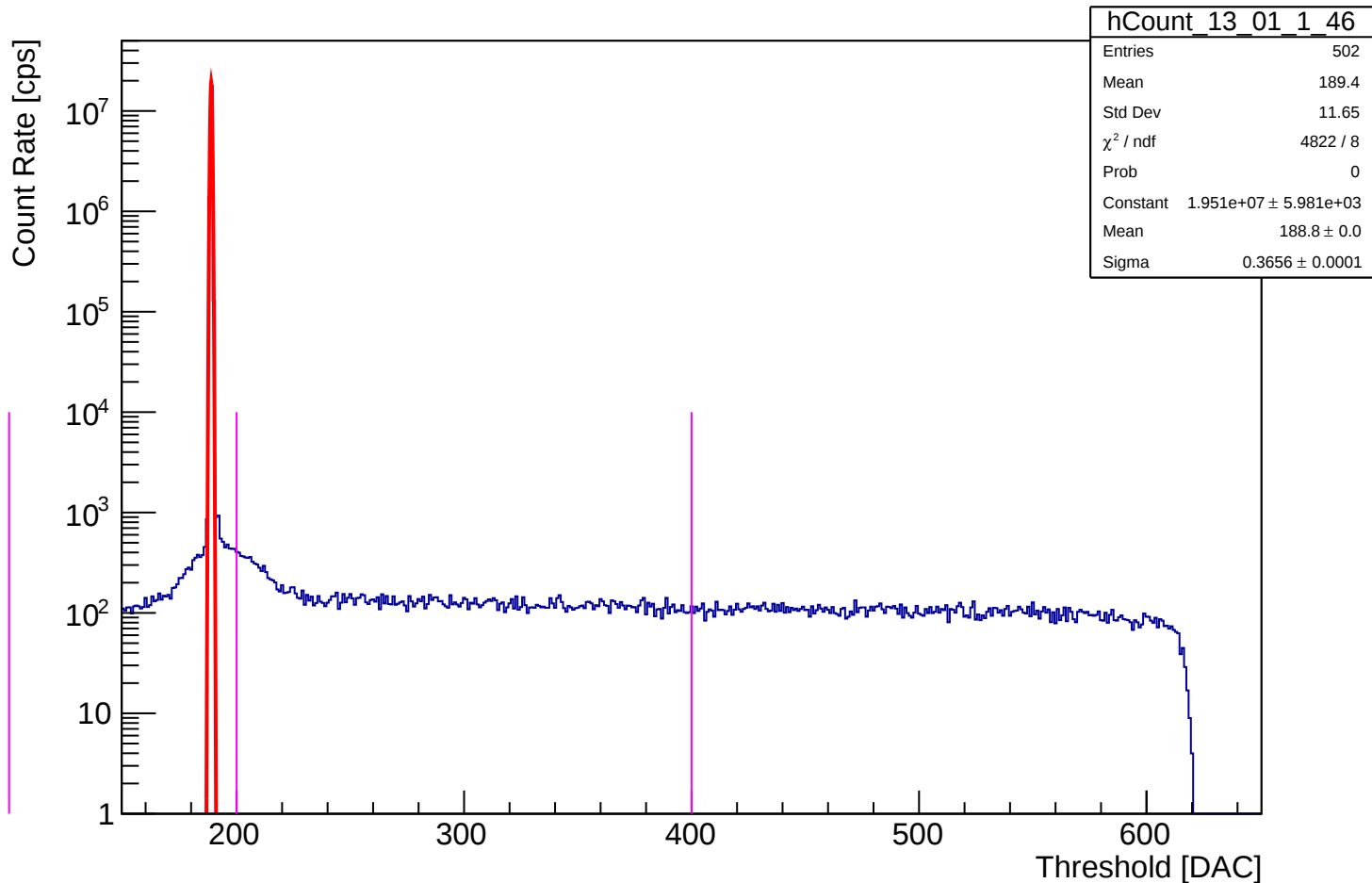
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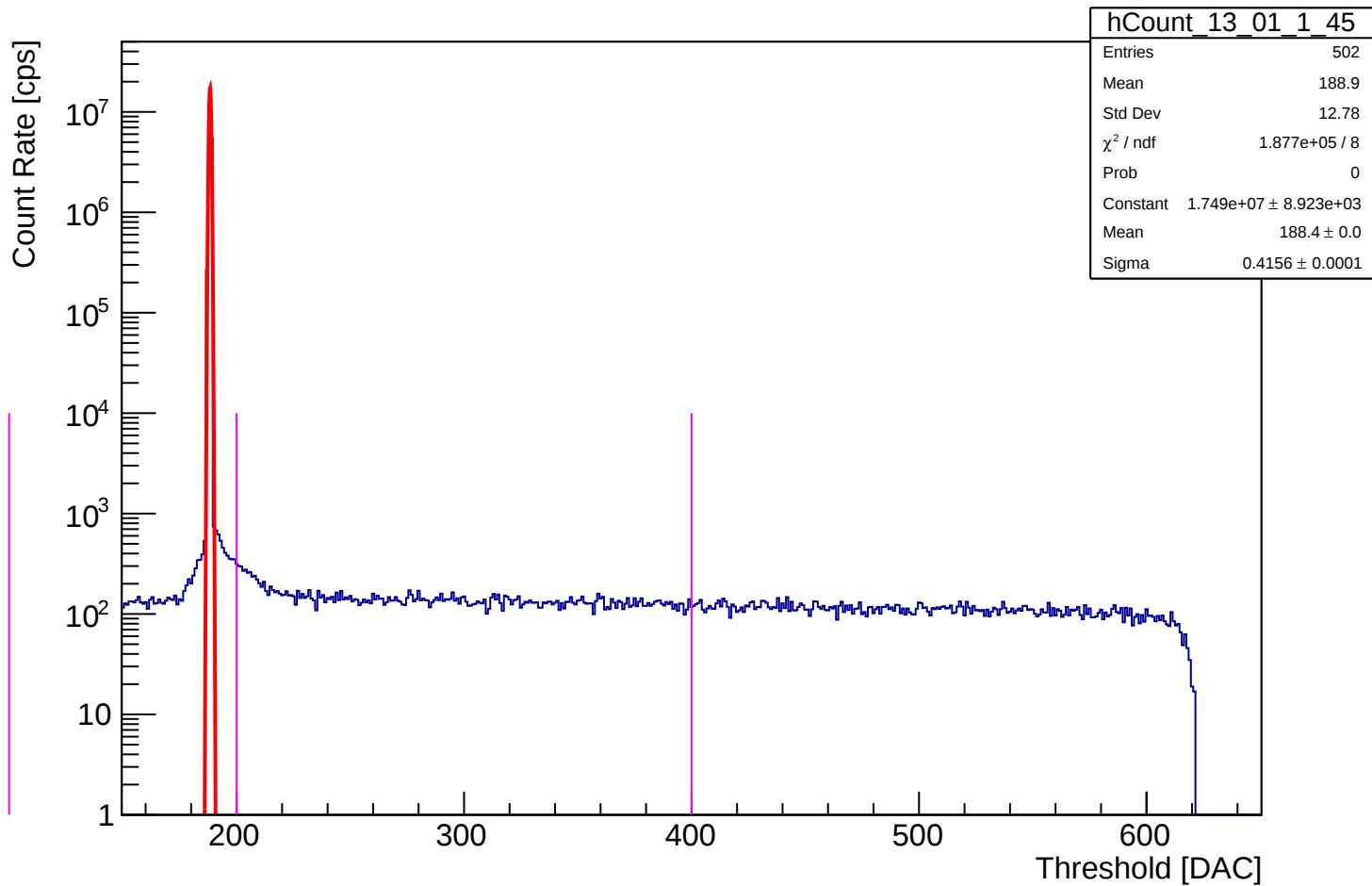
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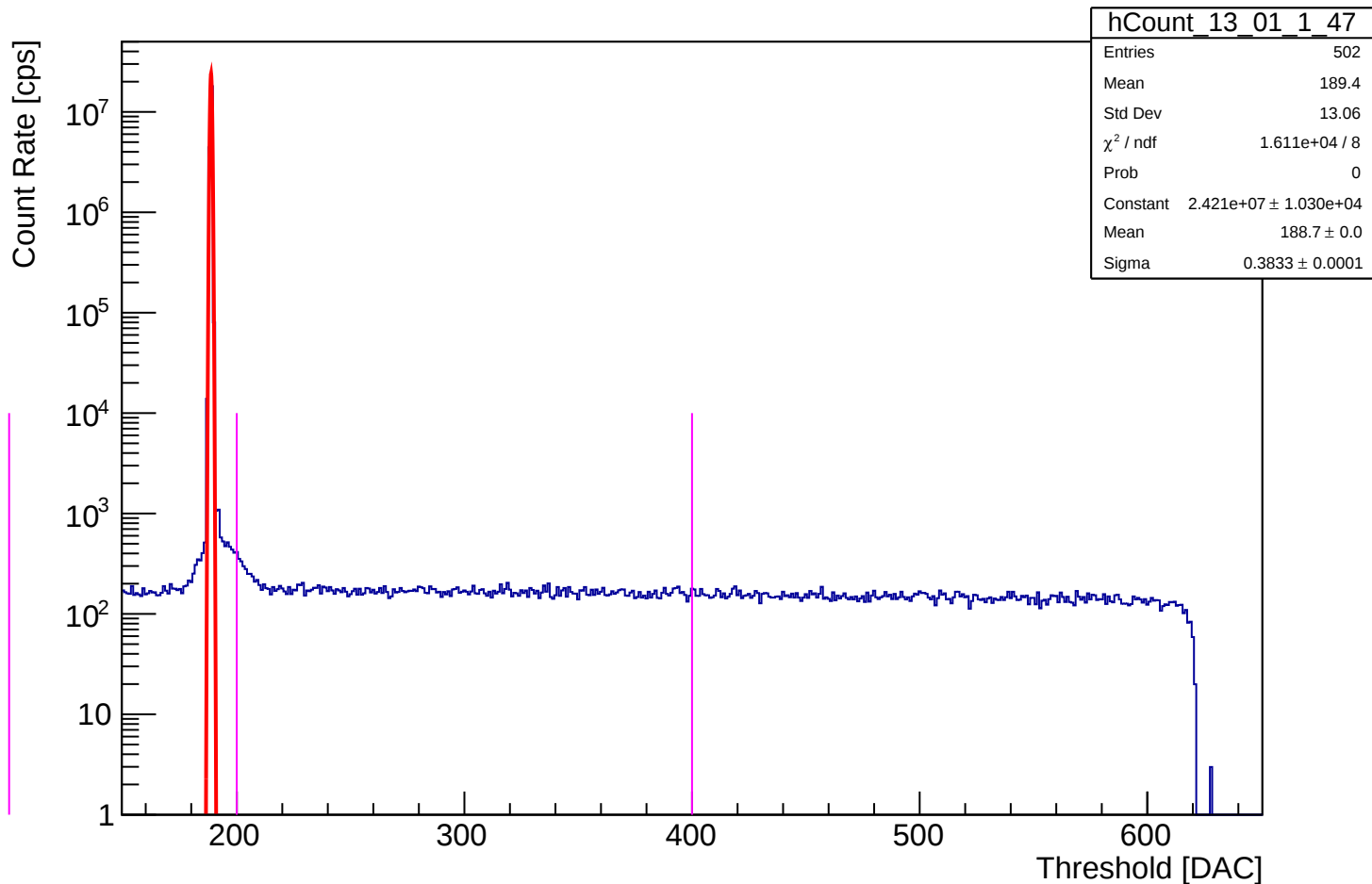
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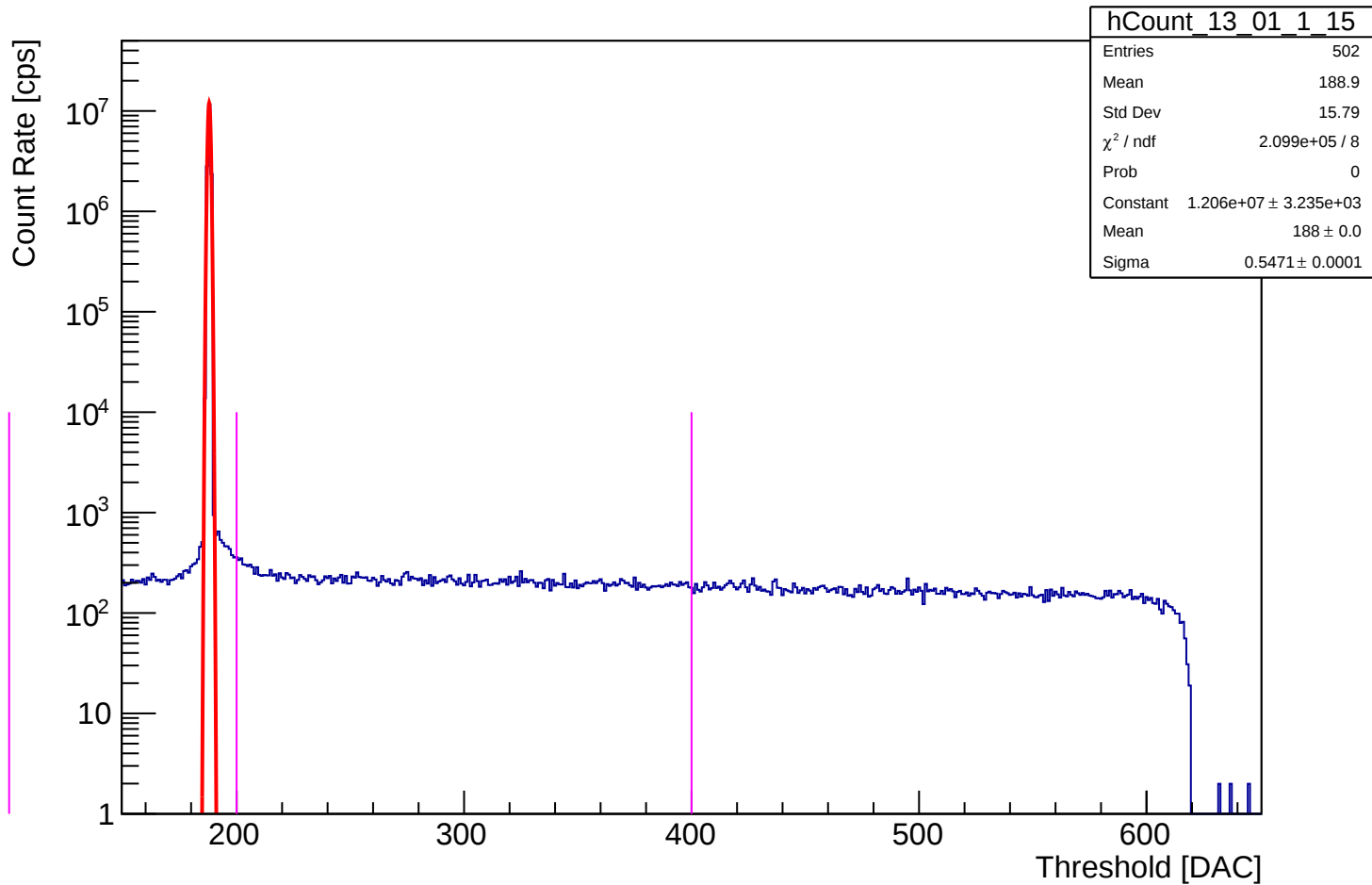
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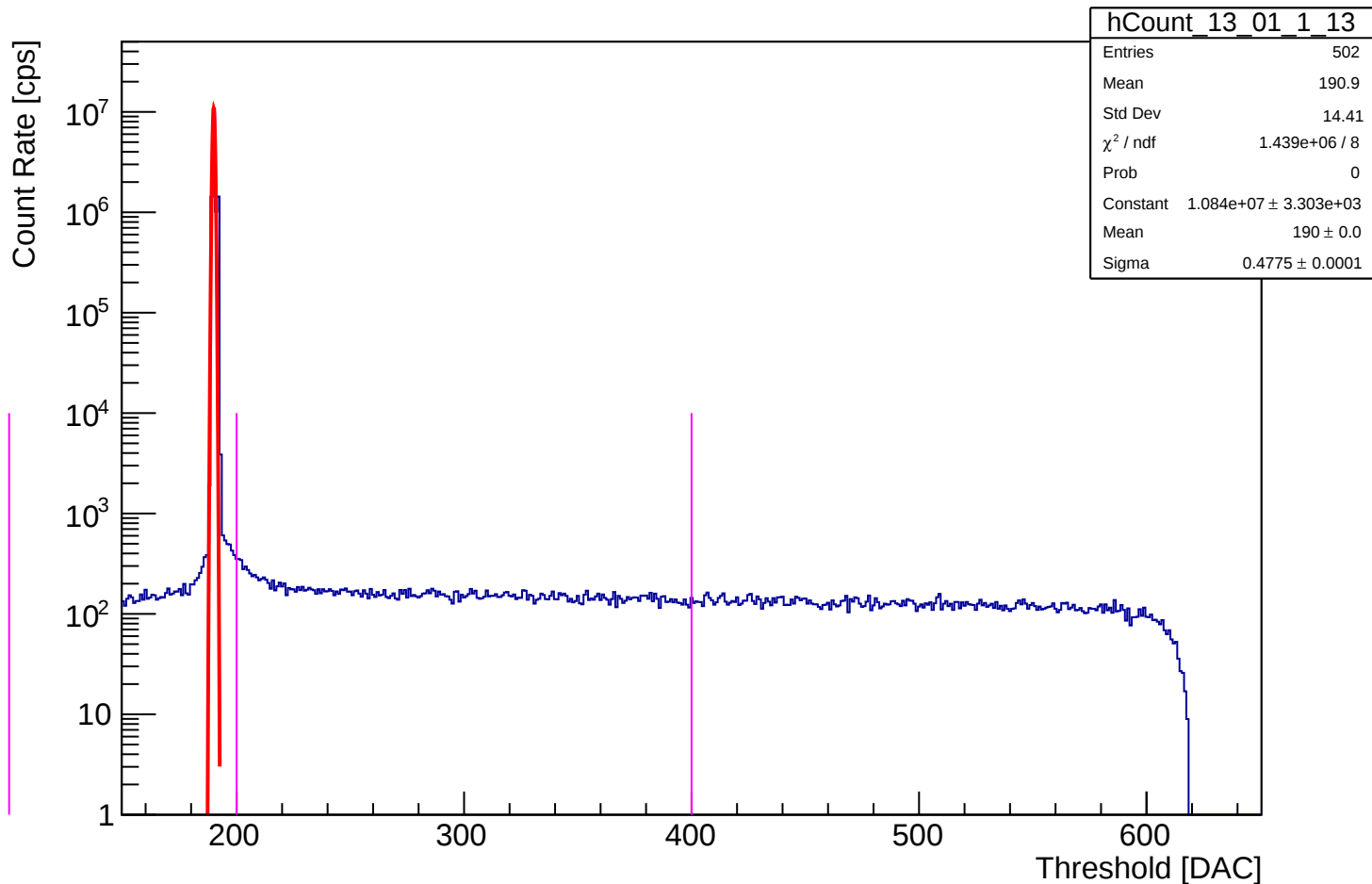
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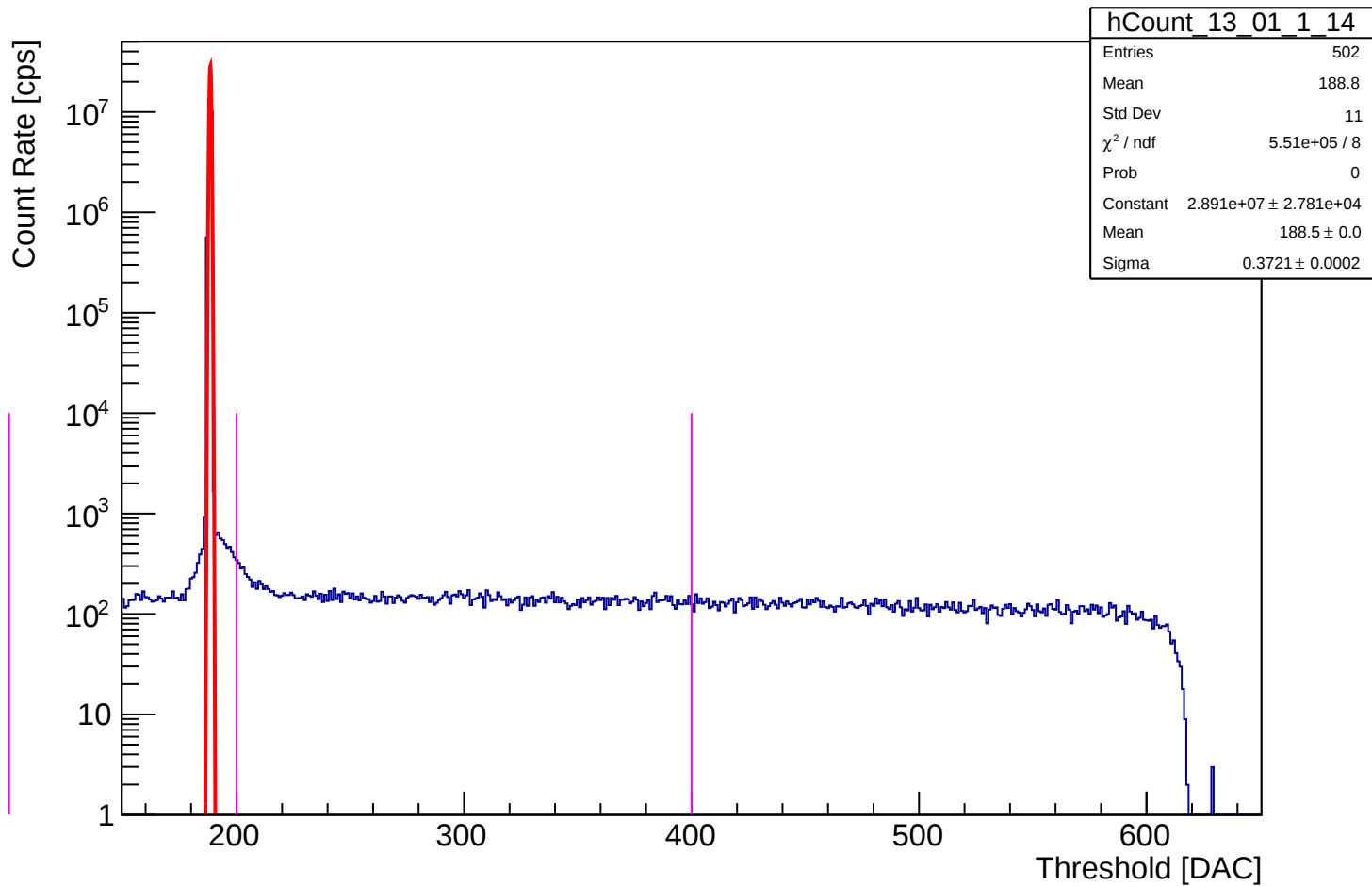
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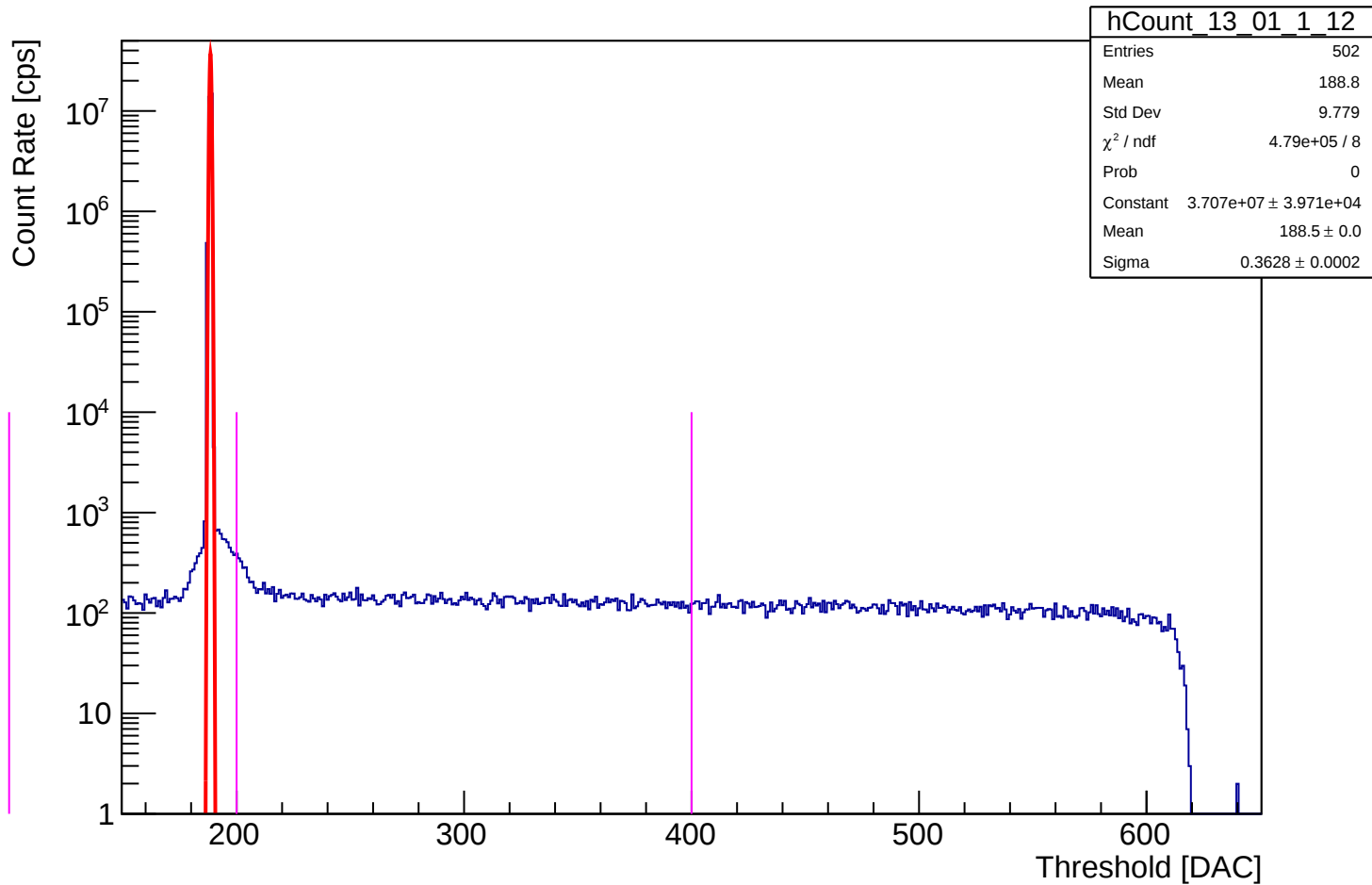
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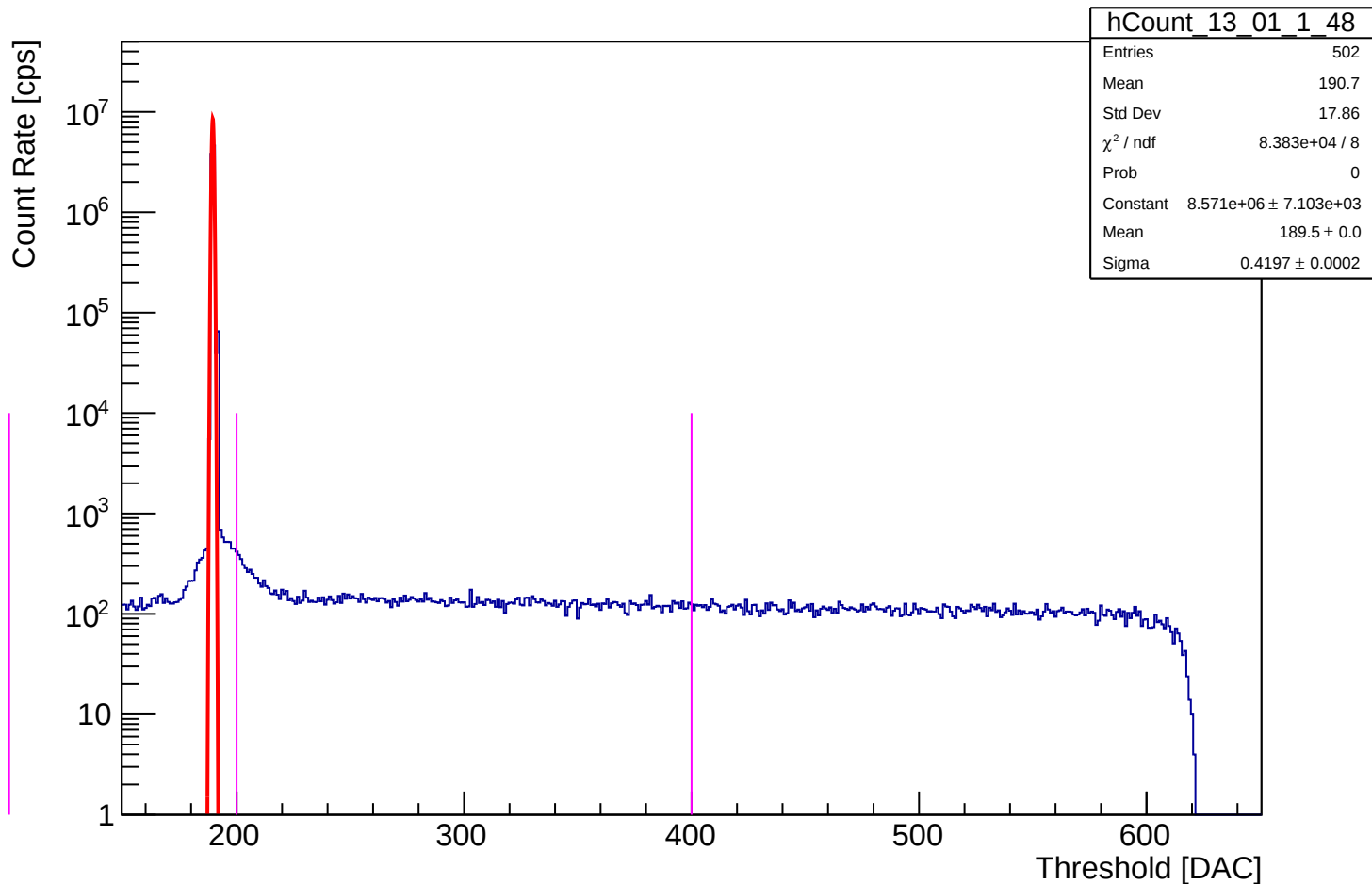
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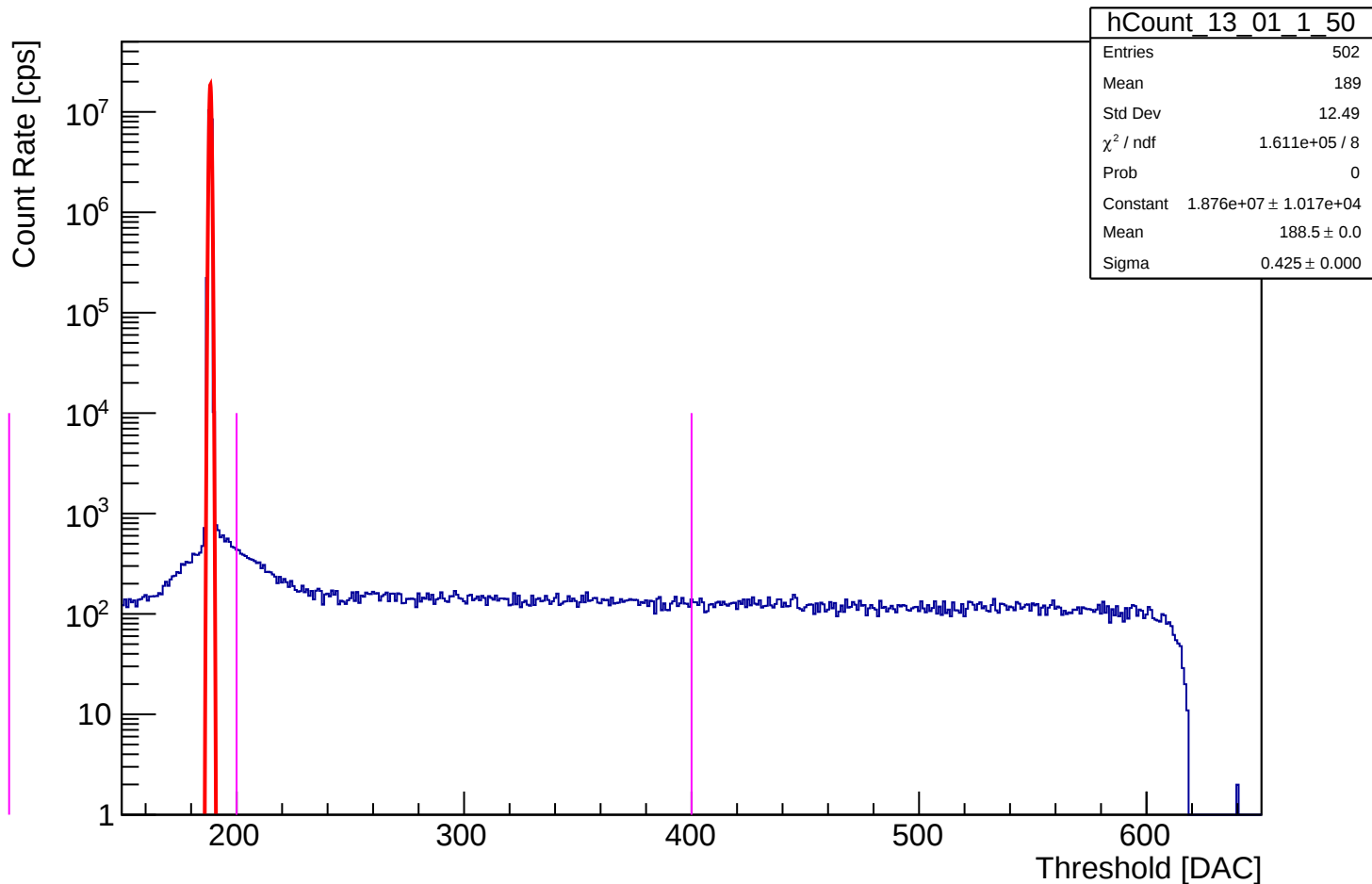
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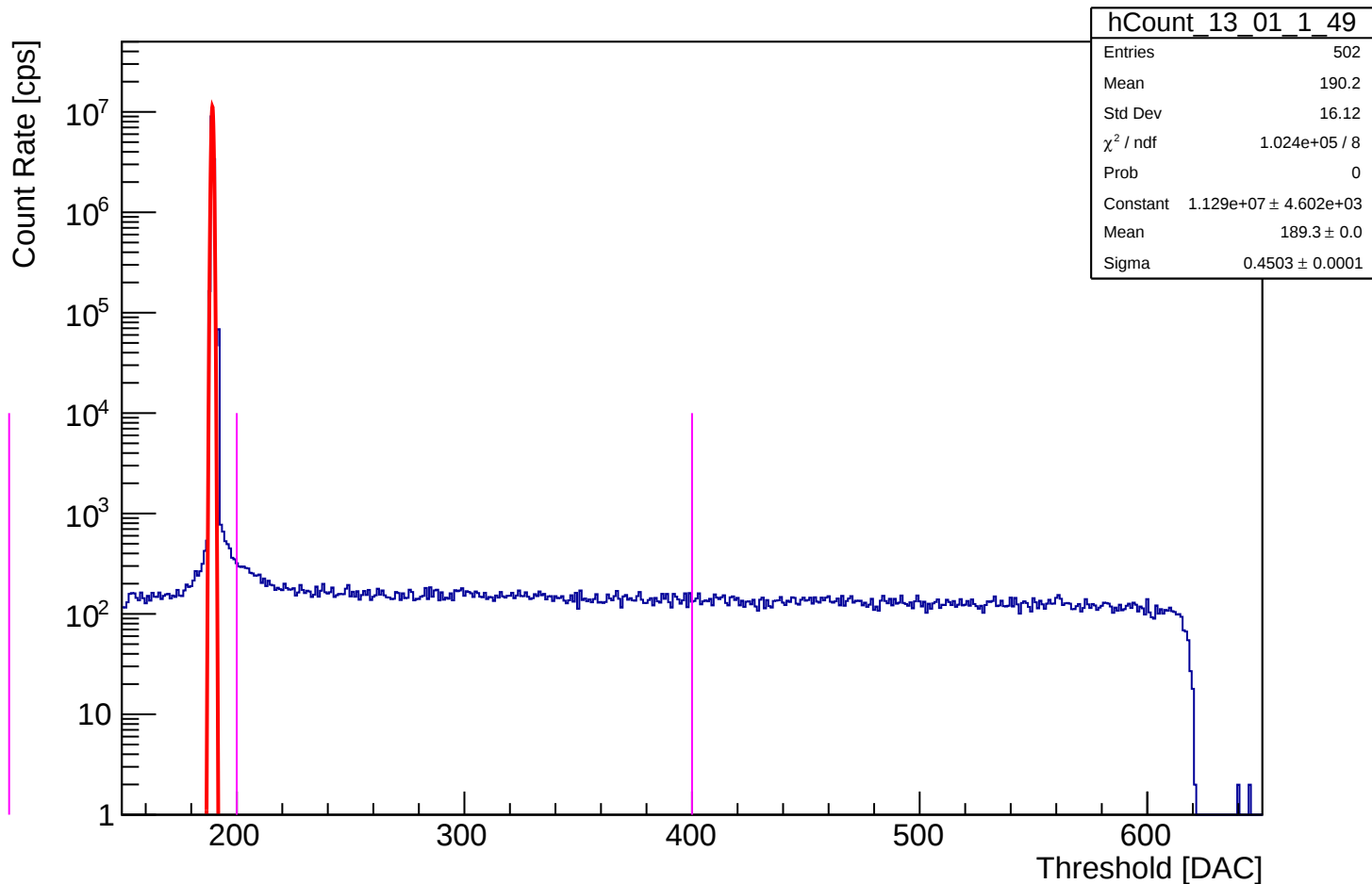


Row 1 Tile 1 Pmt 5 Pixel 37 Slot 13 Fiber 1 Asic 1 Channel 48

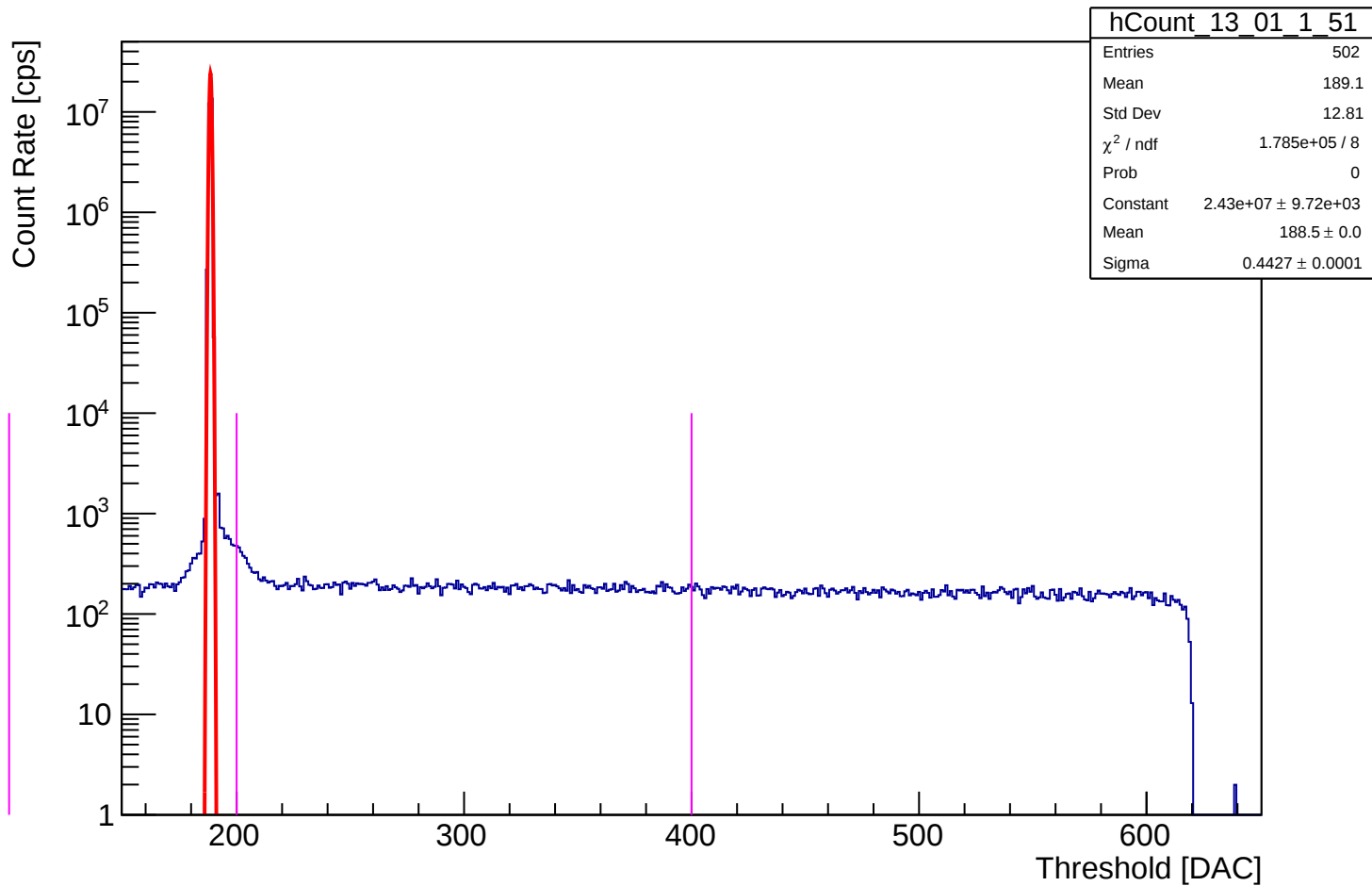


Row 1 Tile 1 Pmt 5 Pixel 38 Slot 13 Fiber 1 Asic 1 Channel 50

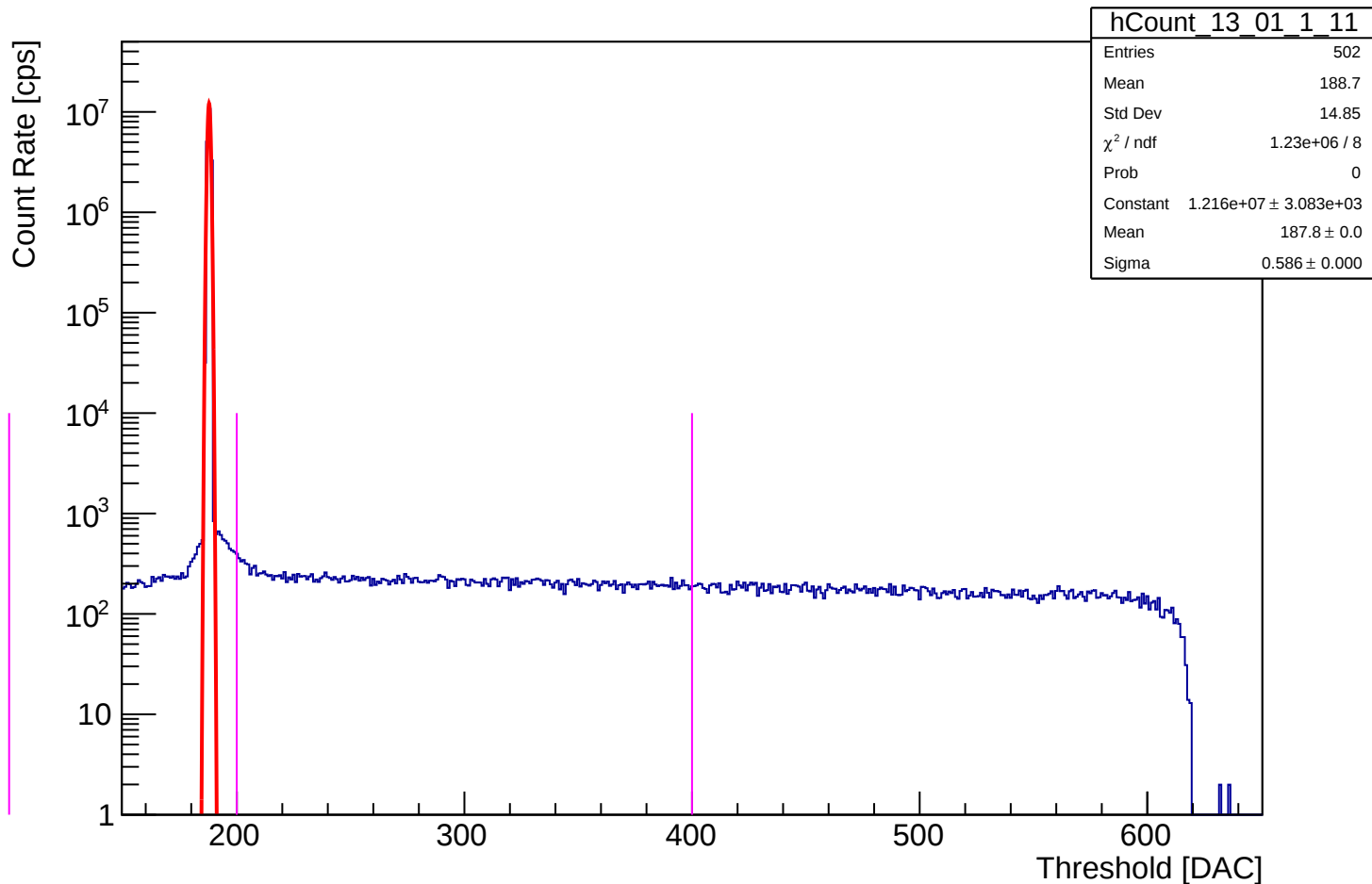


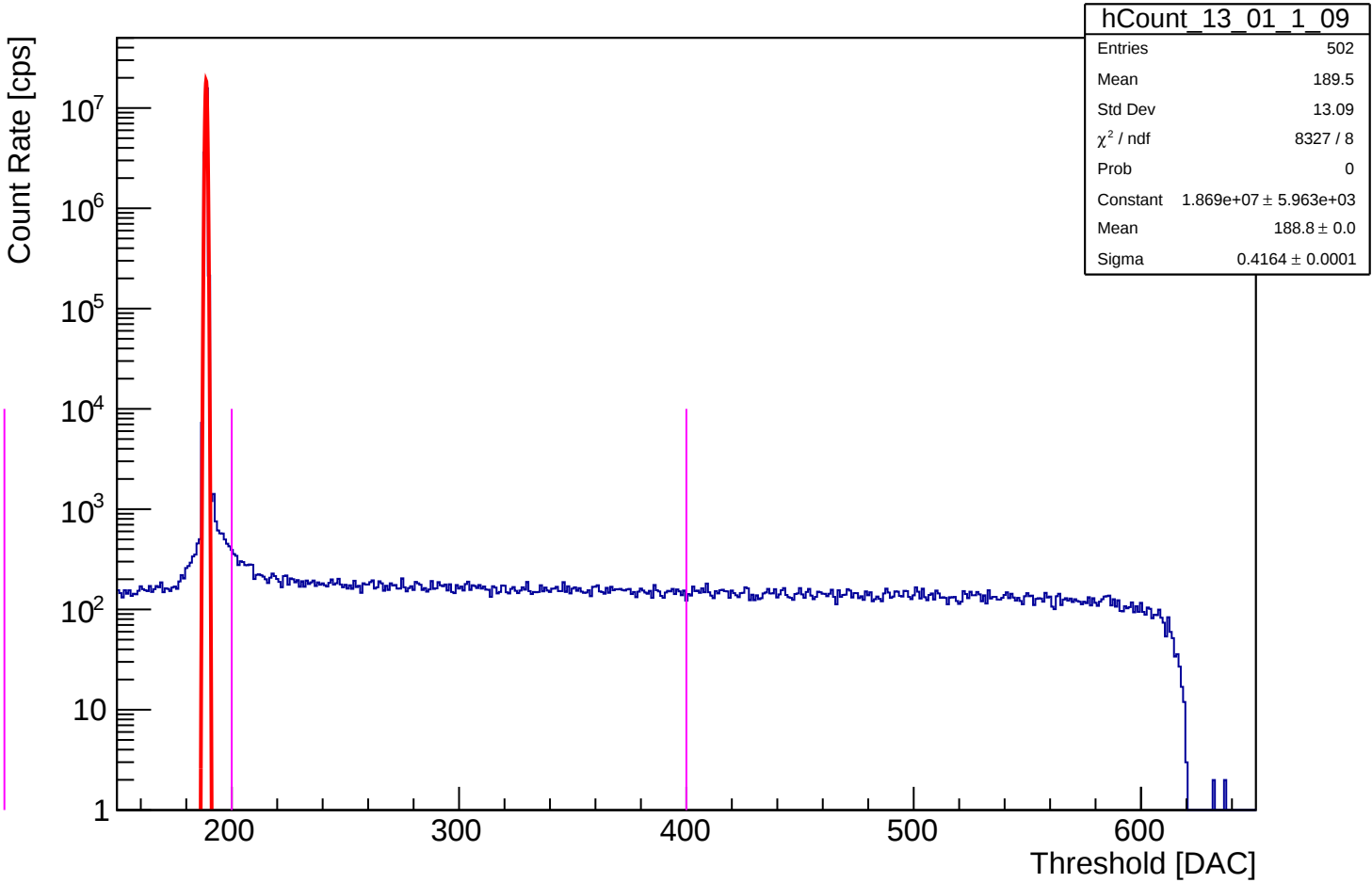


Row 1 Tile 1 Pmt 5 Pixel 40 Slot 13 Fiber 1 Asic 1 Channel 51

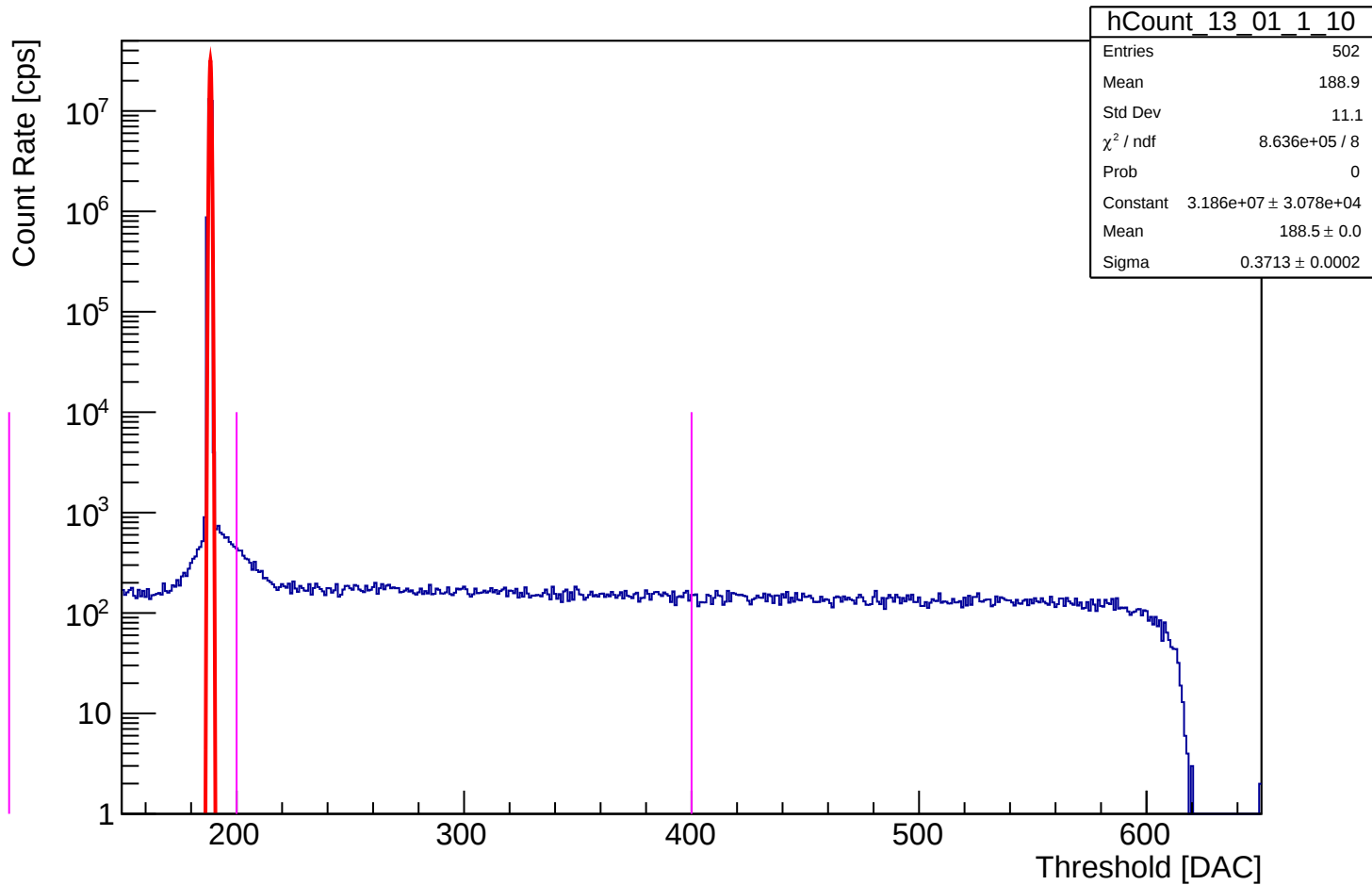


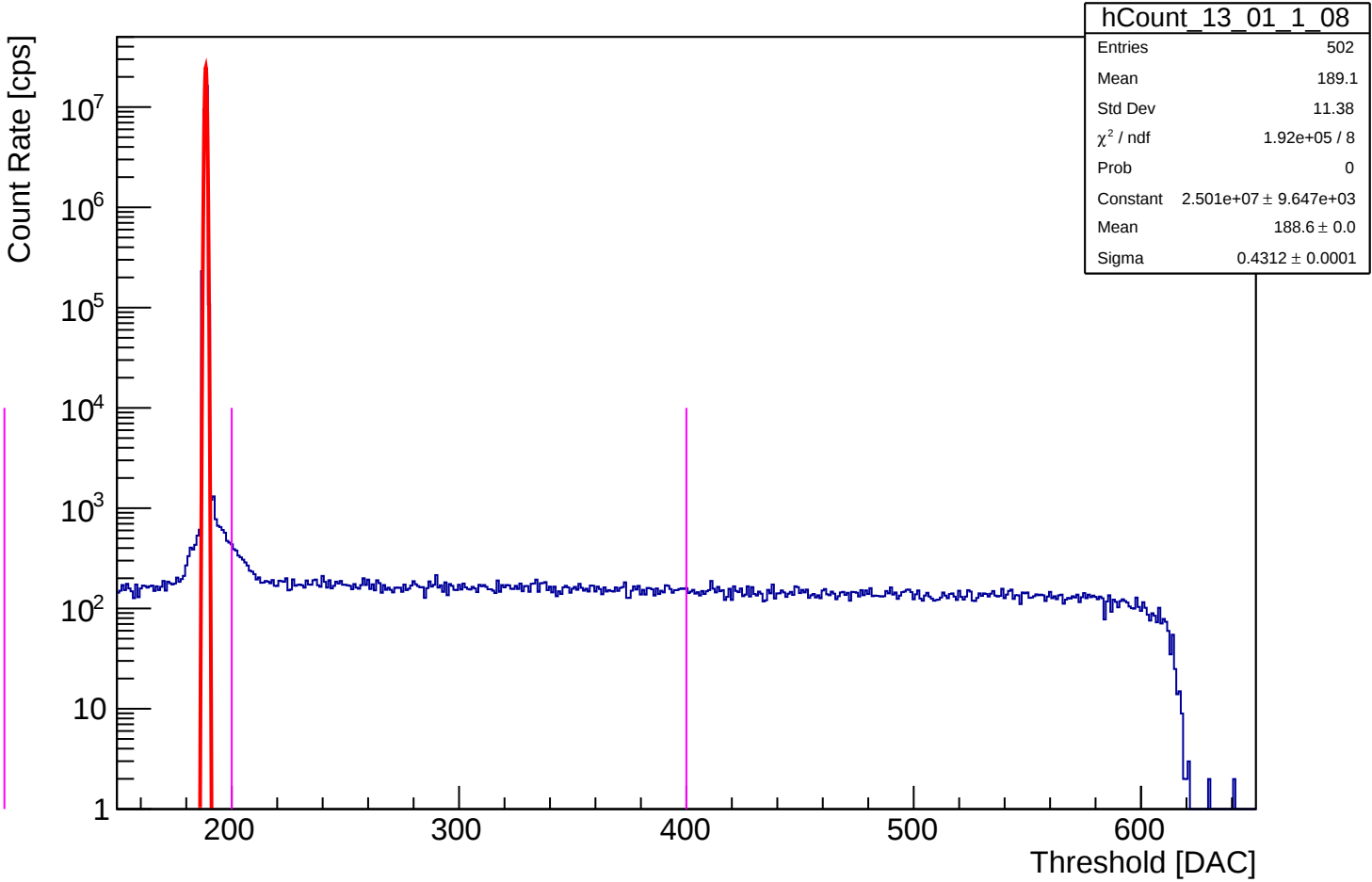
Row 1 Tile 1 Pmt 5 Pixel 41 Slot 13 Fiber 1 Asic 1 Channel 11



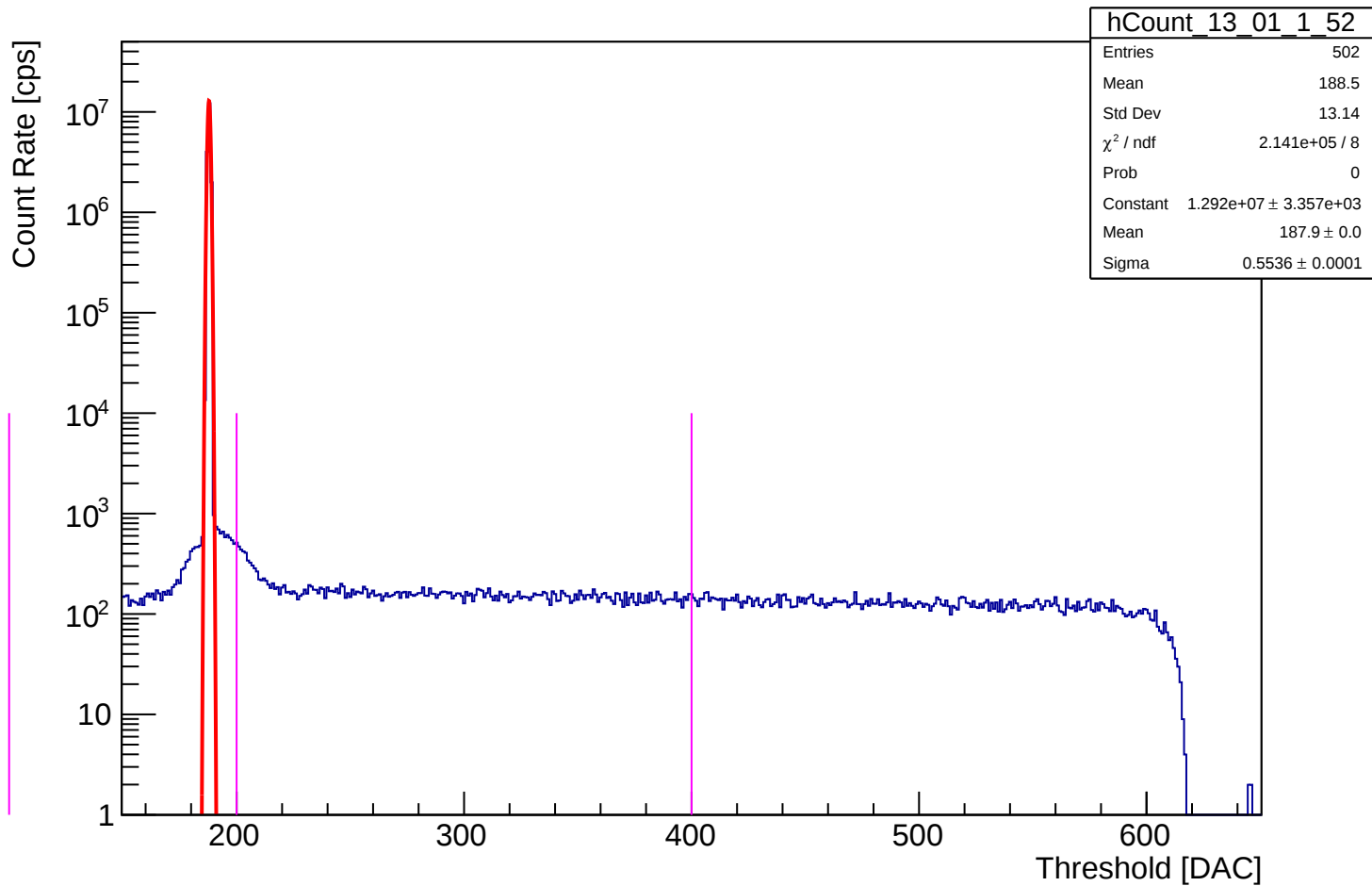


Row 1 Tile 1 Pmt 5 Pixel 43 Slot 13 Fiber 1 Asic 1 Channel 10

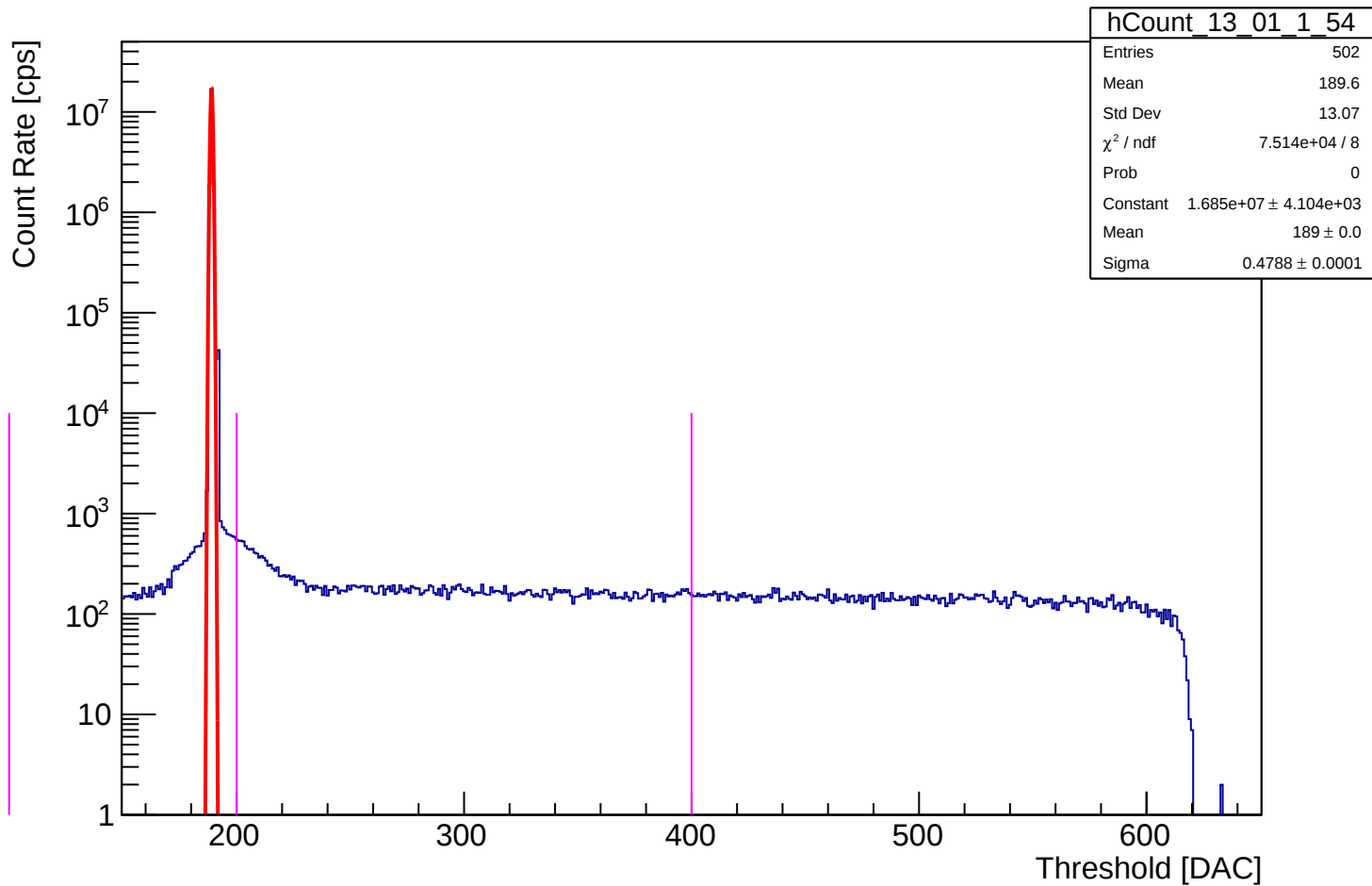




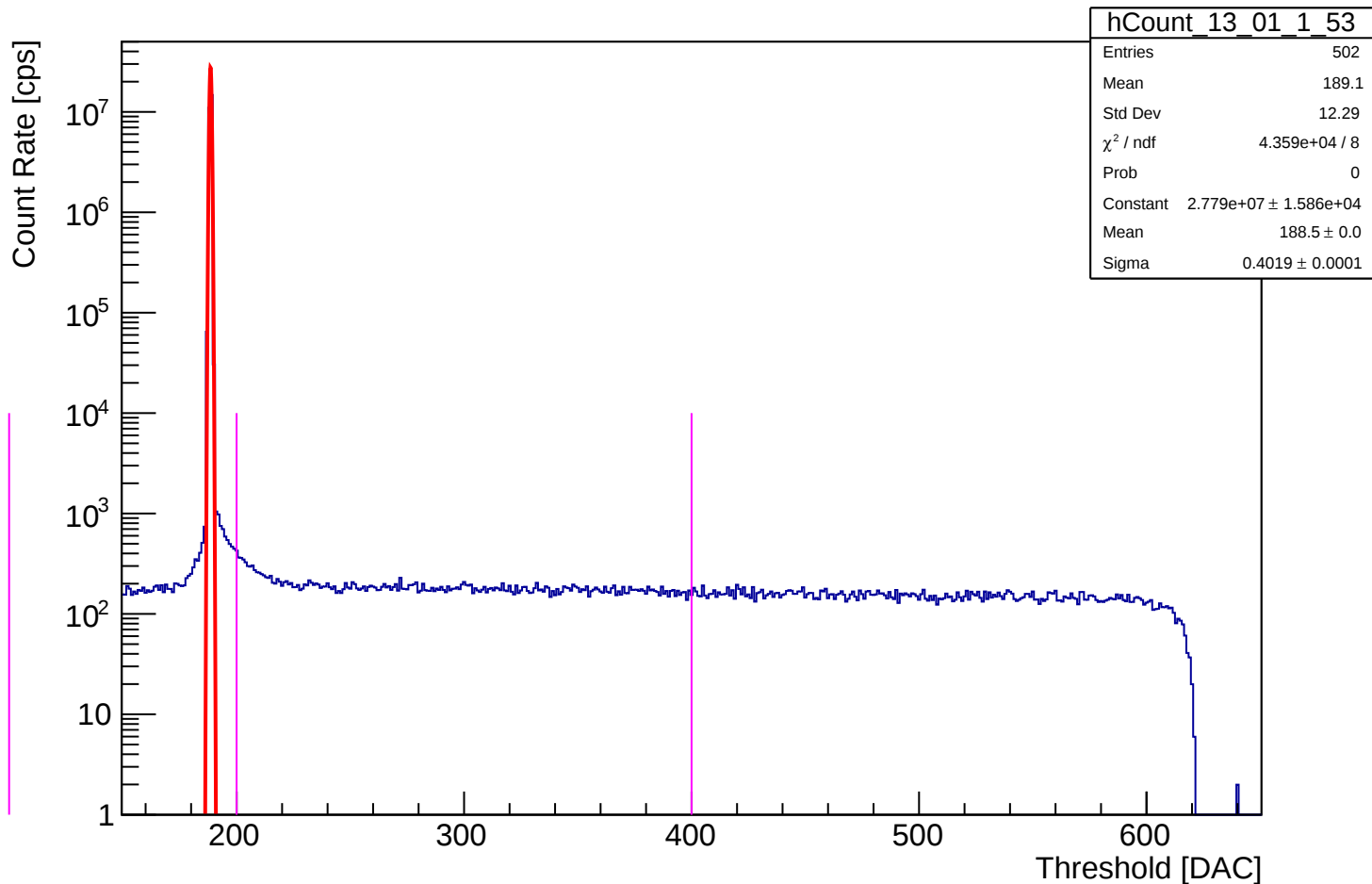
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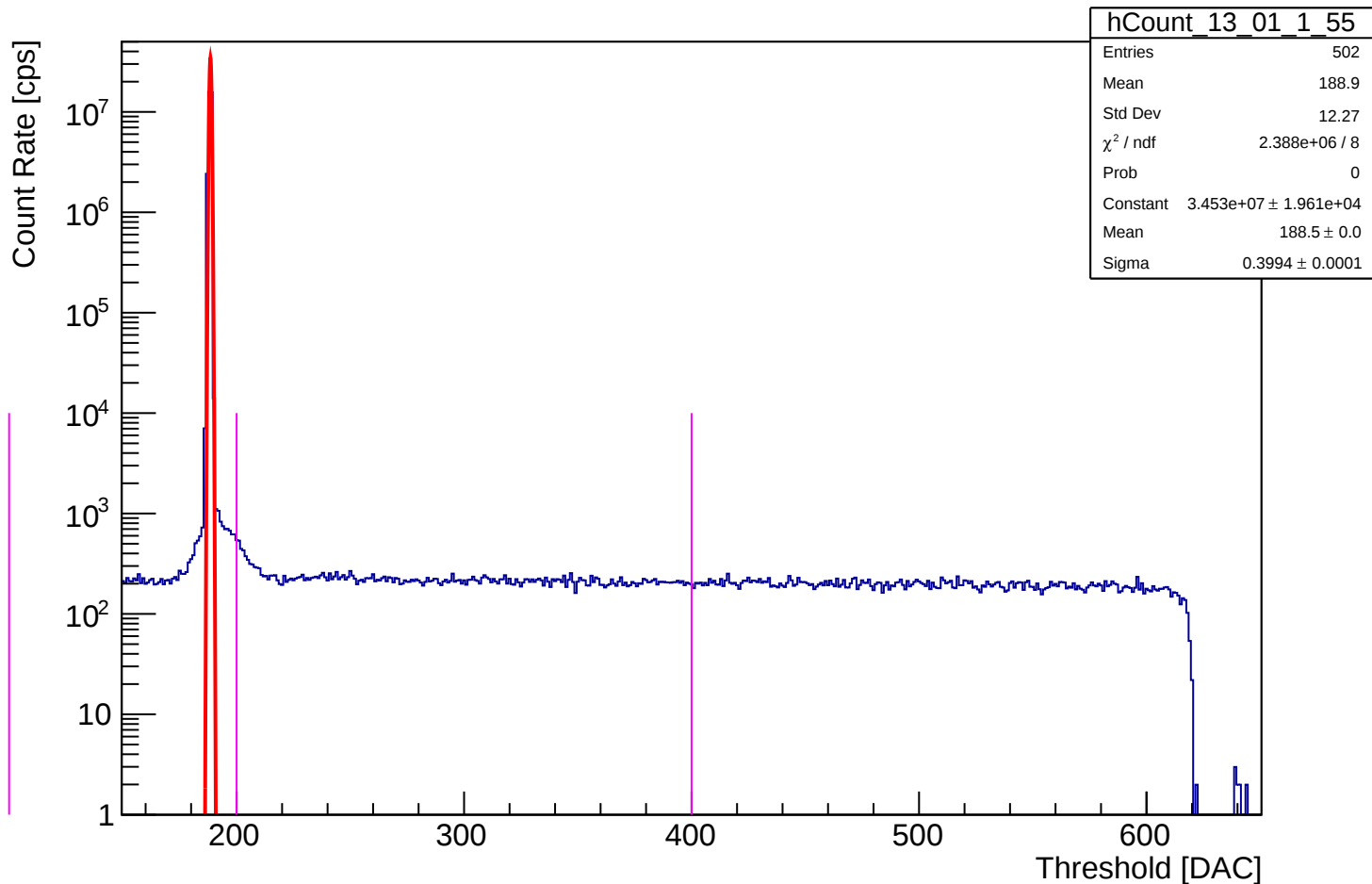
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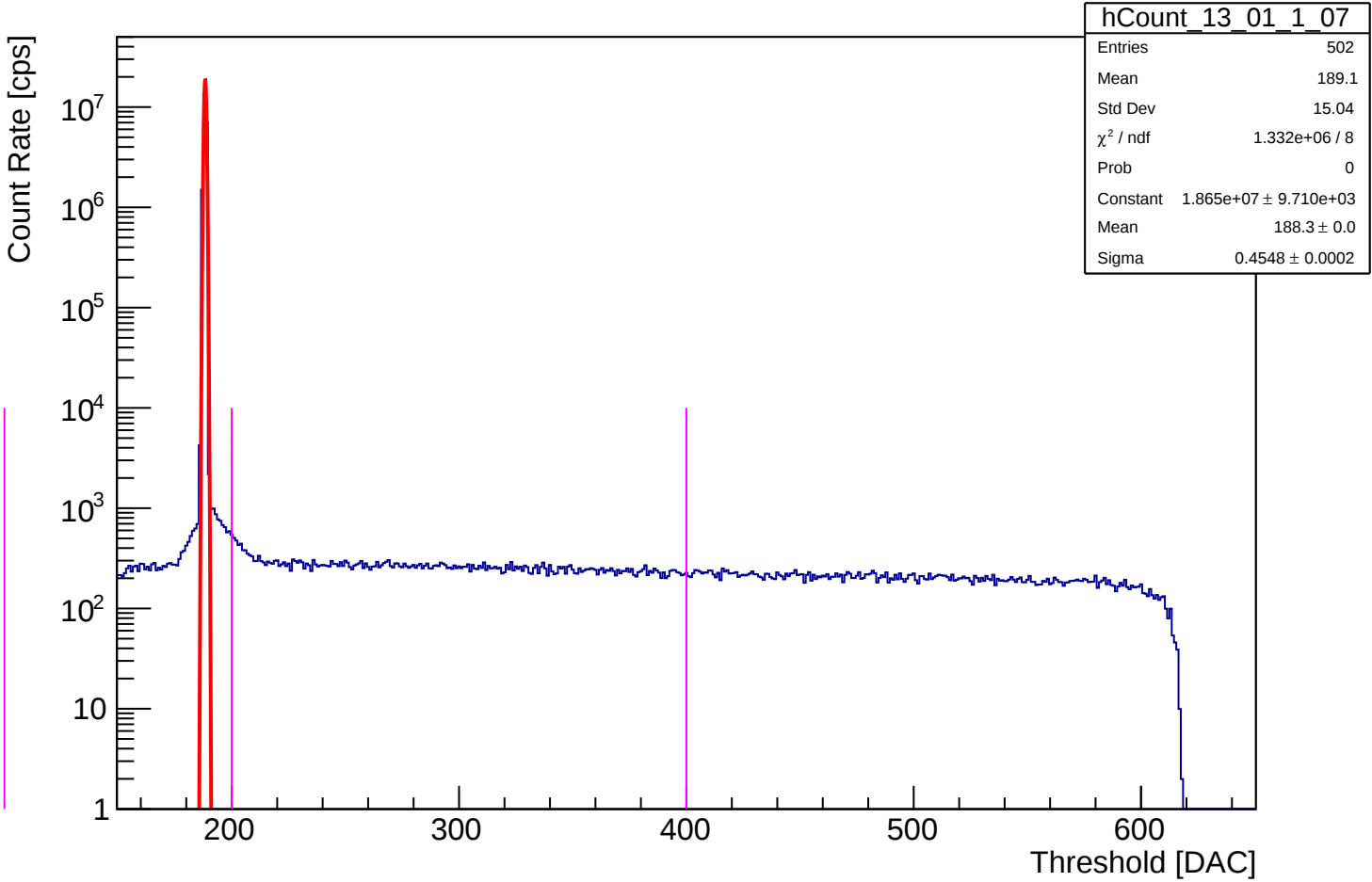


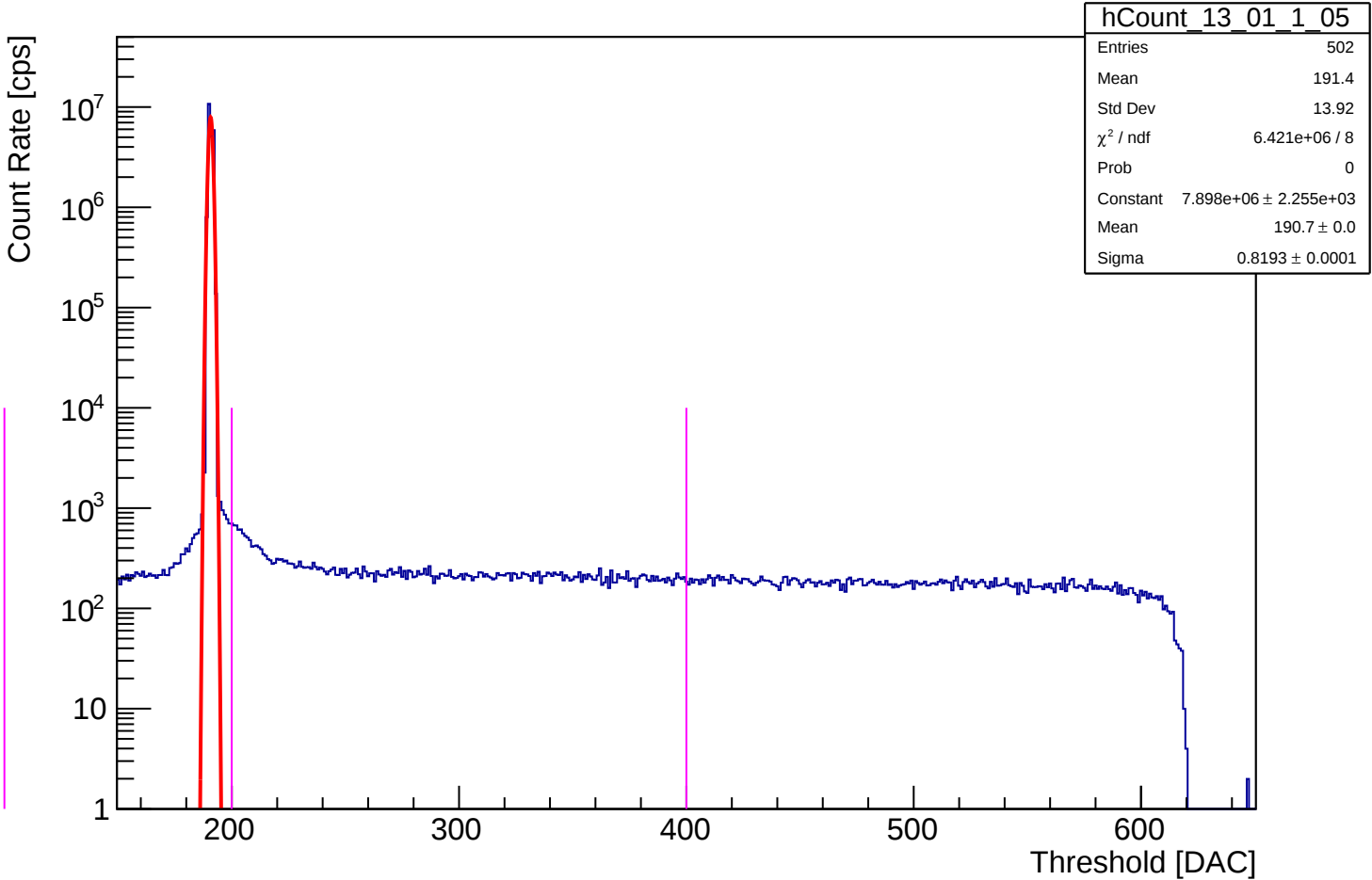
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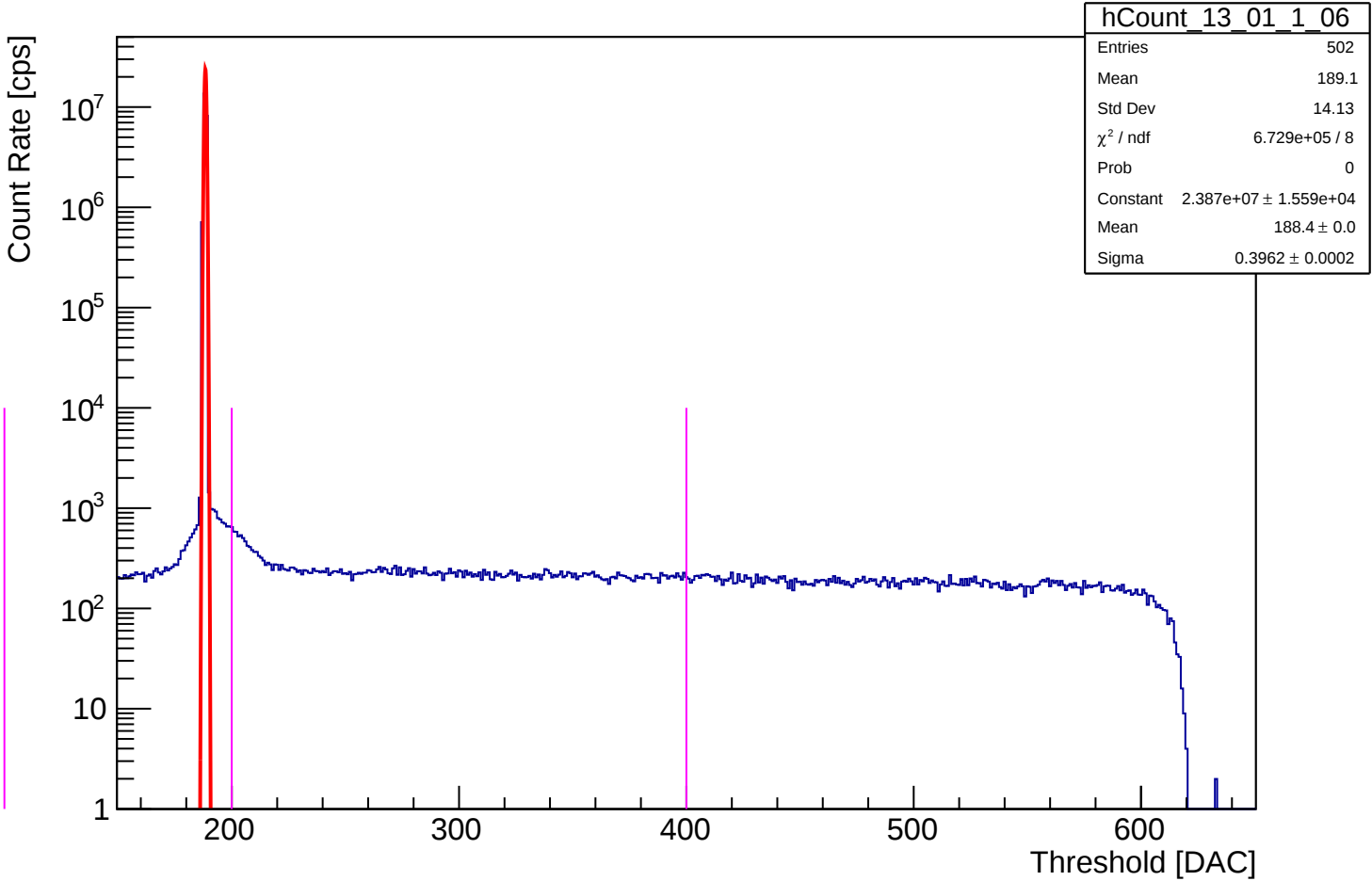


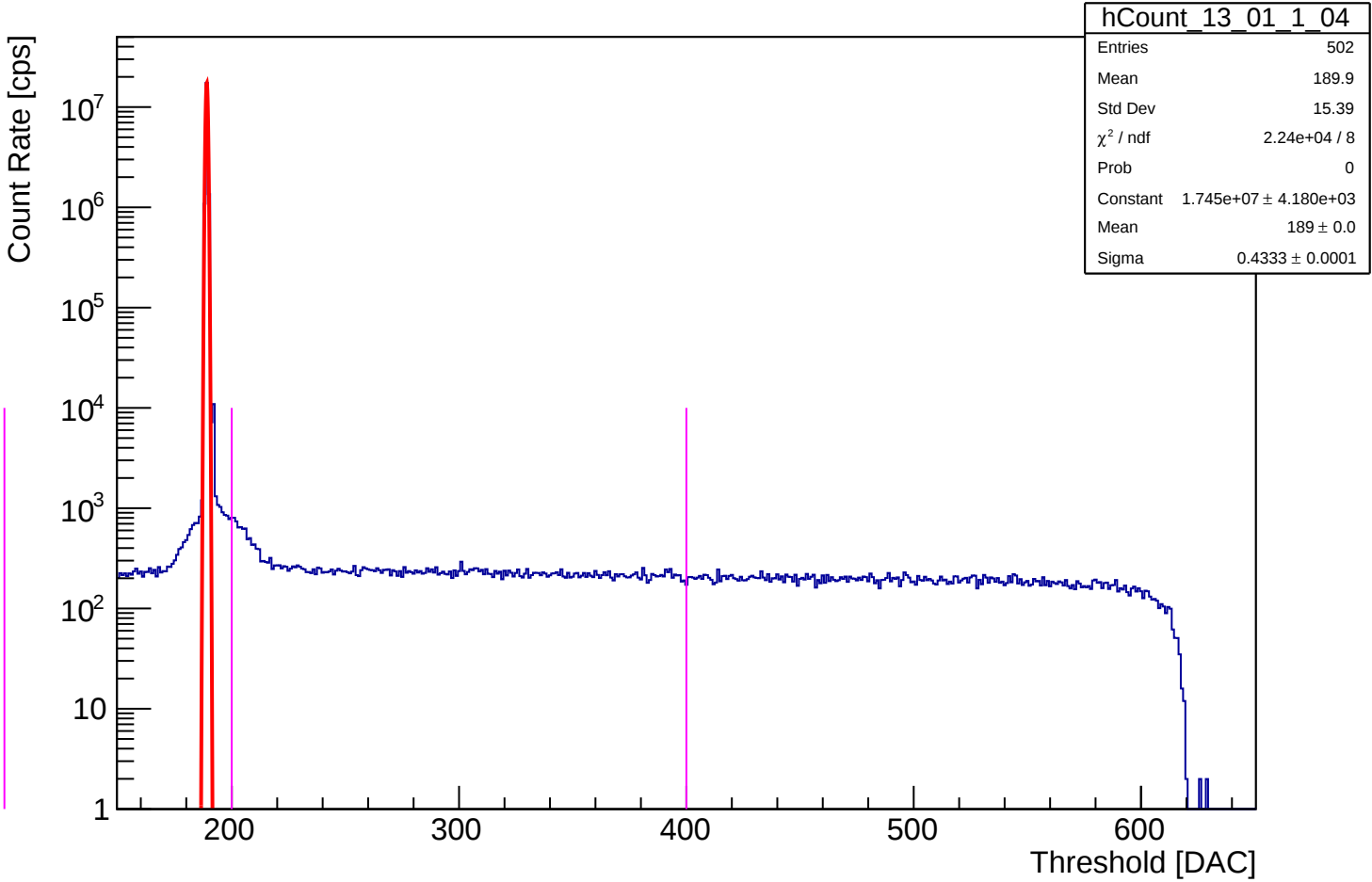
Row 1 Tile 1 Pmt 5 Pixel 48 Slot 13 Fiber 1 Asic 1 Channel 55



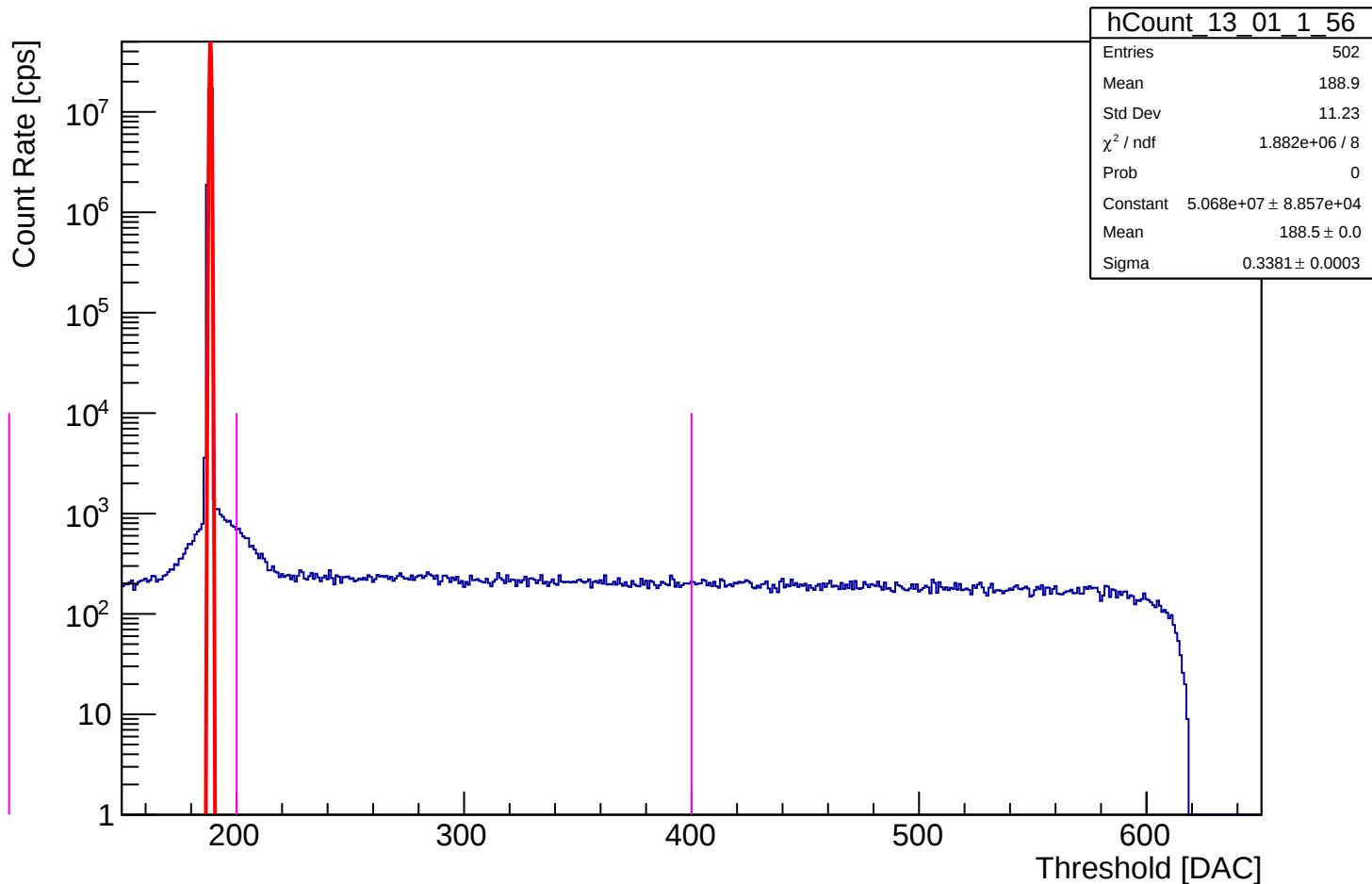




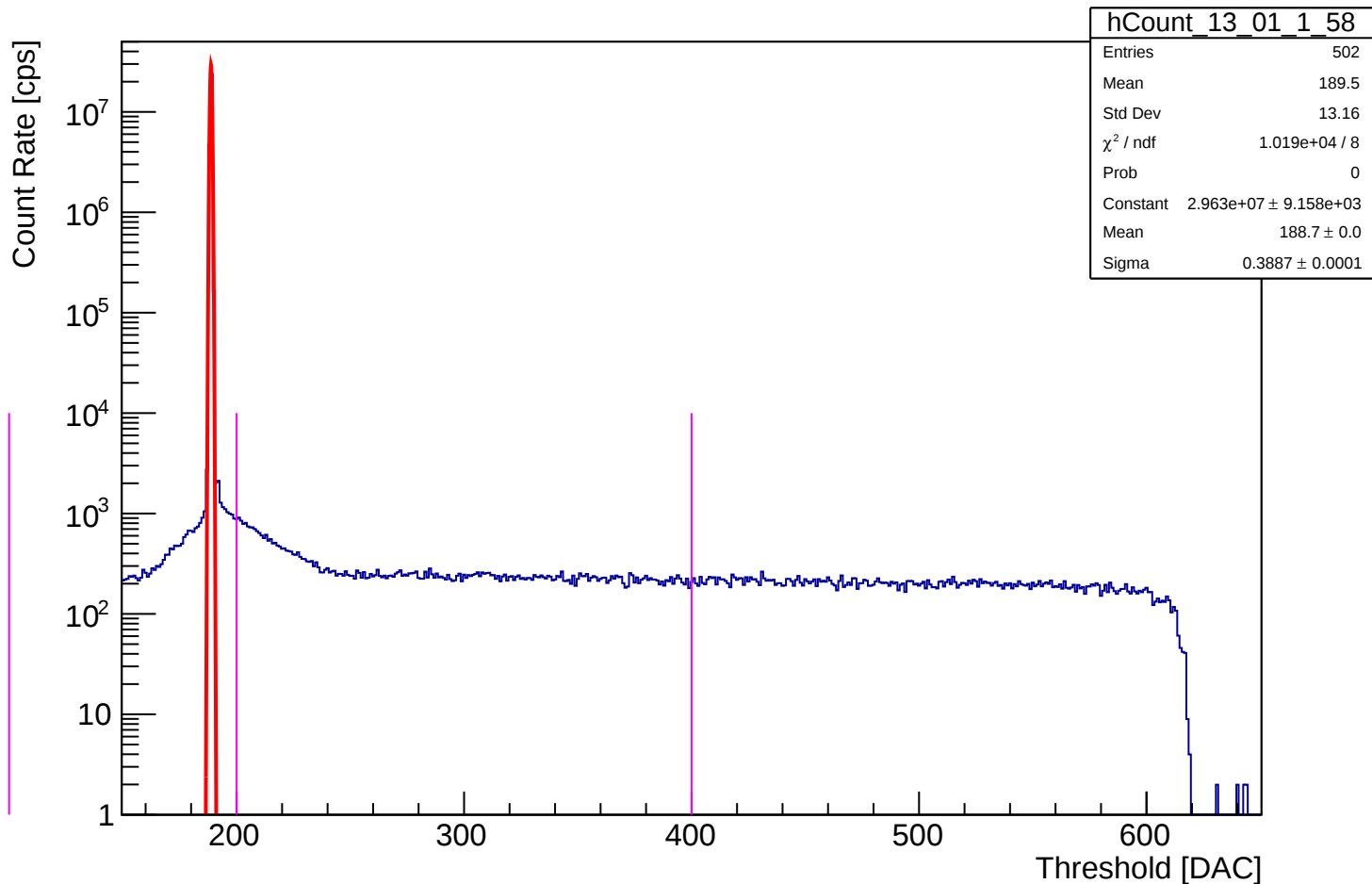




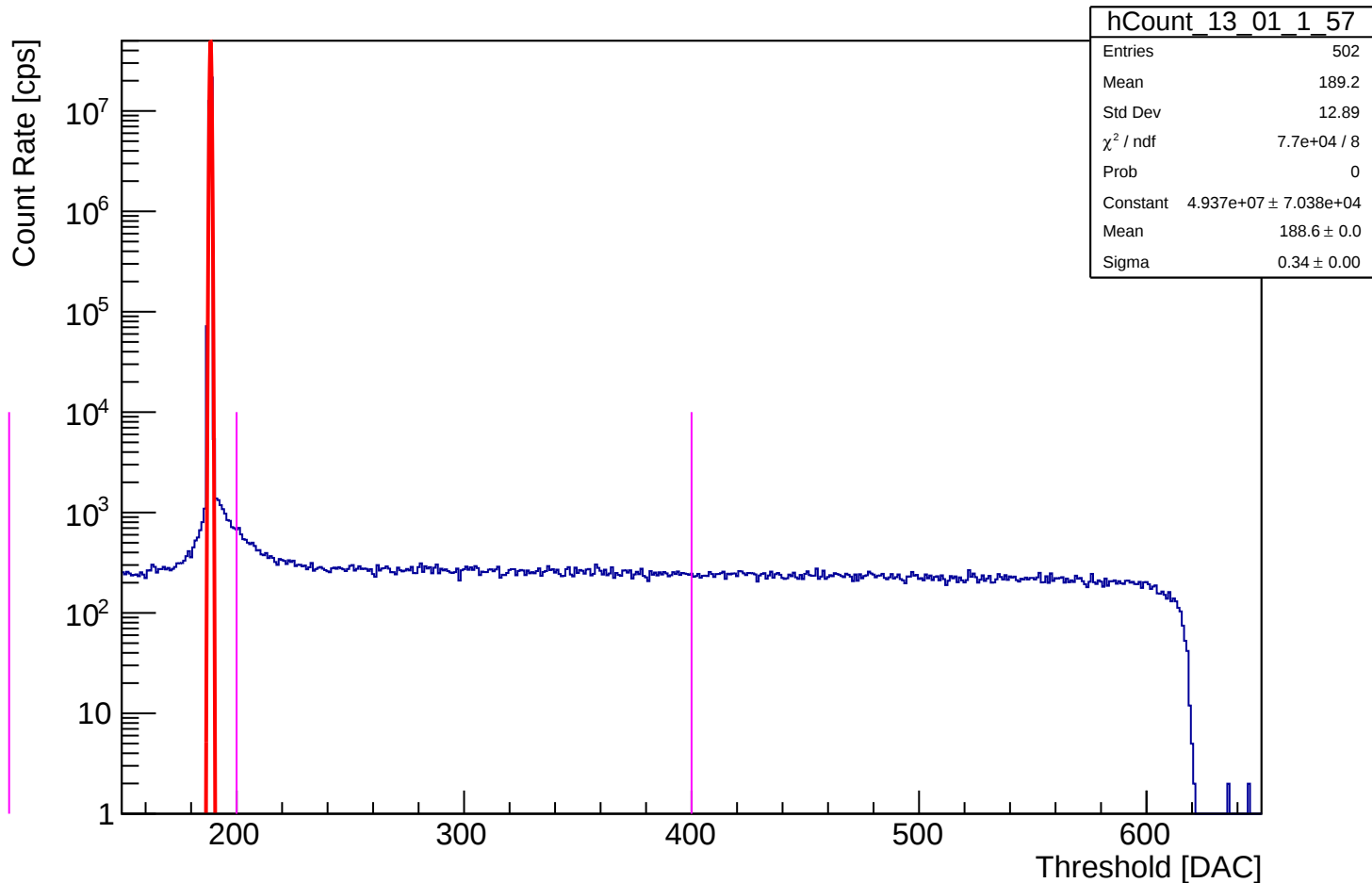
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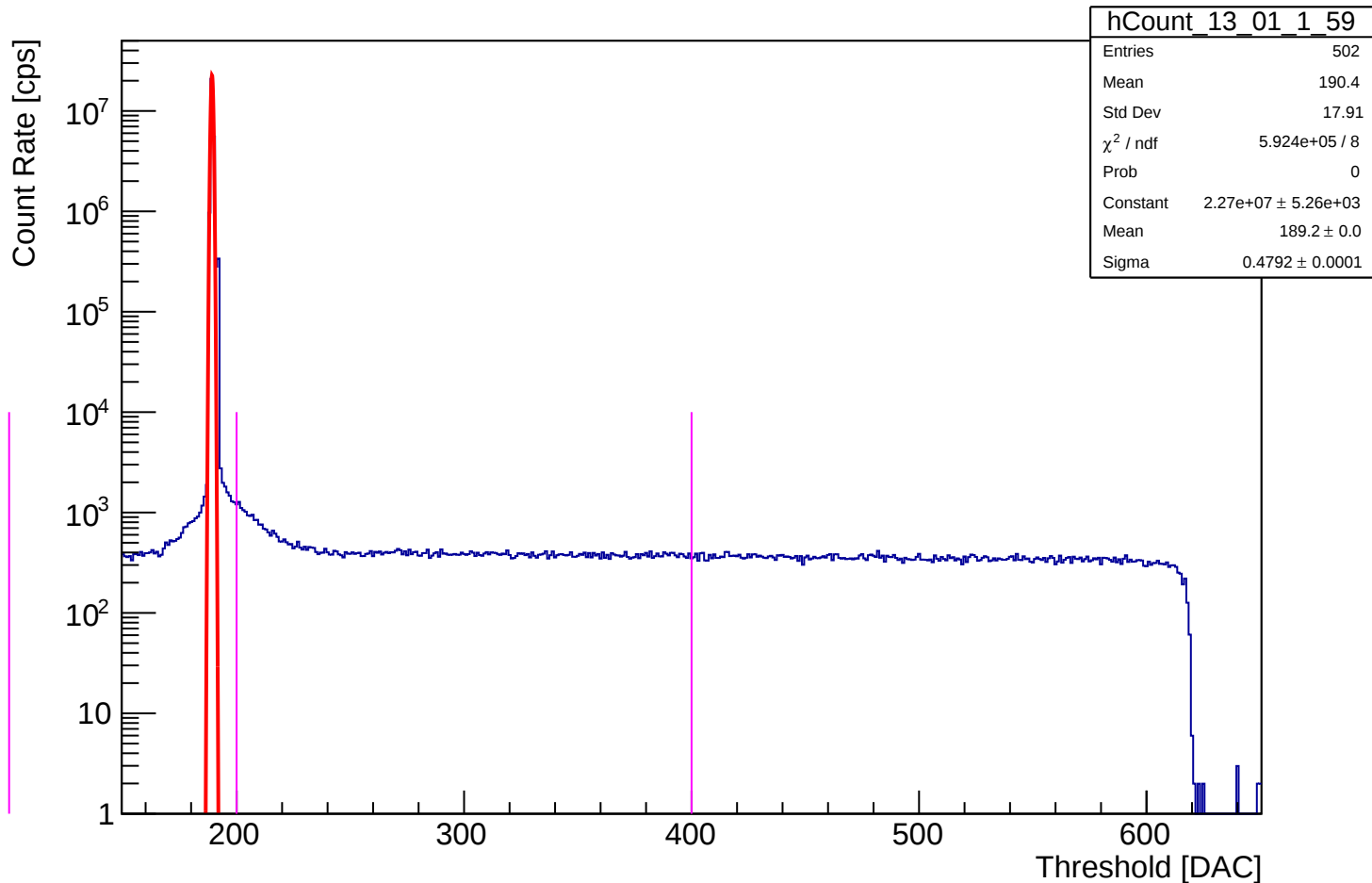
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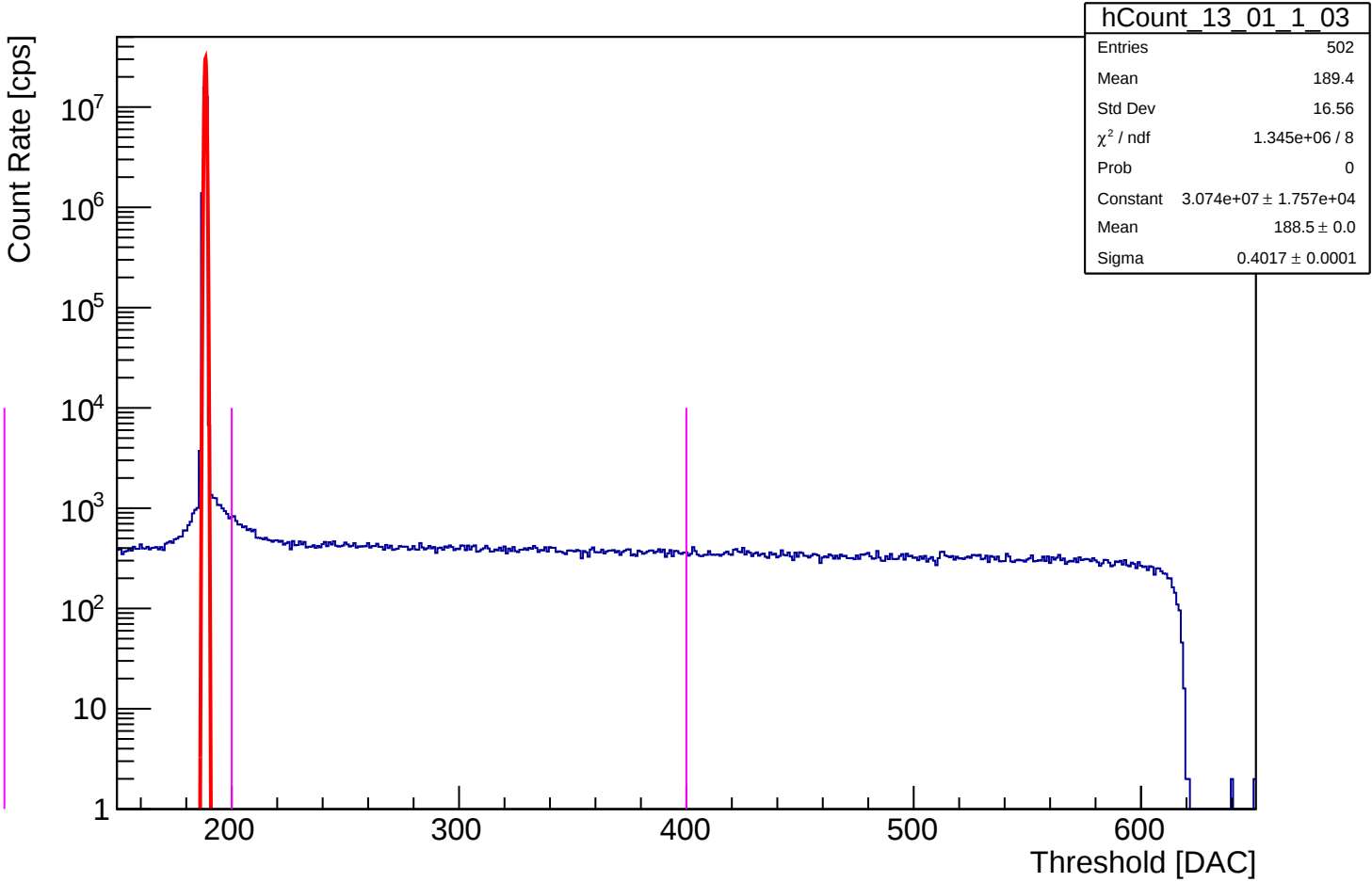


Row 1 Tile 1 Pmt 5 Pixel 55 Slot 13 Fiber 1 Asic 1 Channel 57

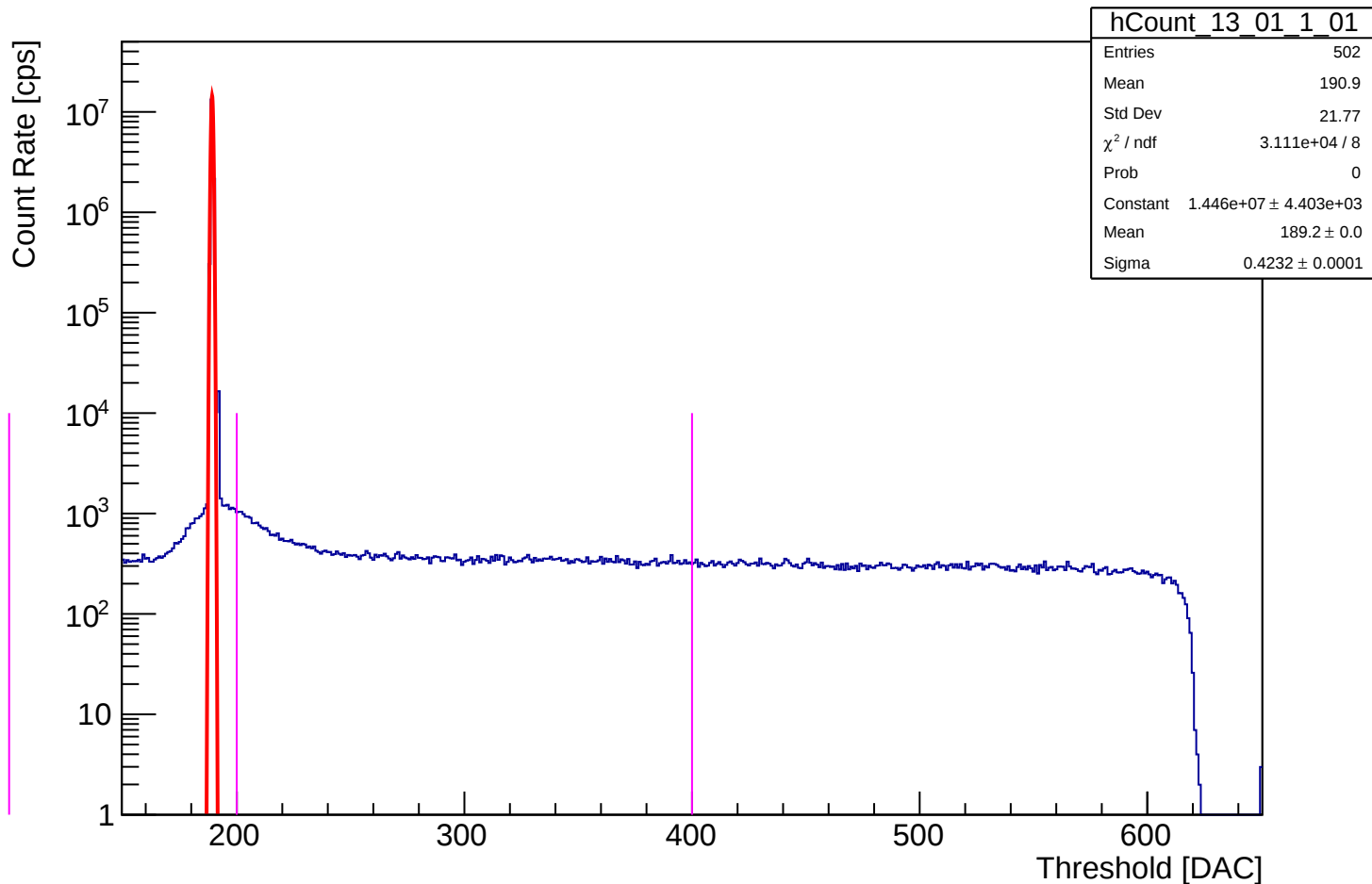


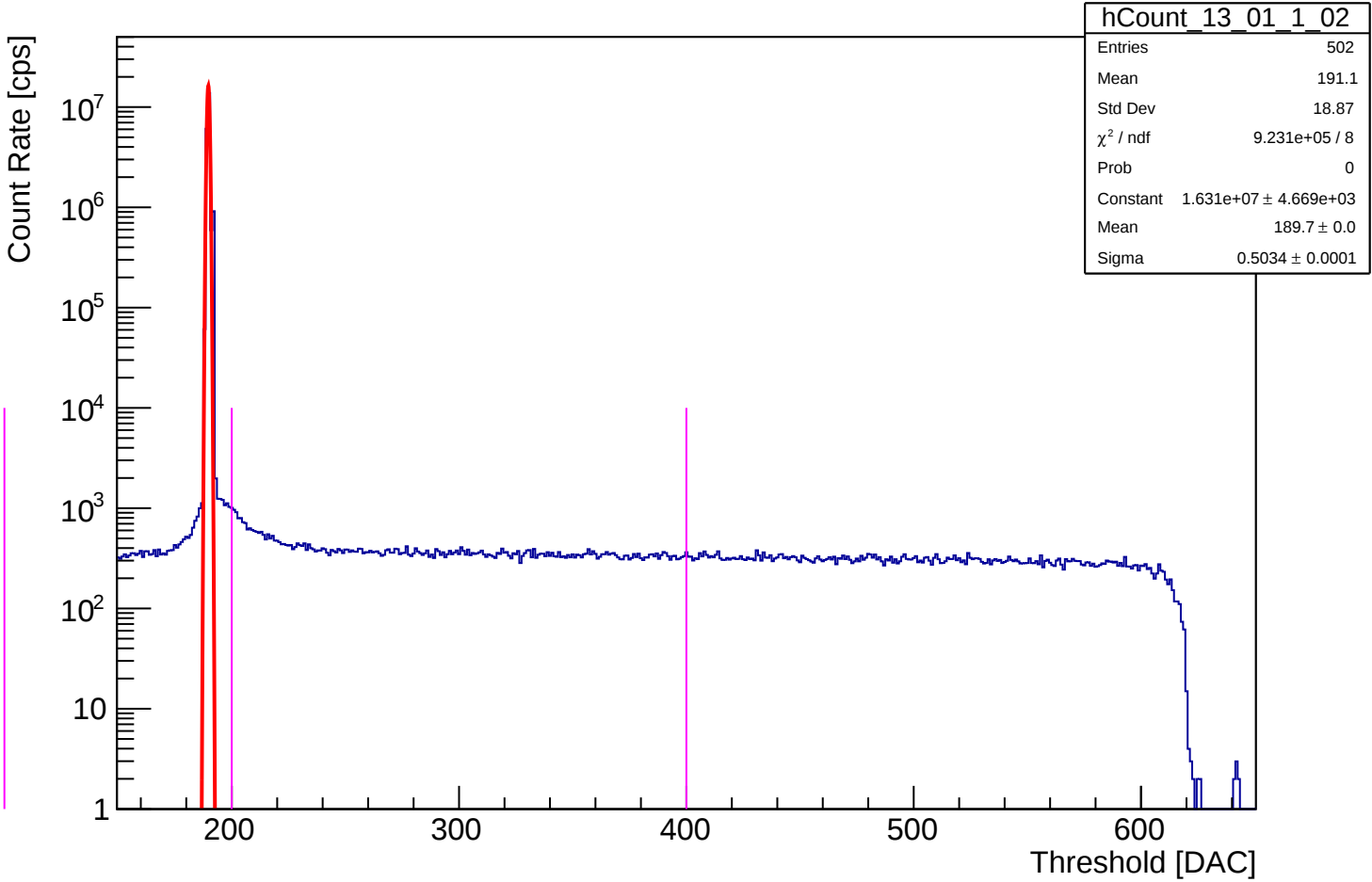
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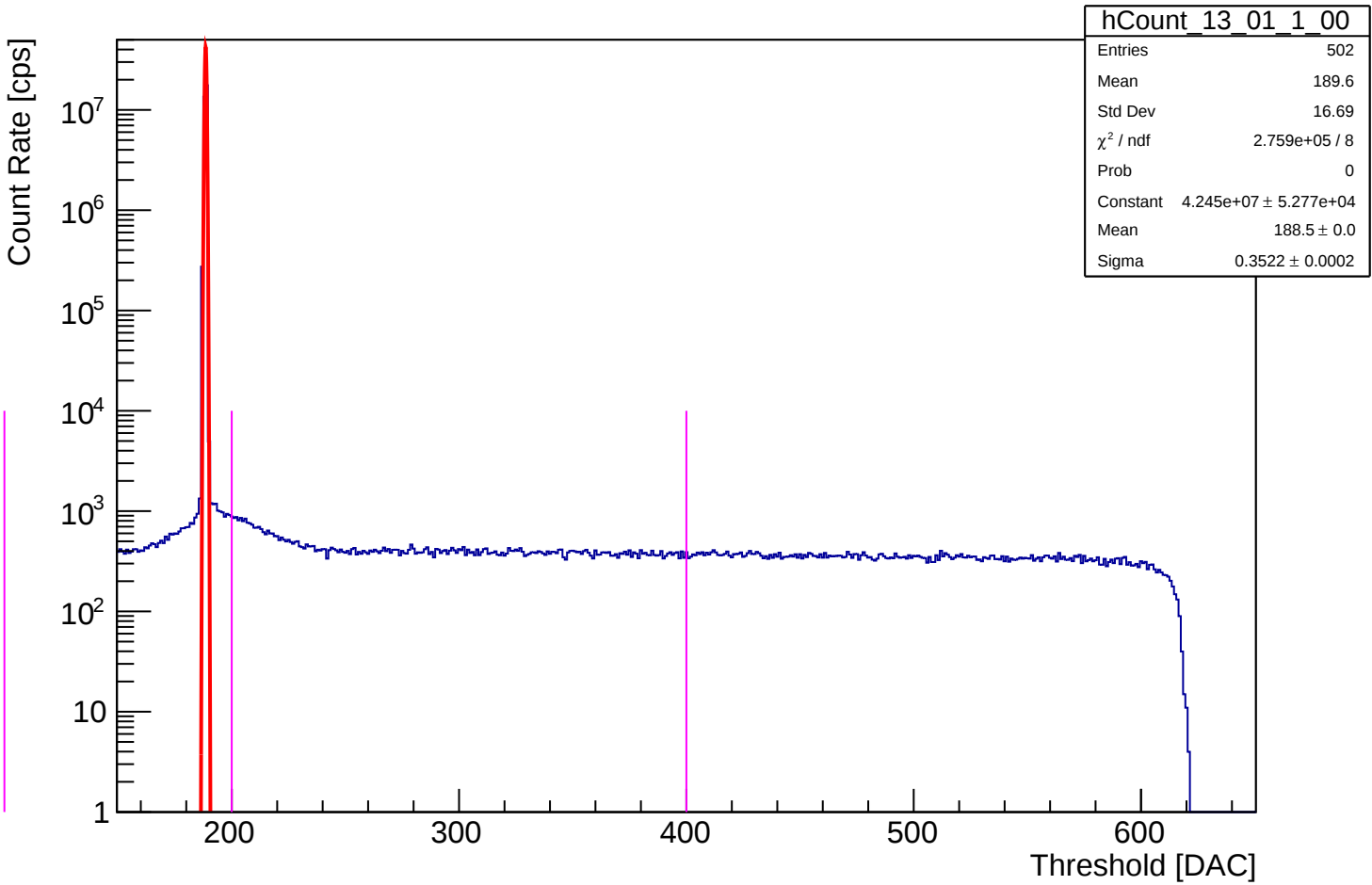




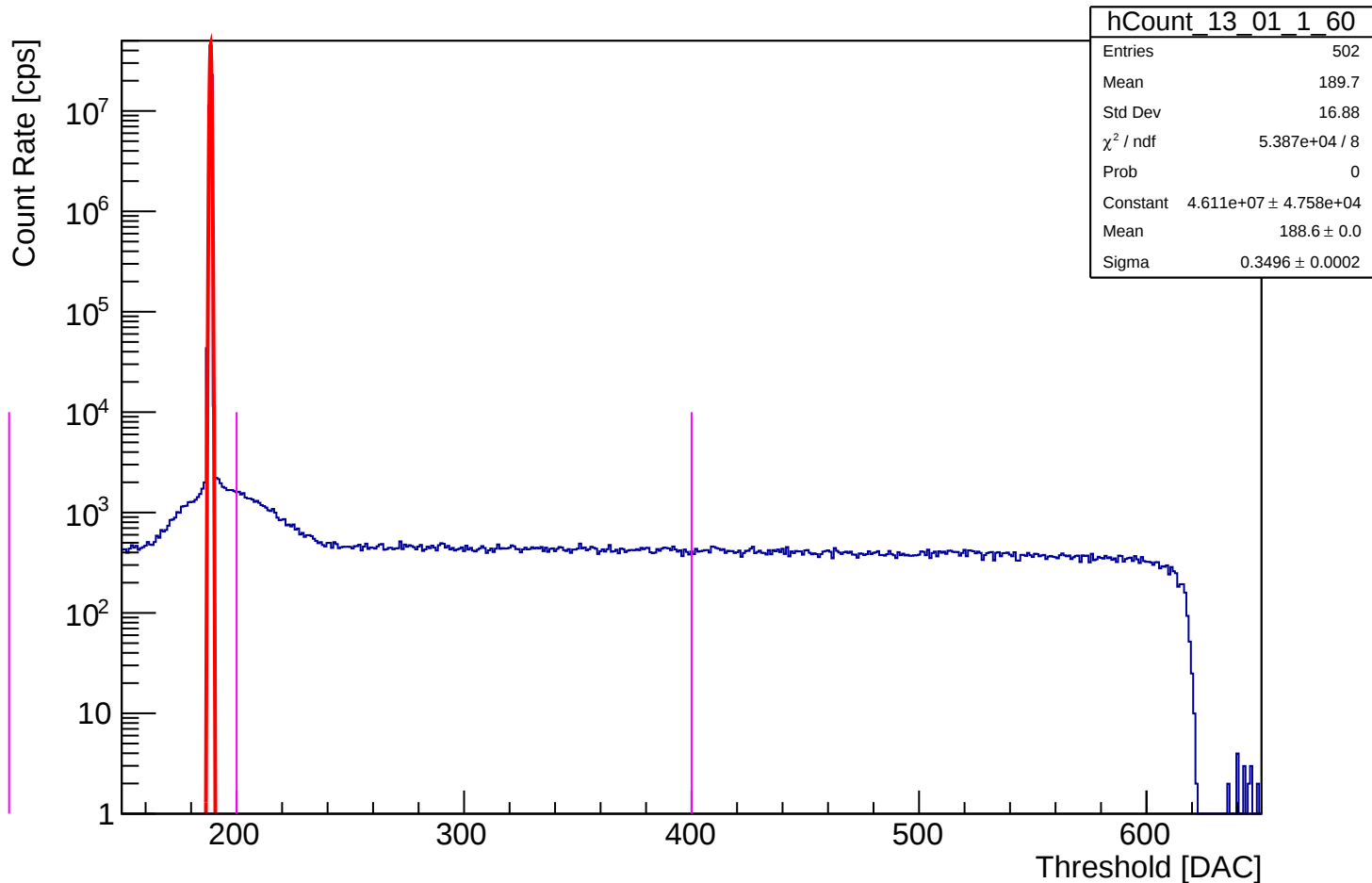
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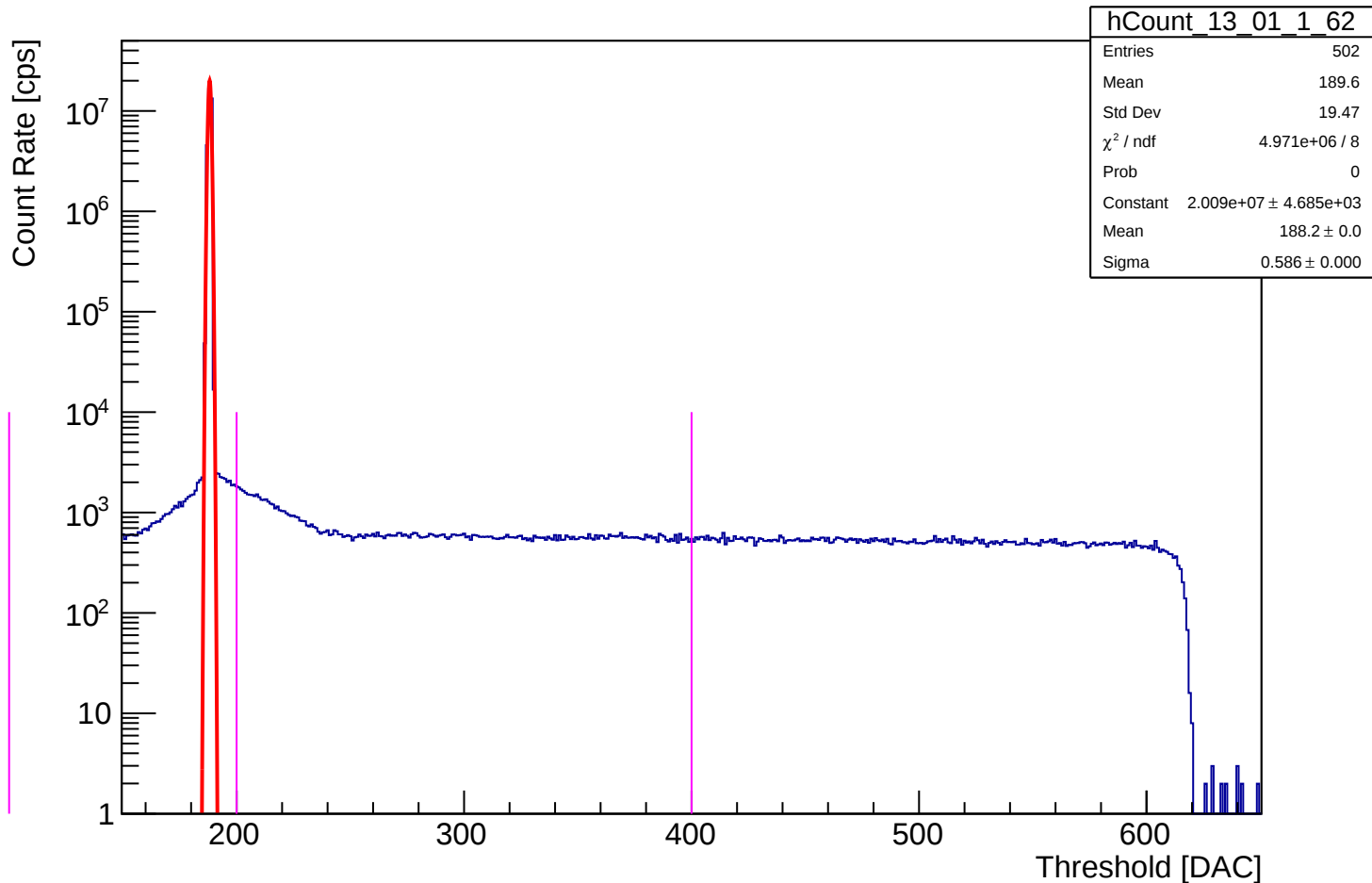




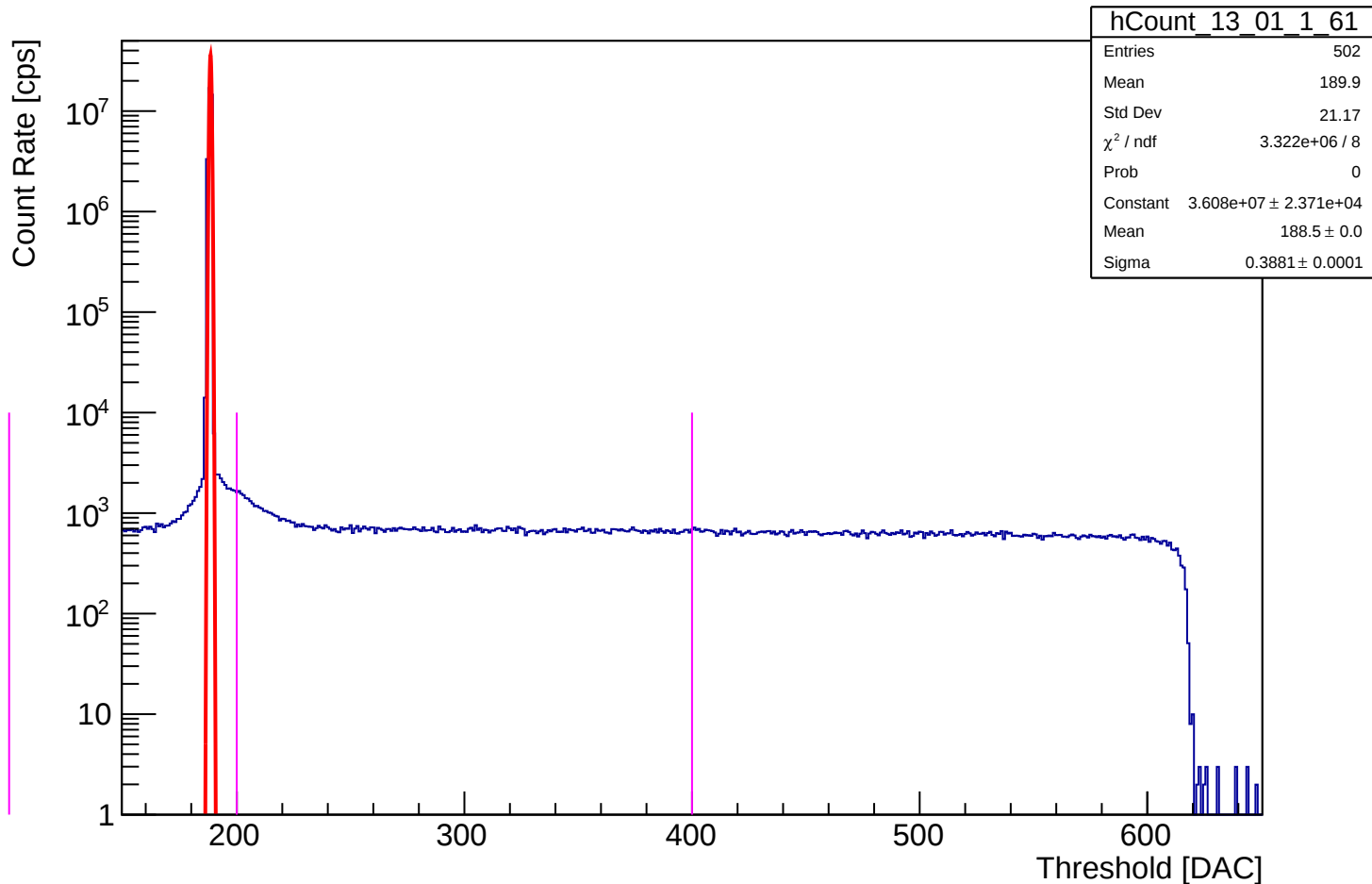
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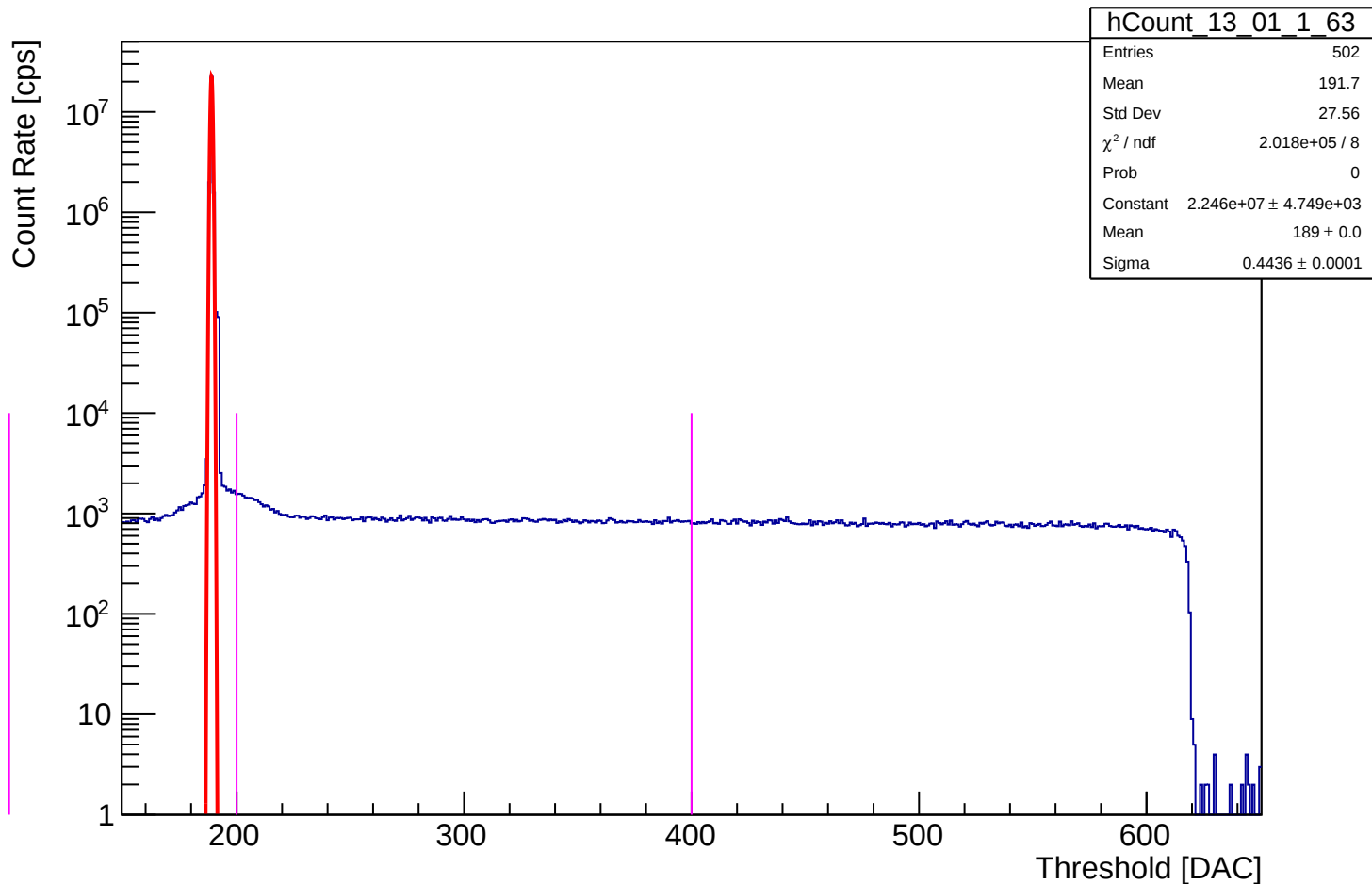
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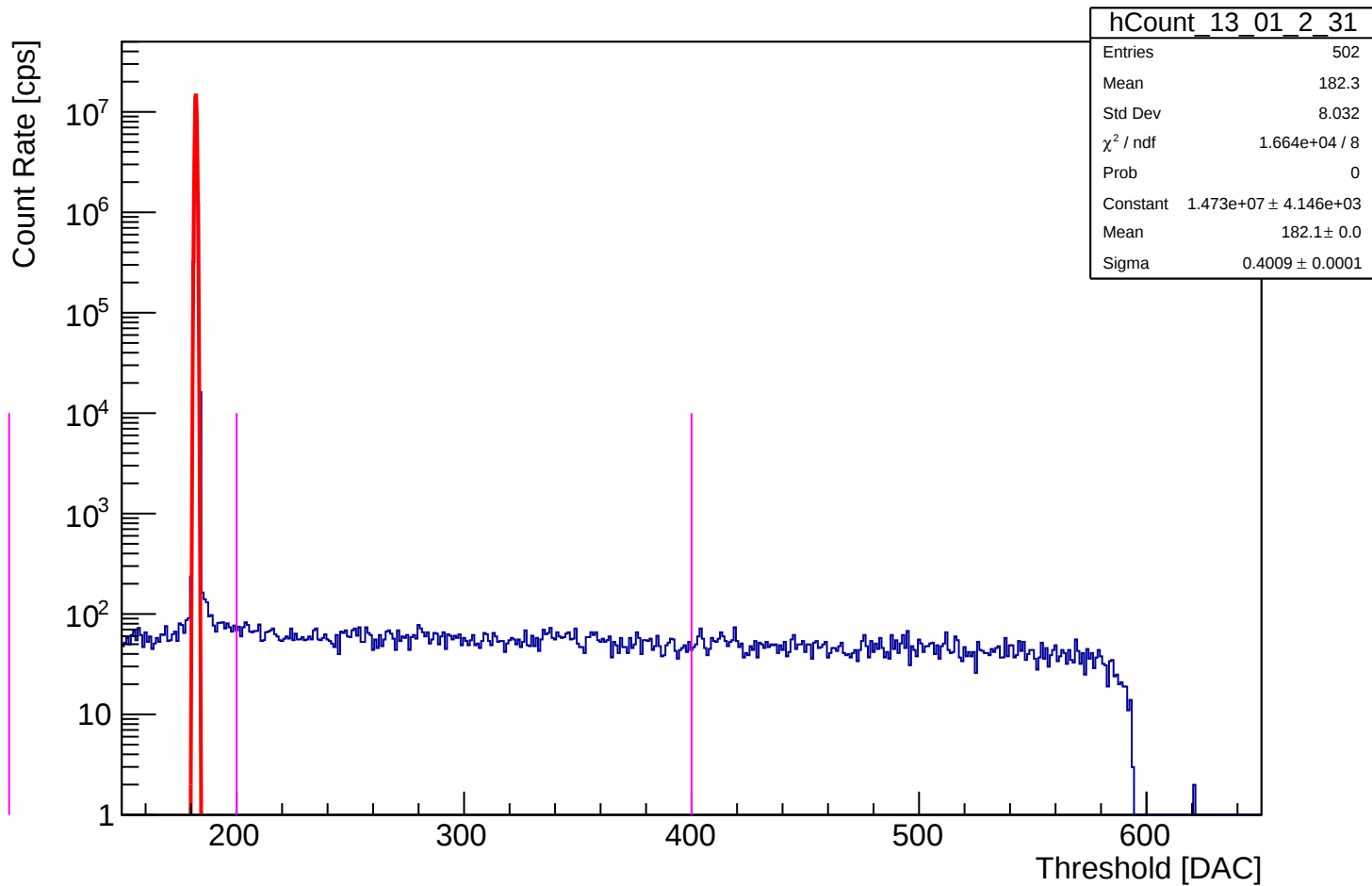
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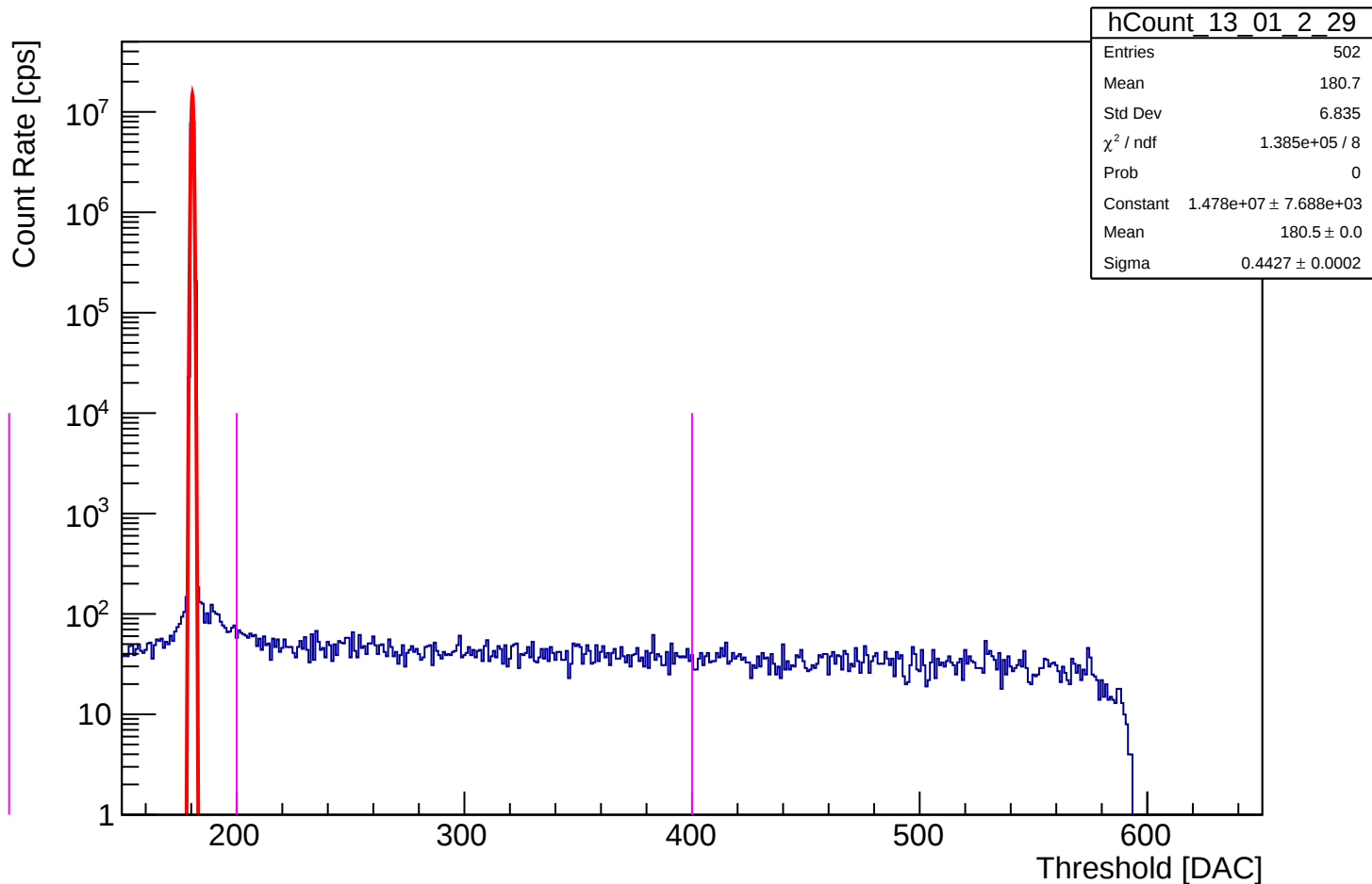
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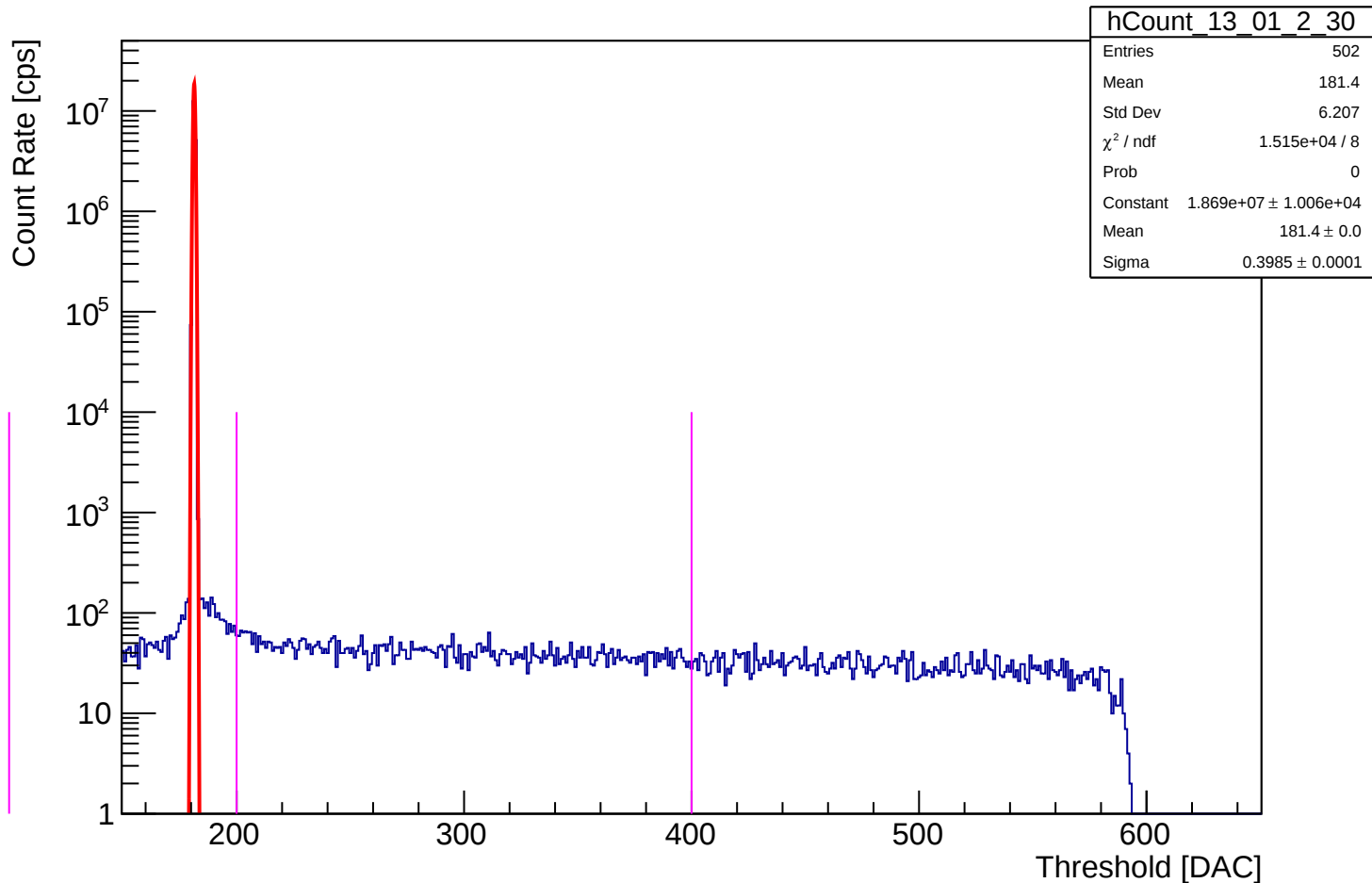
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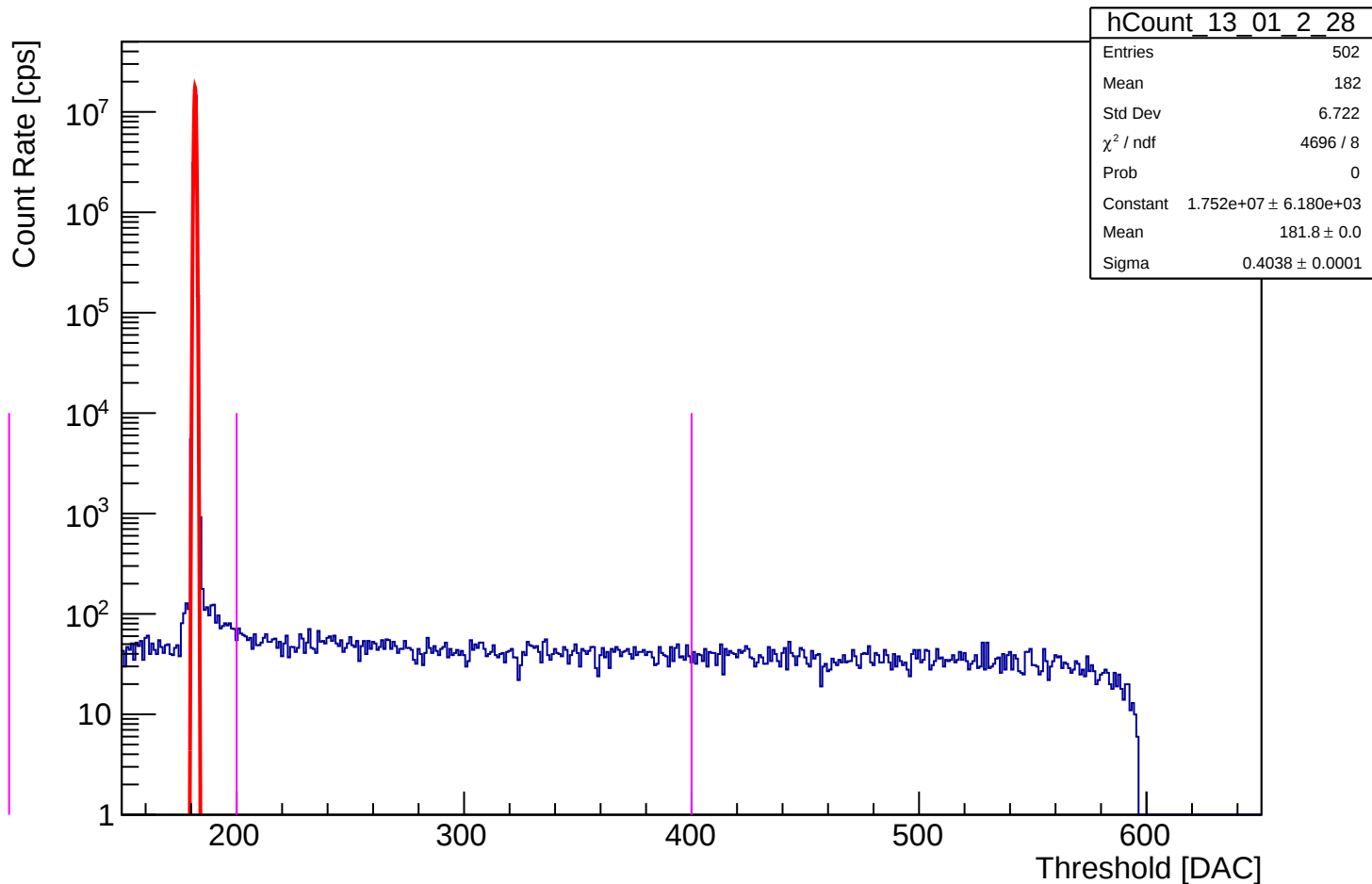
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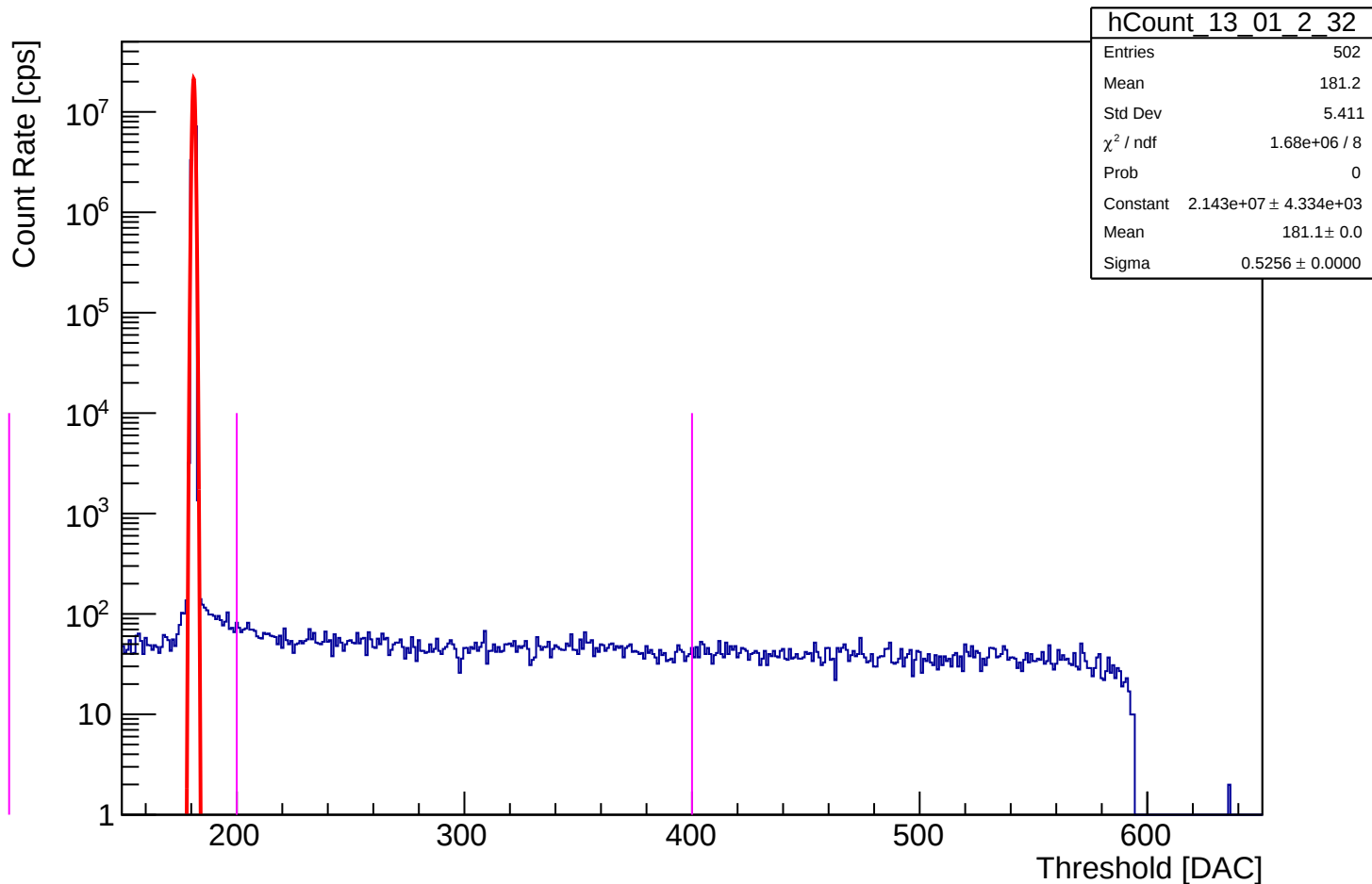
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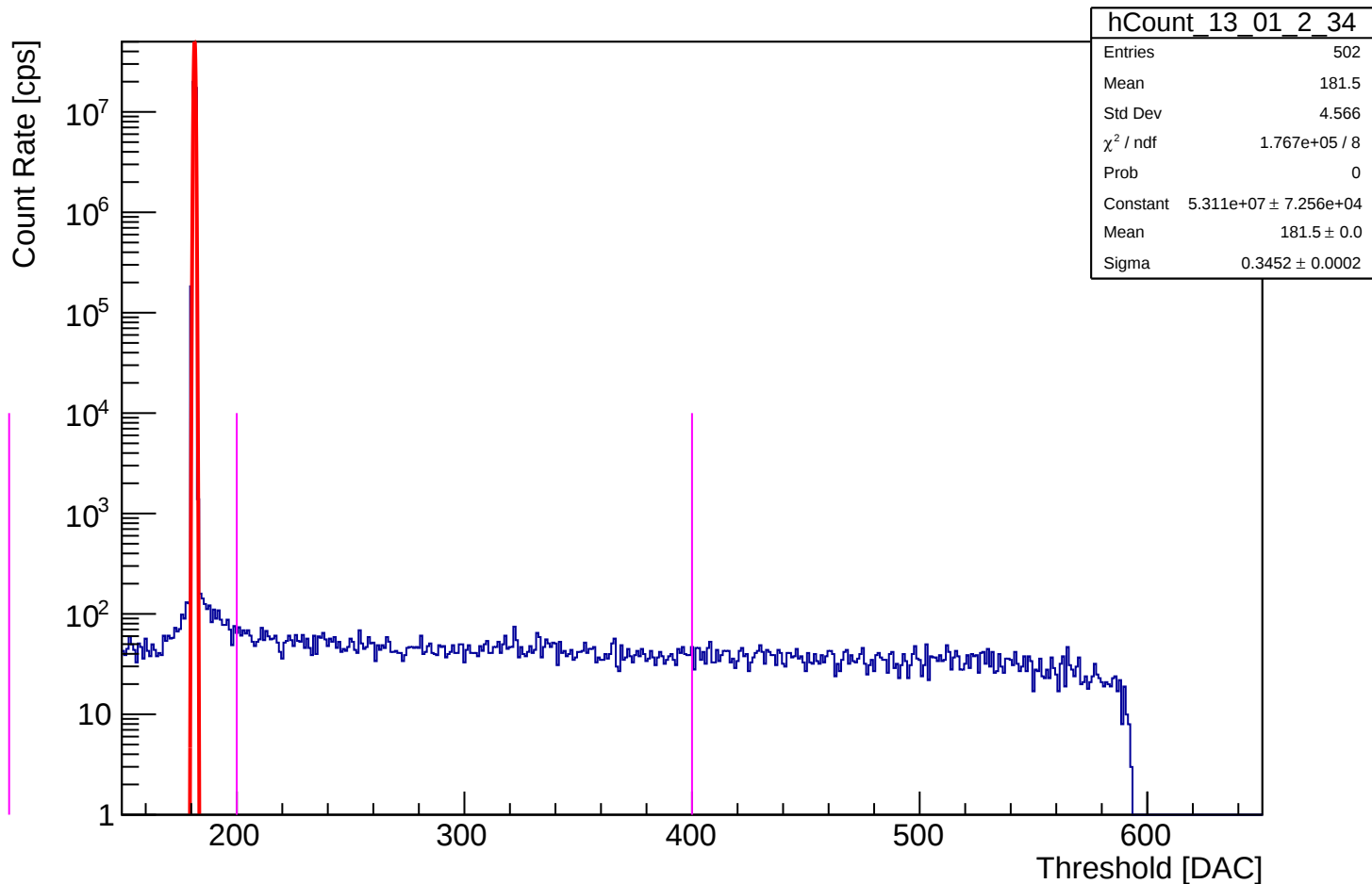
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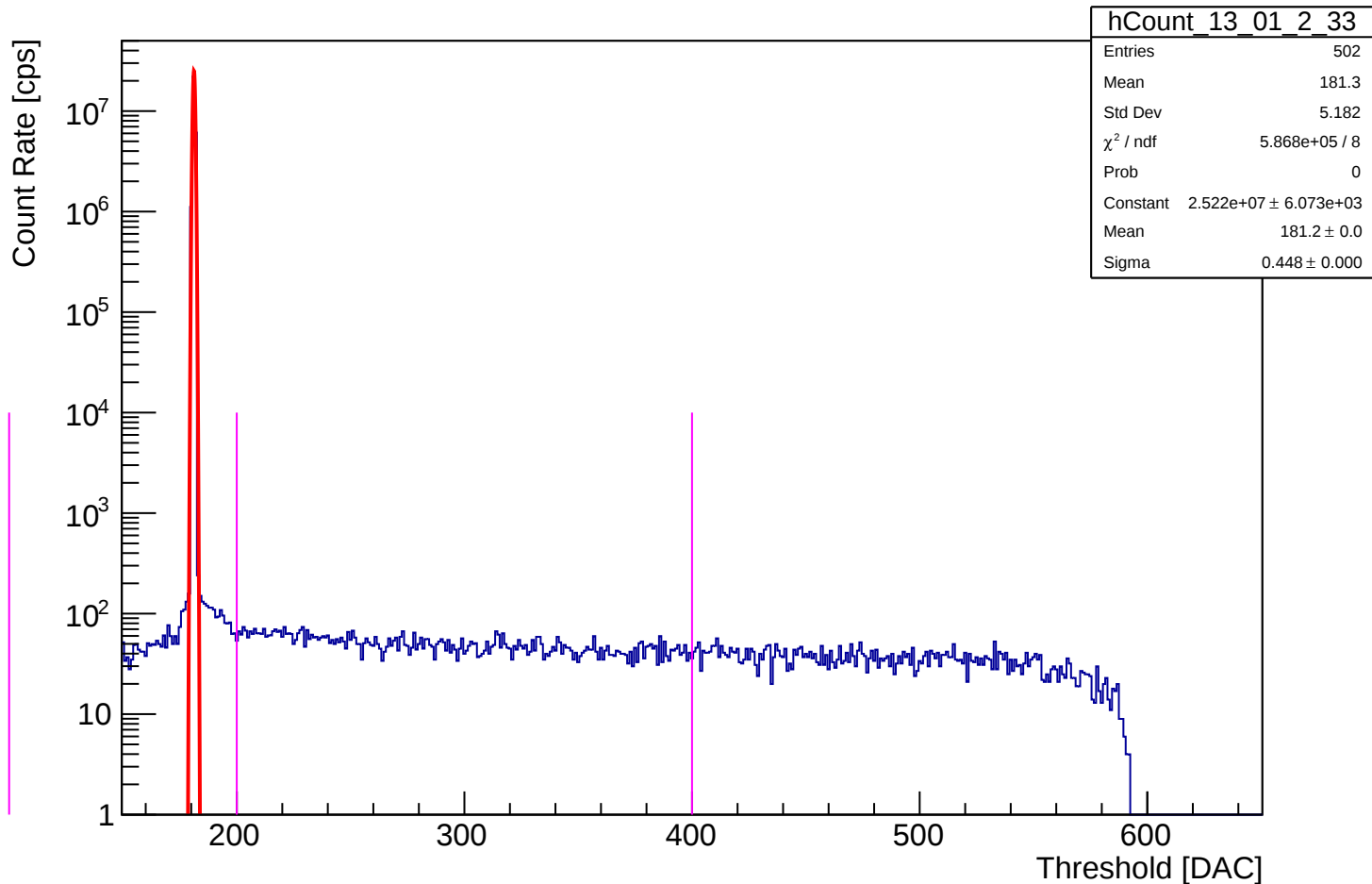
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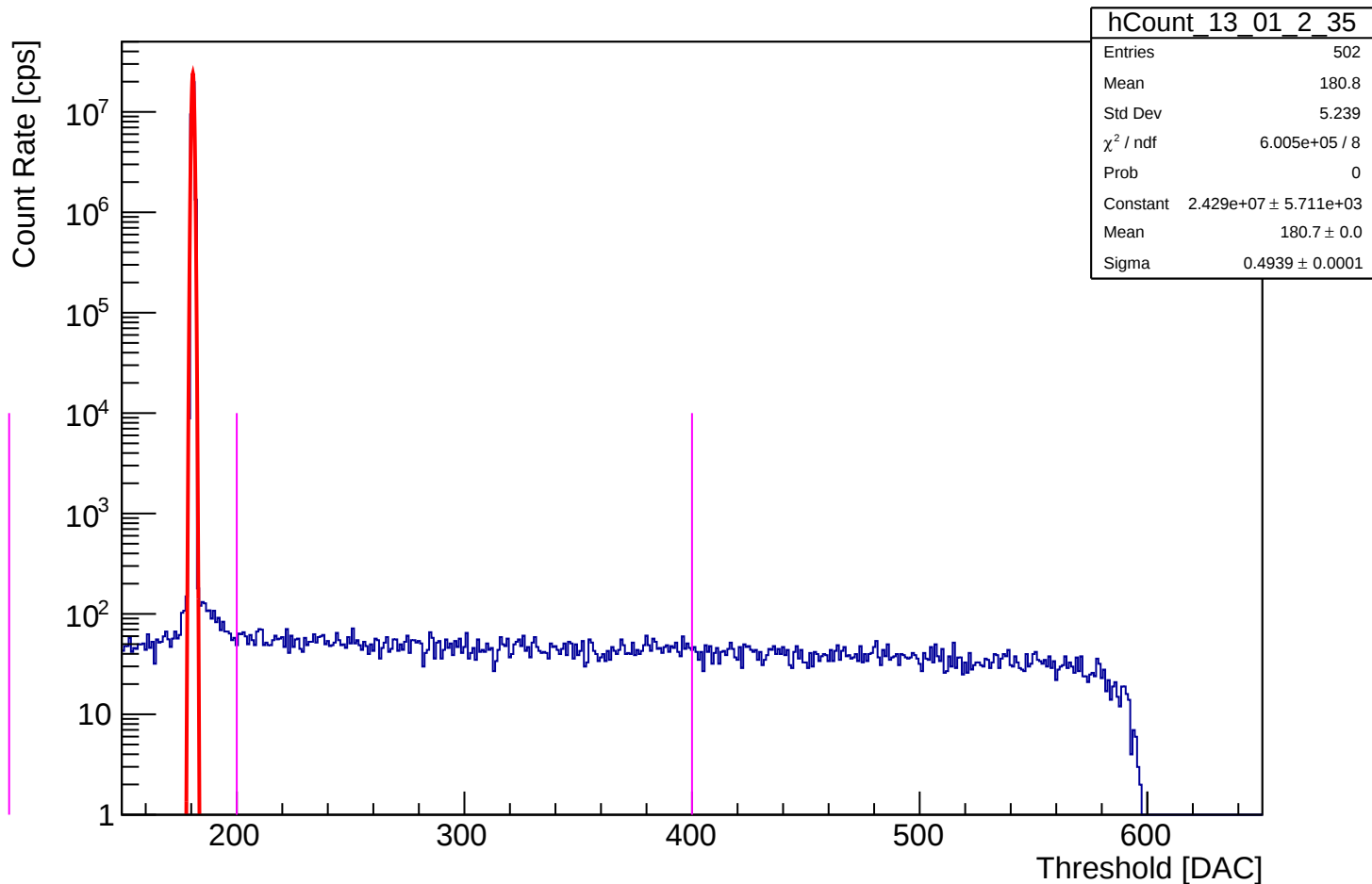
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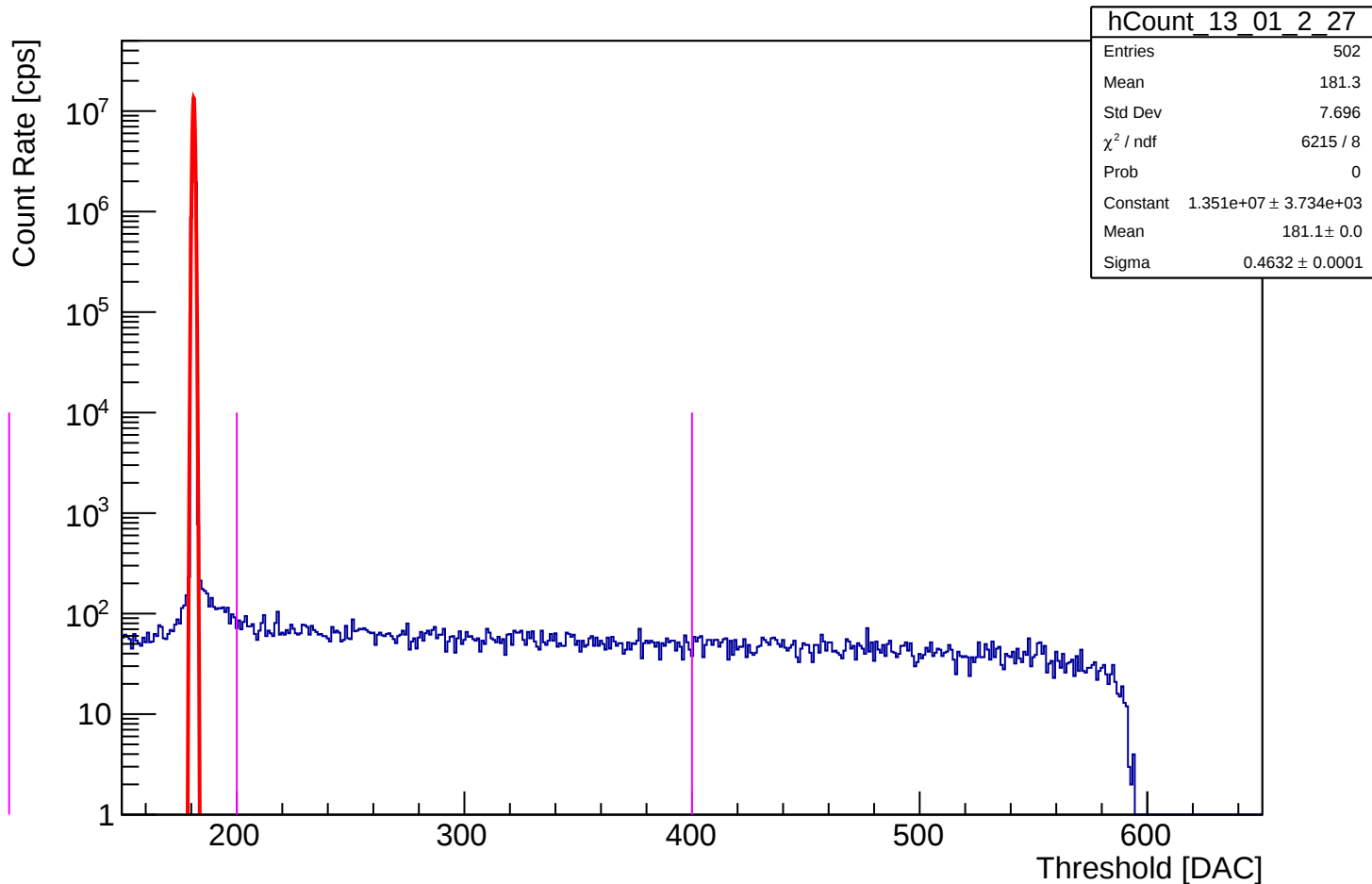
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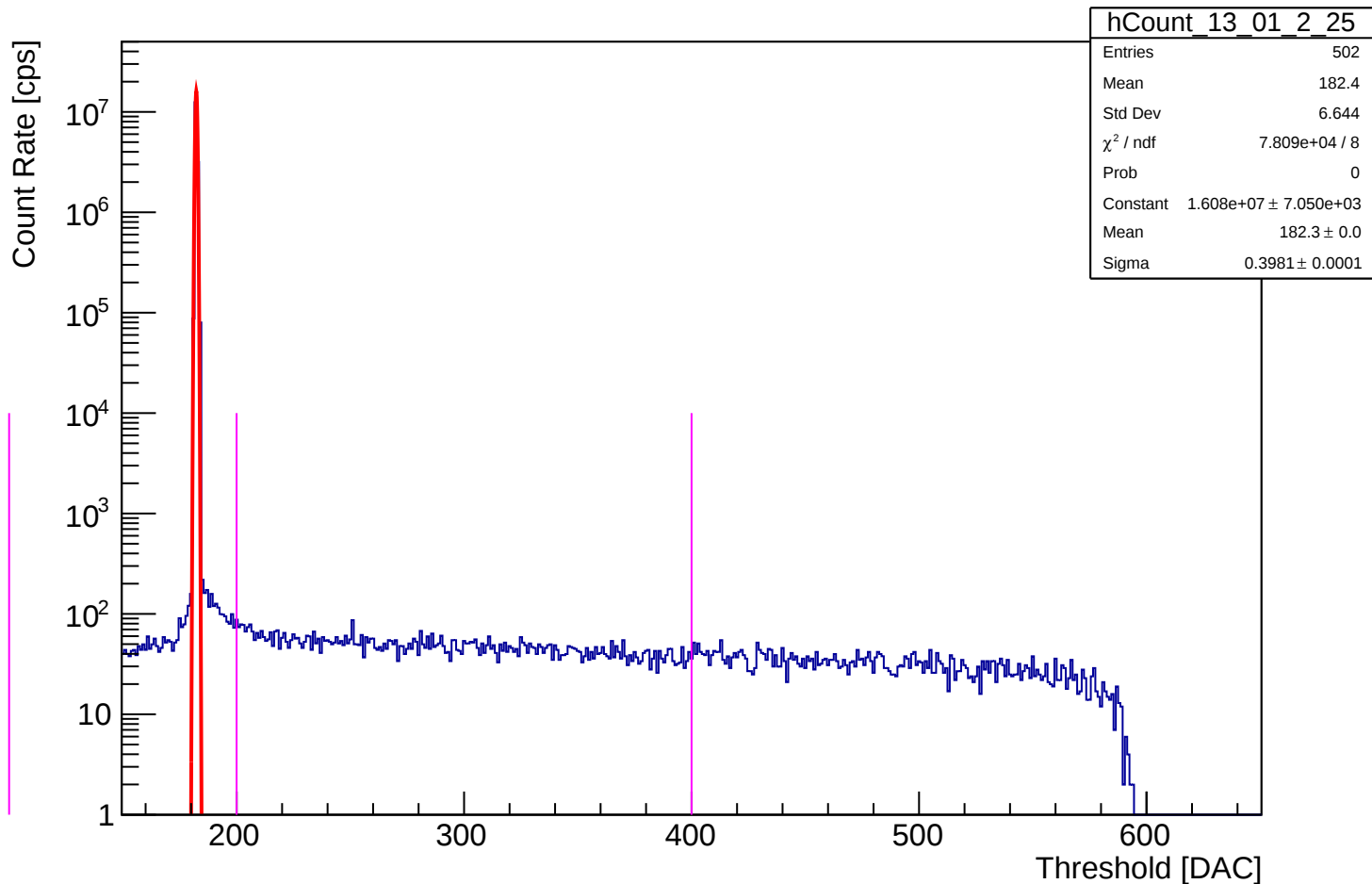
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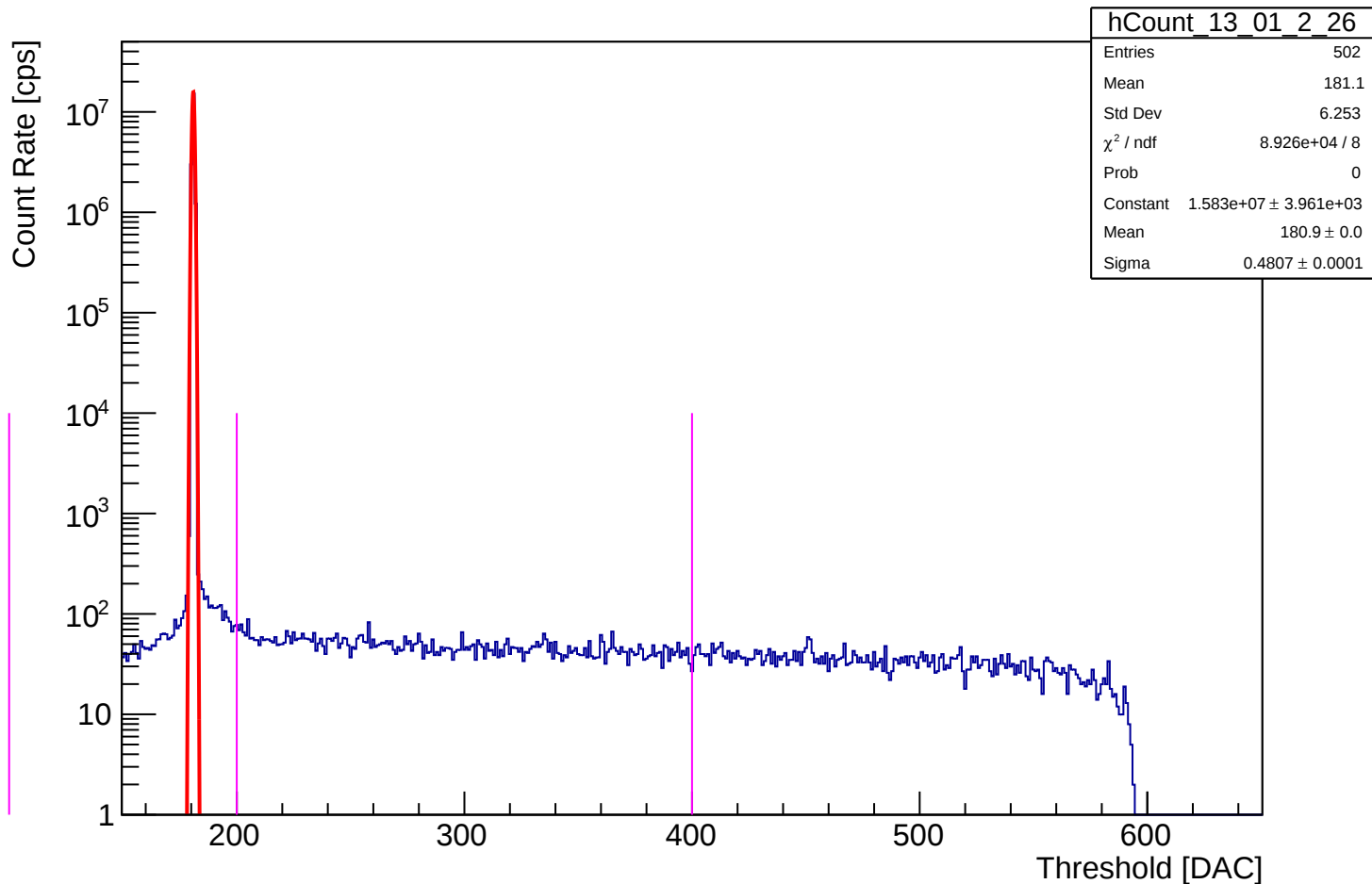
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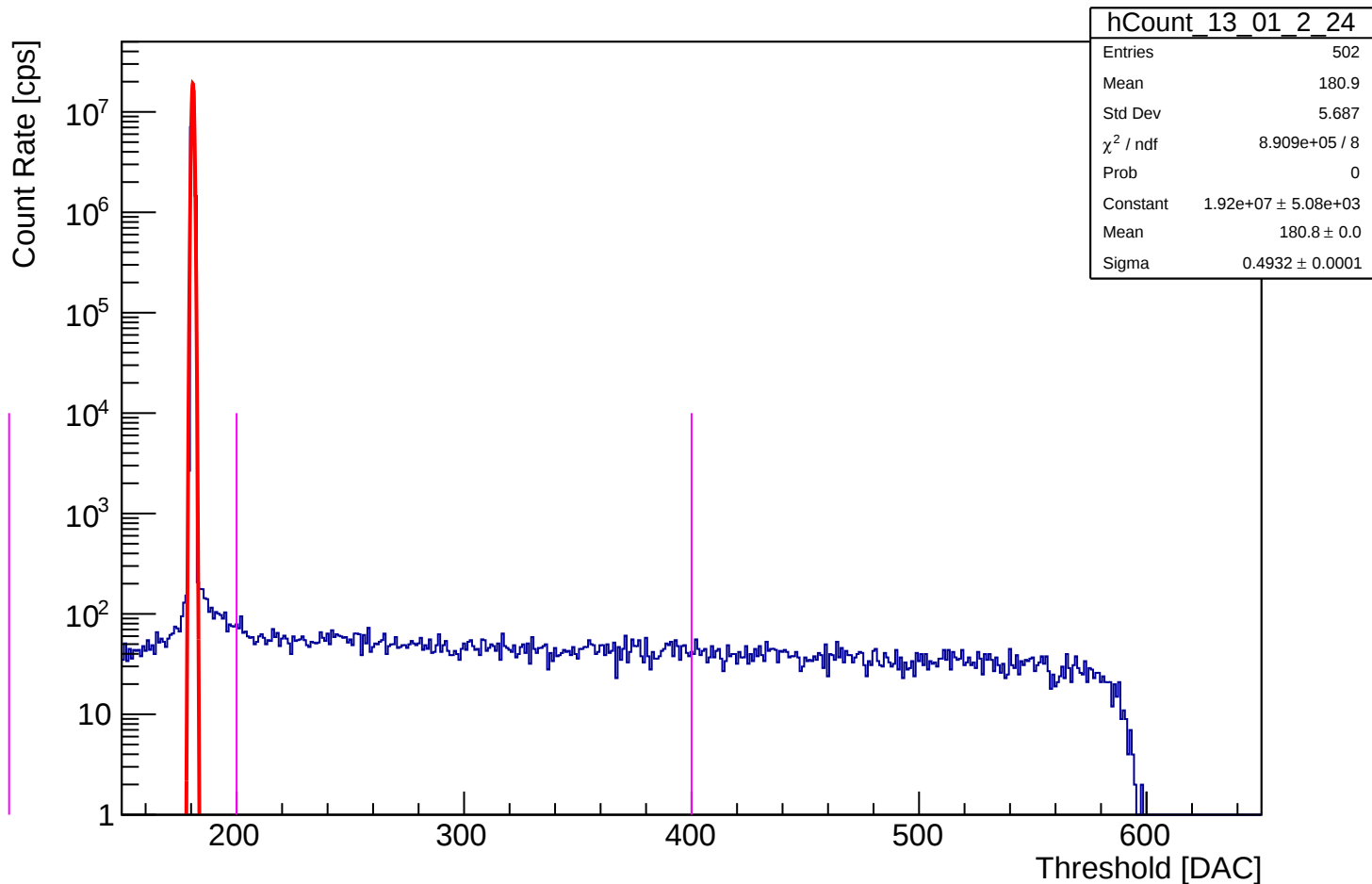
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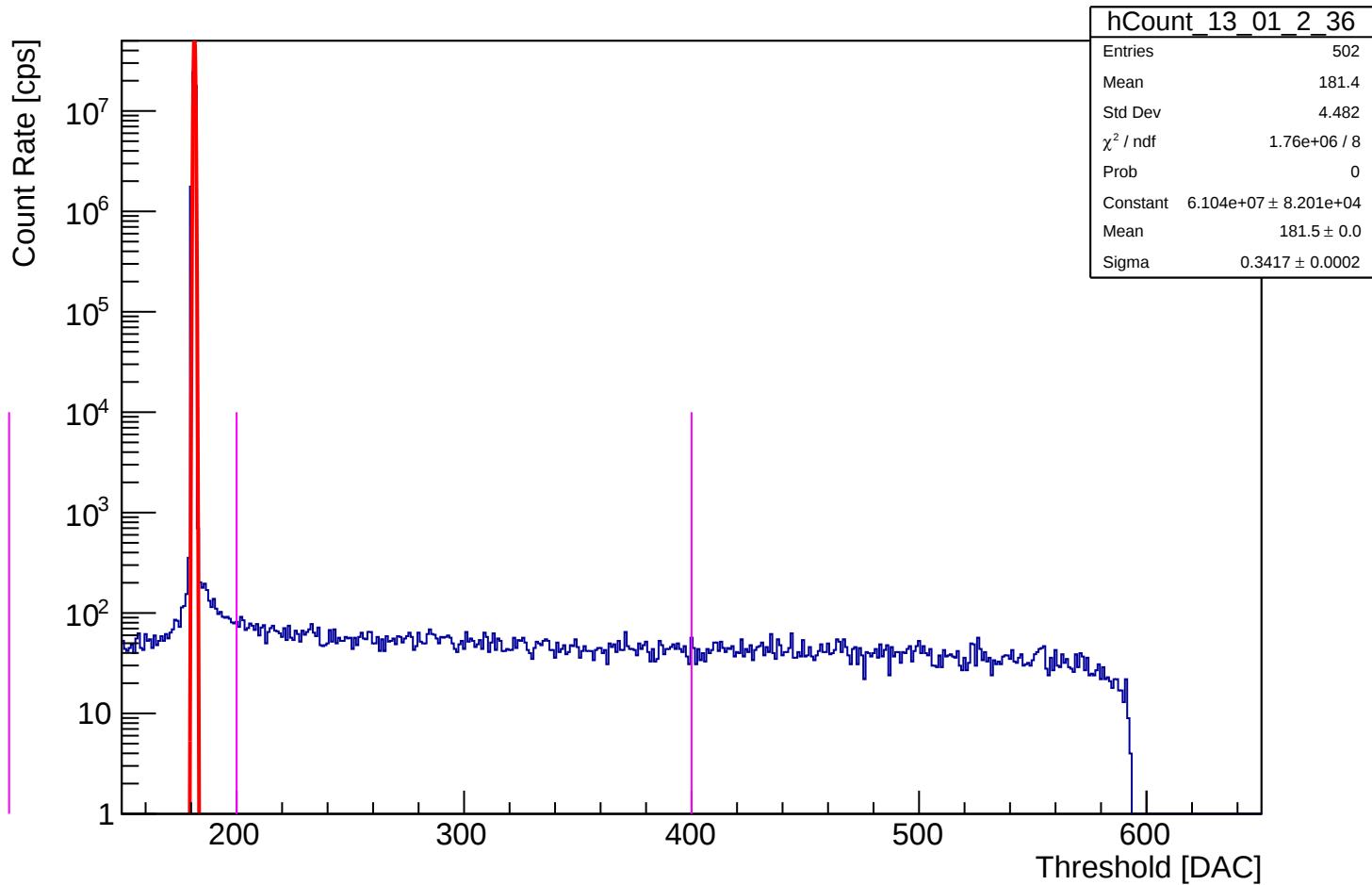
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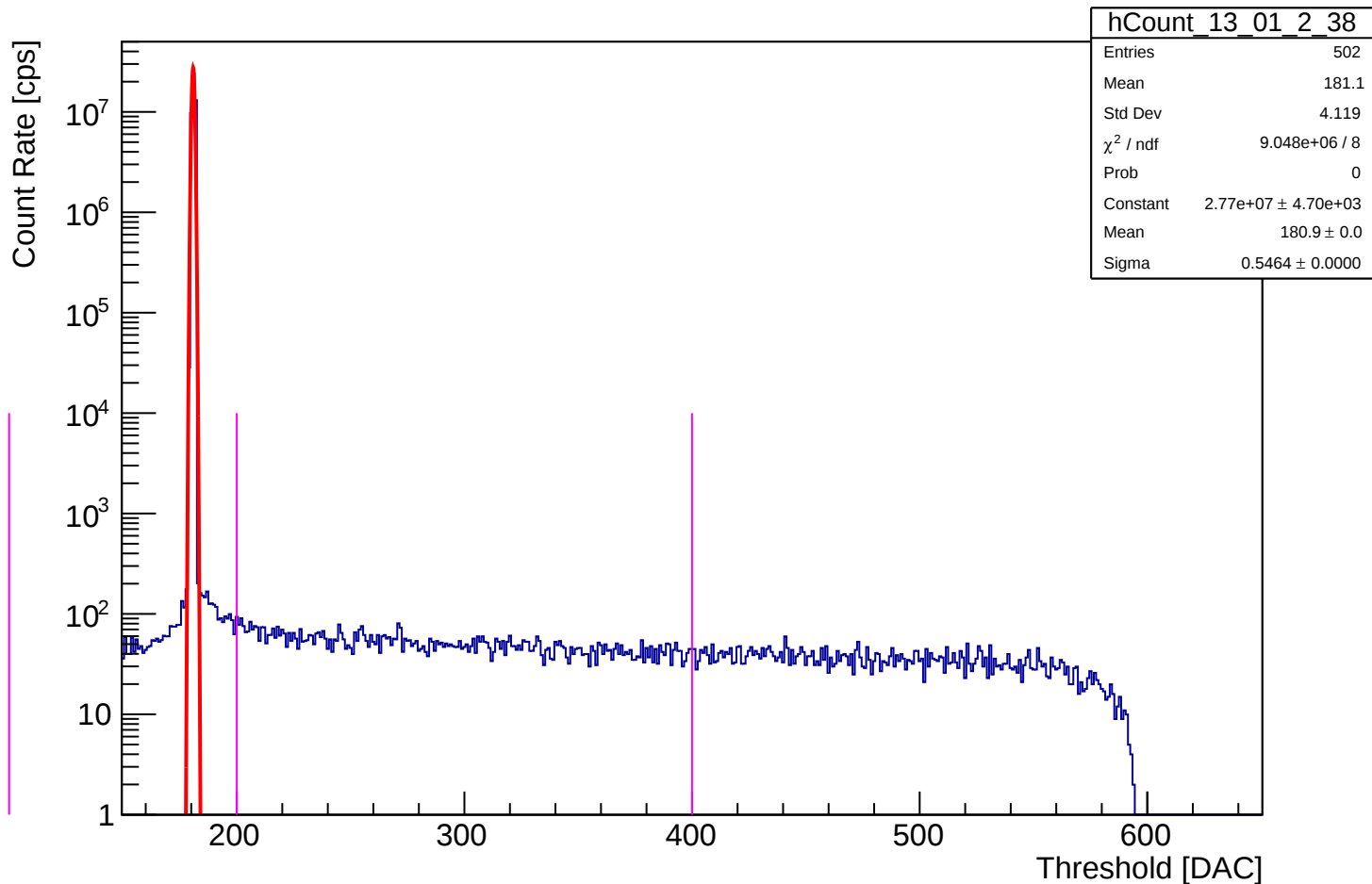
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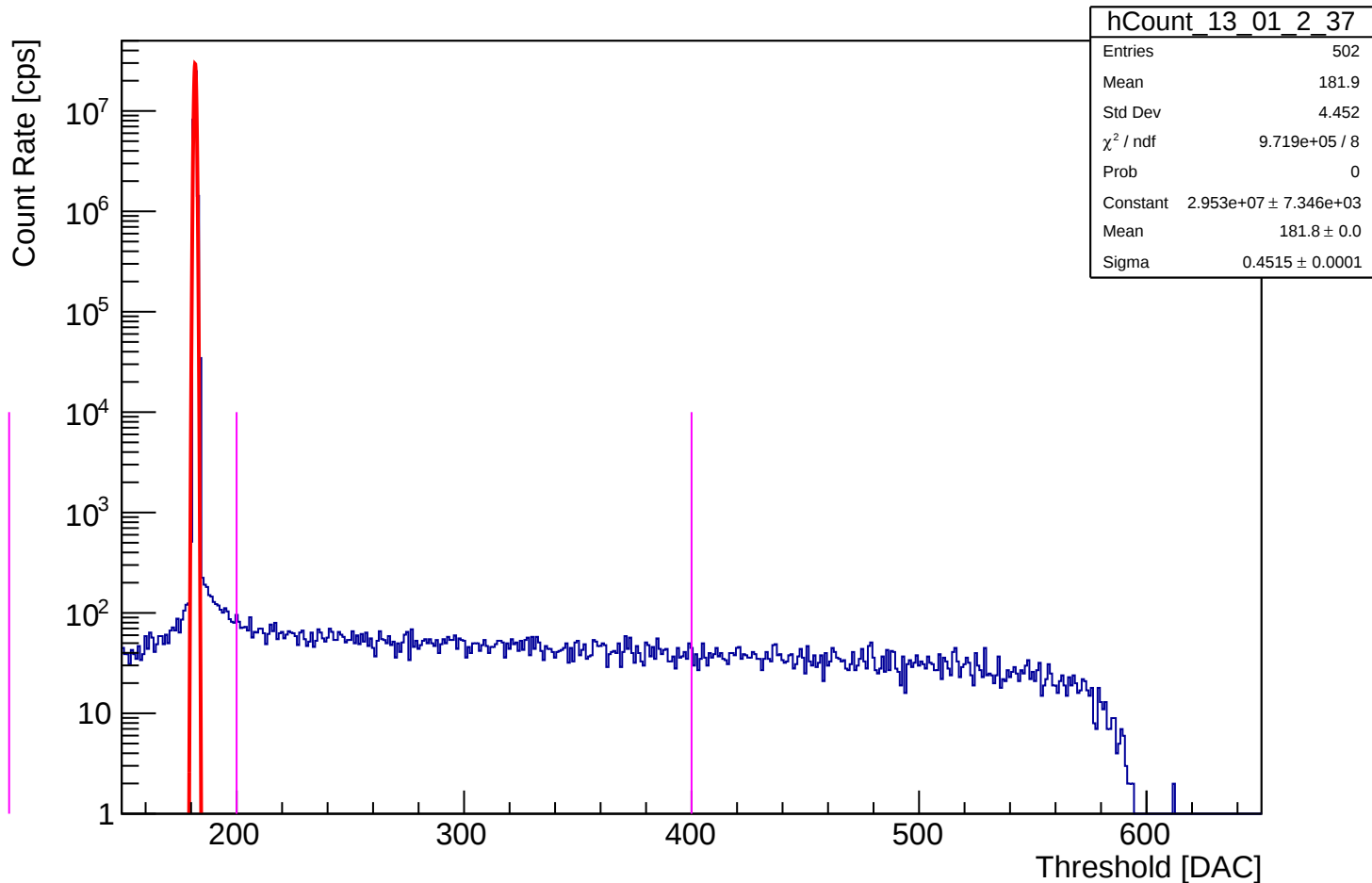
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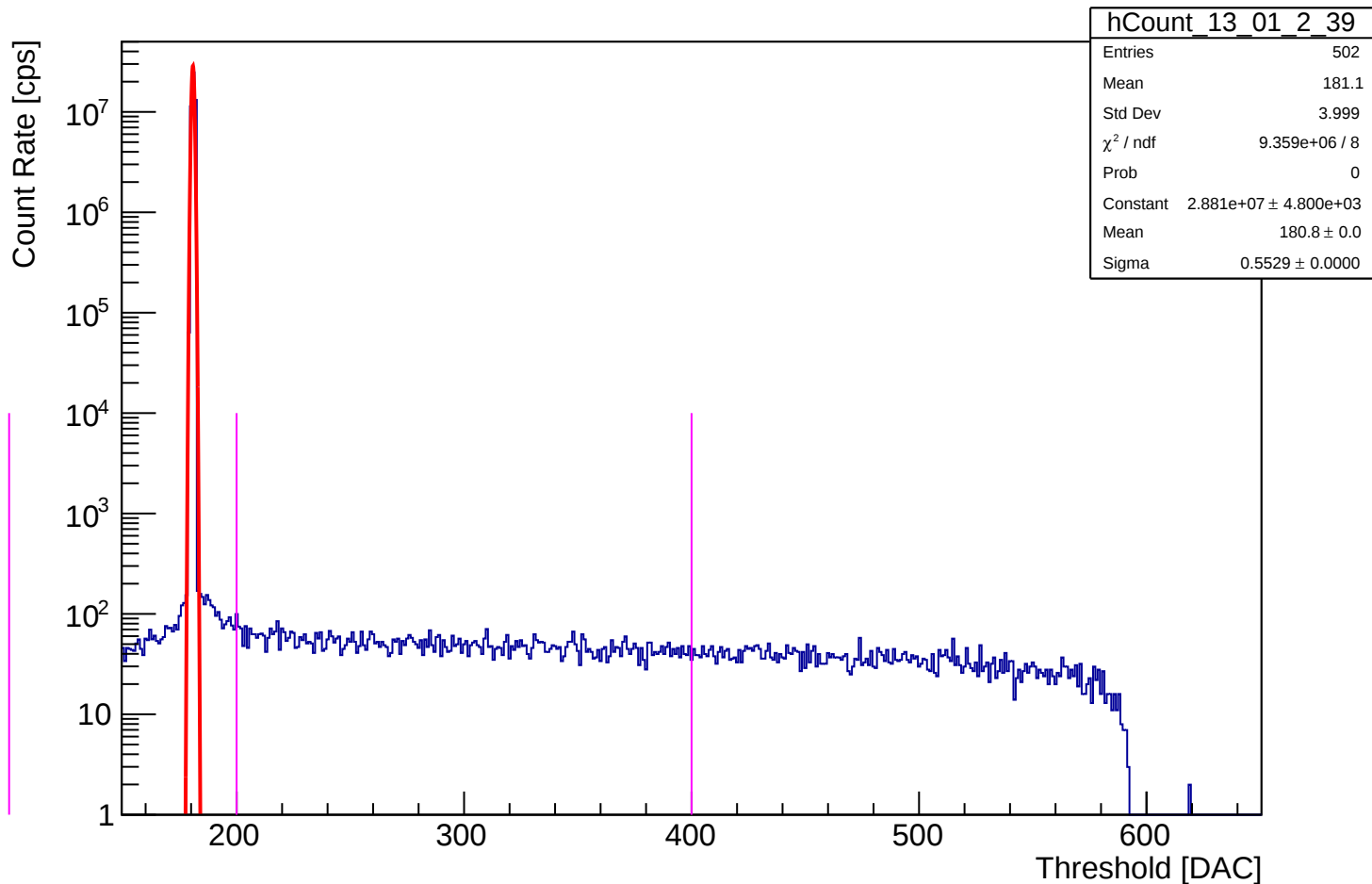
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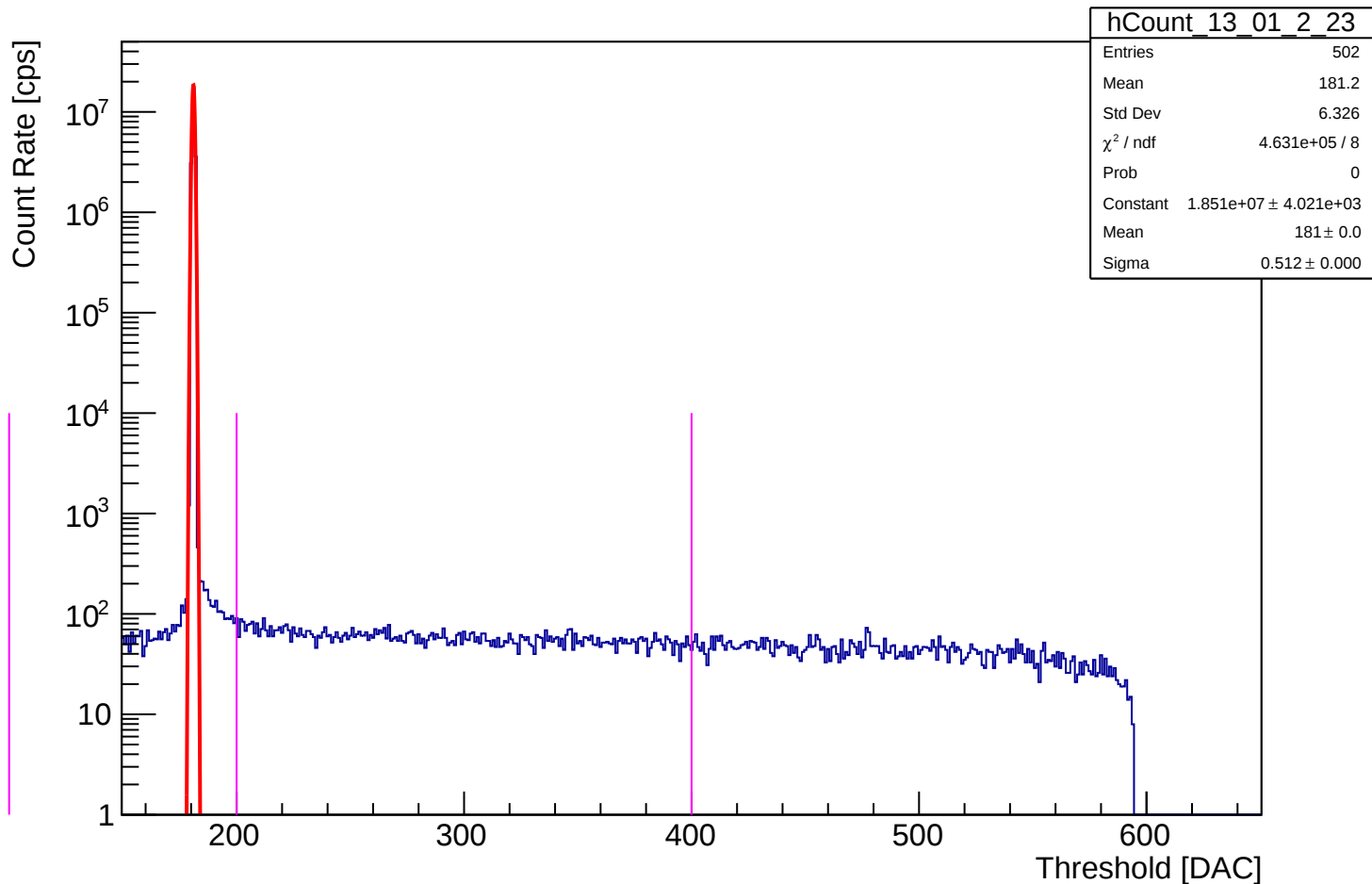
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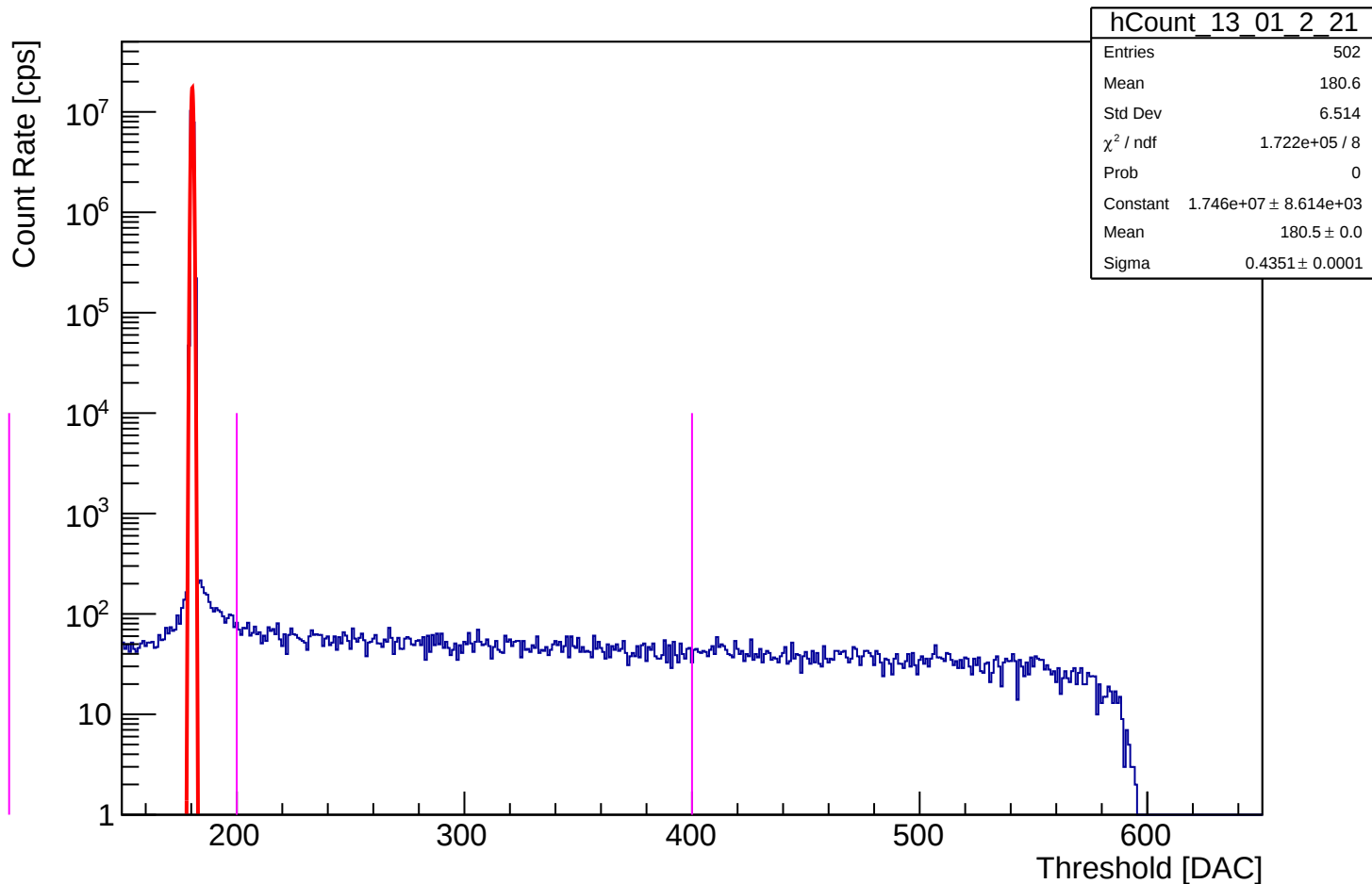
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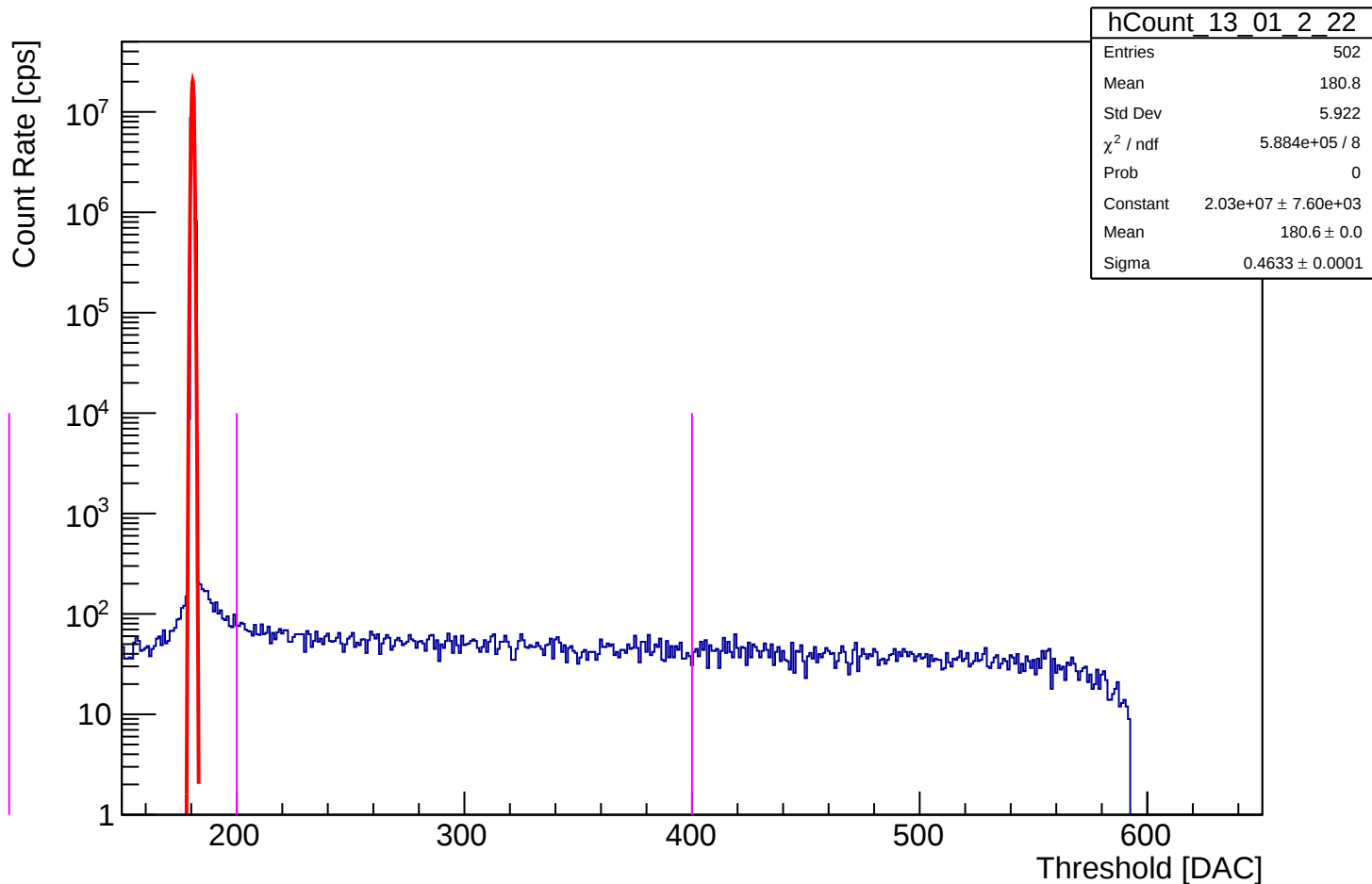
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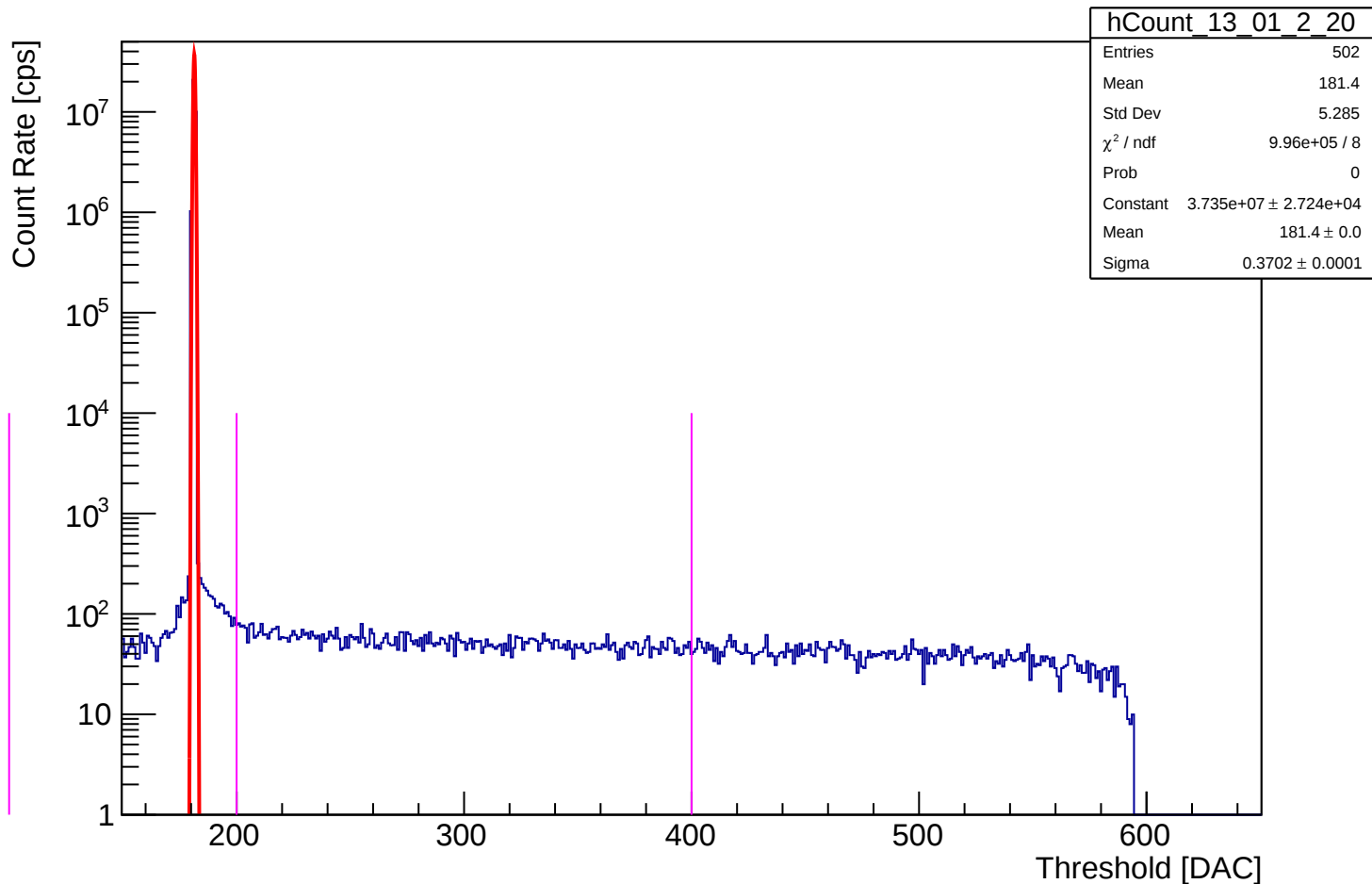
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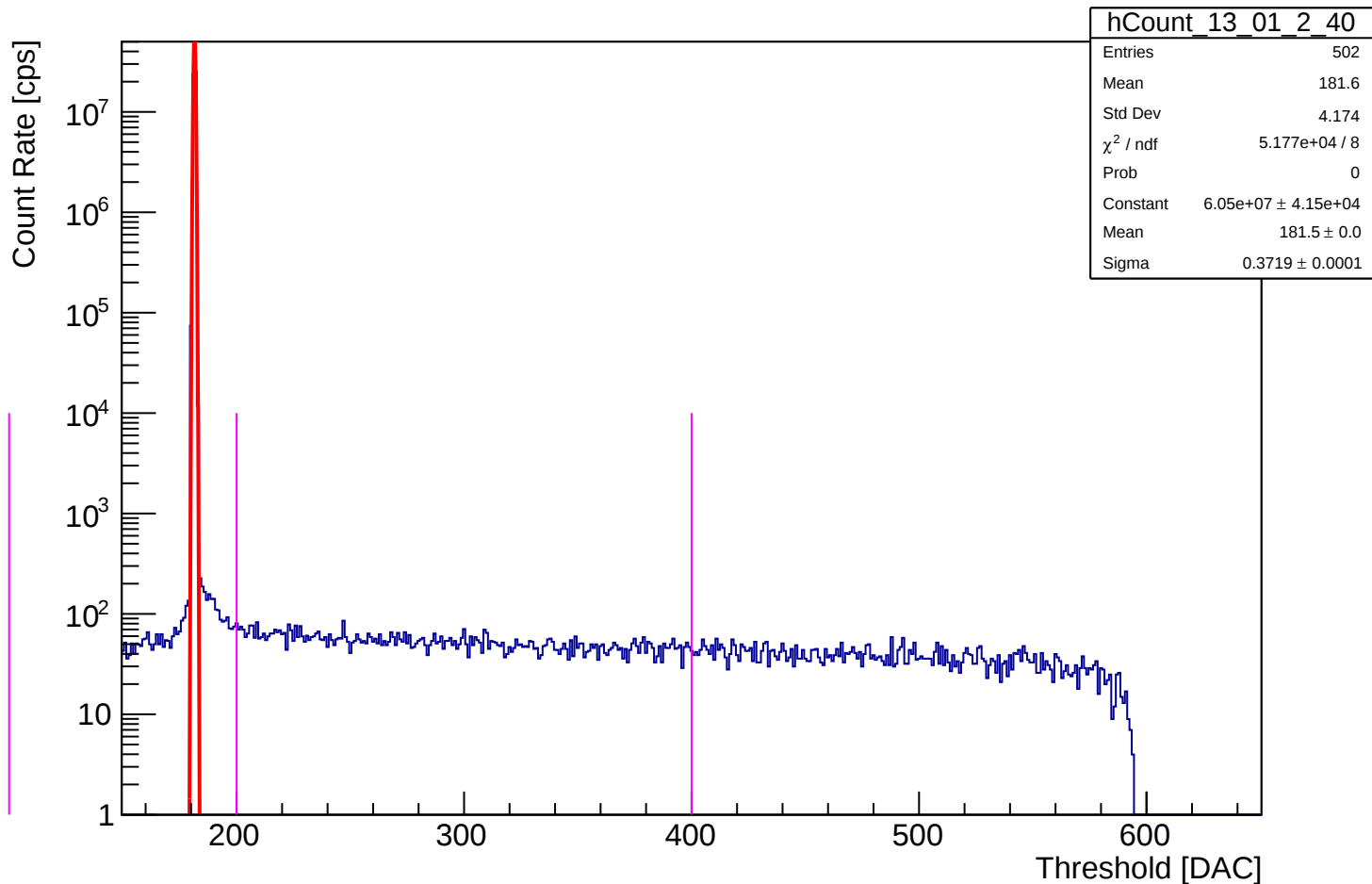
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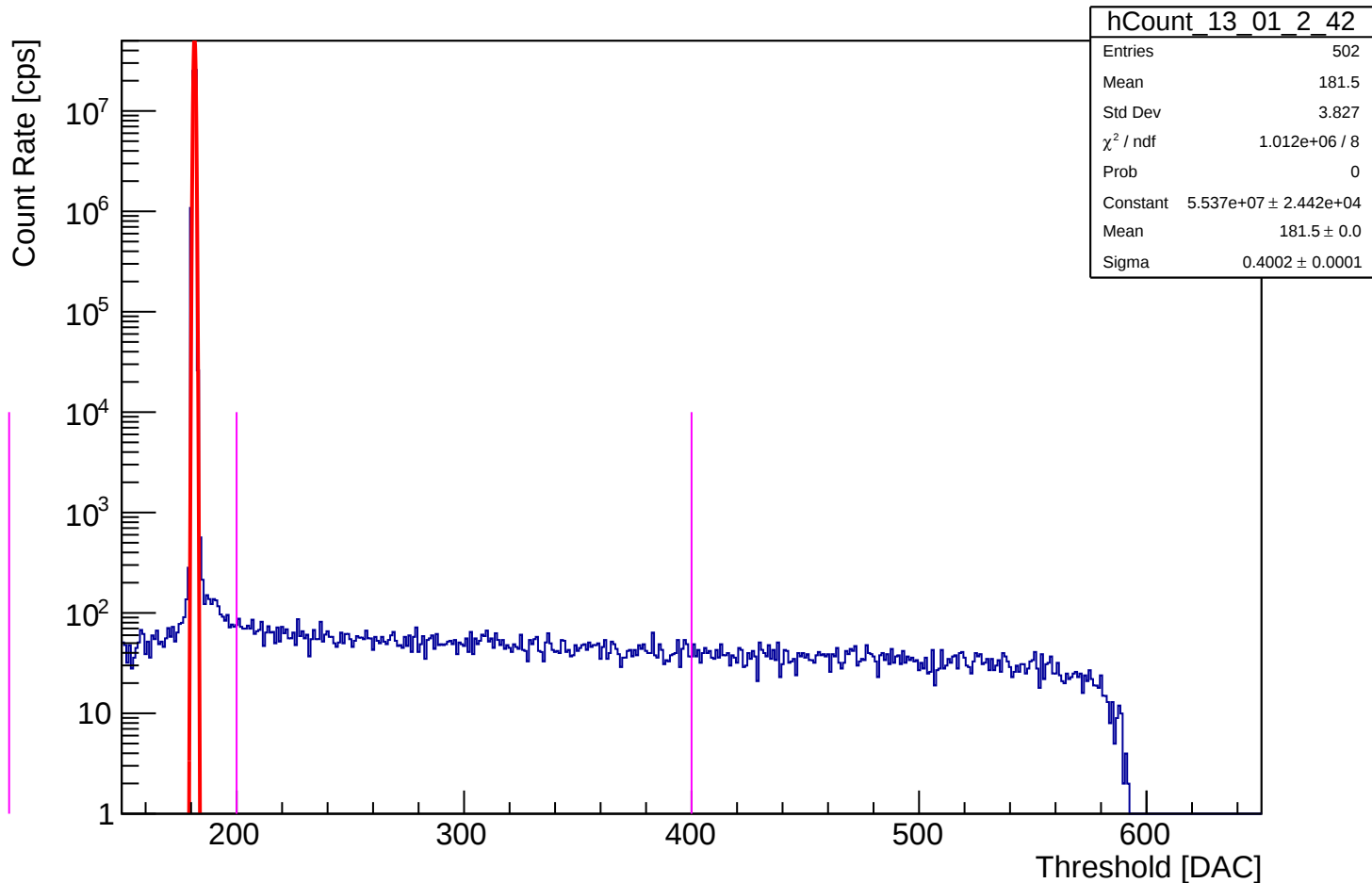
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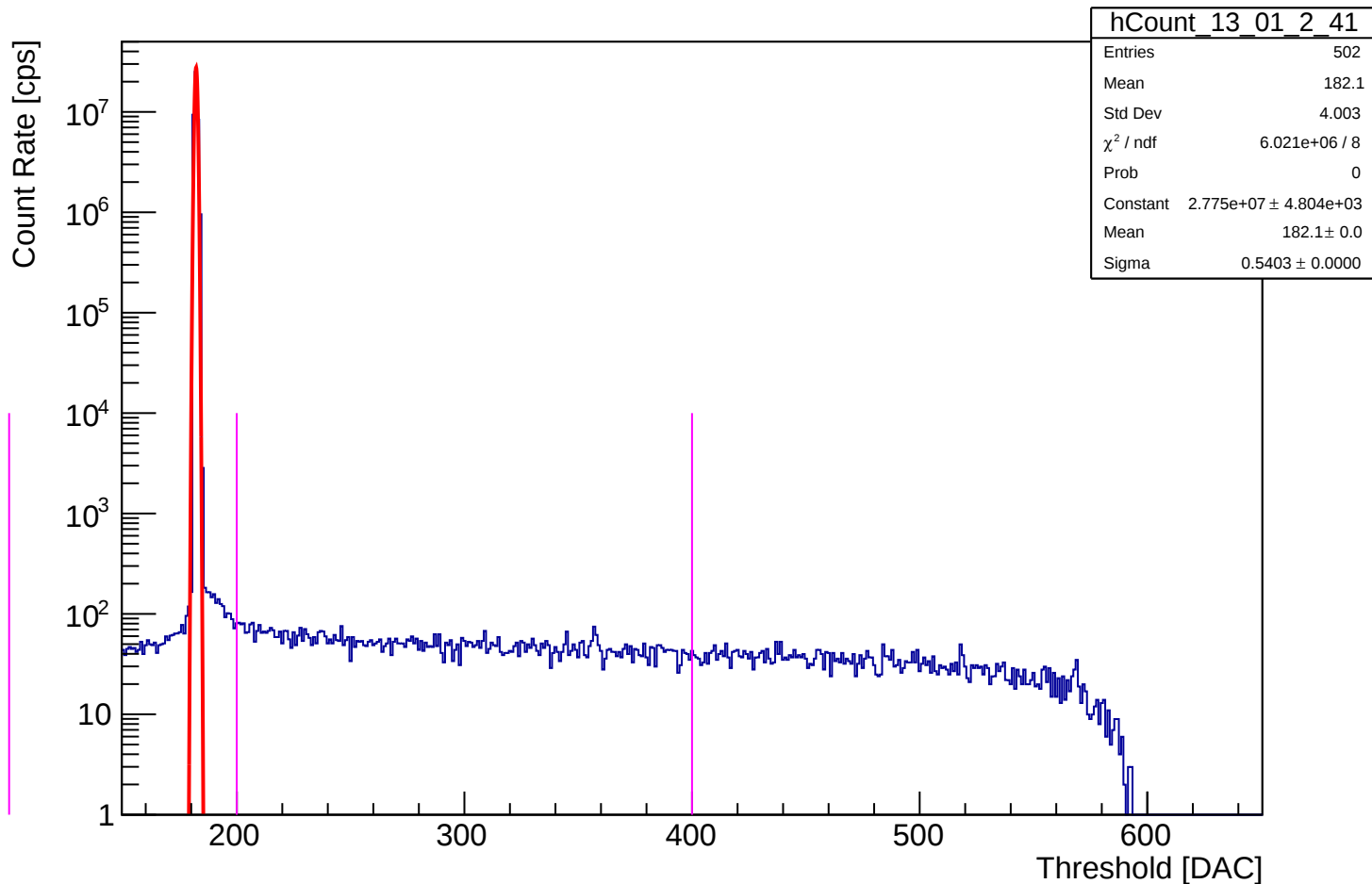
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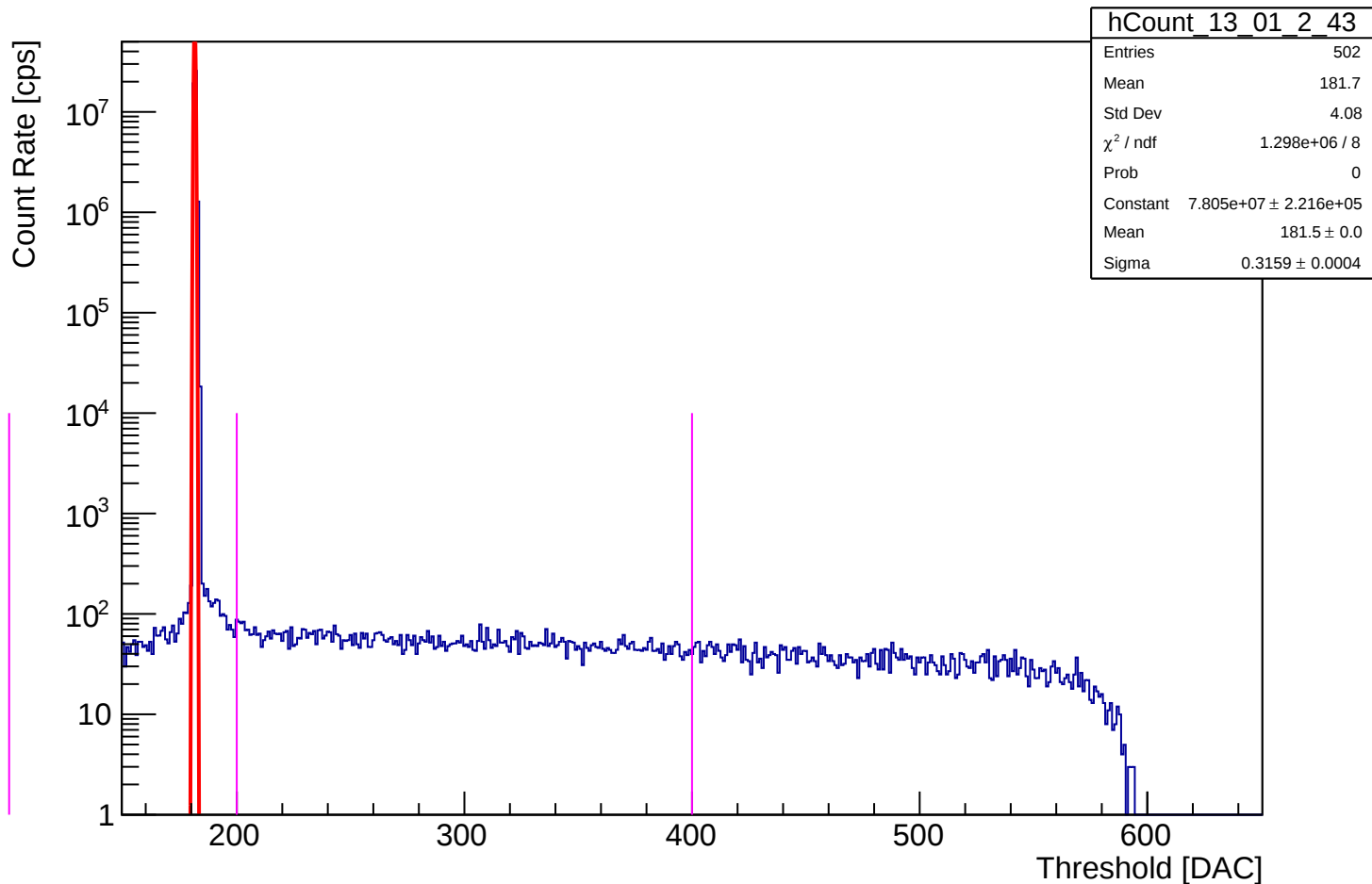
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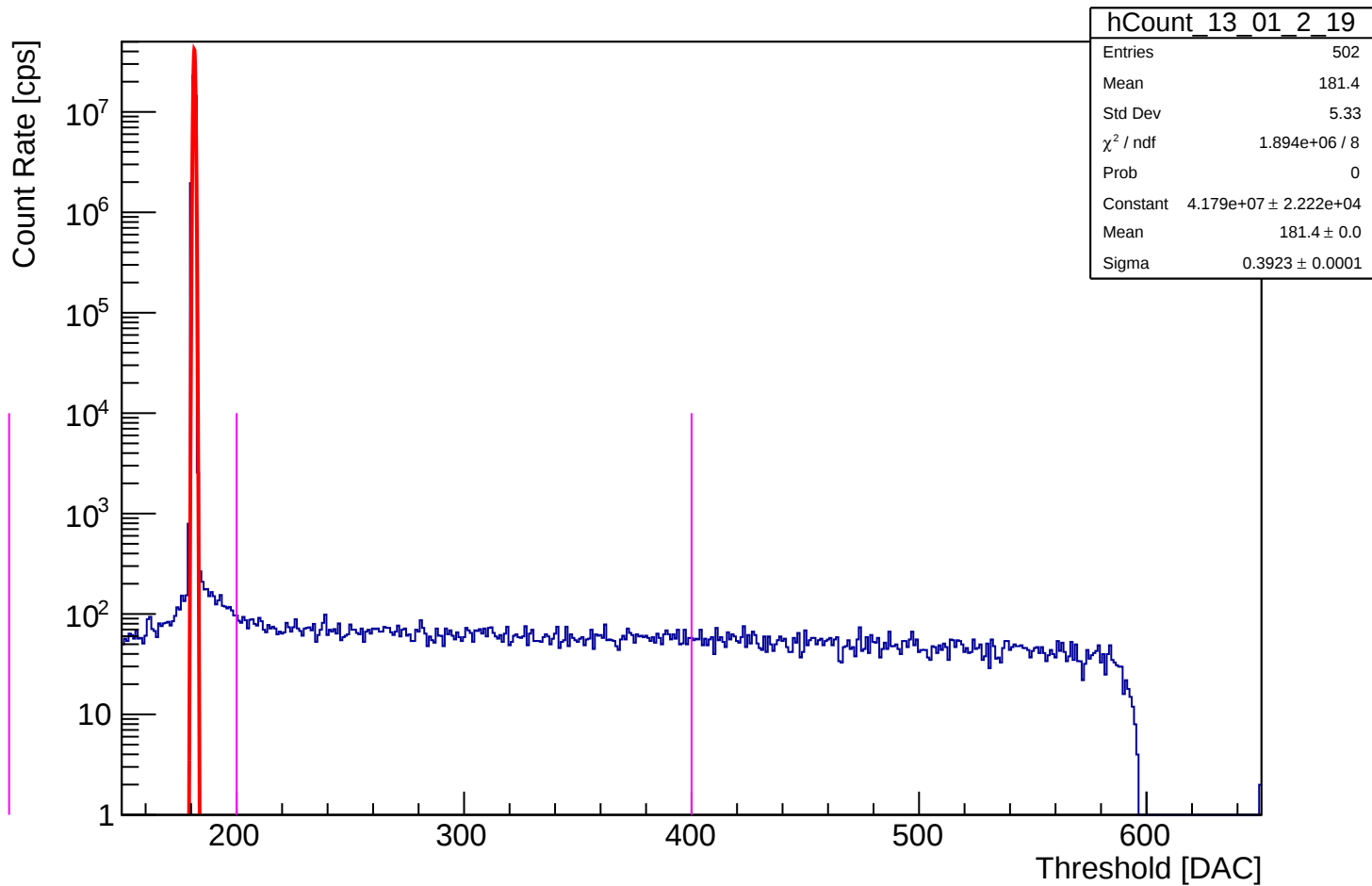
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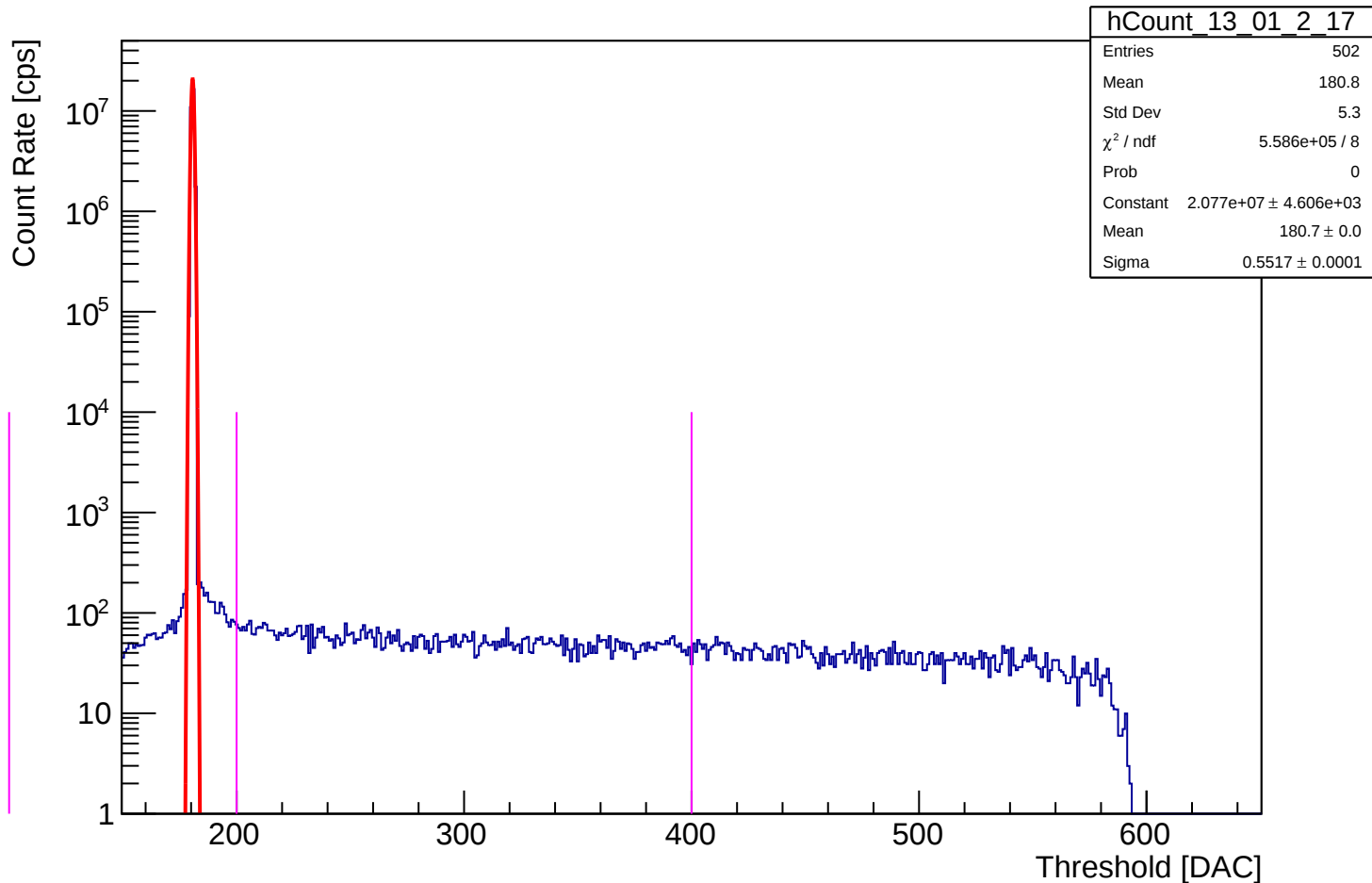
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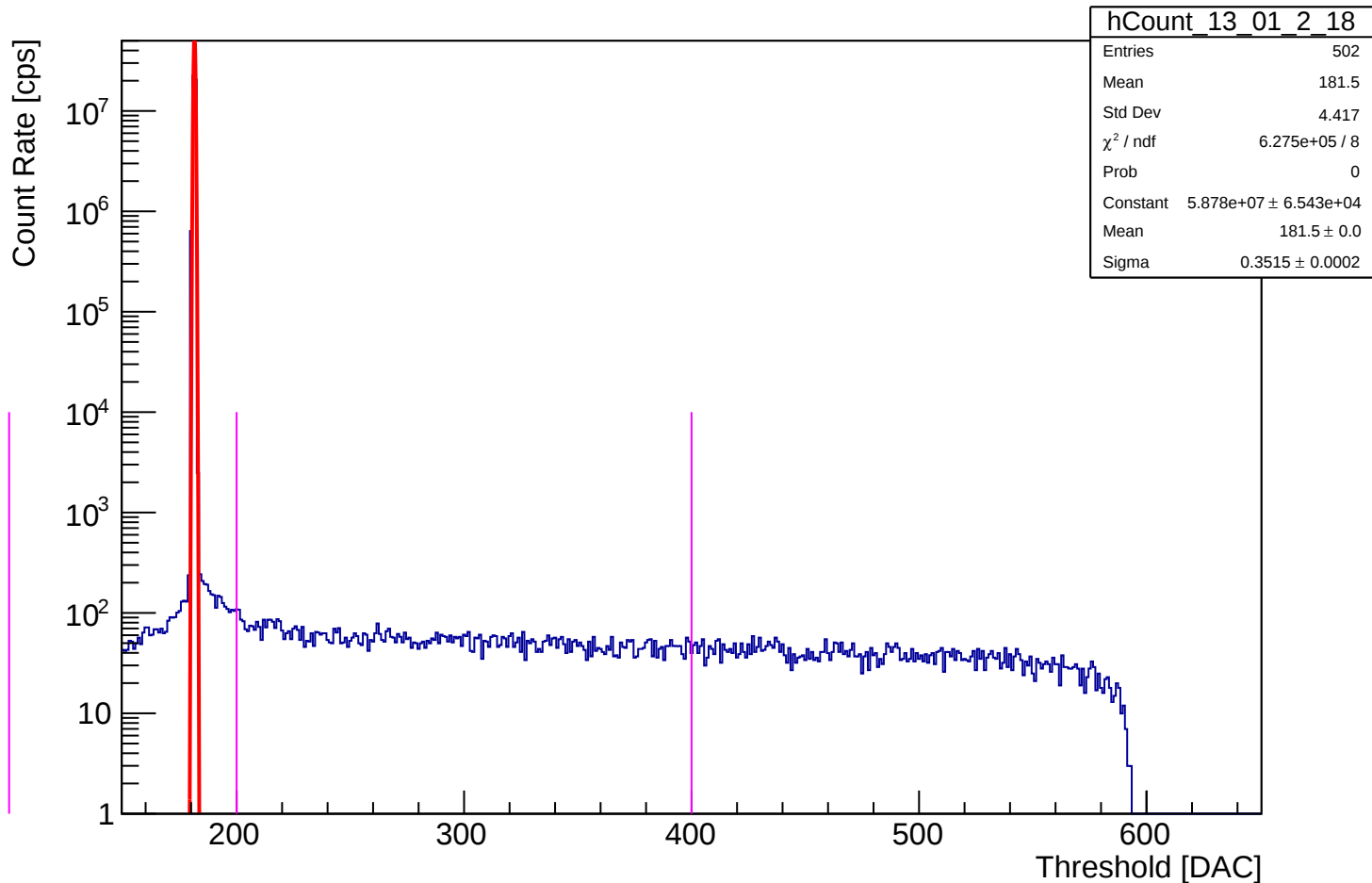
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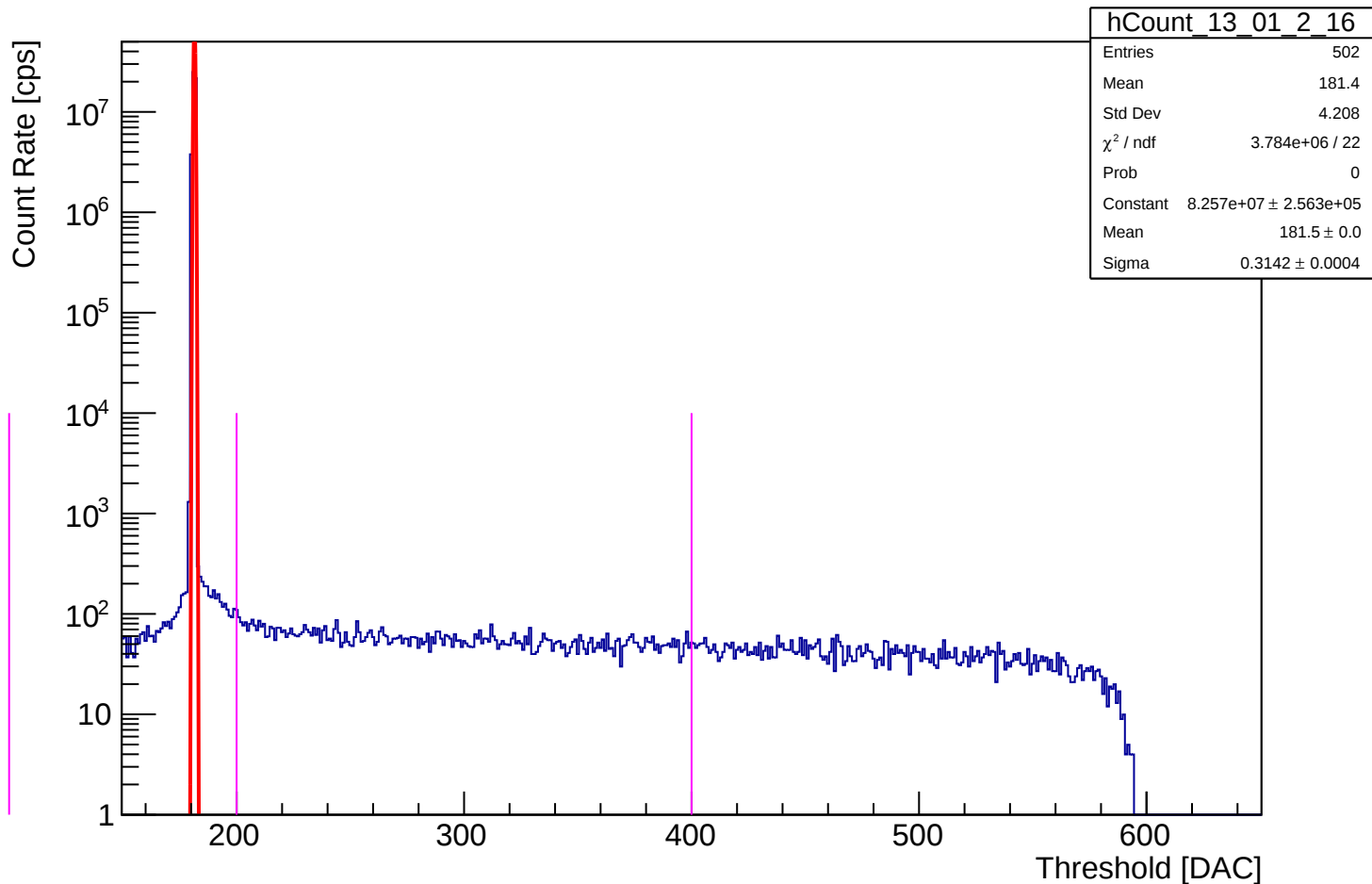
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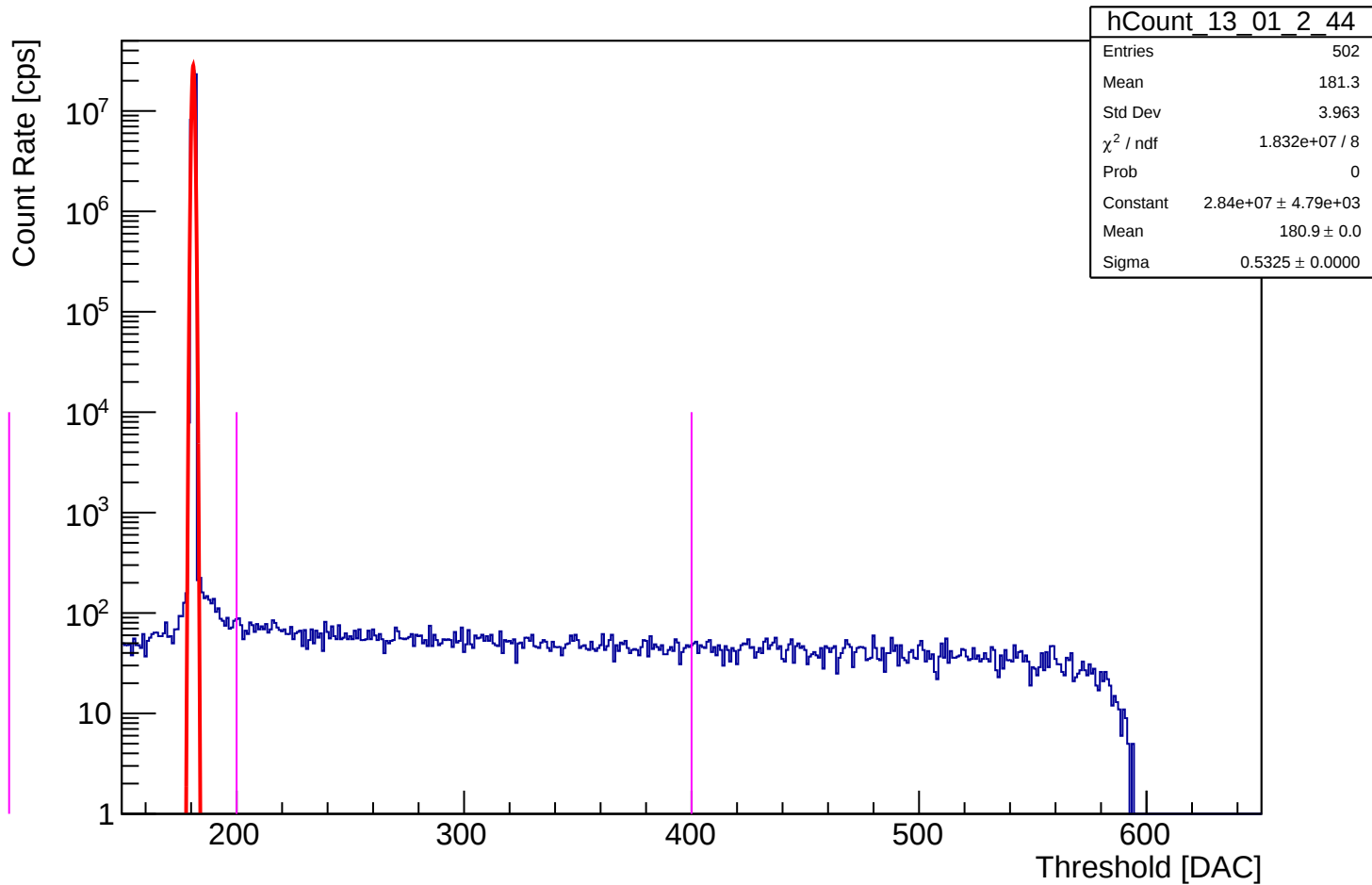
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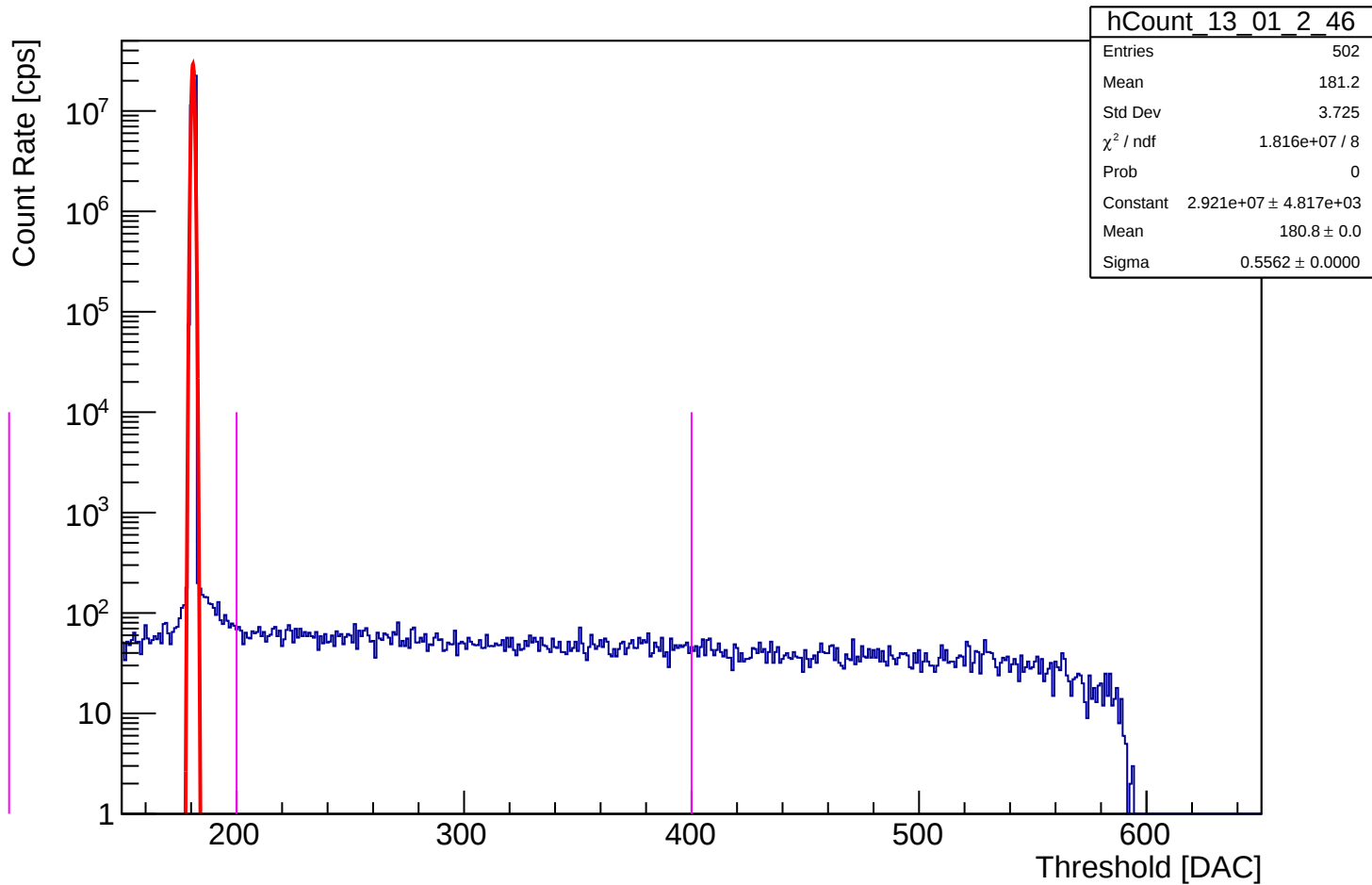
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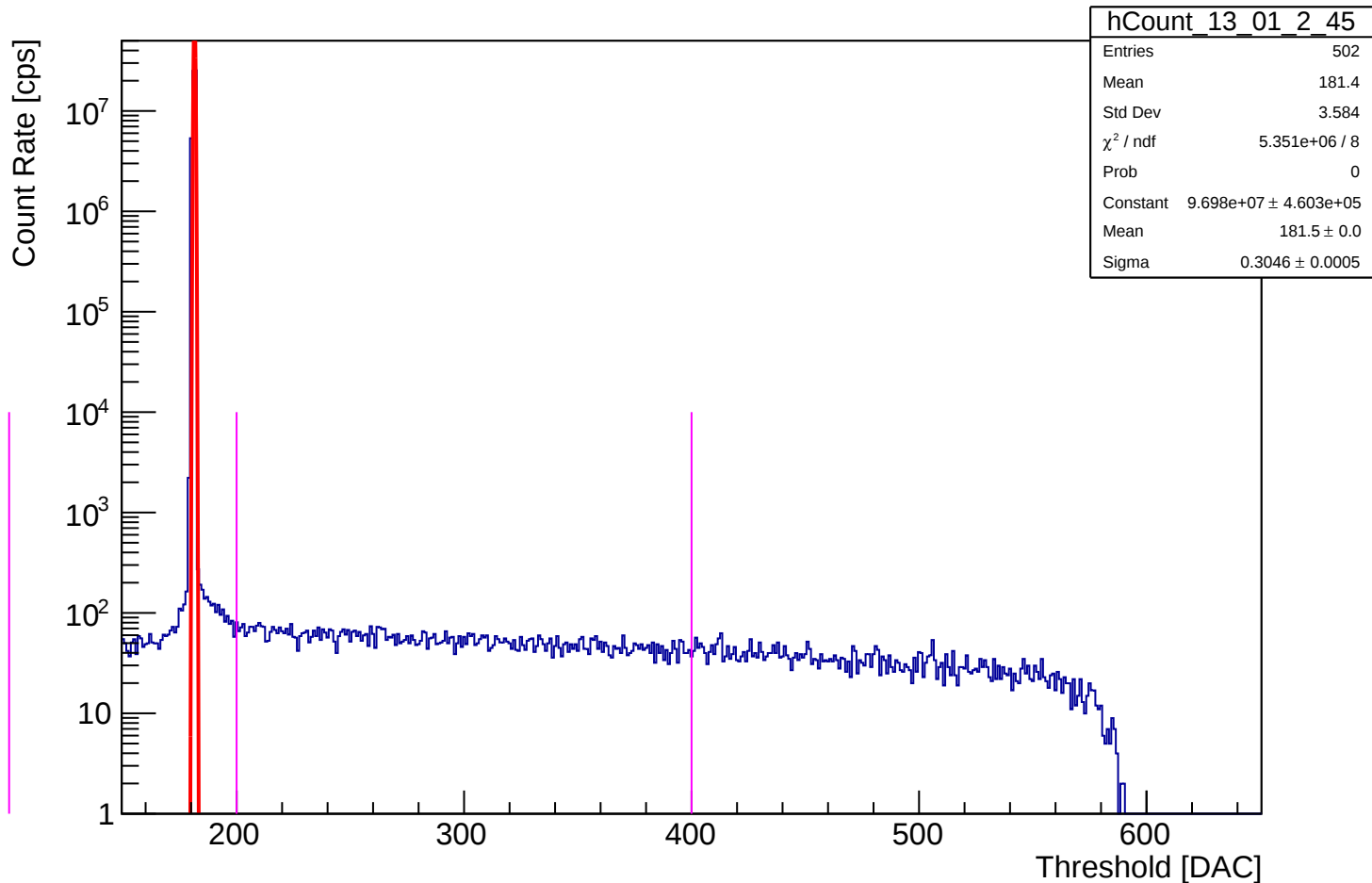
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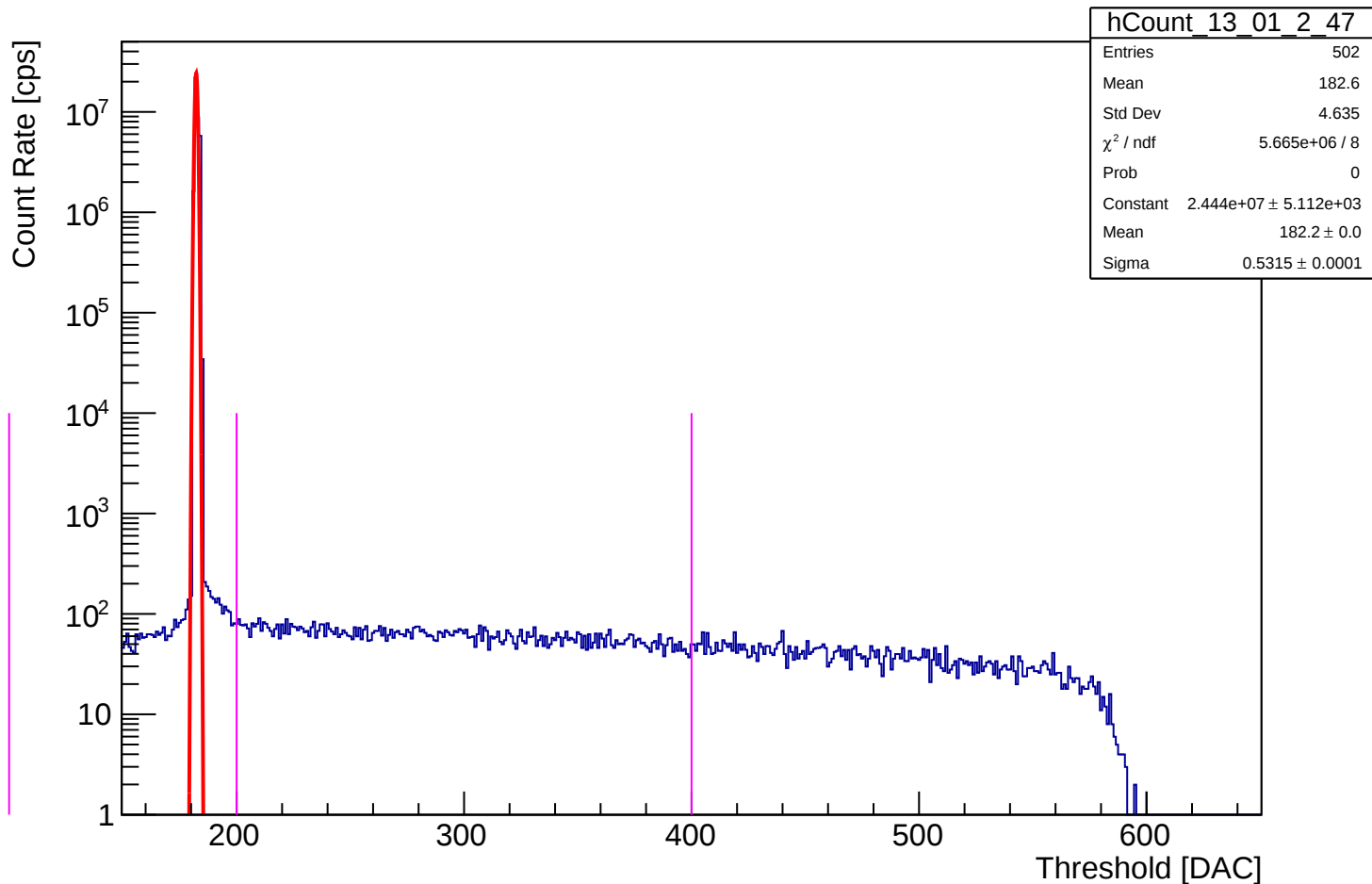
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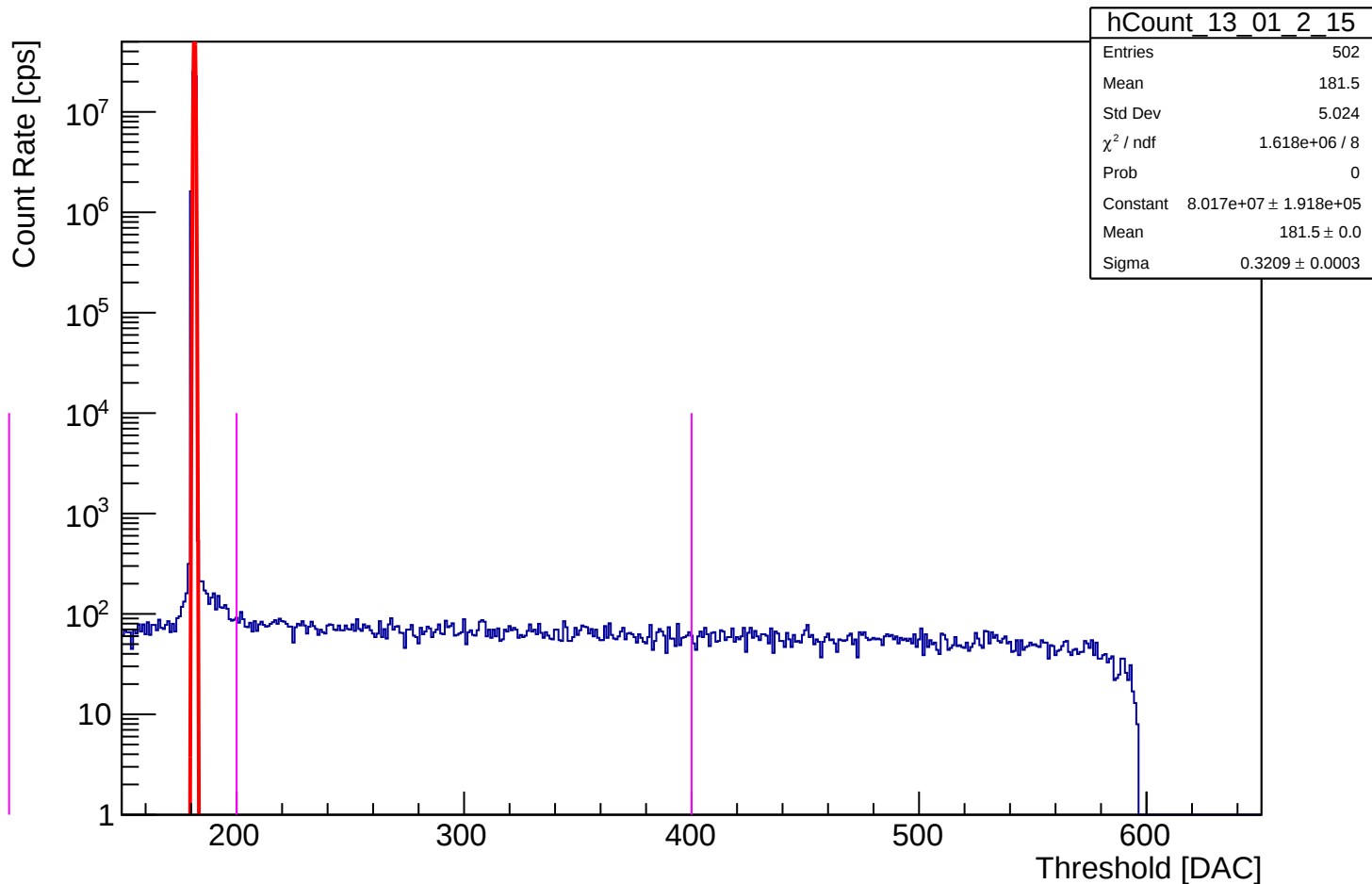
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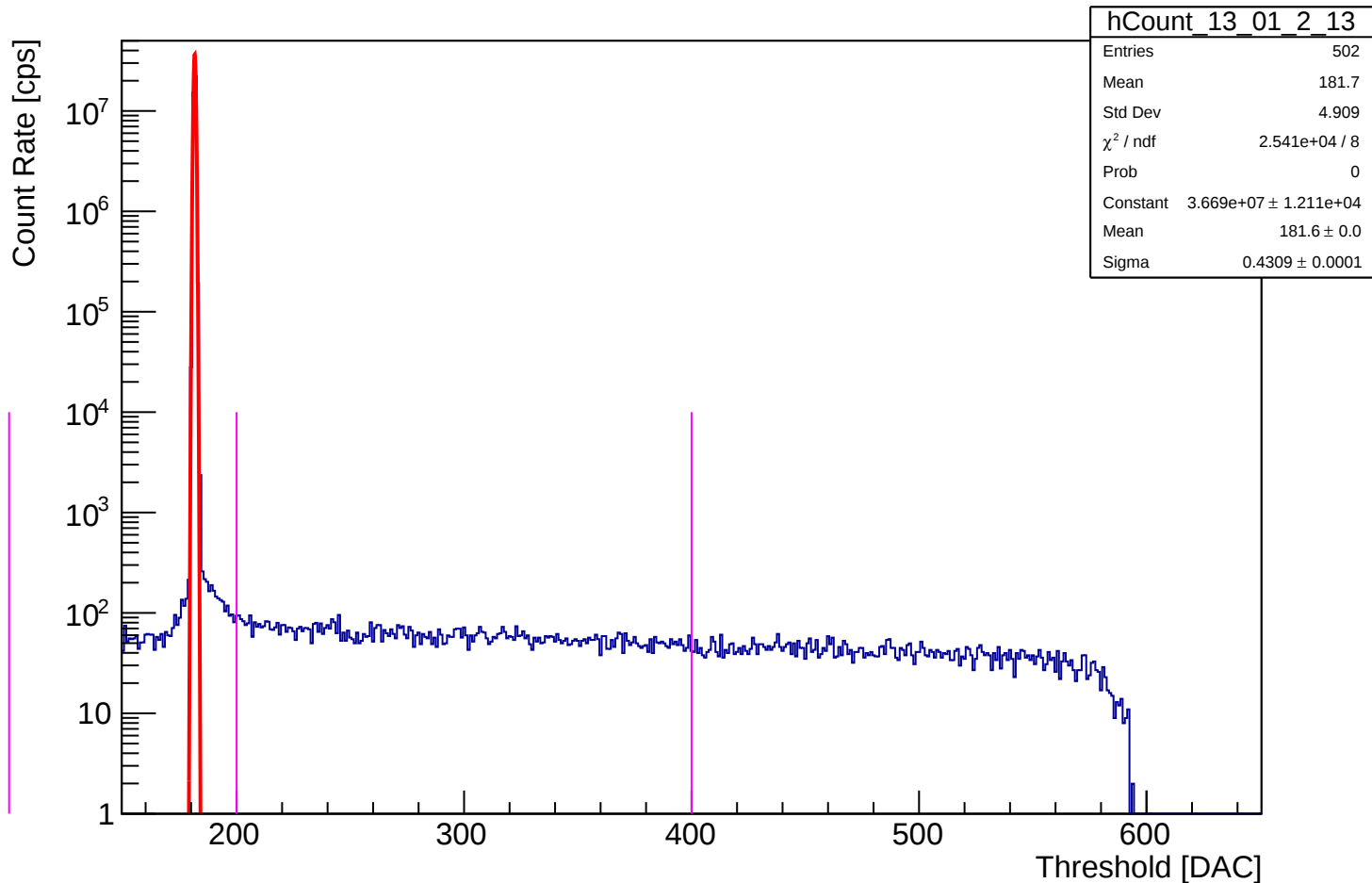
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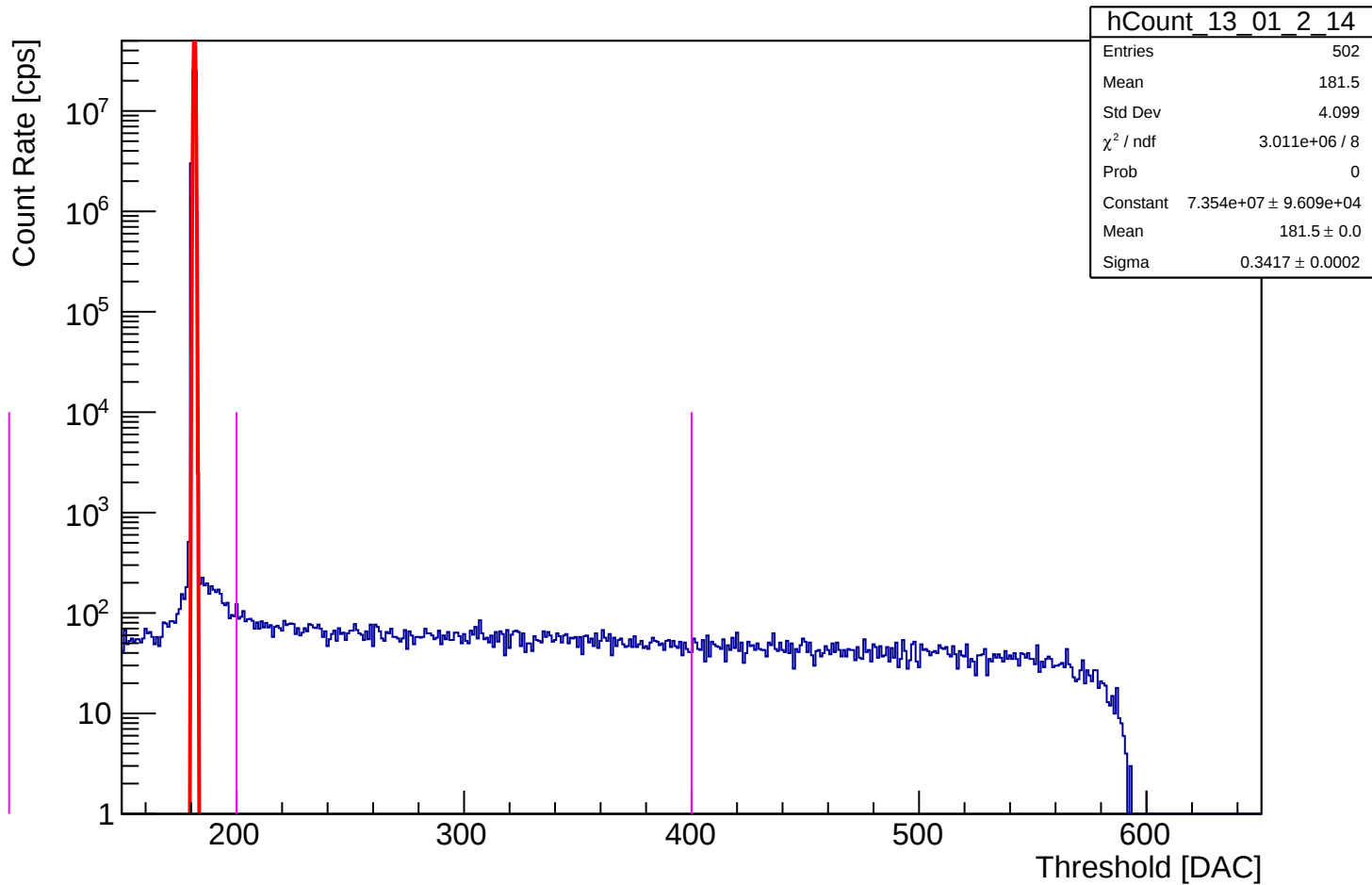
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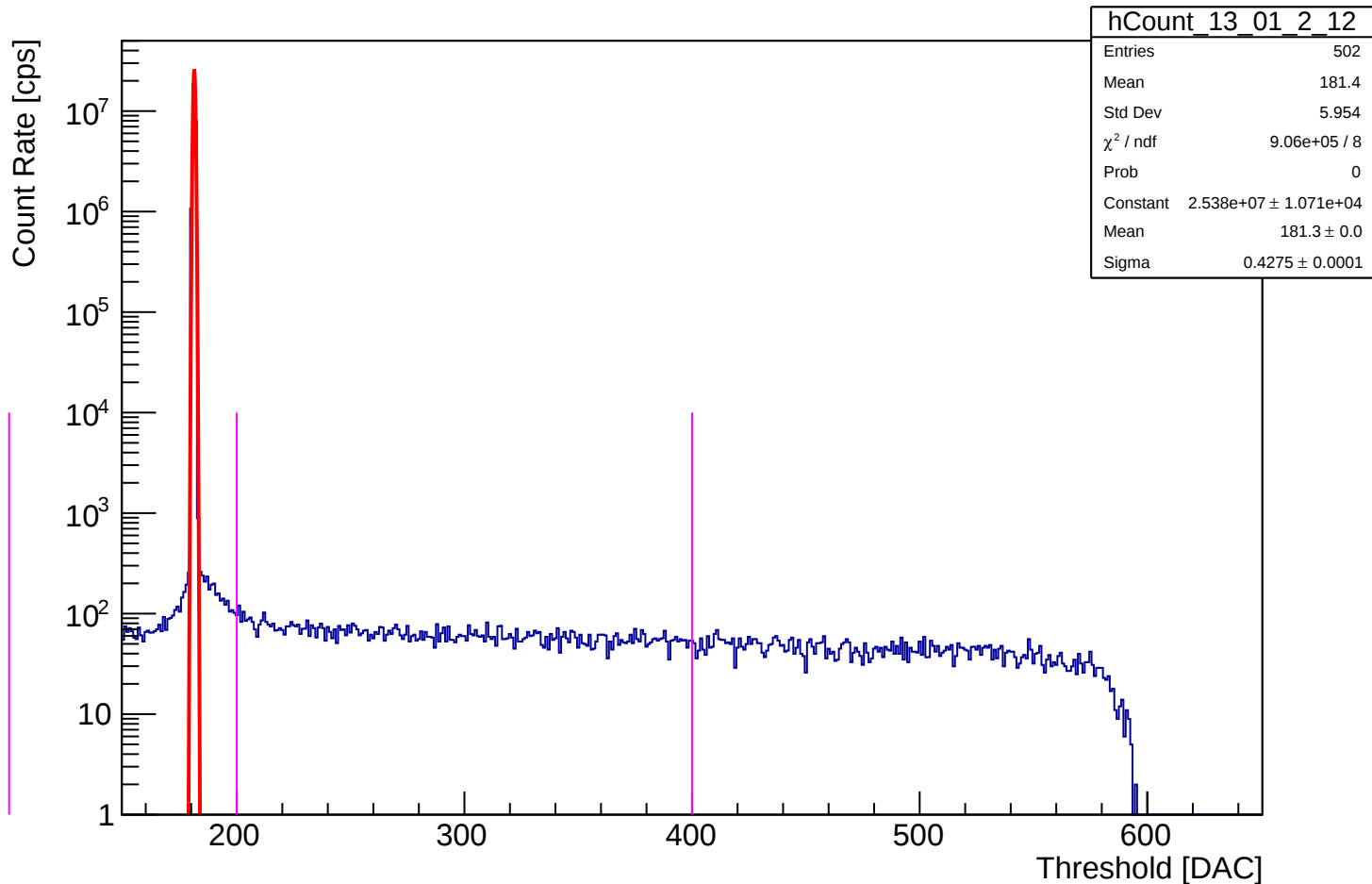
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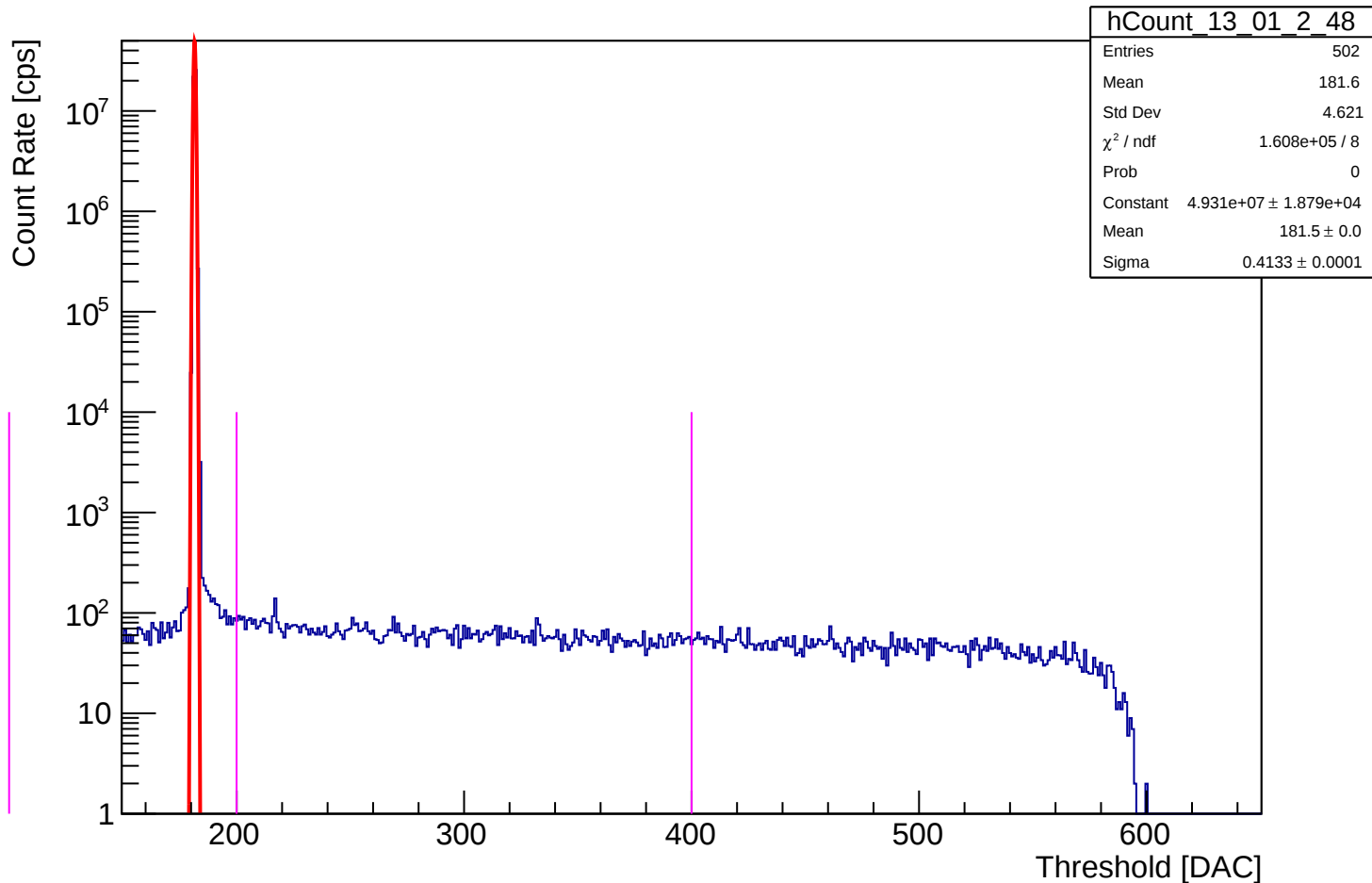
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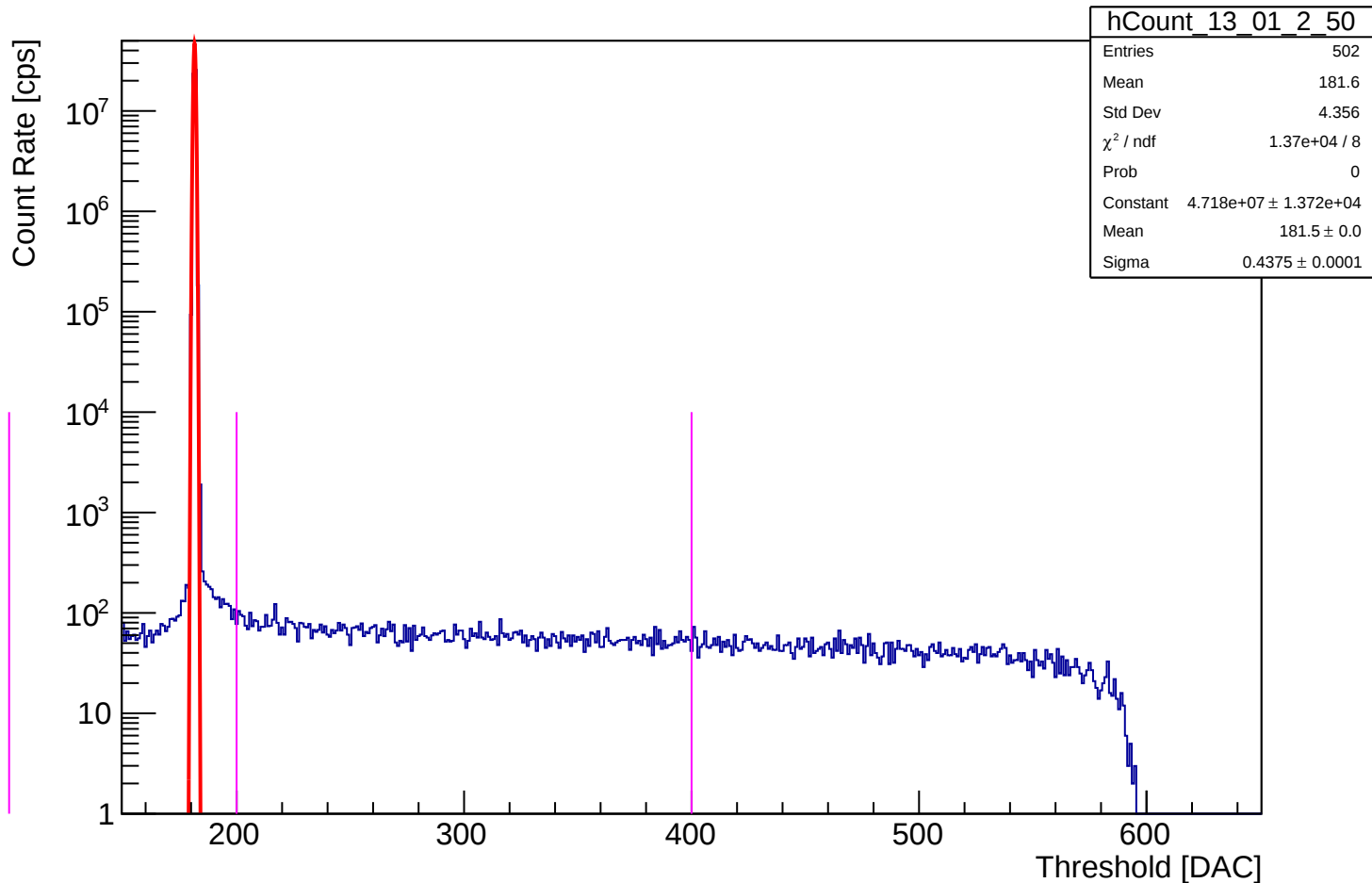
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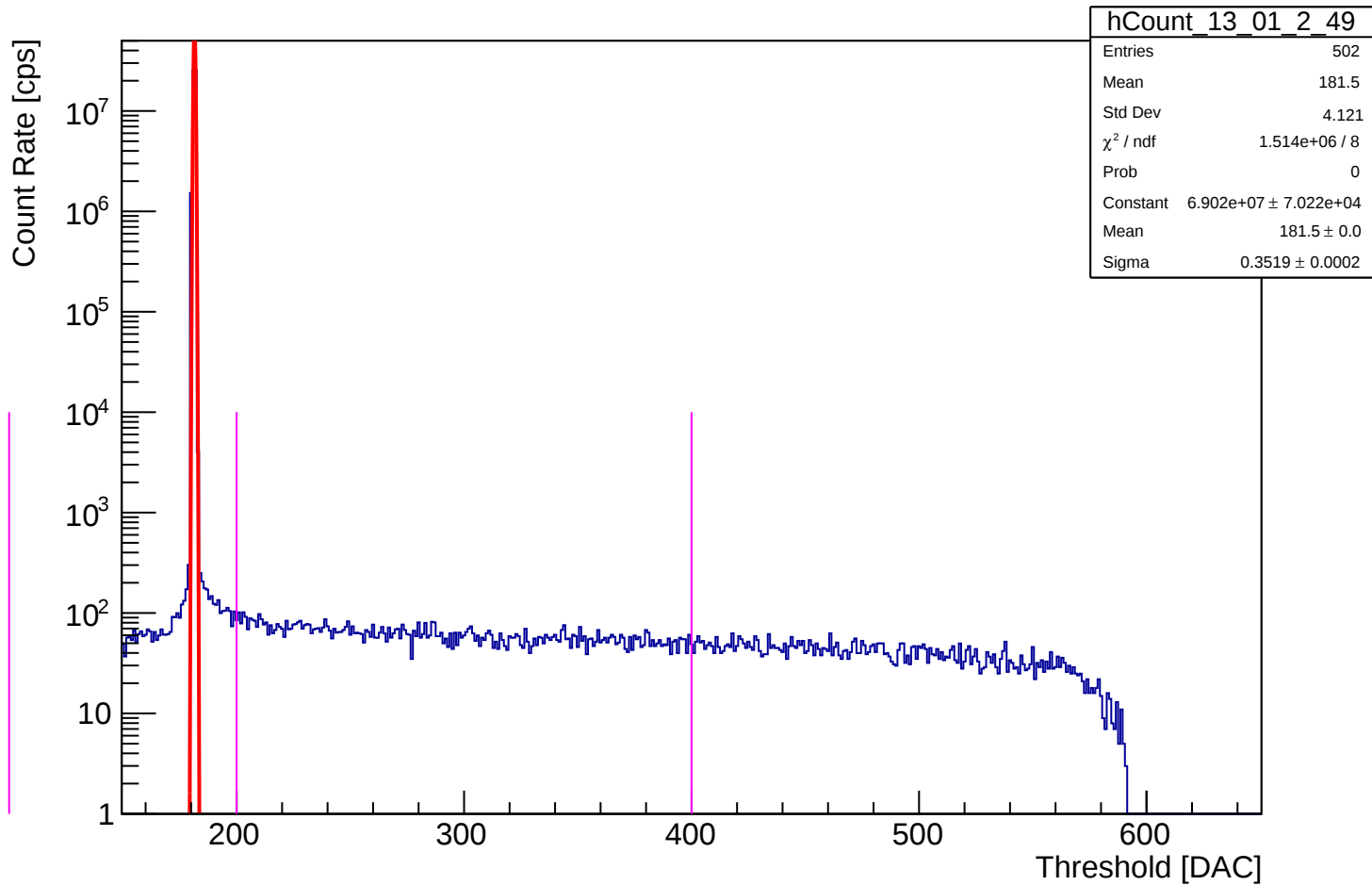
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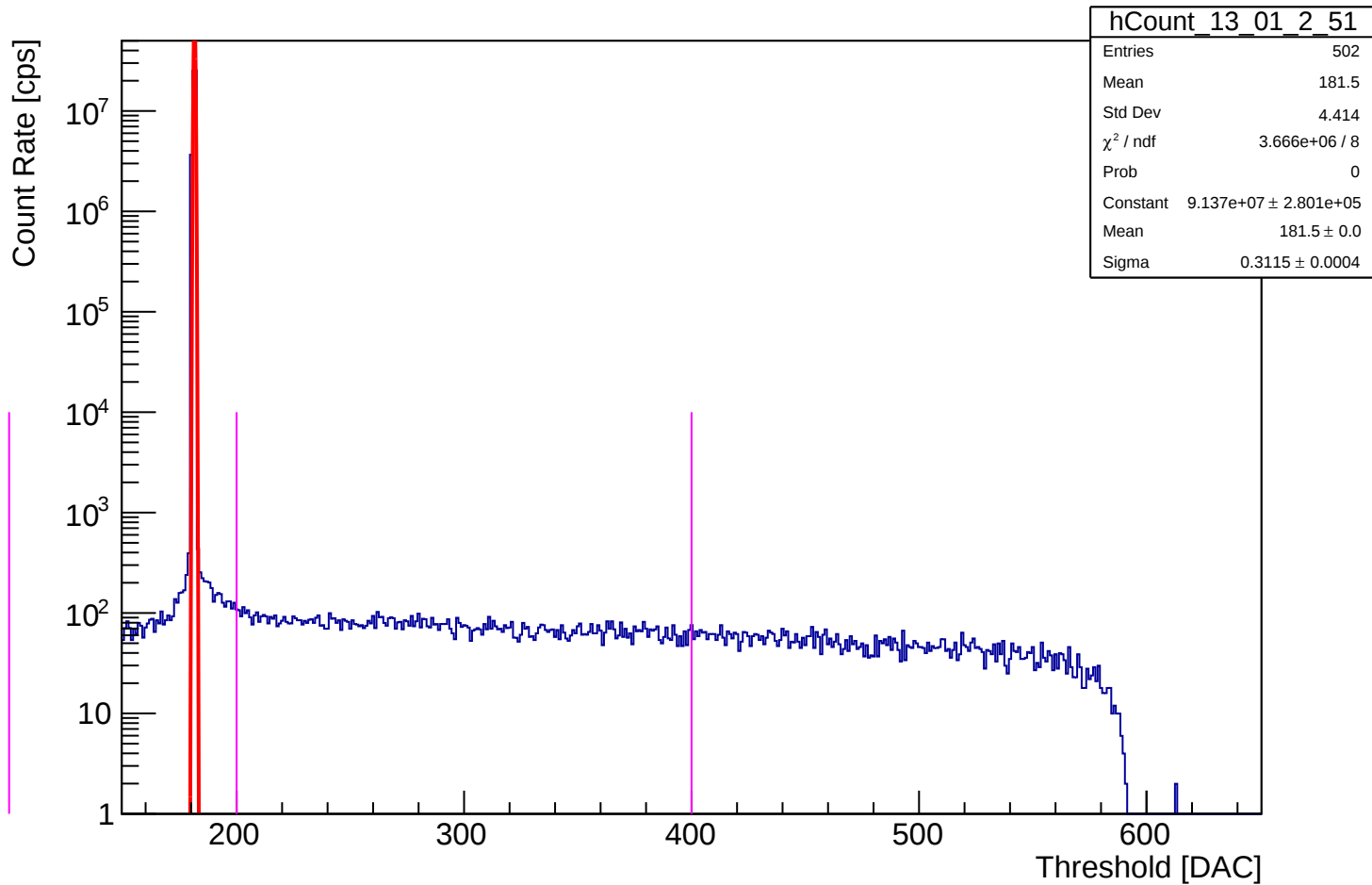
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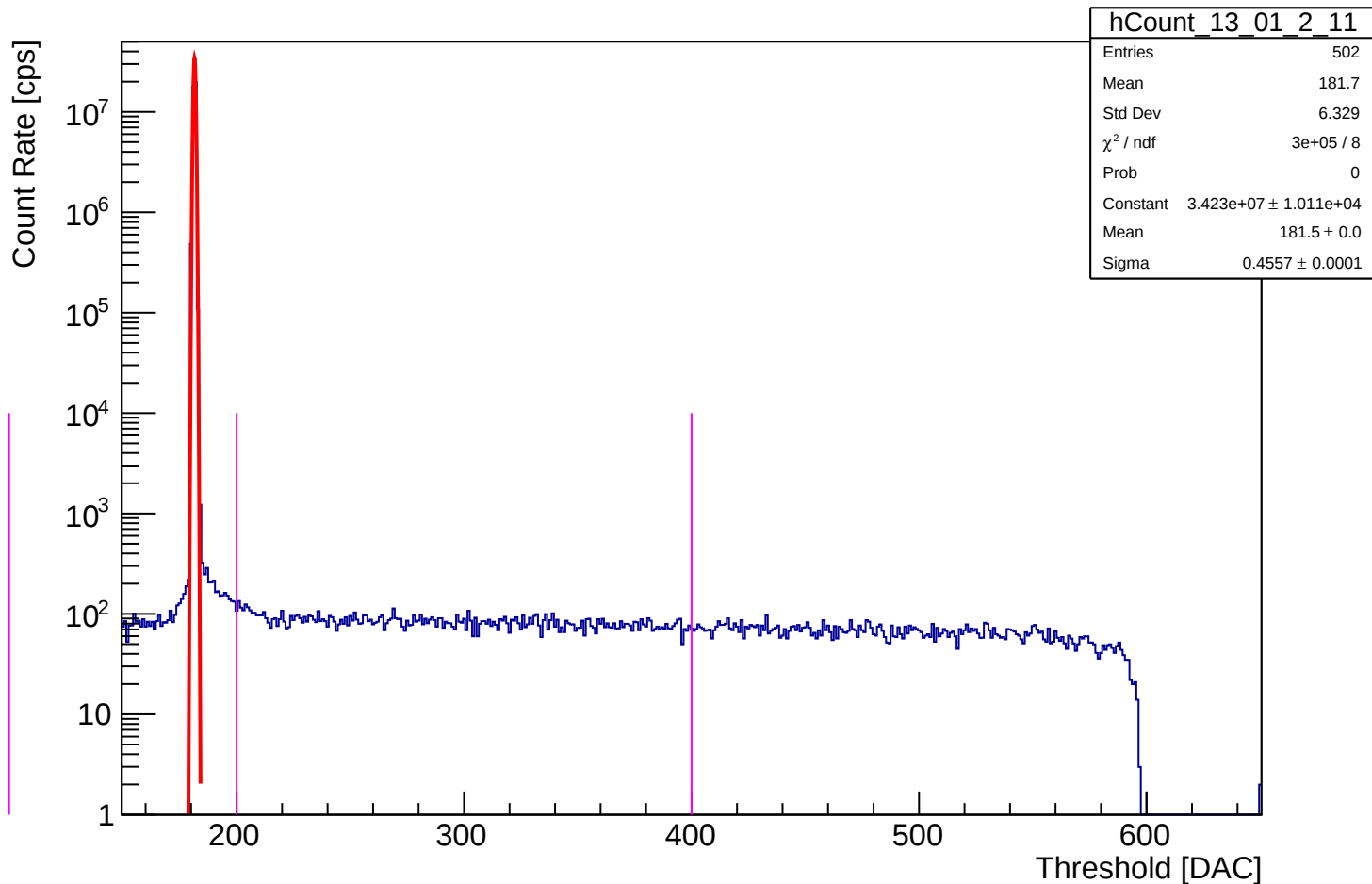
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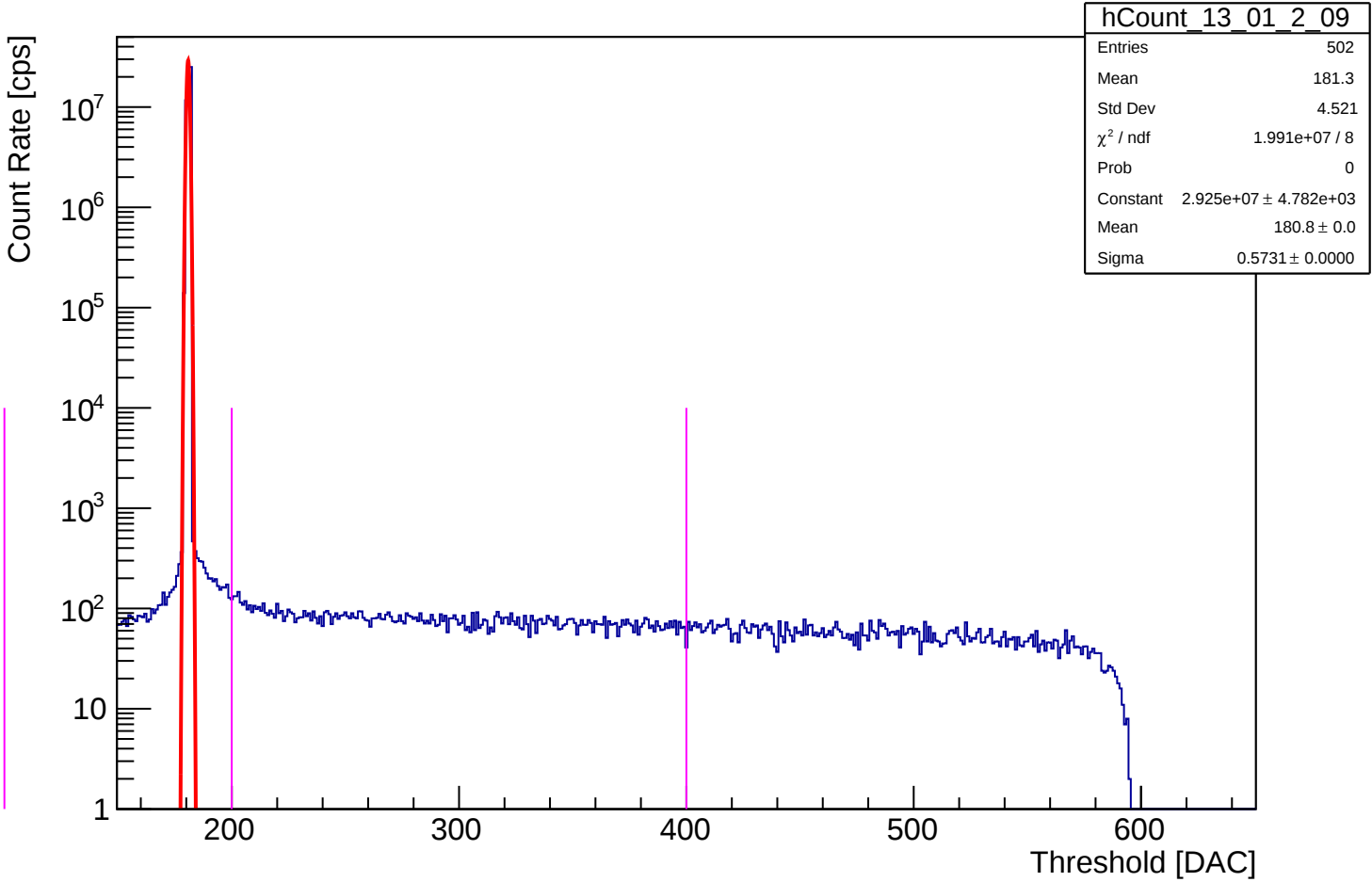


Row 1 Tile 1 Pmt 6 Pixel 40 Slot 13 Fiber 1 Asic 2 Channel 51

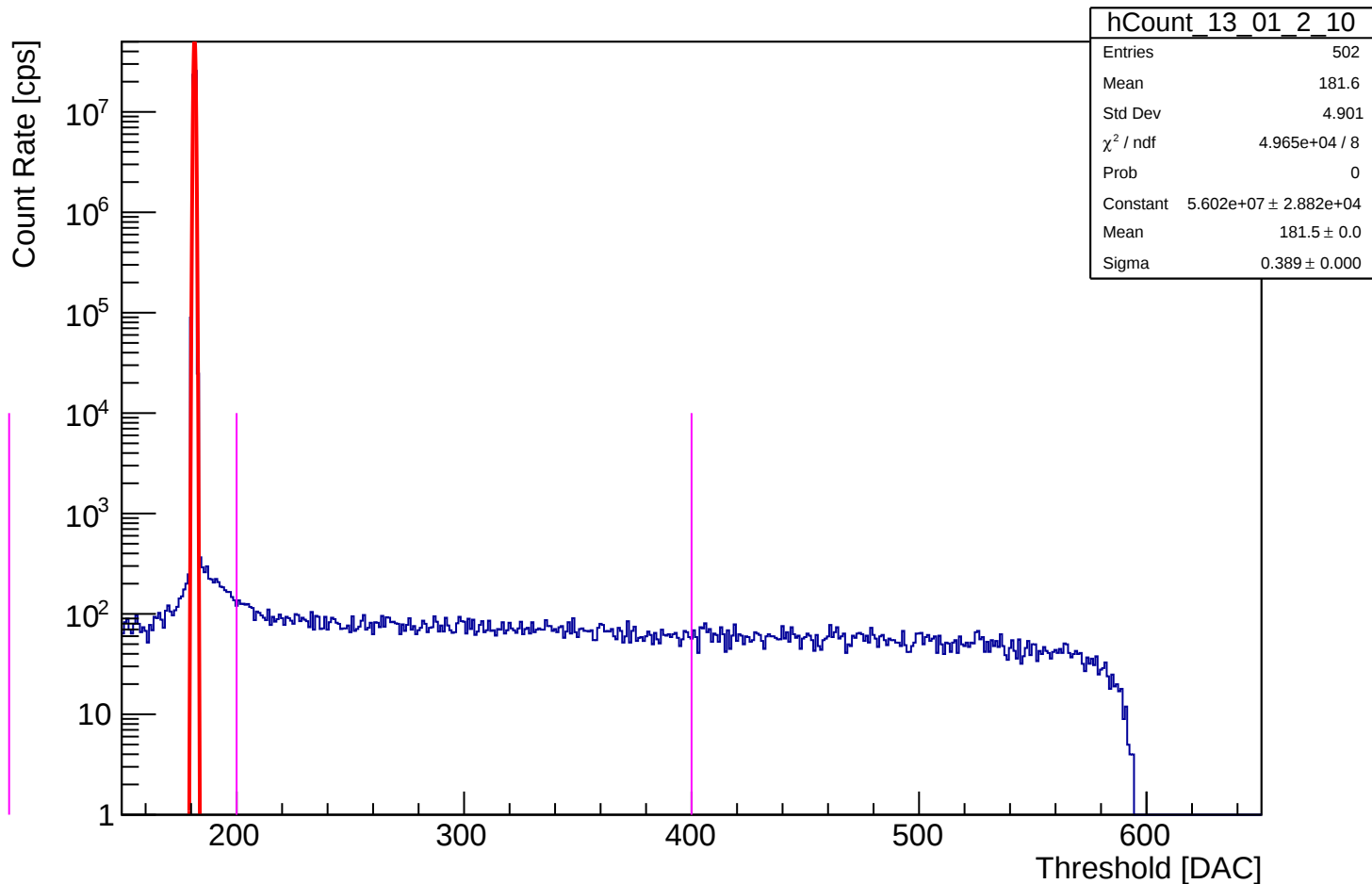


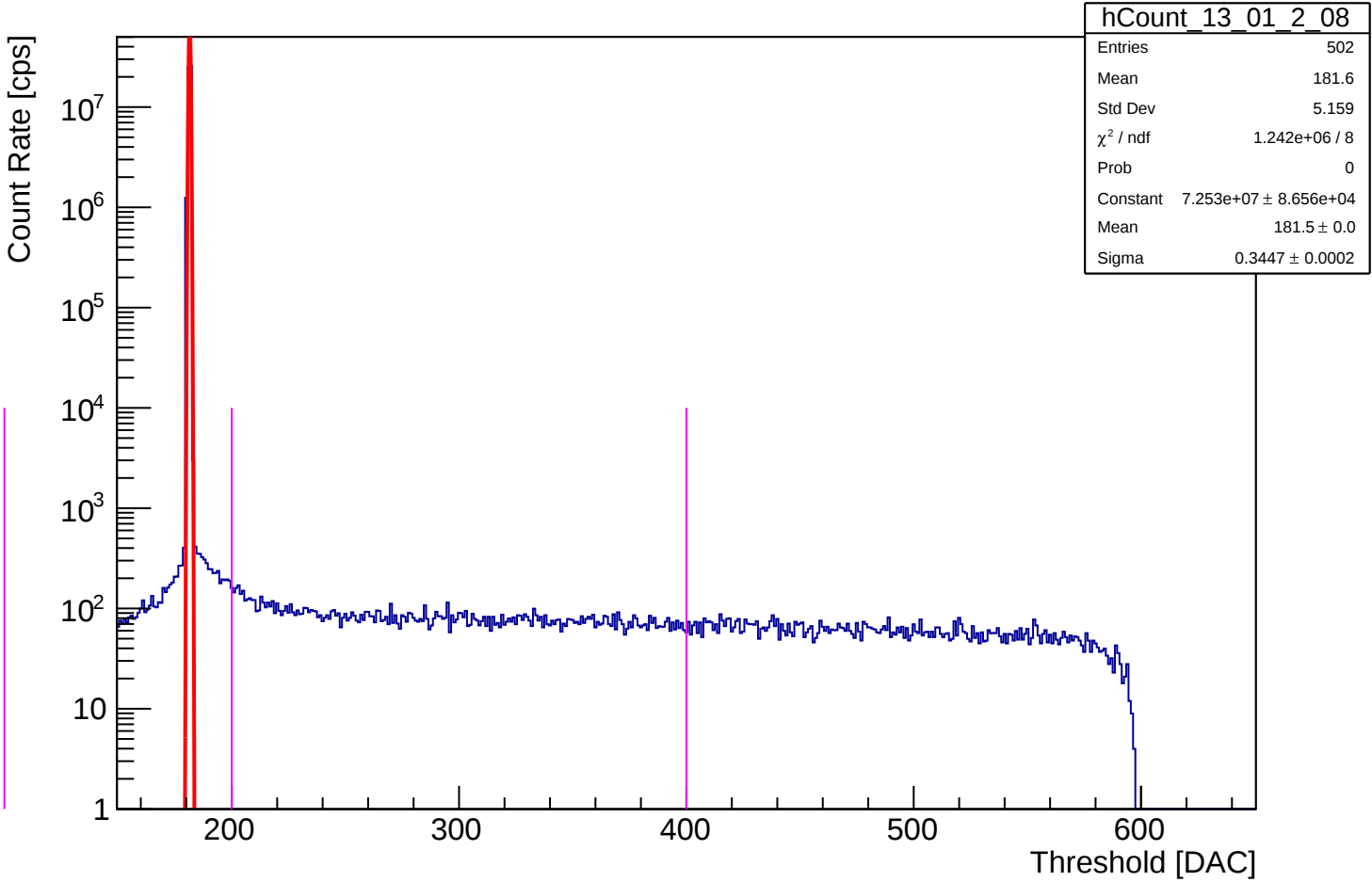
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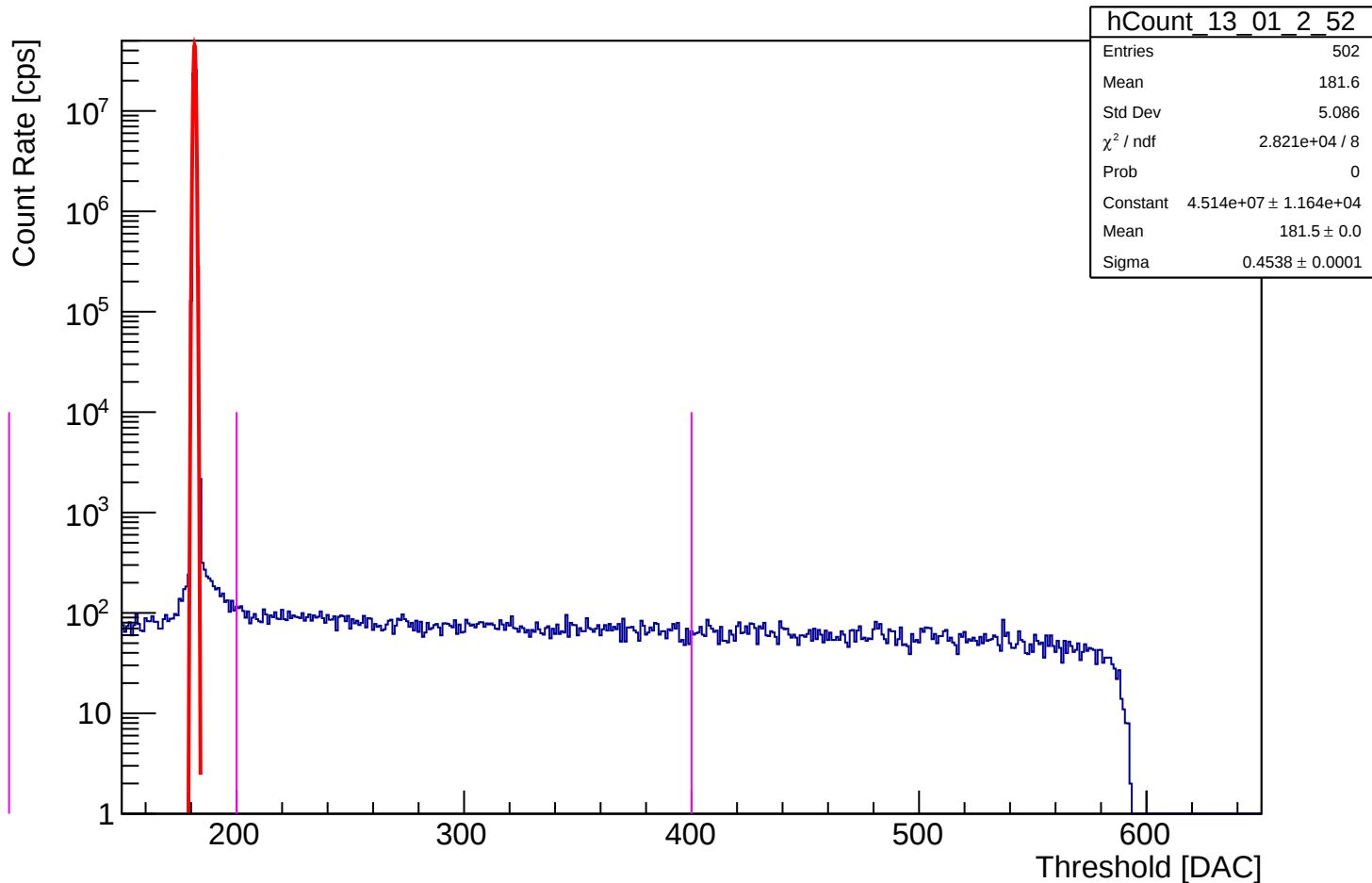


Row 1 Tile 1 Pmt 6 Pixel 43 Slot 13 Fiber 1 Asic 2 Channel 10

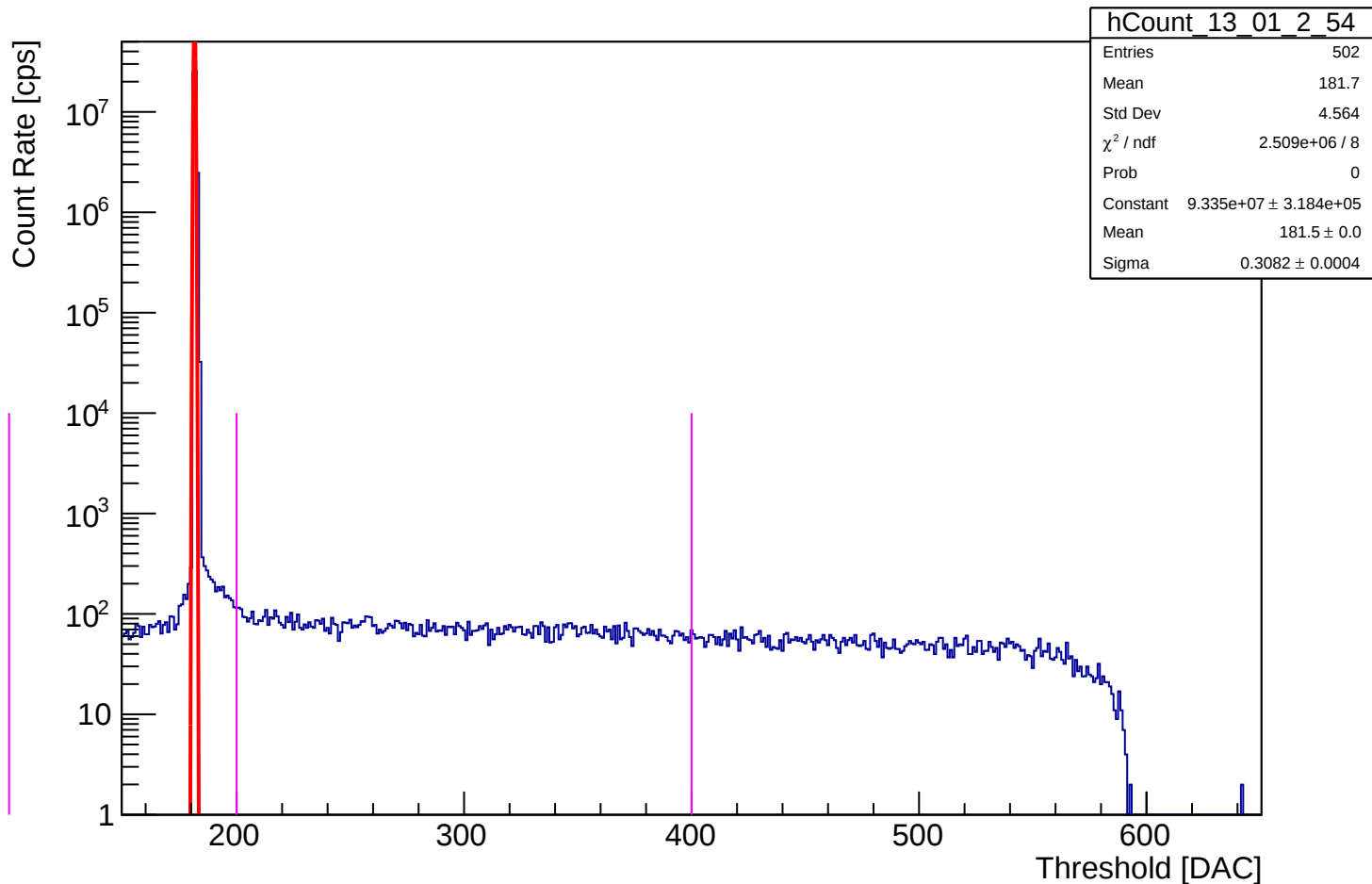




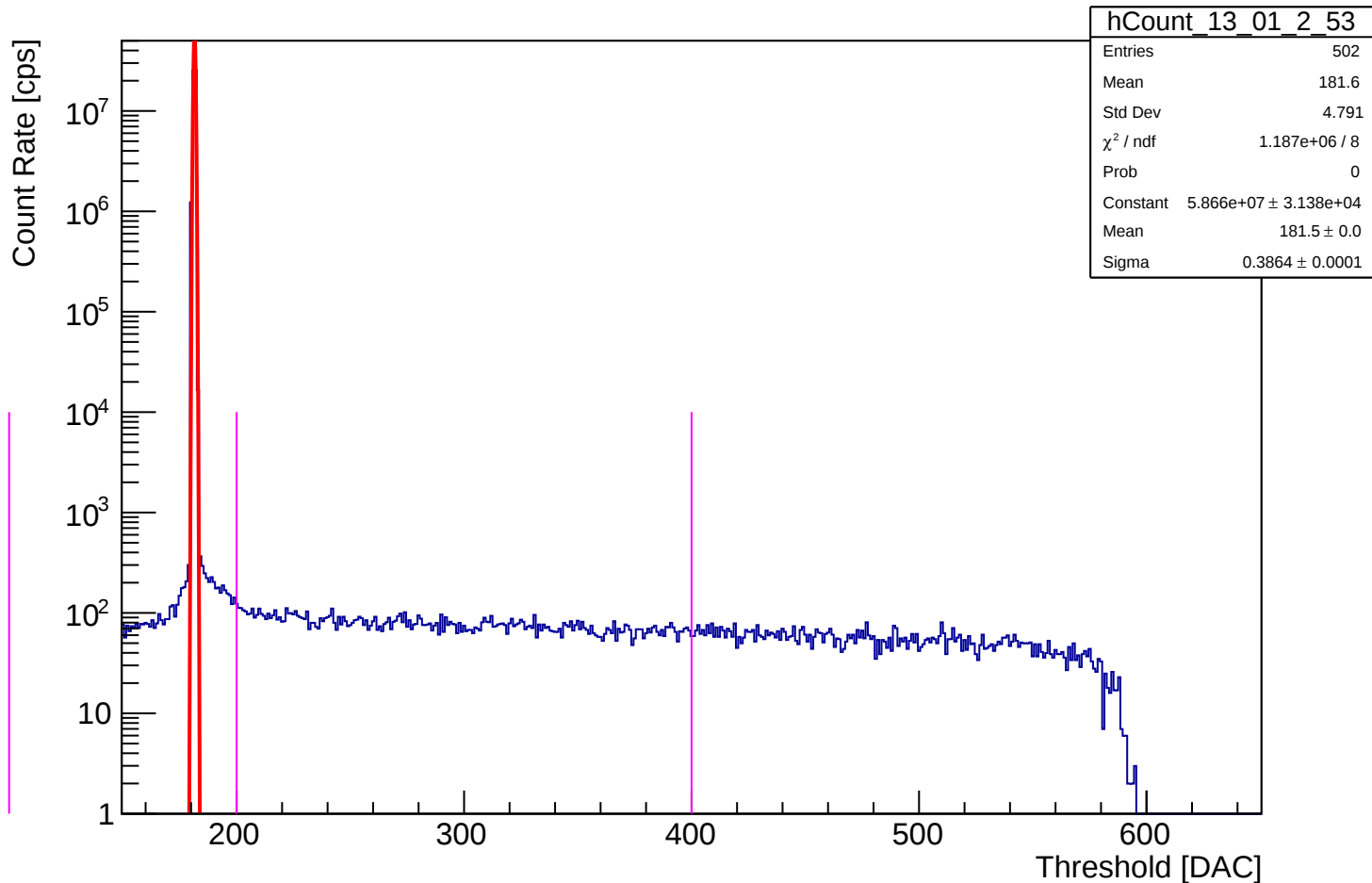
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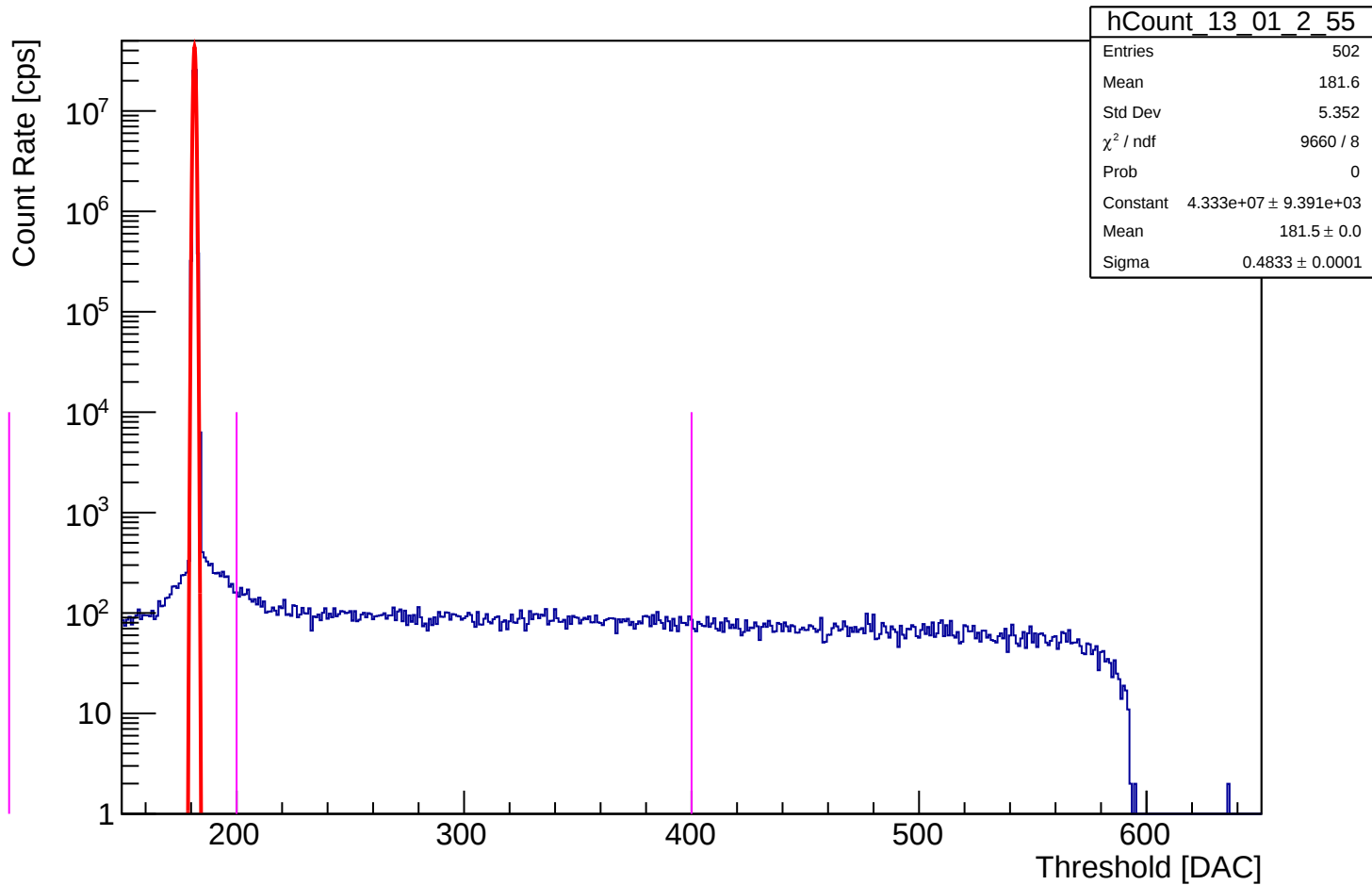
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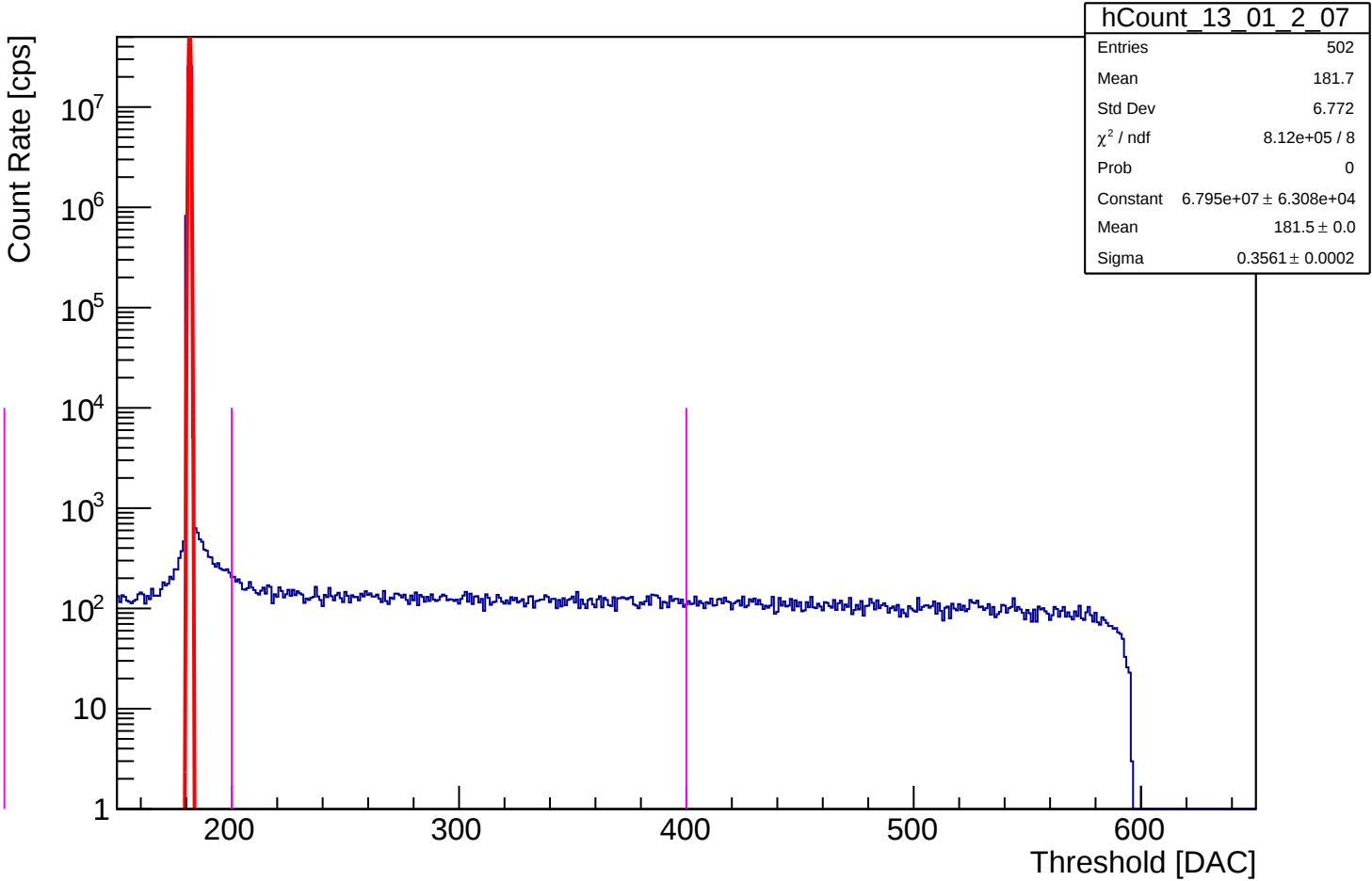


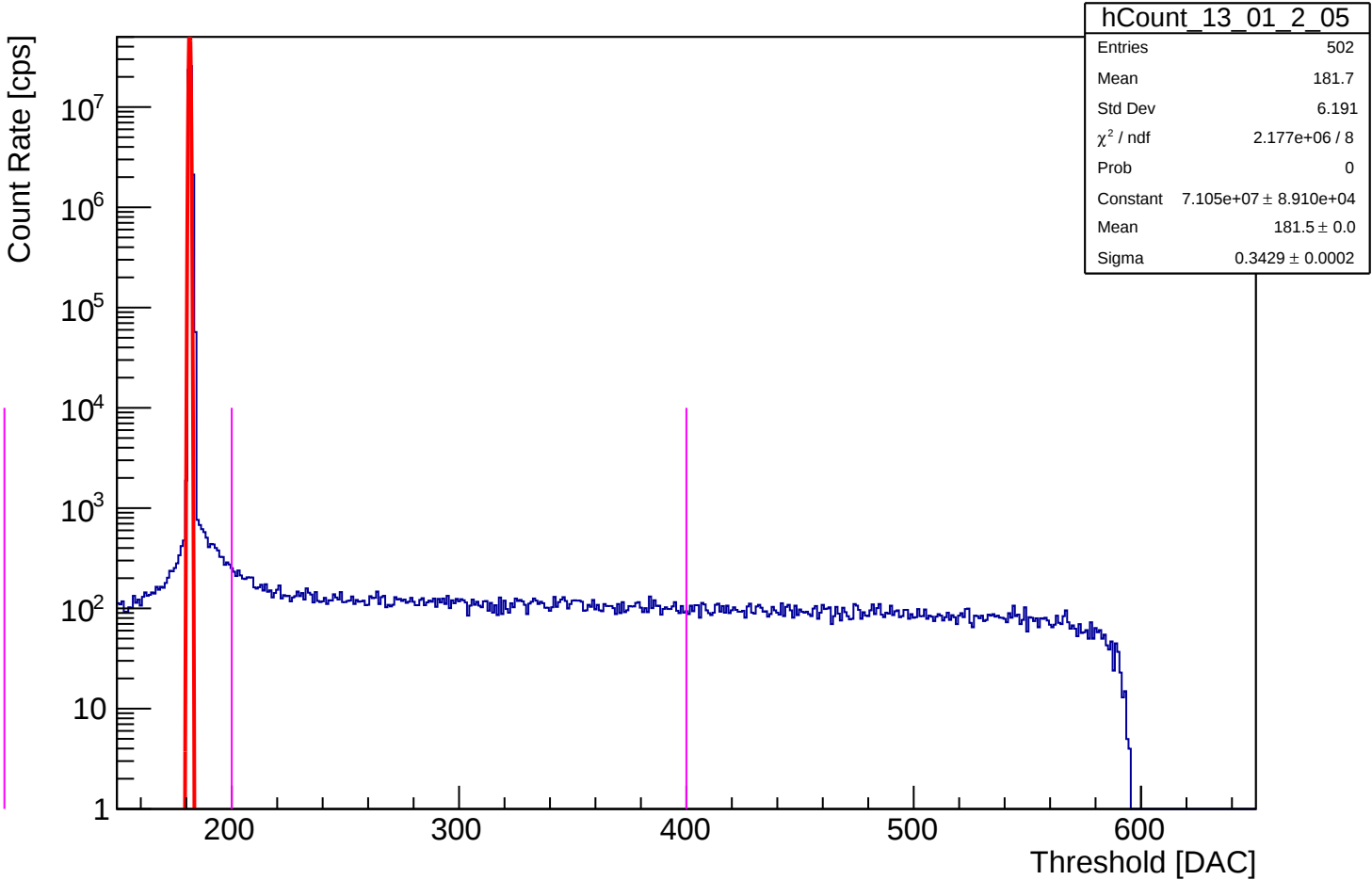
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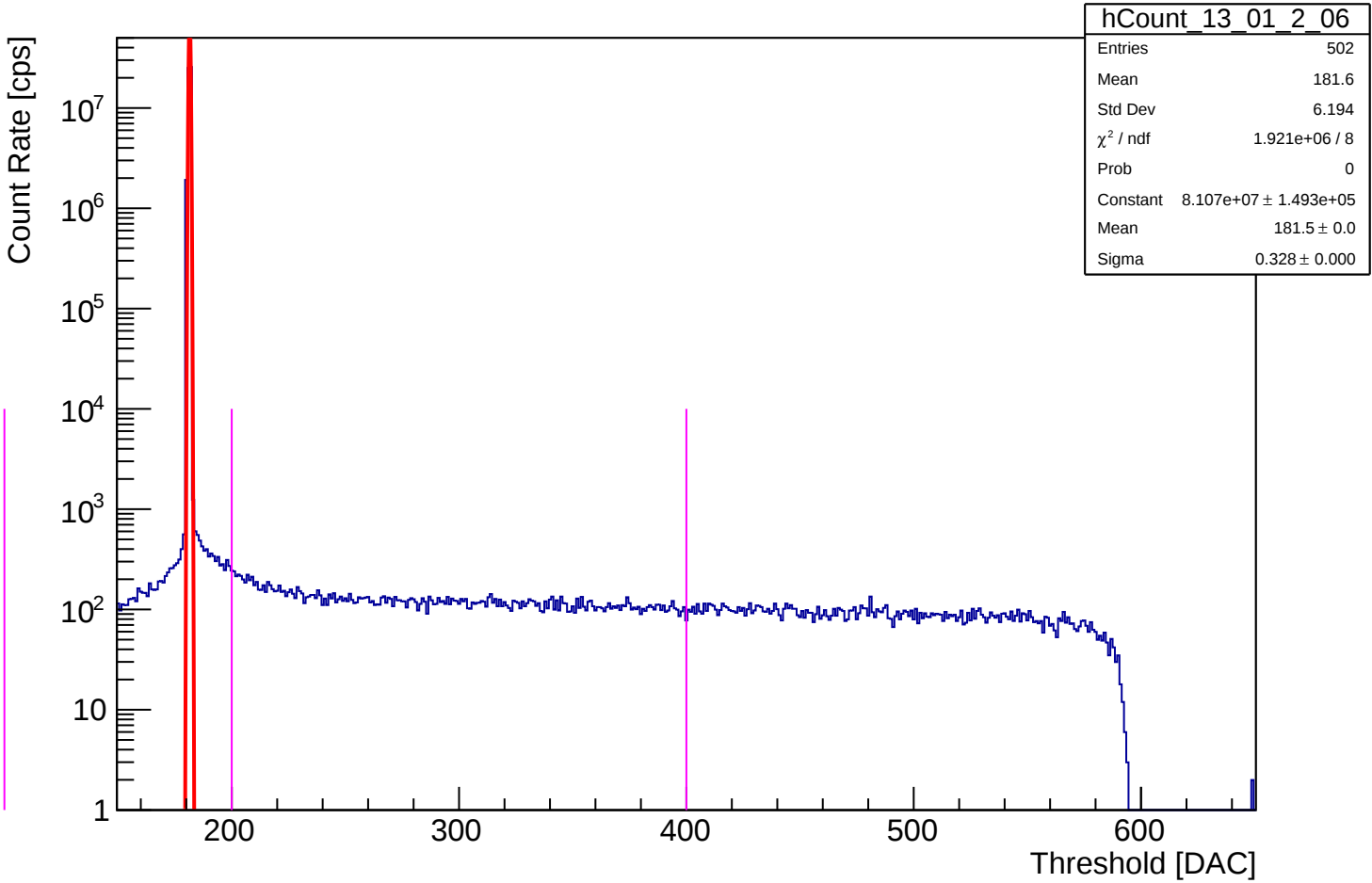


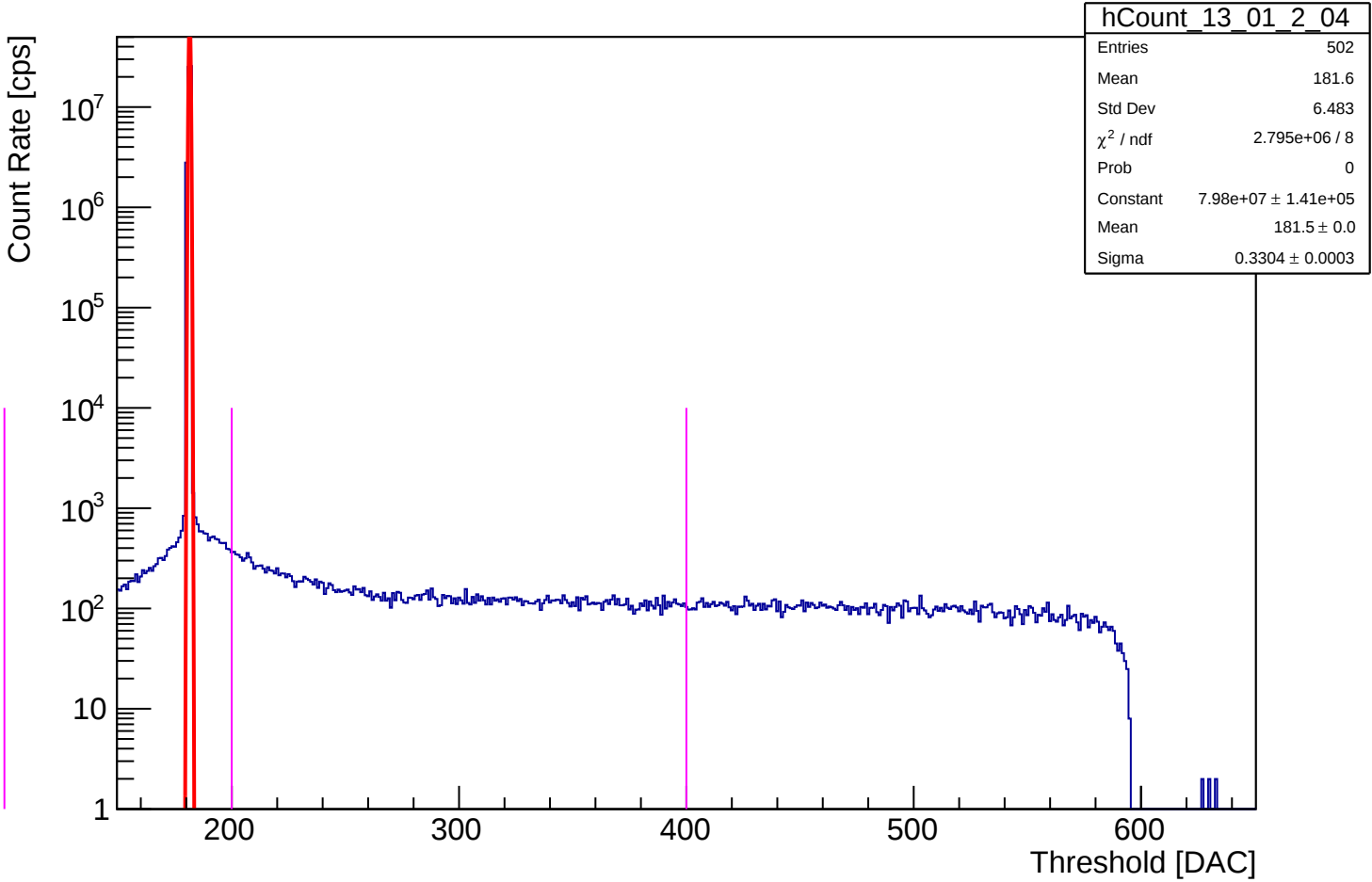
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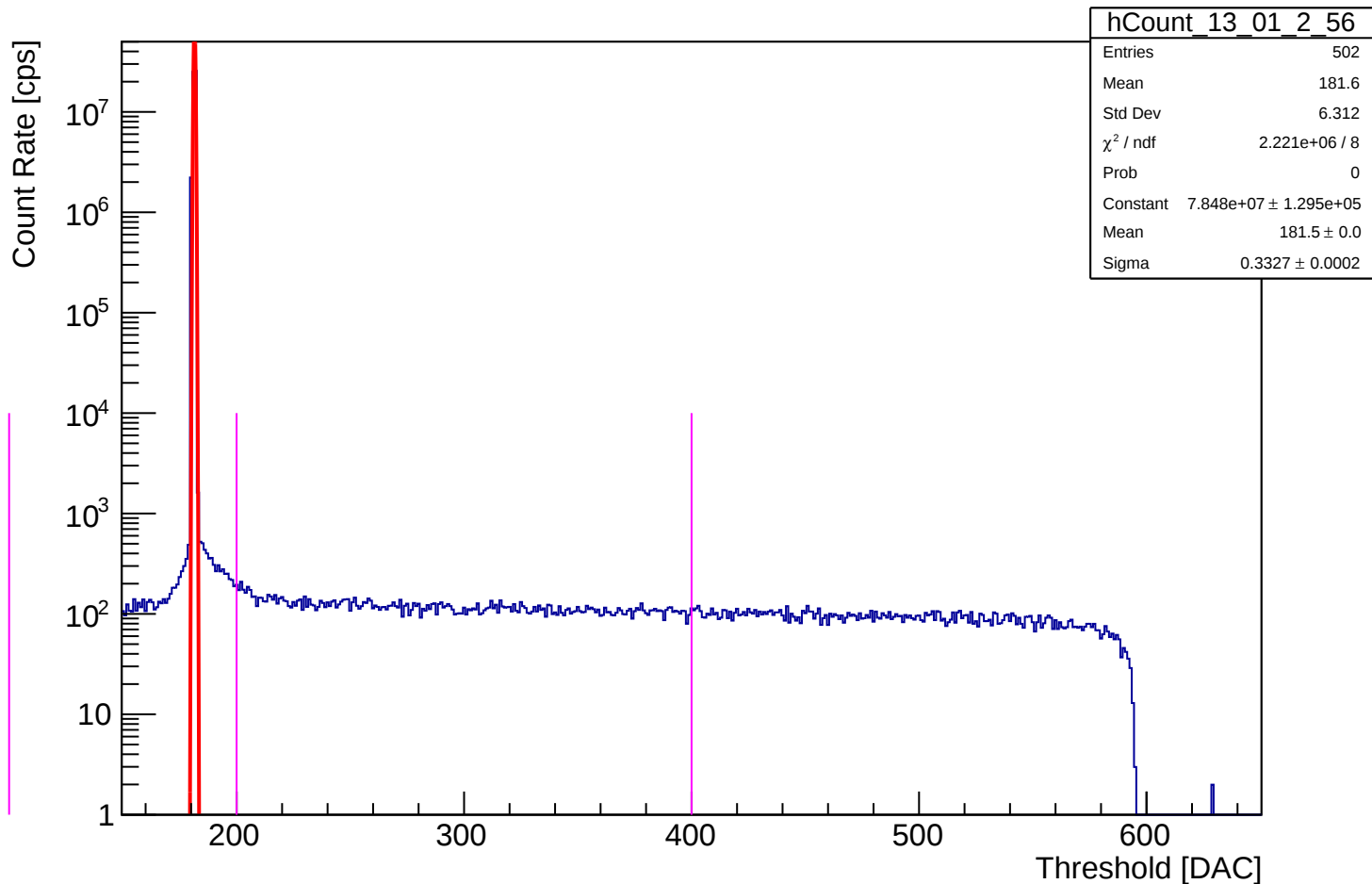




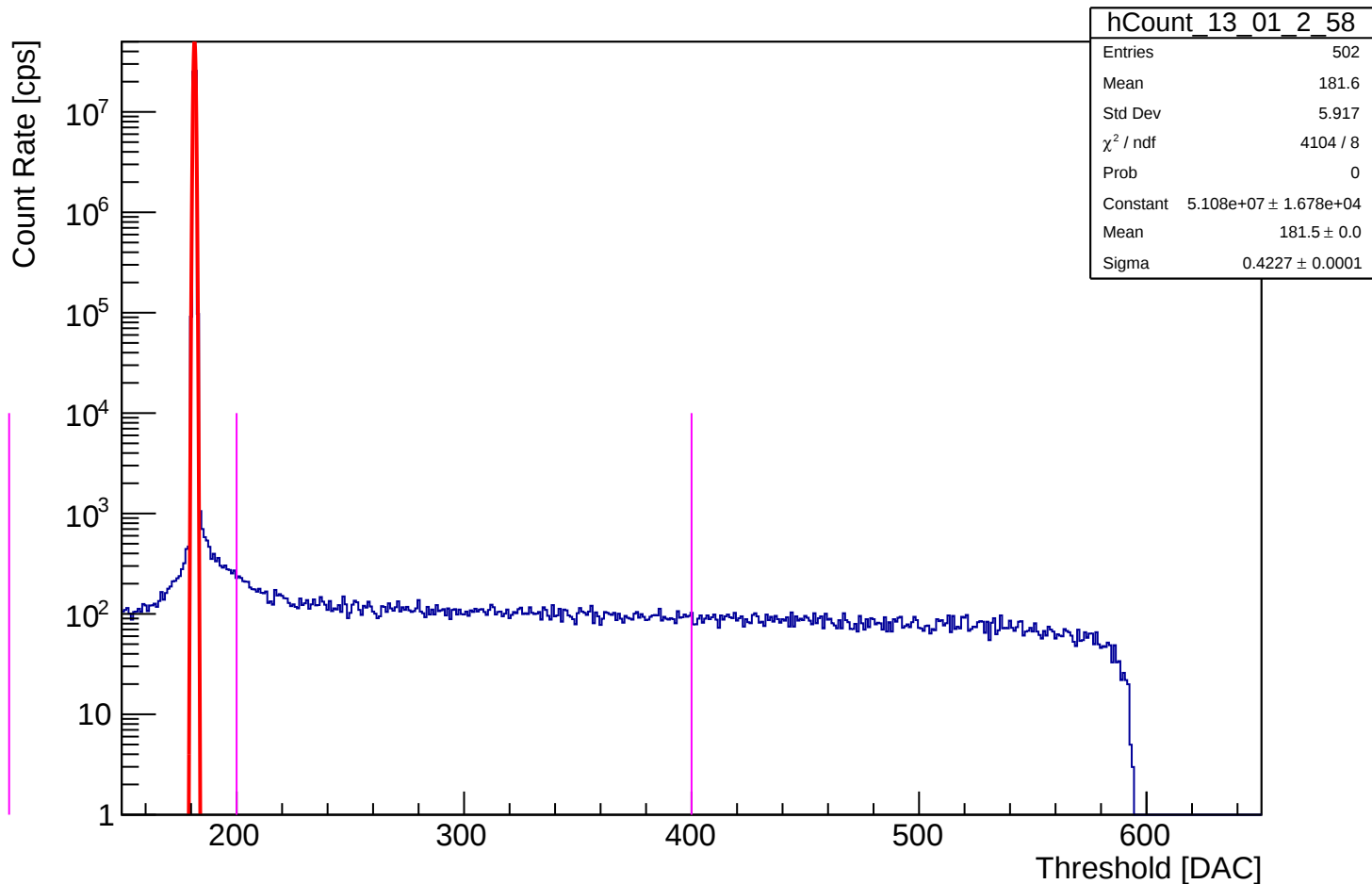




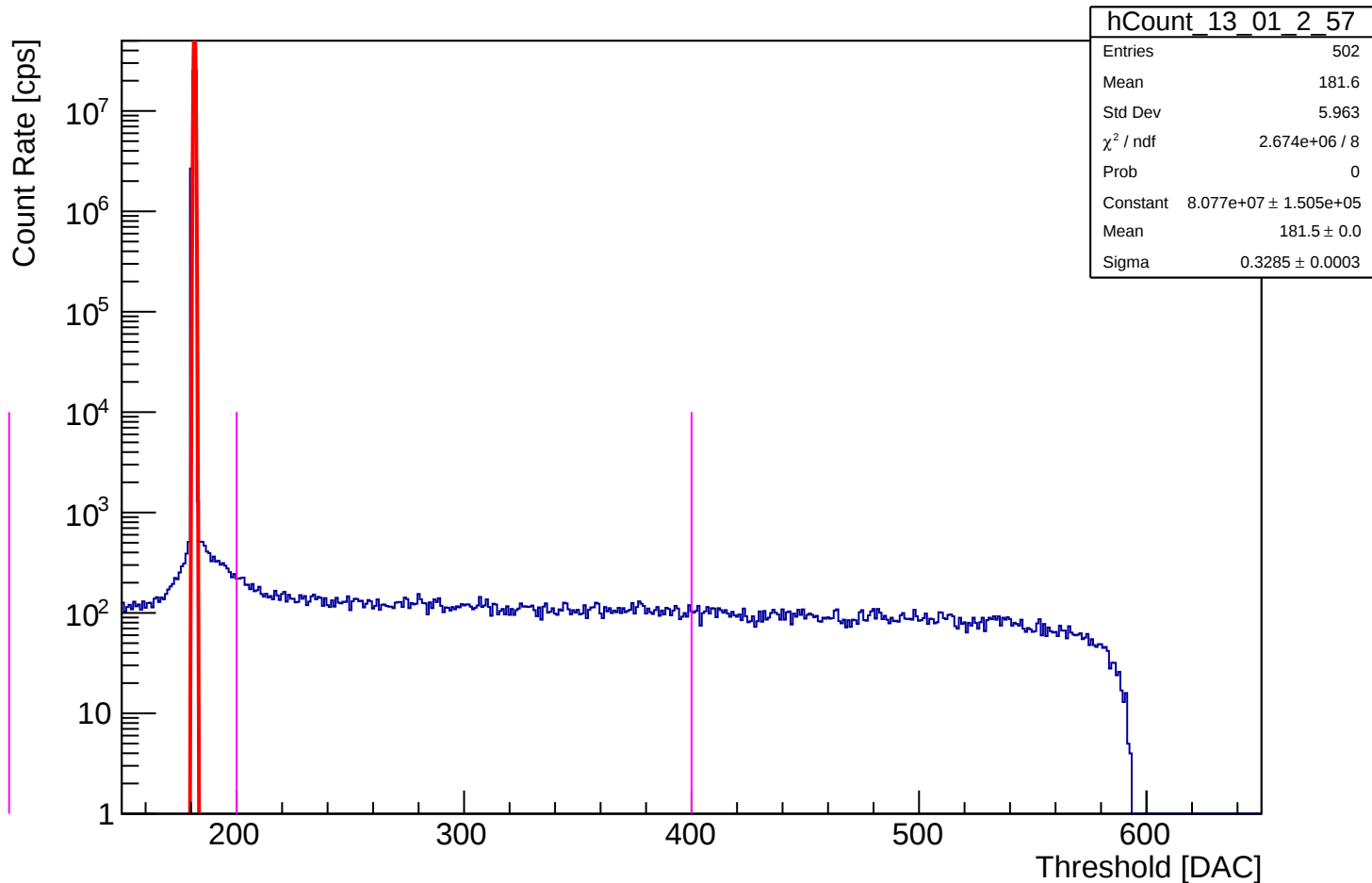
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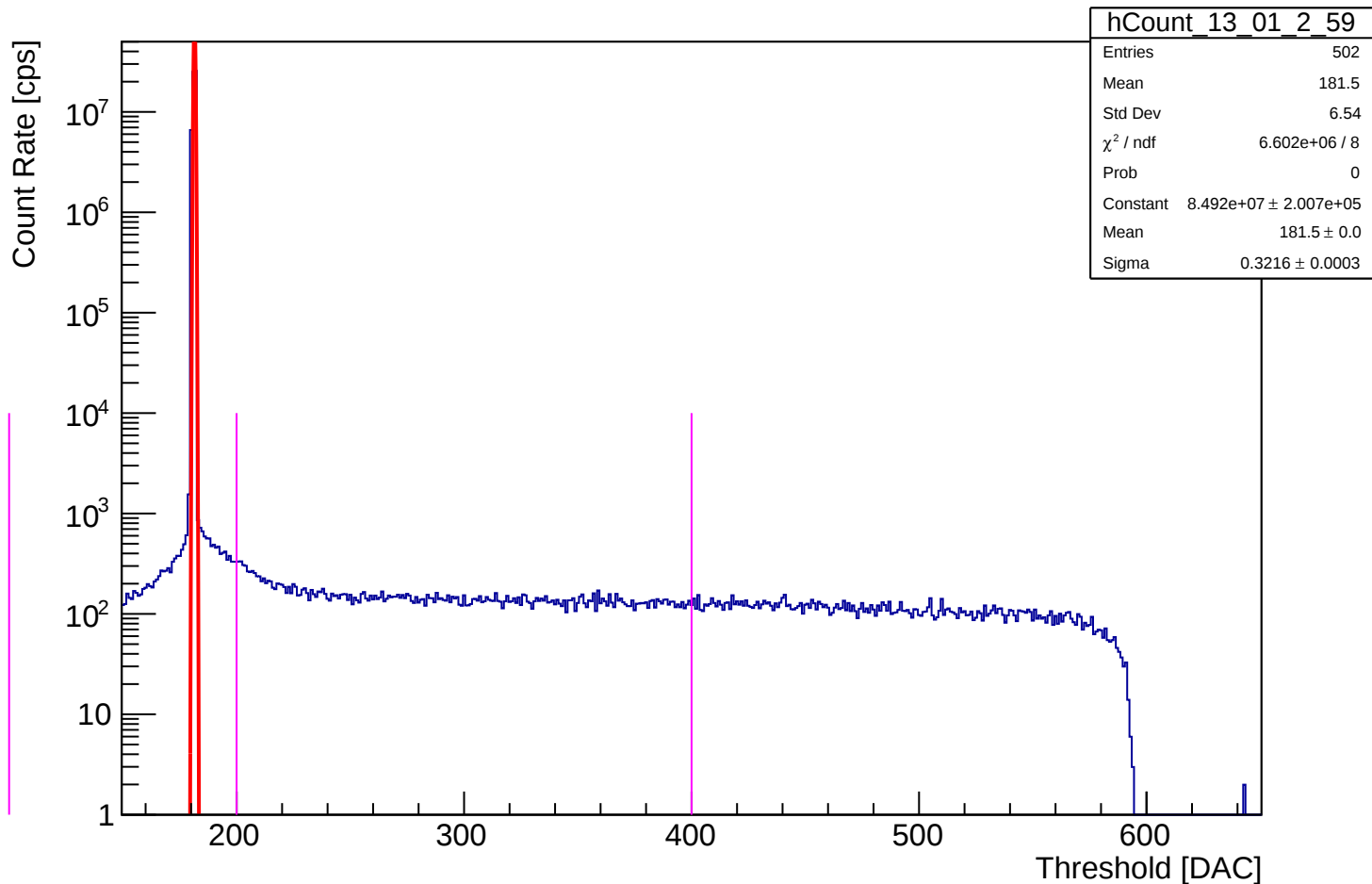
Row 1 Tile 1 Pmt 6 Pixel 54 Slot 13 Fiber 1 Asic 2 Channel 58

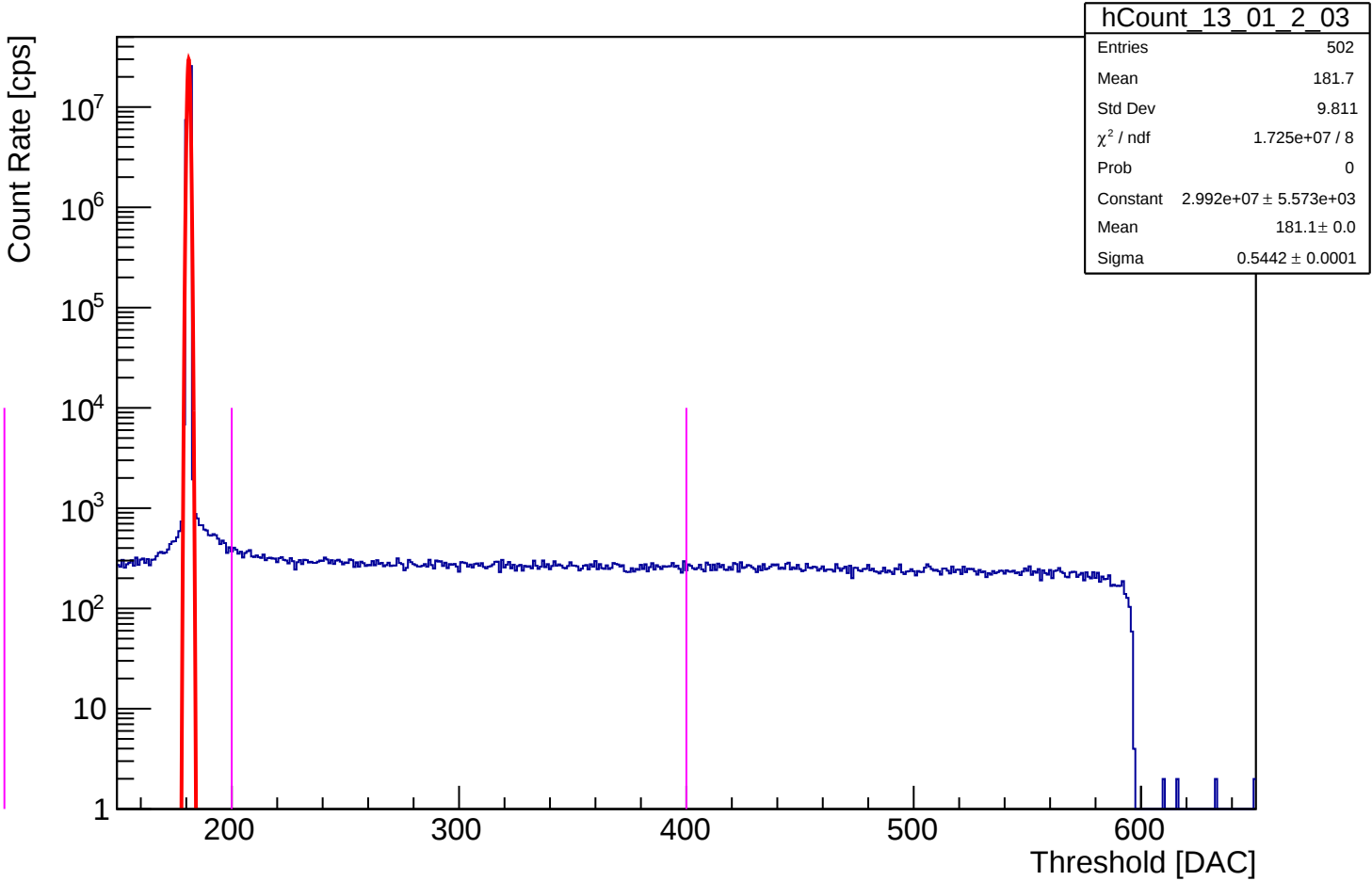


Row 1 Tile 1 Pmt 6 Pixel 55 Slot 13 Fiber 1 Asic 2 Channel 57

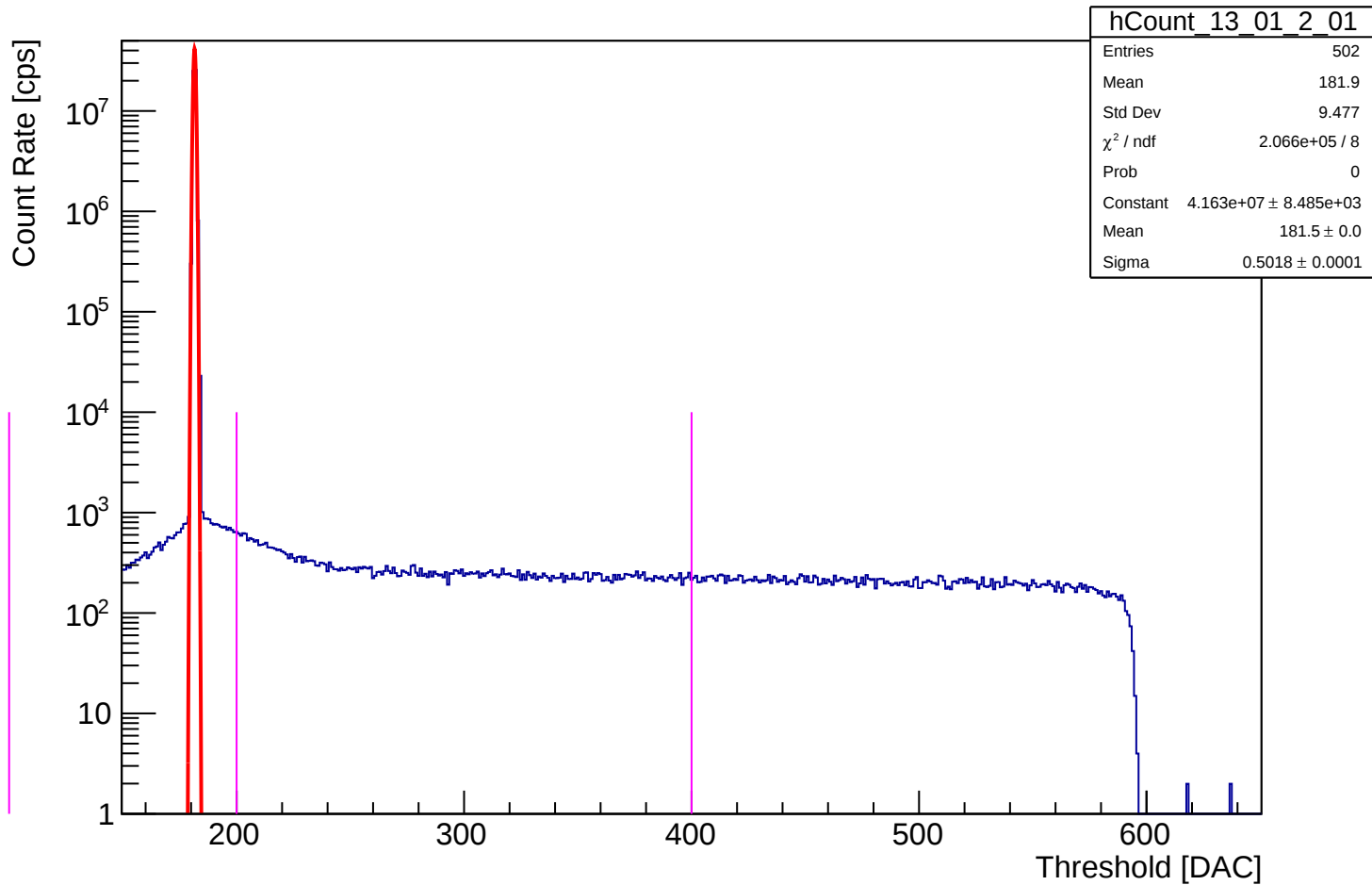


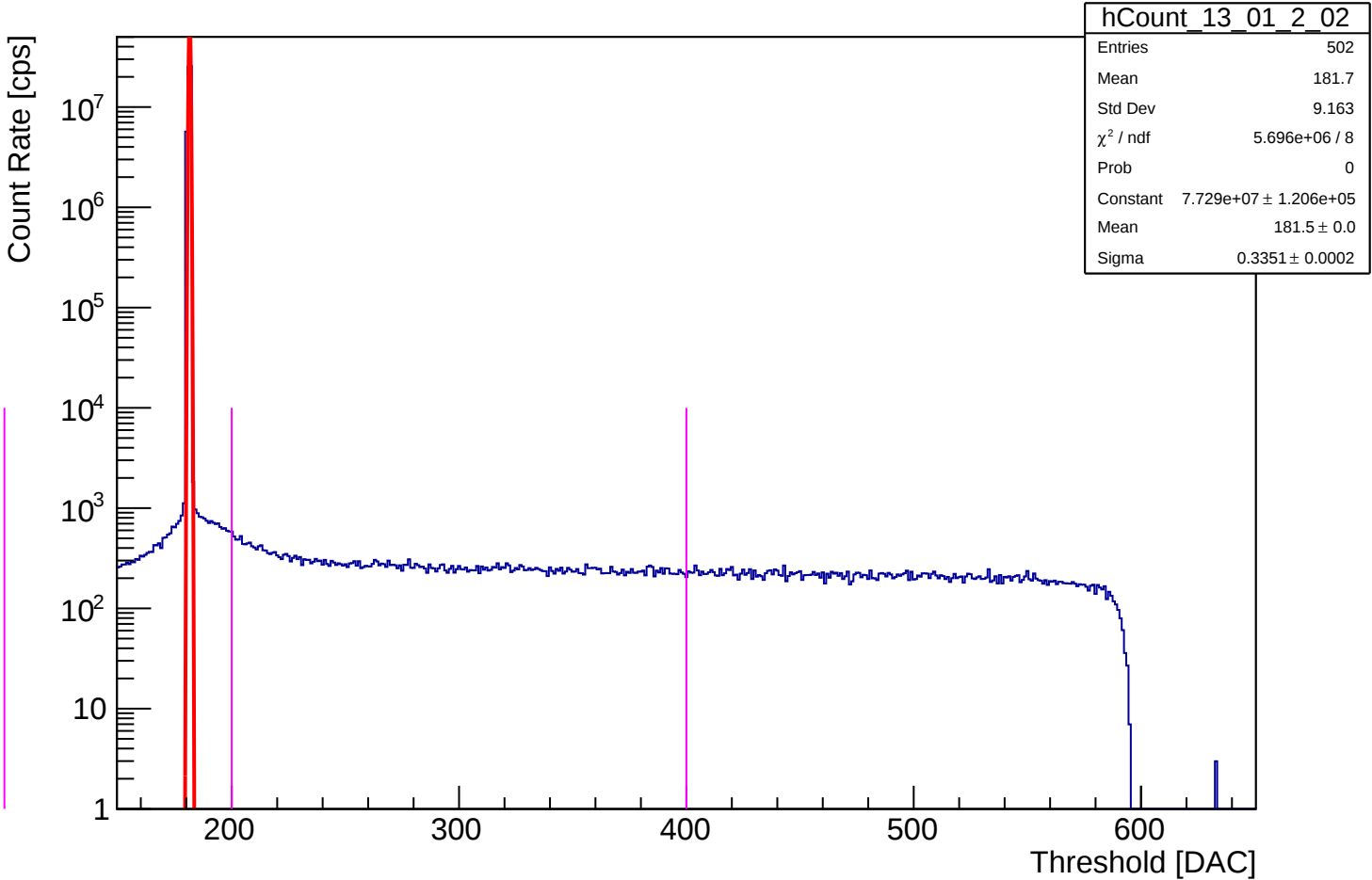
Row 1 Tile 1 Pmt 6 Pixel 56 Slot 13 Fiber 1 Asic 2 Channel 59

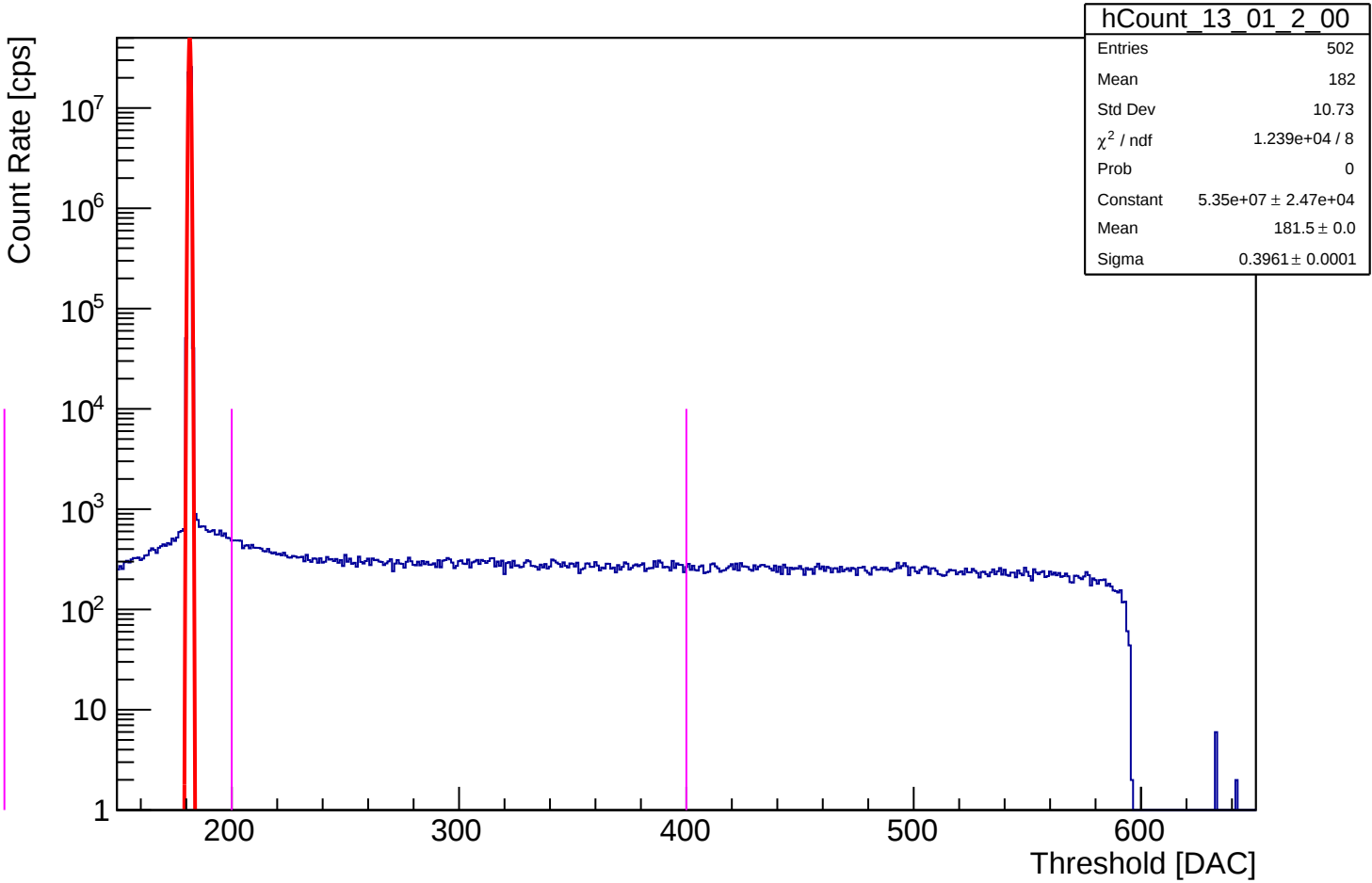




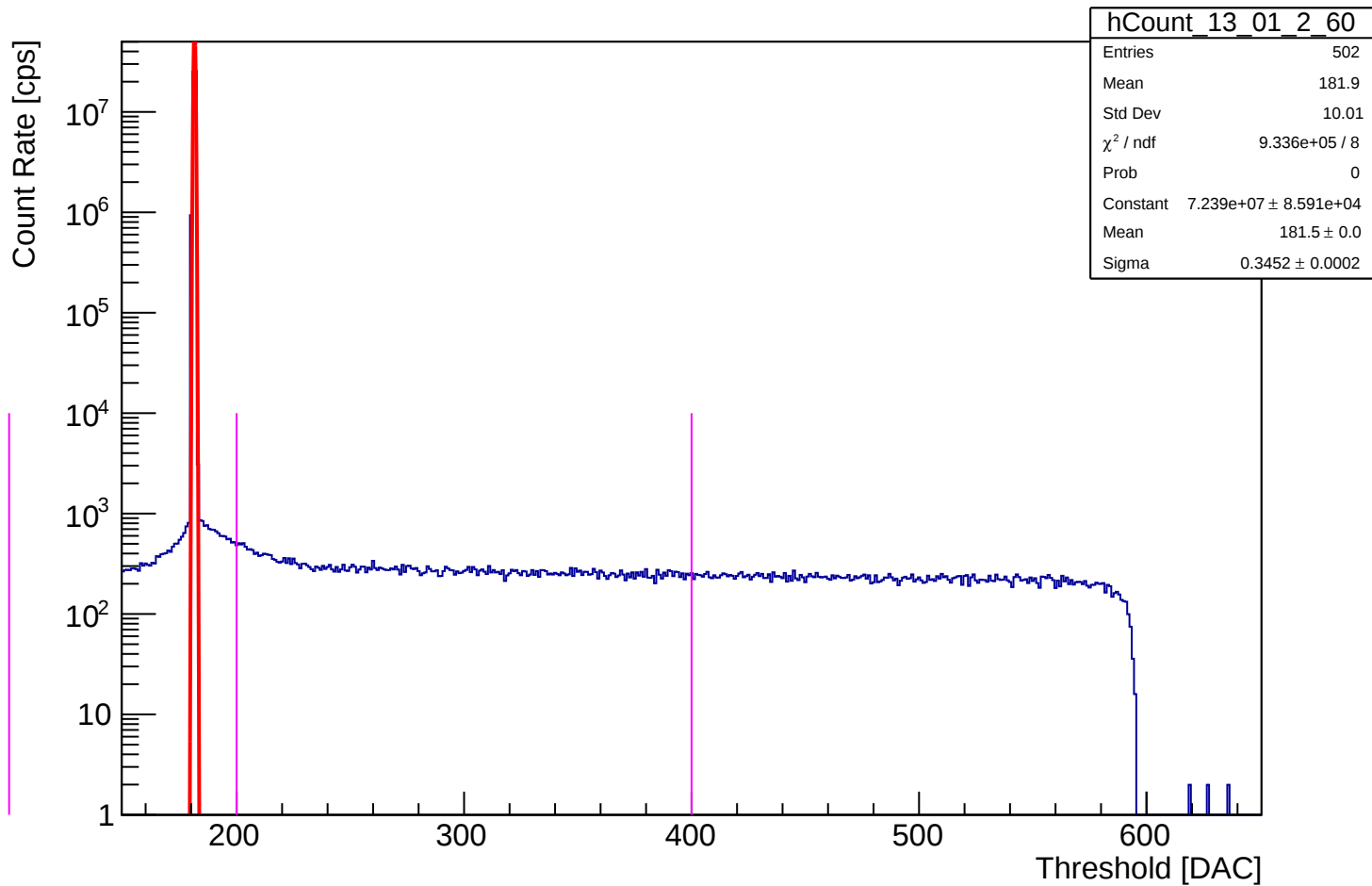
Row 1 Tile 1 Pmt 6 Pixel 58 Slot 13 Fiber 1 Asic 2 Channel 1



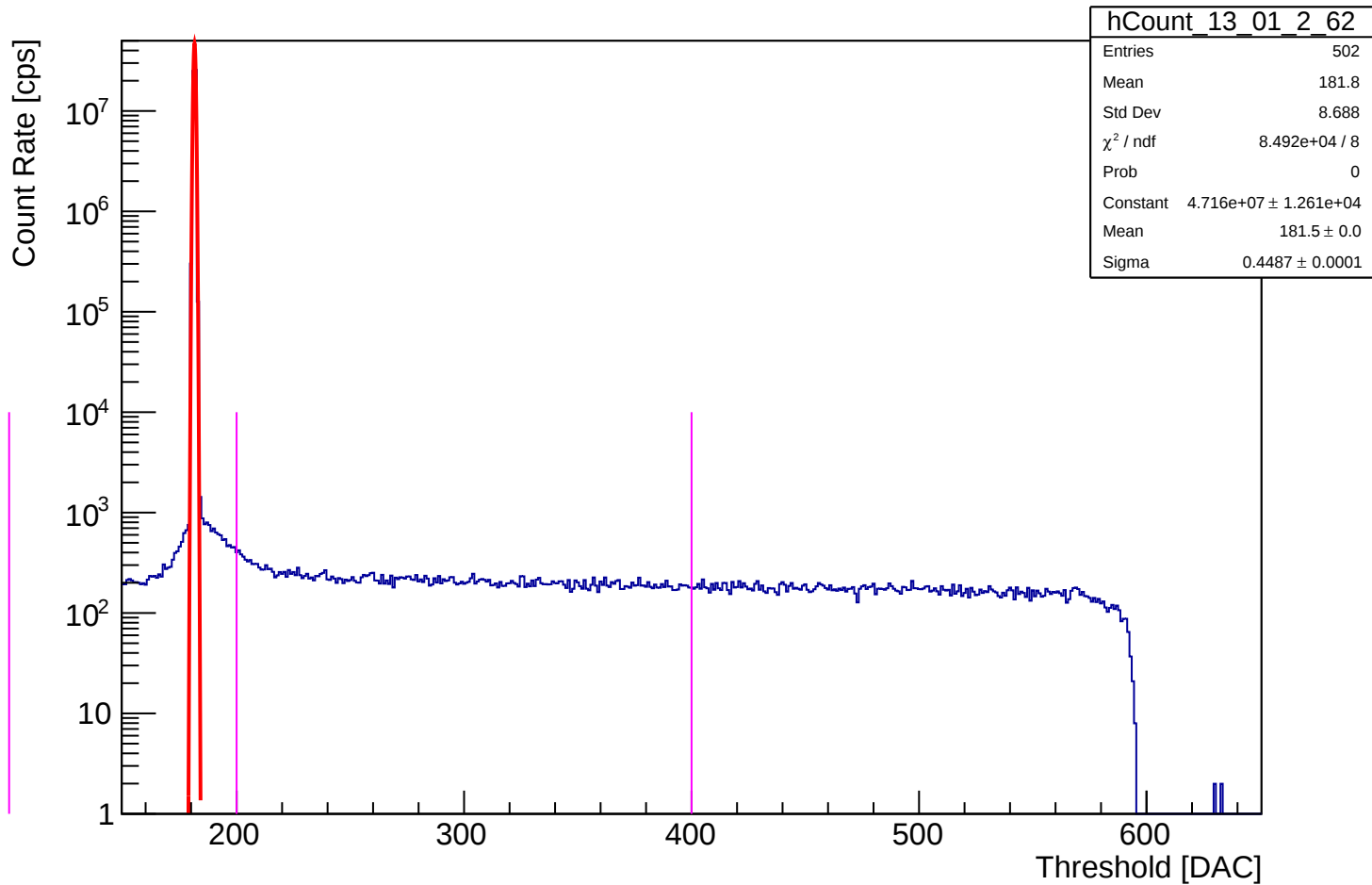




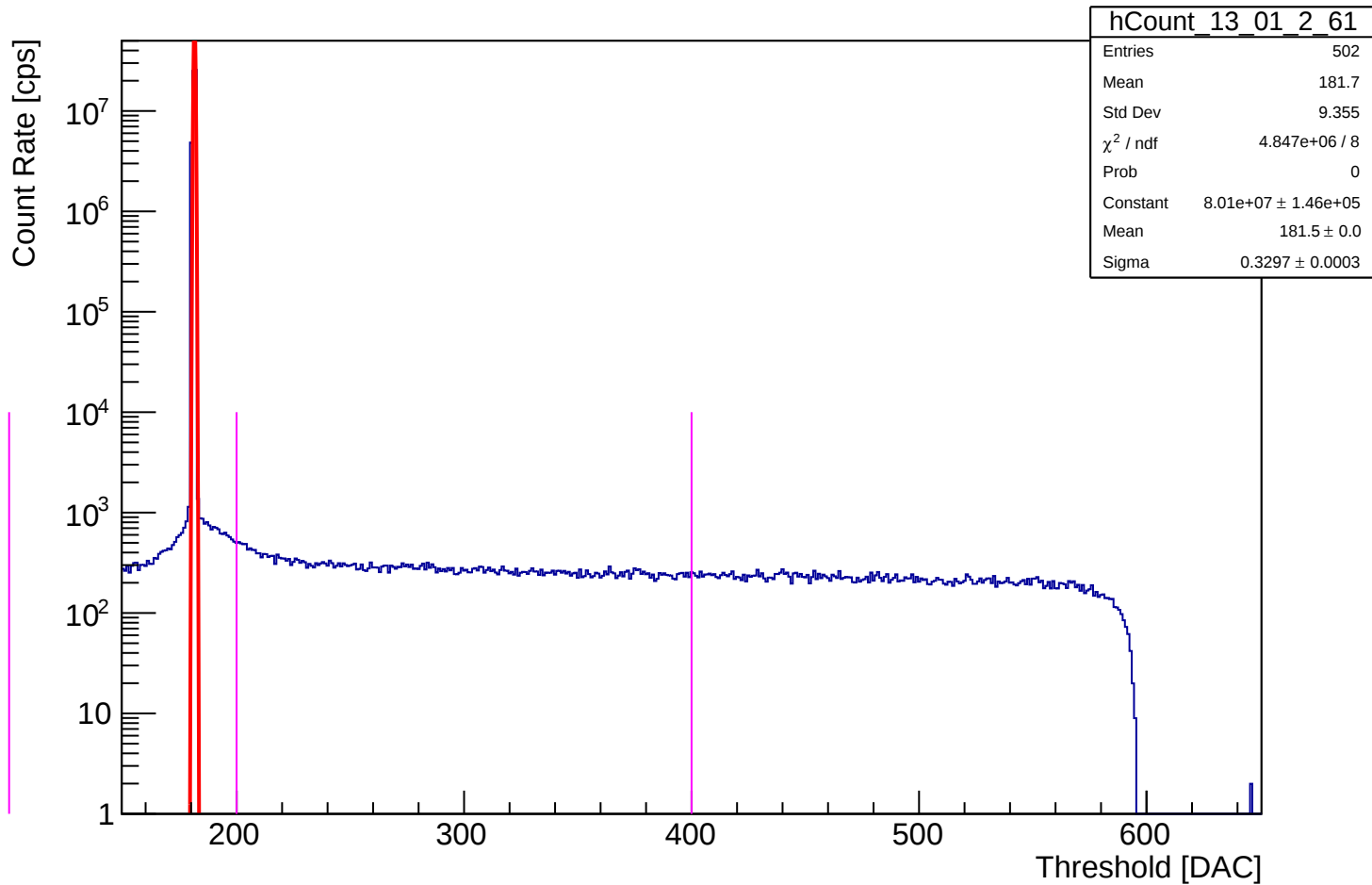
Row 1 Tile 1 Pmt 6 Pixel 61 Slot 13 Fiber 1 Asic 2 Channel 60



Row 1 Tile 1 Pmt 6 Pixel 62 Slot 13 Fiber 1 Asic 2 Channel 62



Row 1 Tile 1 Pmt 6 Pixel 63 Slot 13 Fiber 1 Asic 2 Channel 61



Row 1 Tile 1 Pmt 6 Pixel 64 Slot 13 Fiber 1 Asic 2 Channel 63

