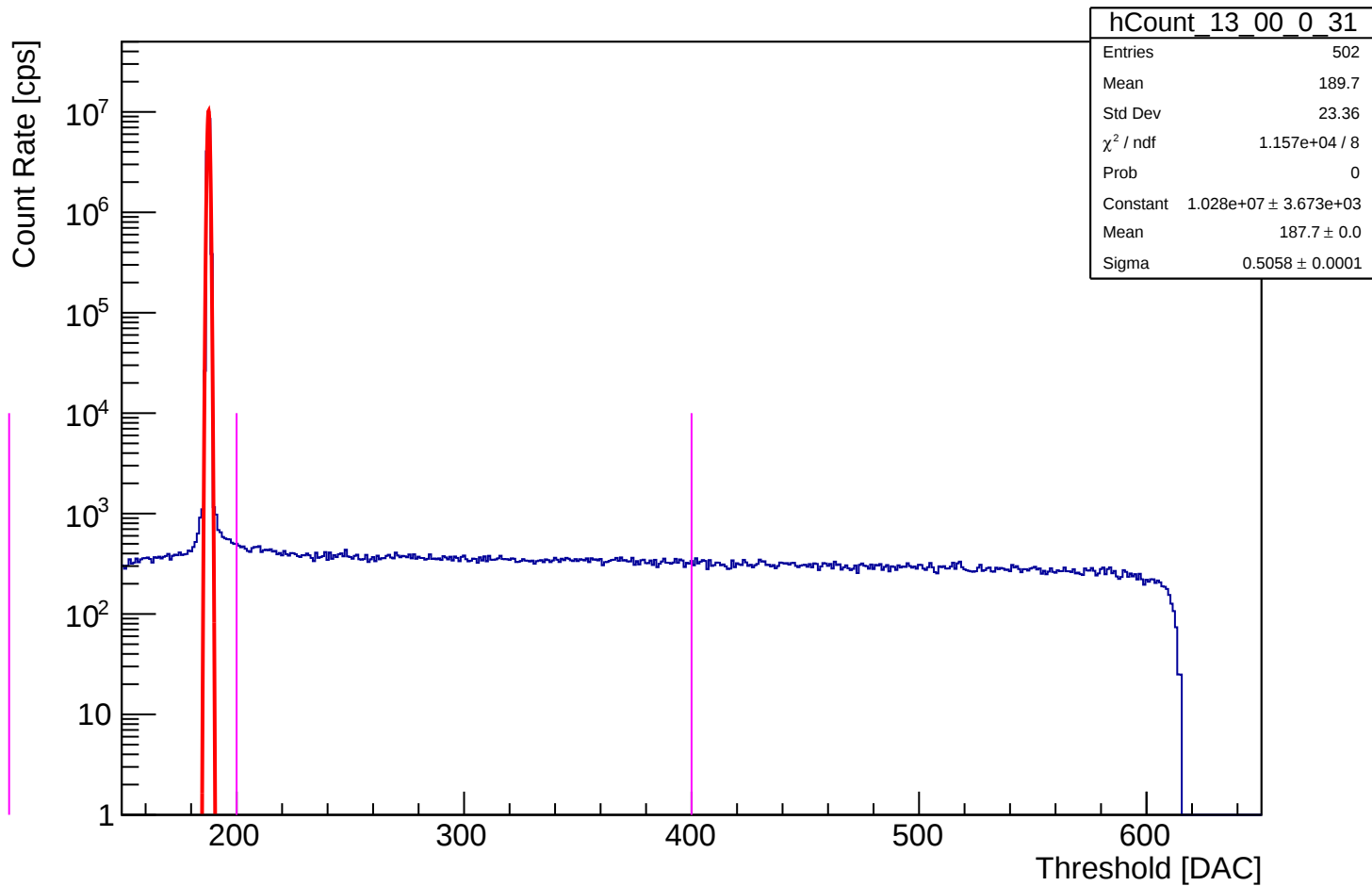
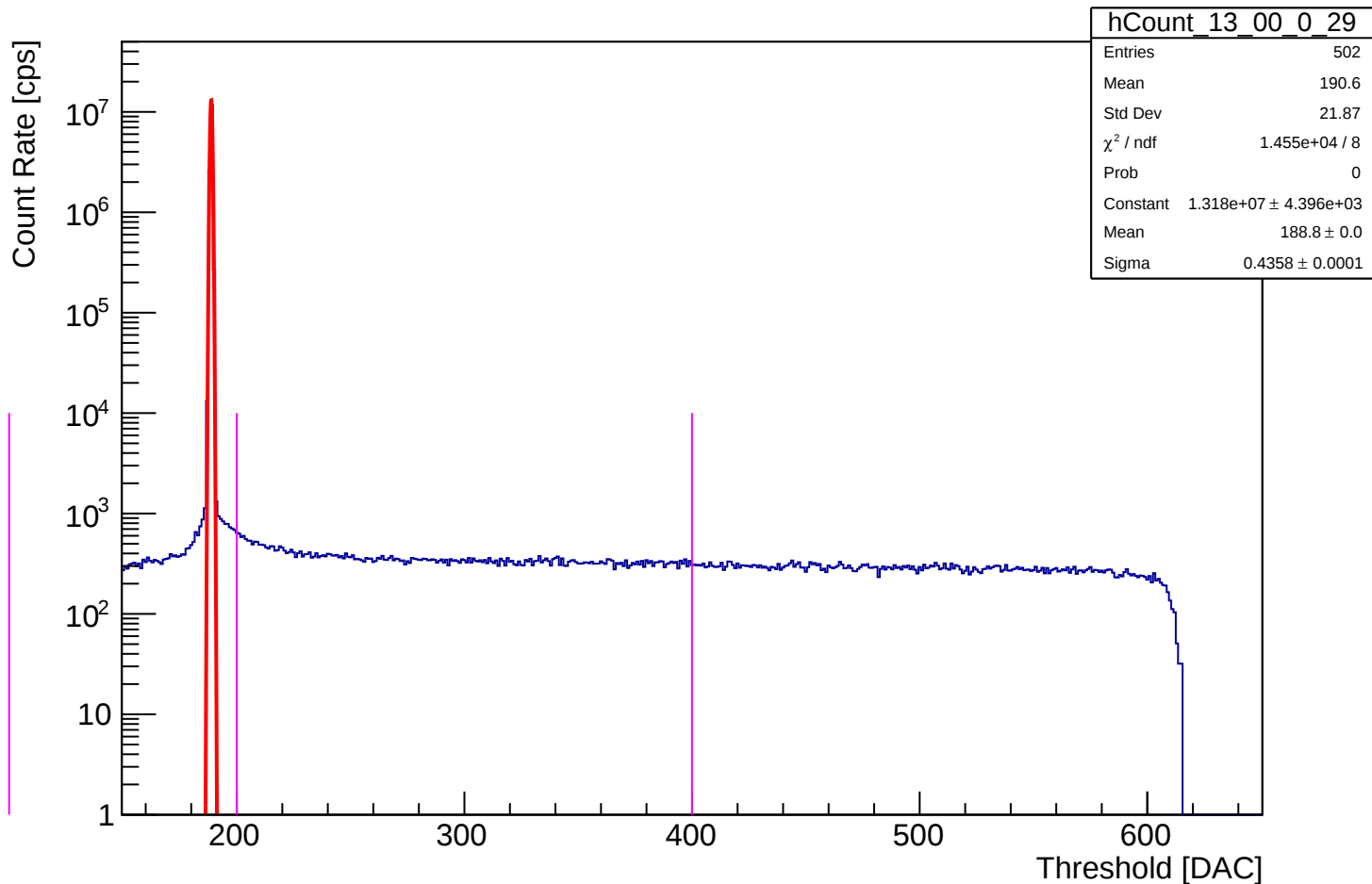


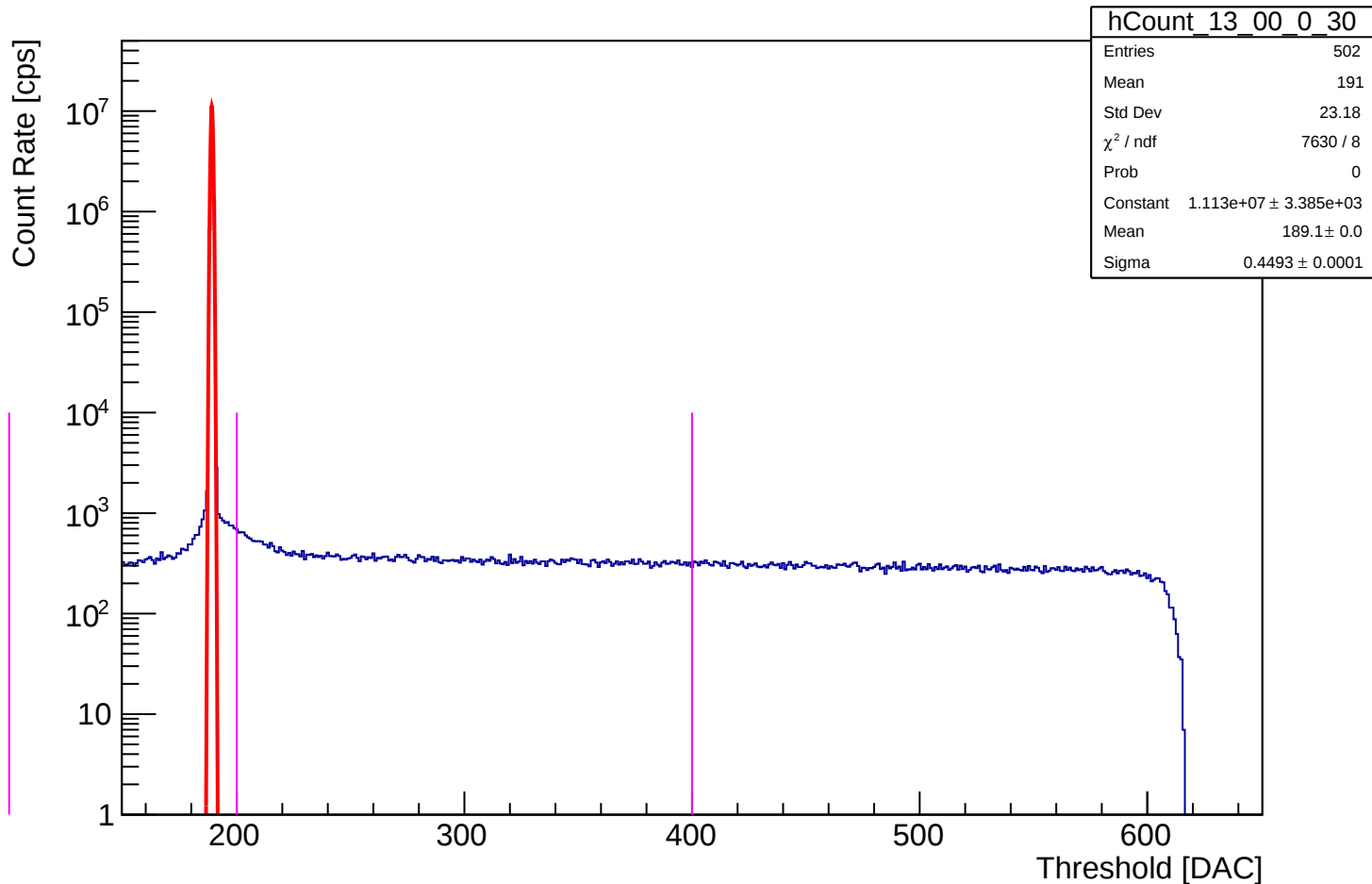
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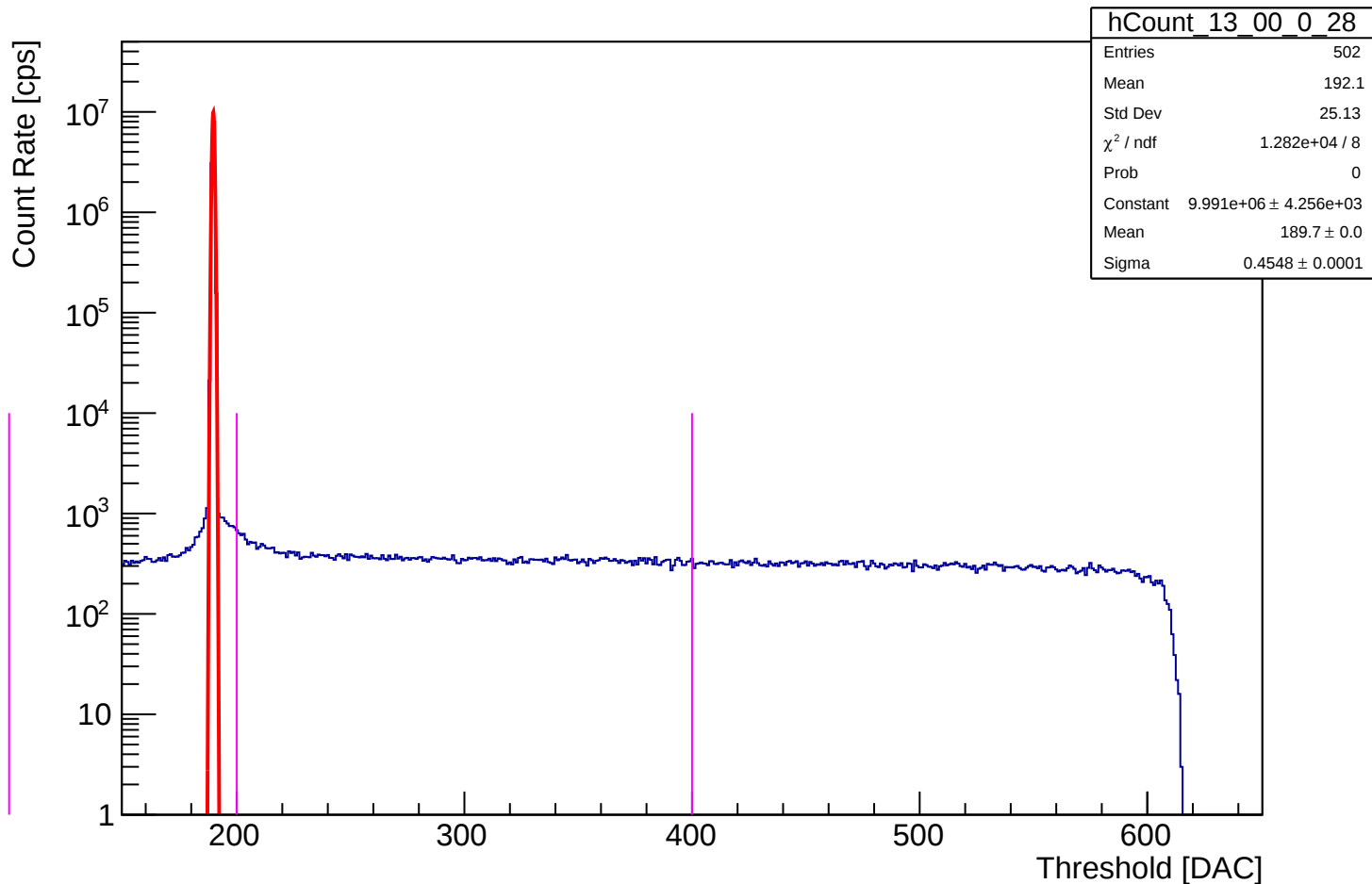
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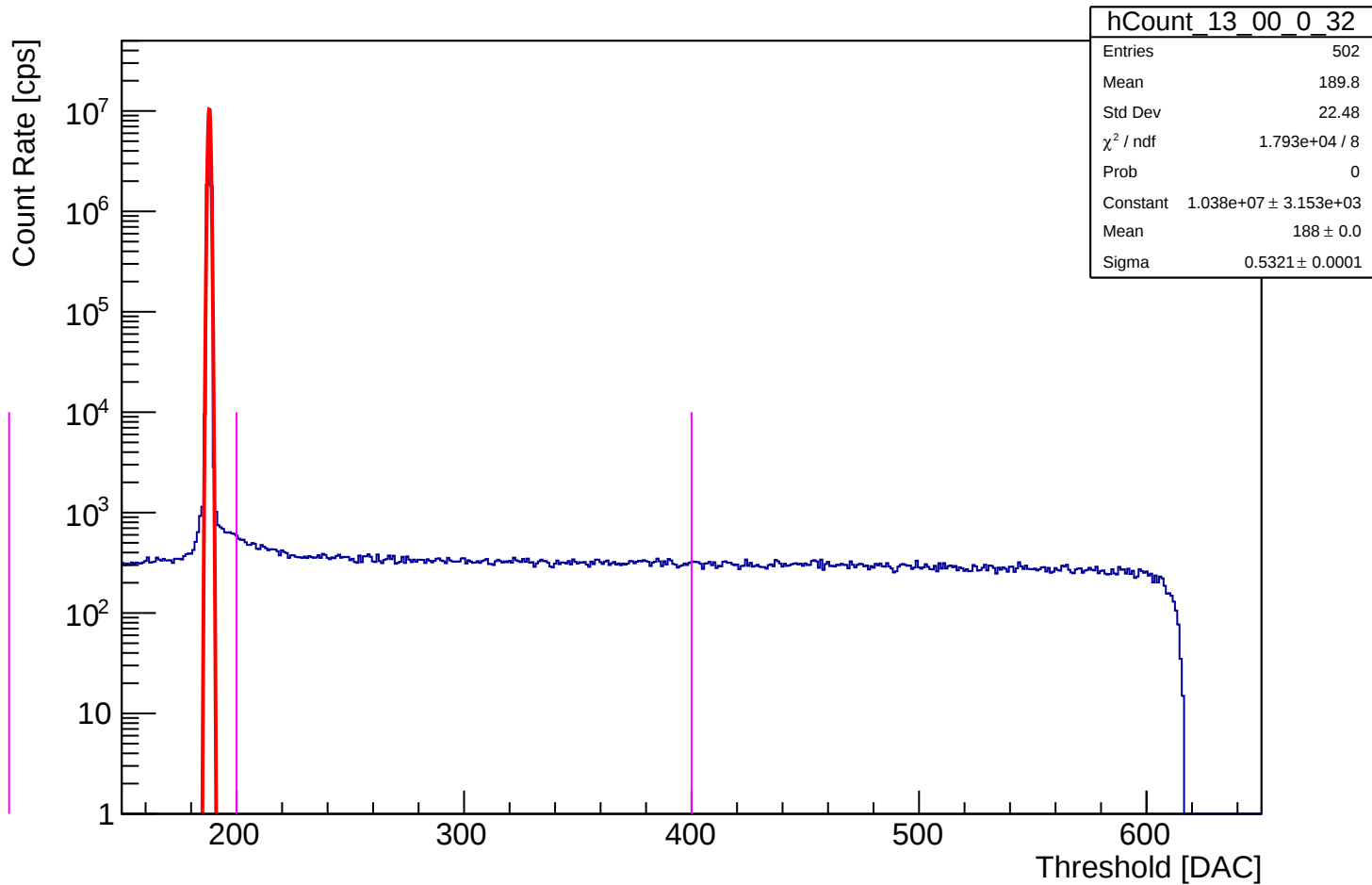
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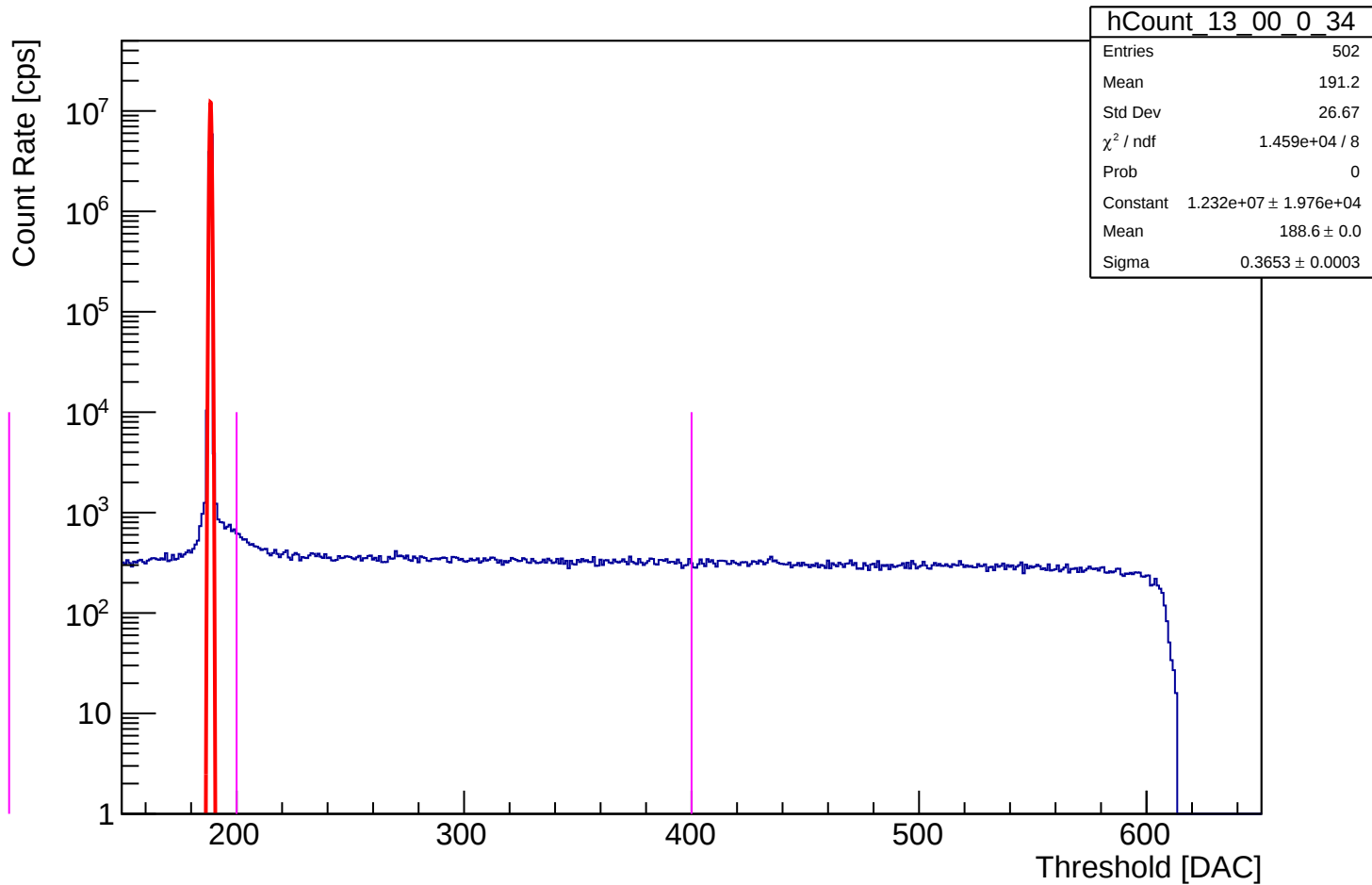
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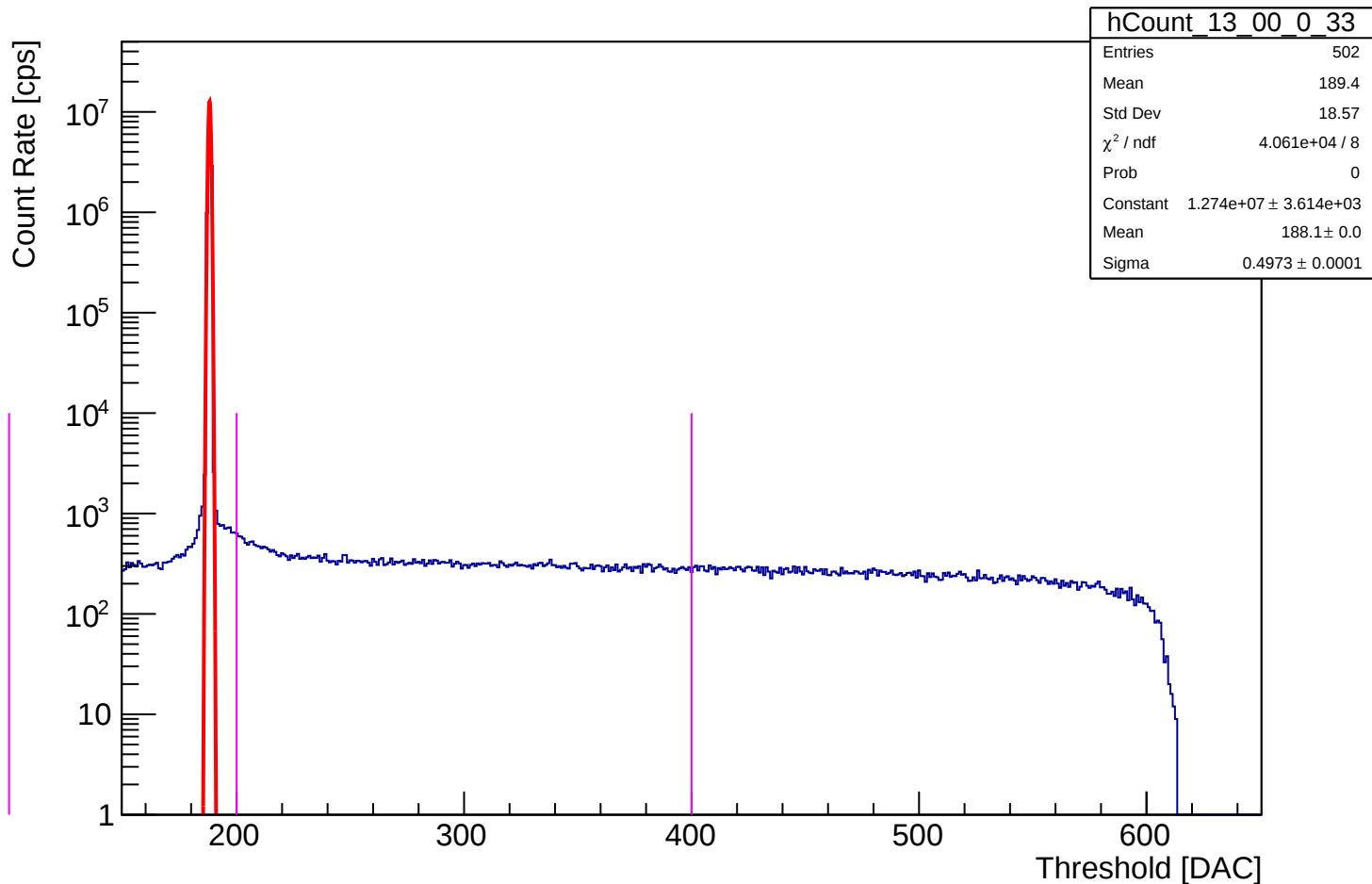
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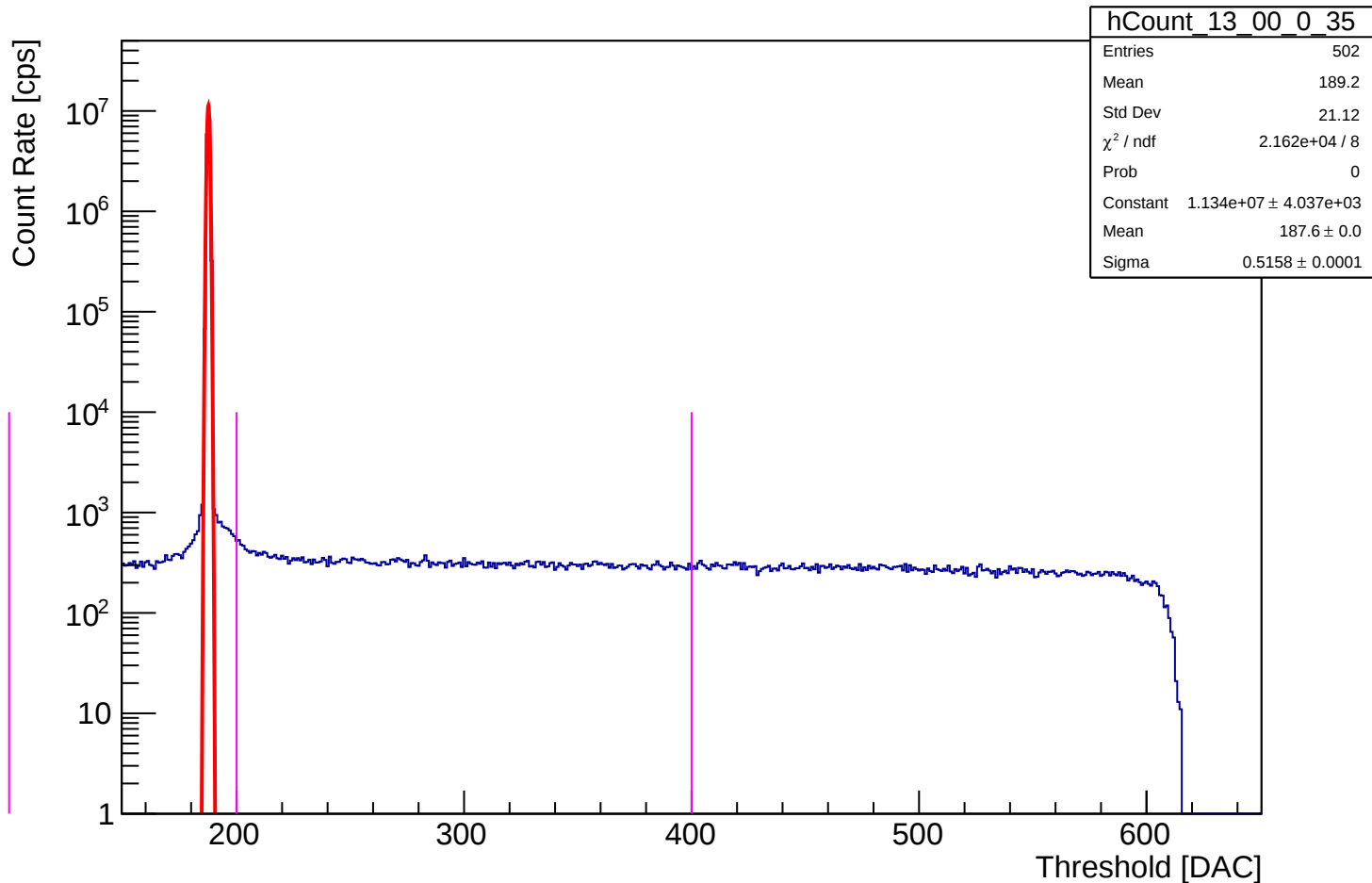
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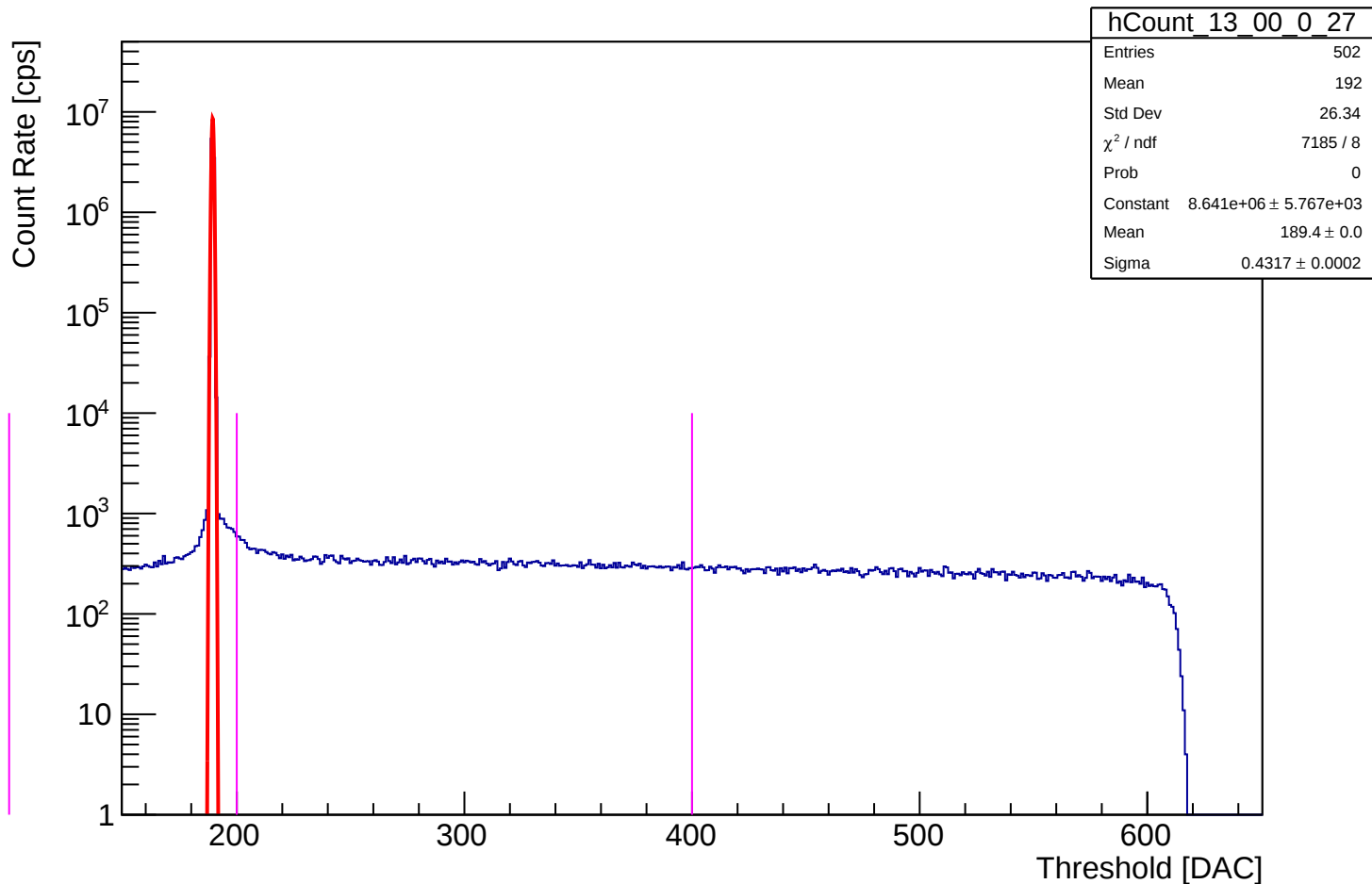
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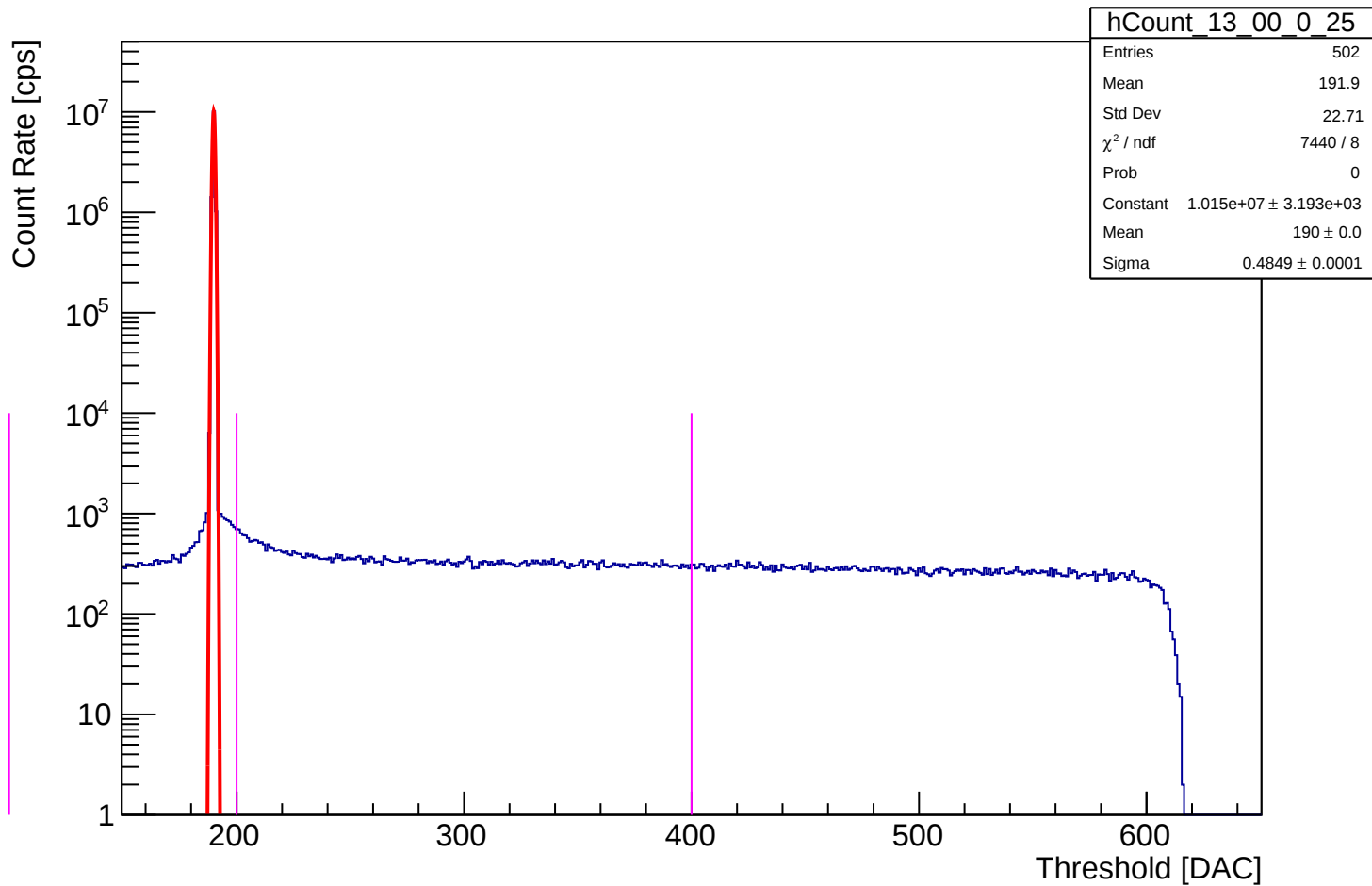
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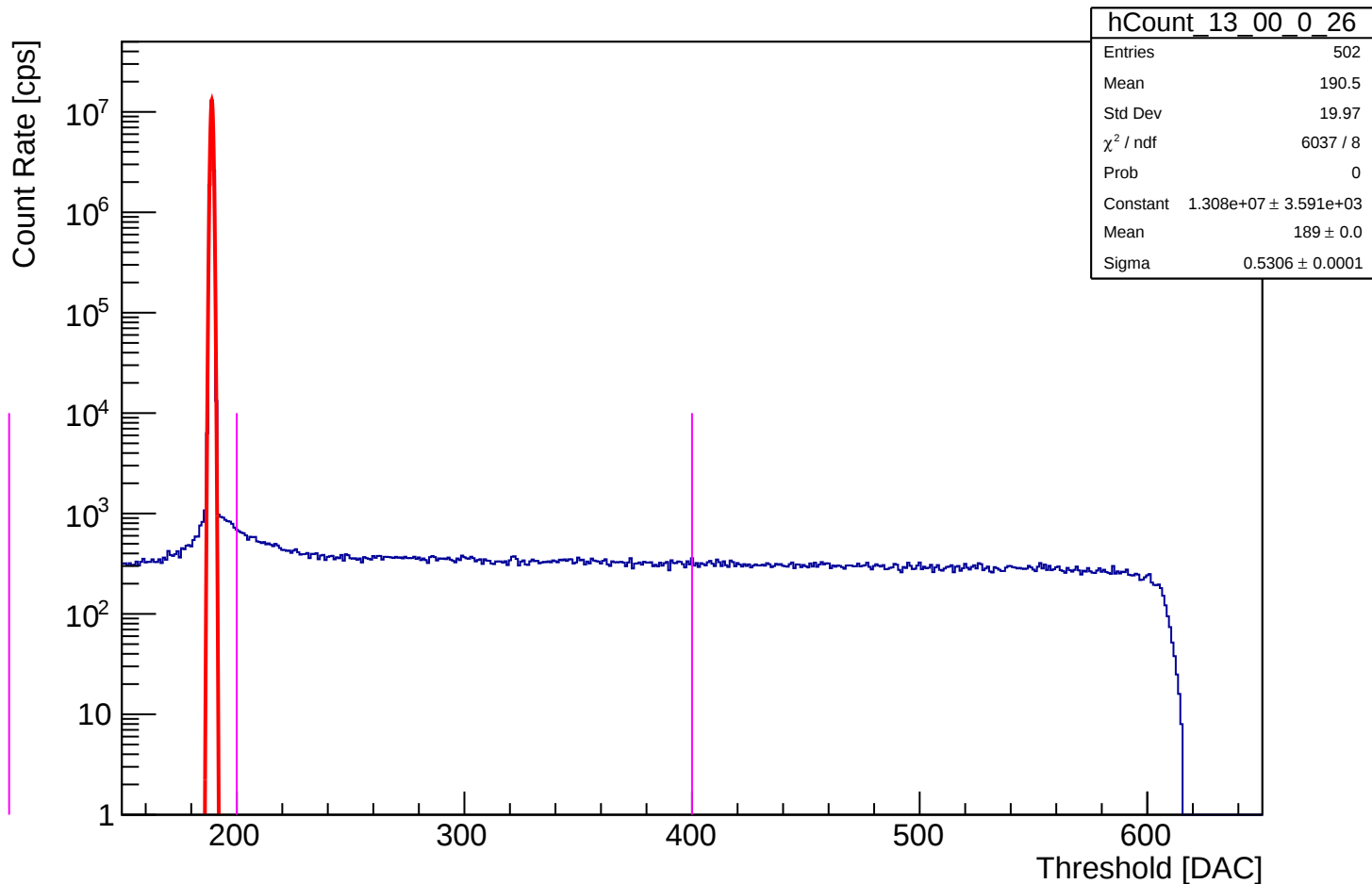
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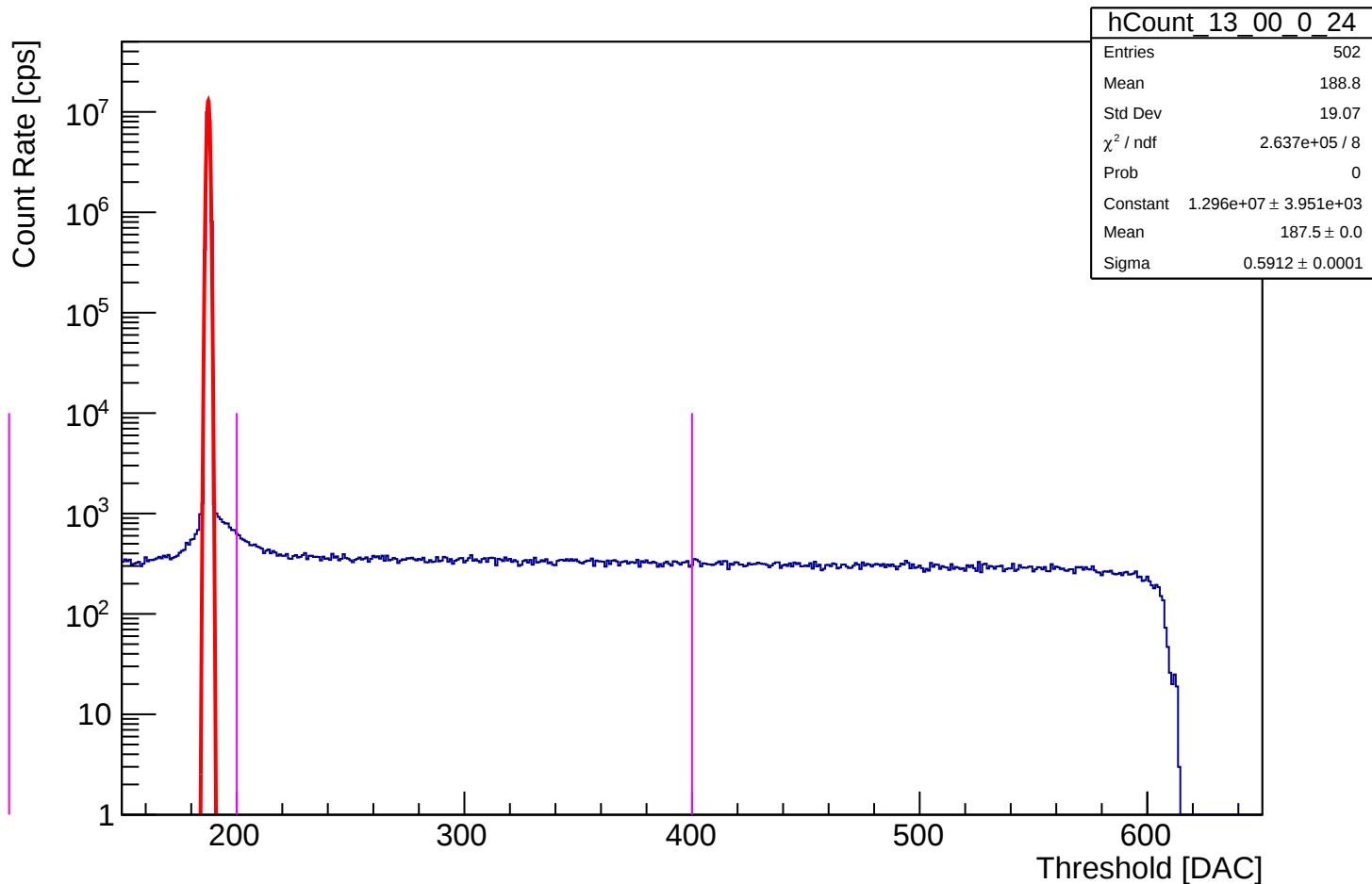
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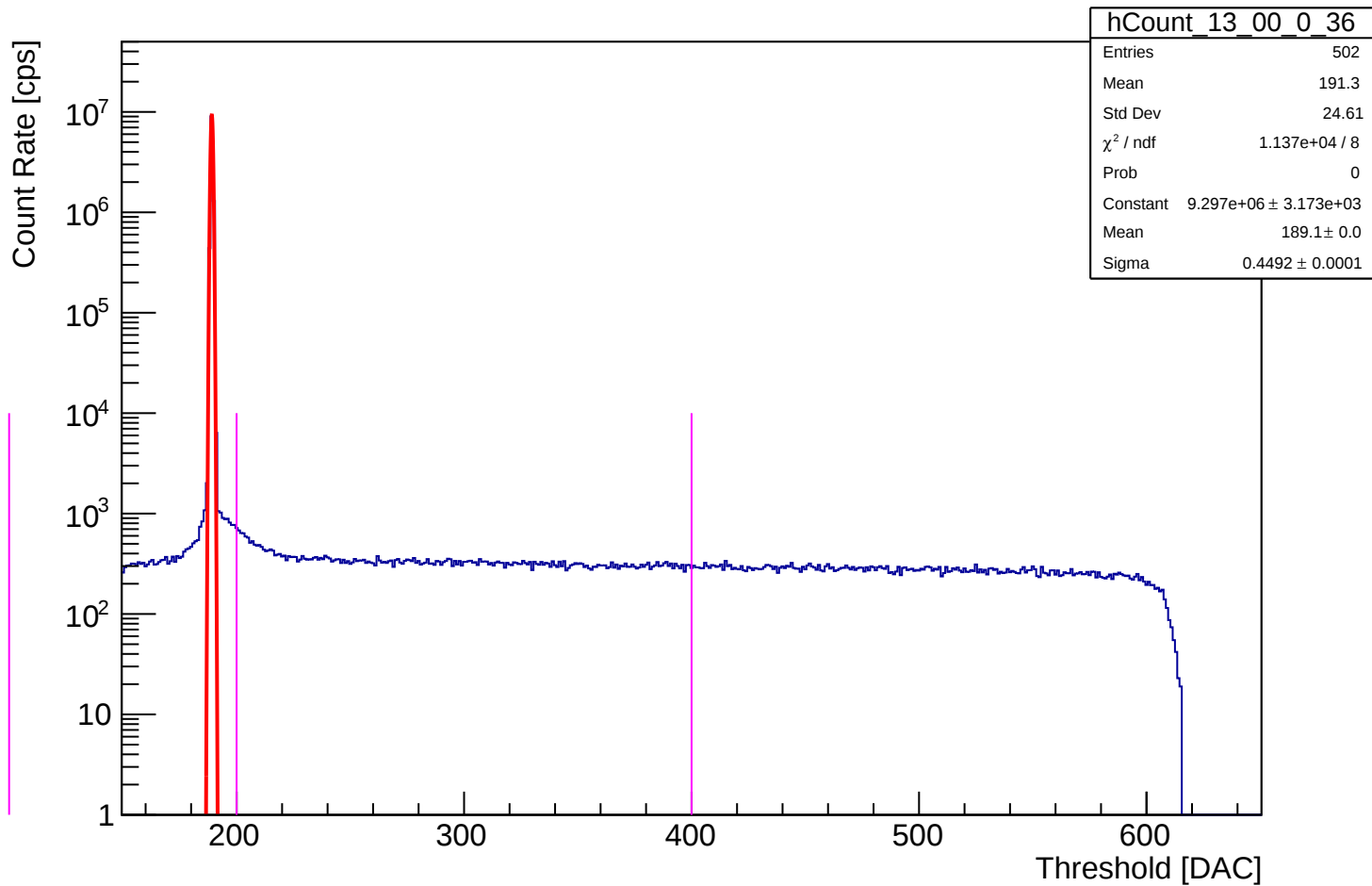
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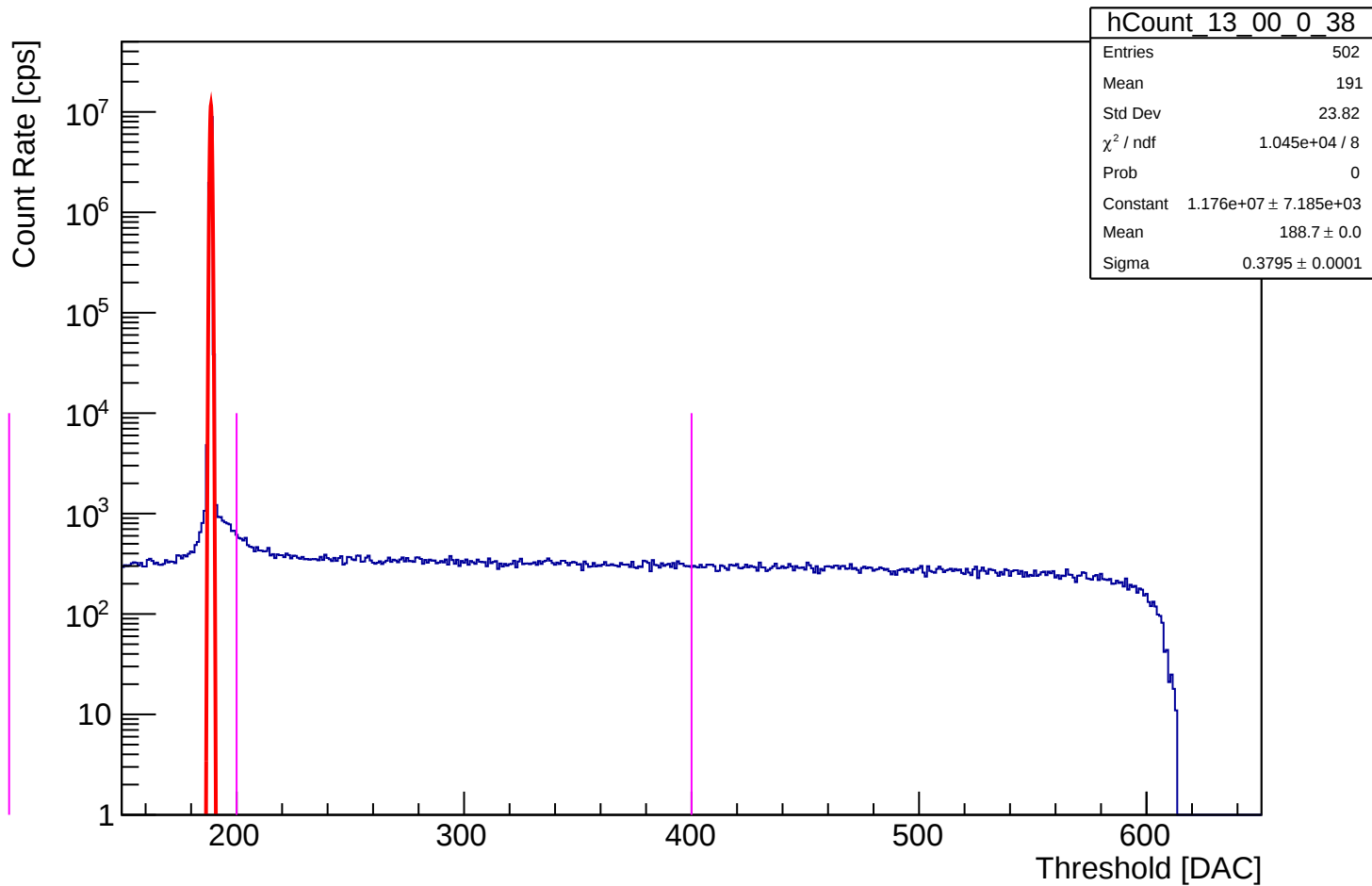
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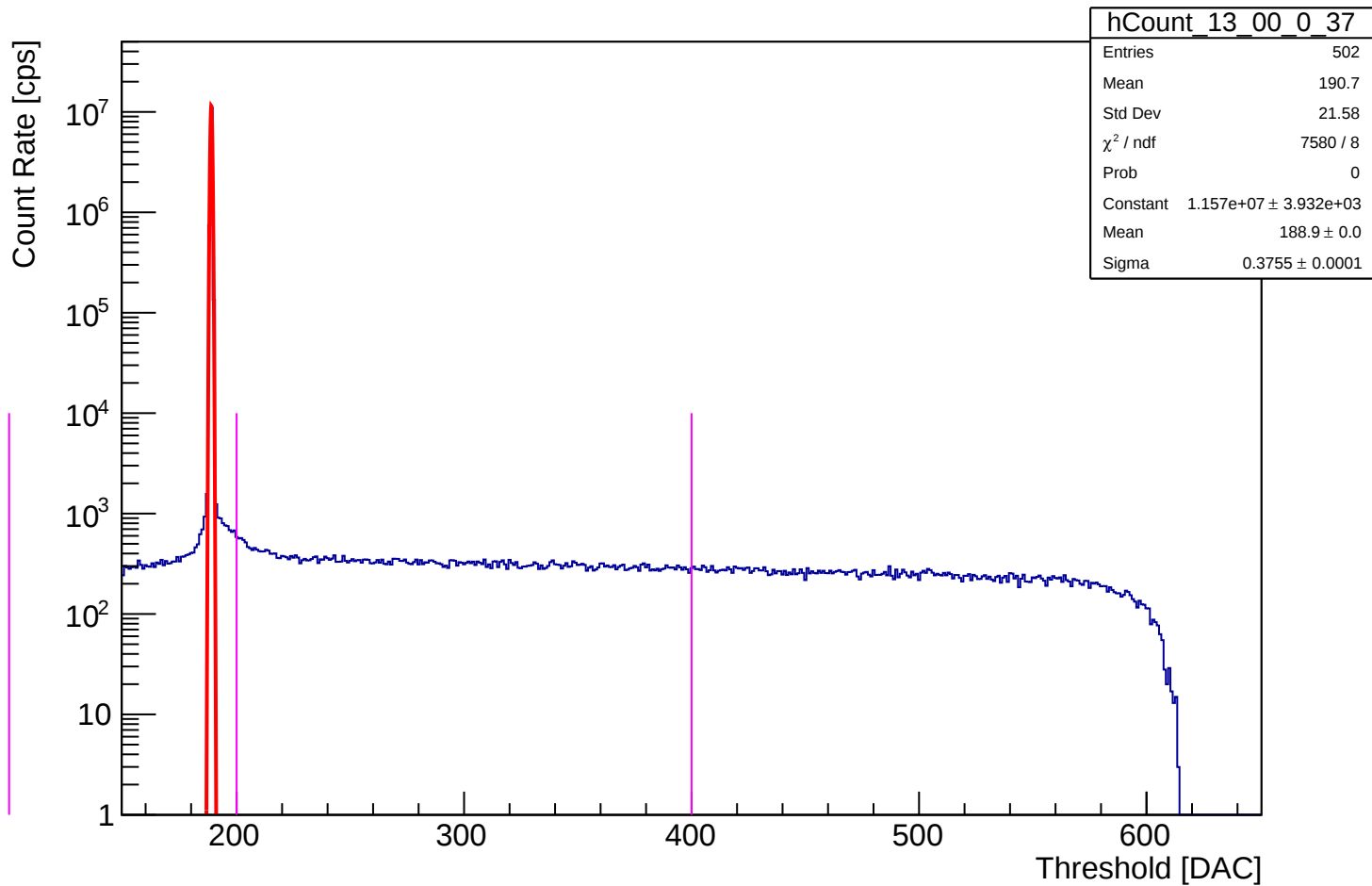
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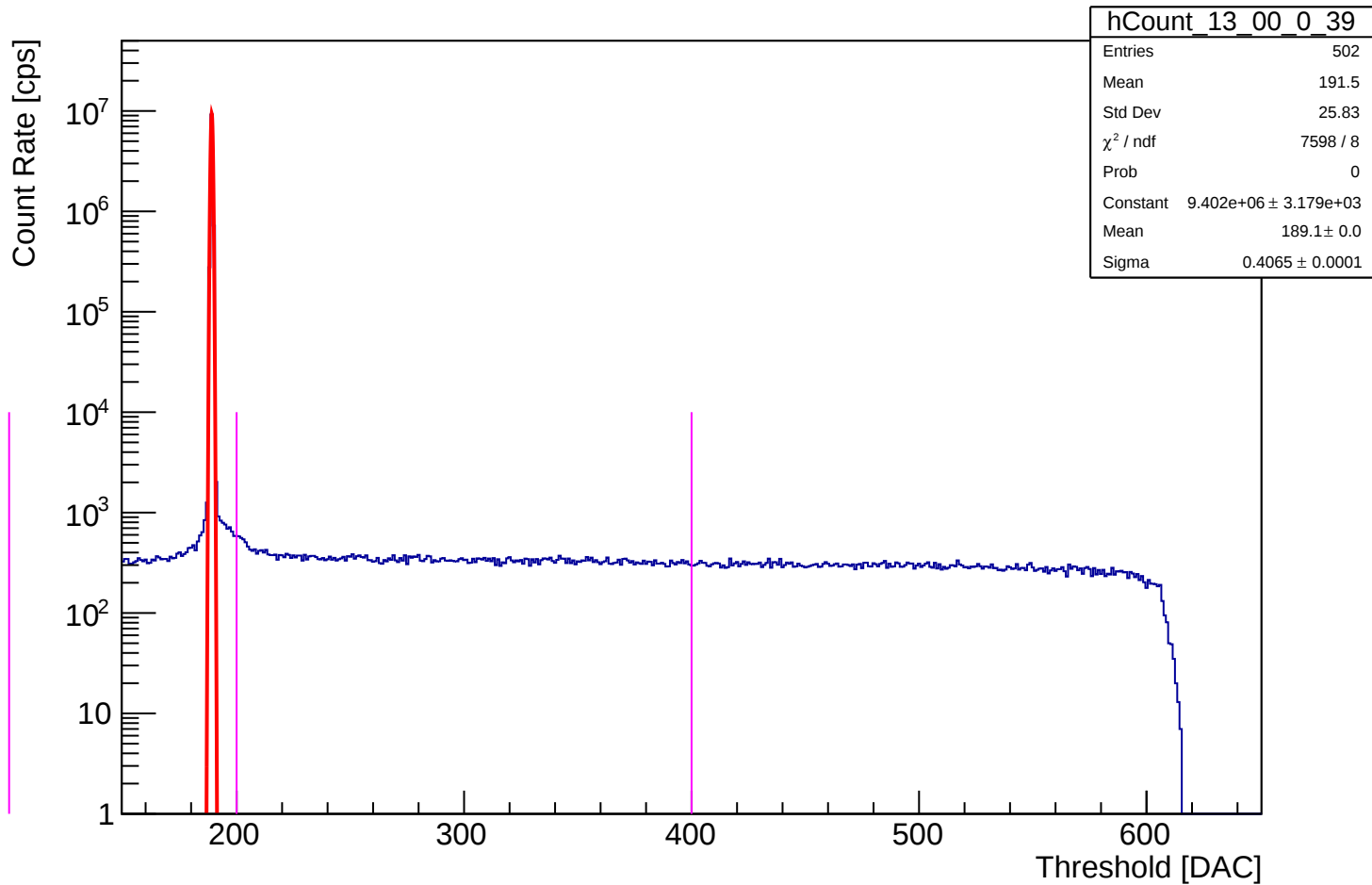
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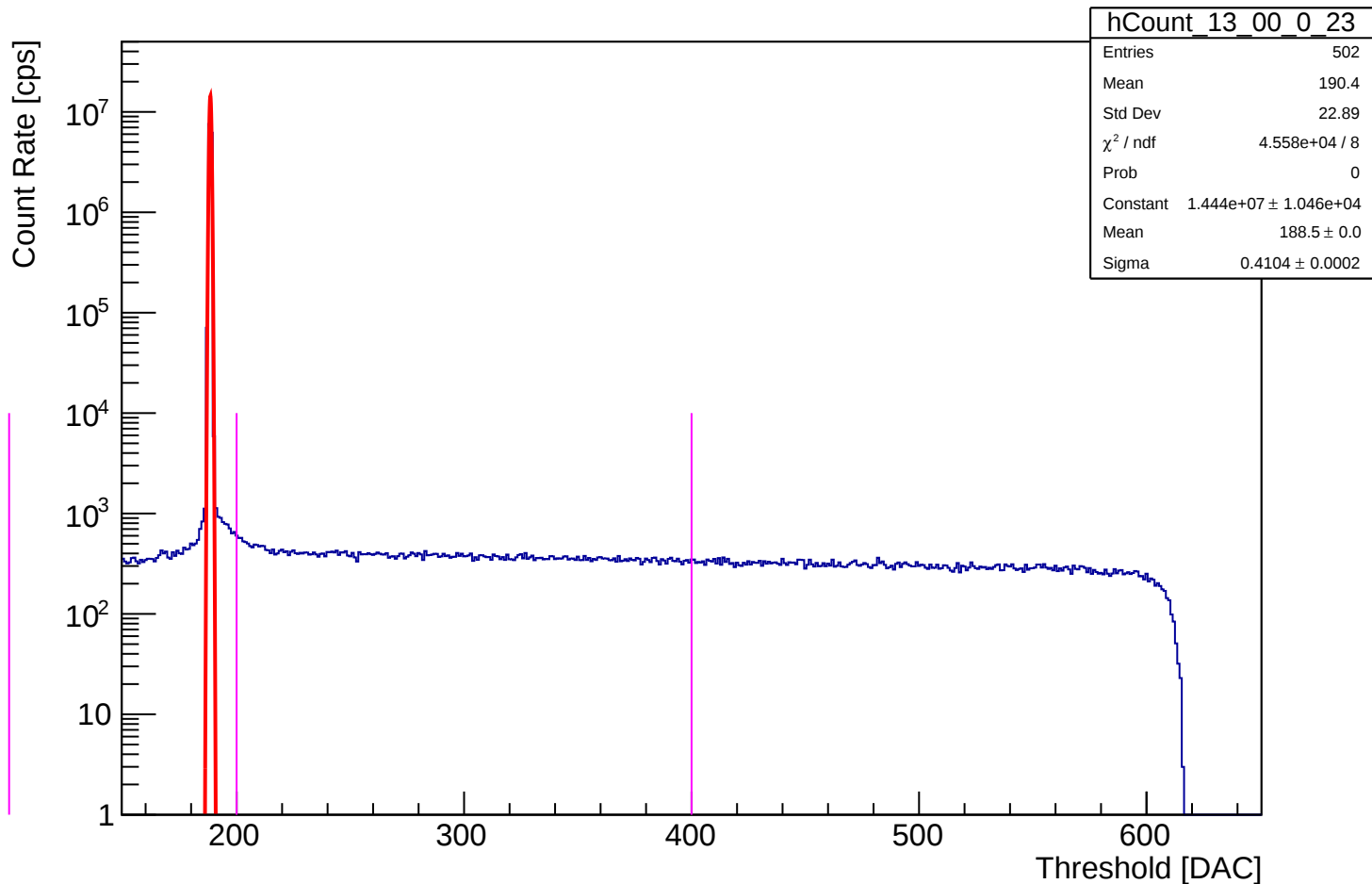
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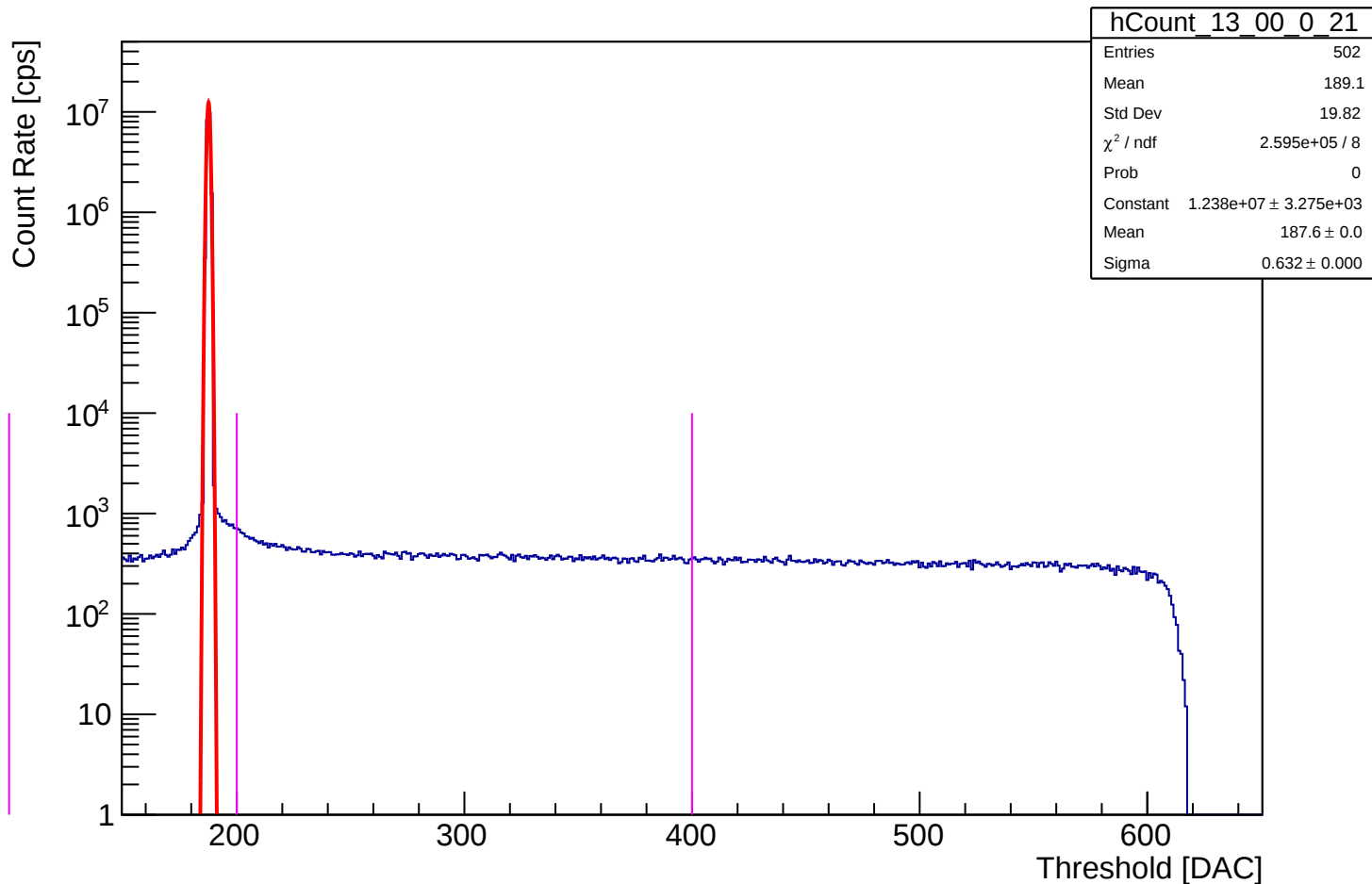
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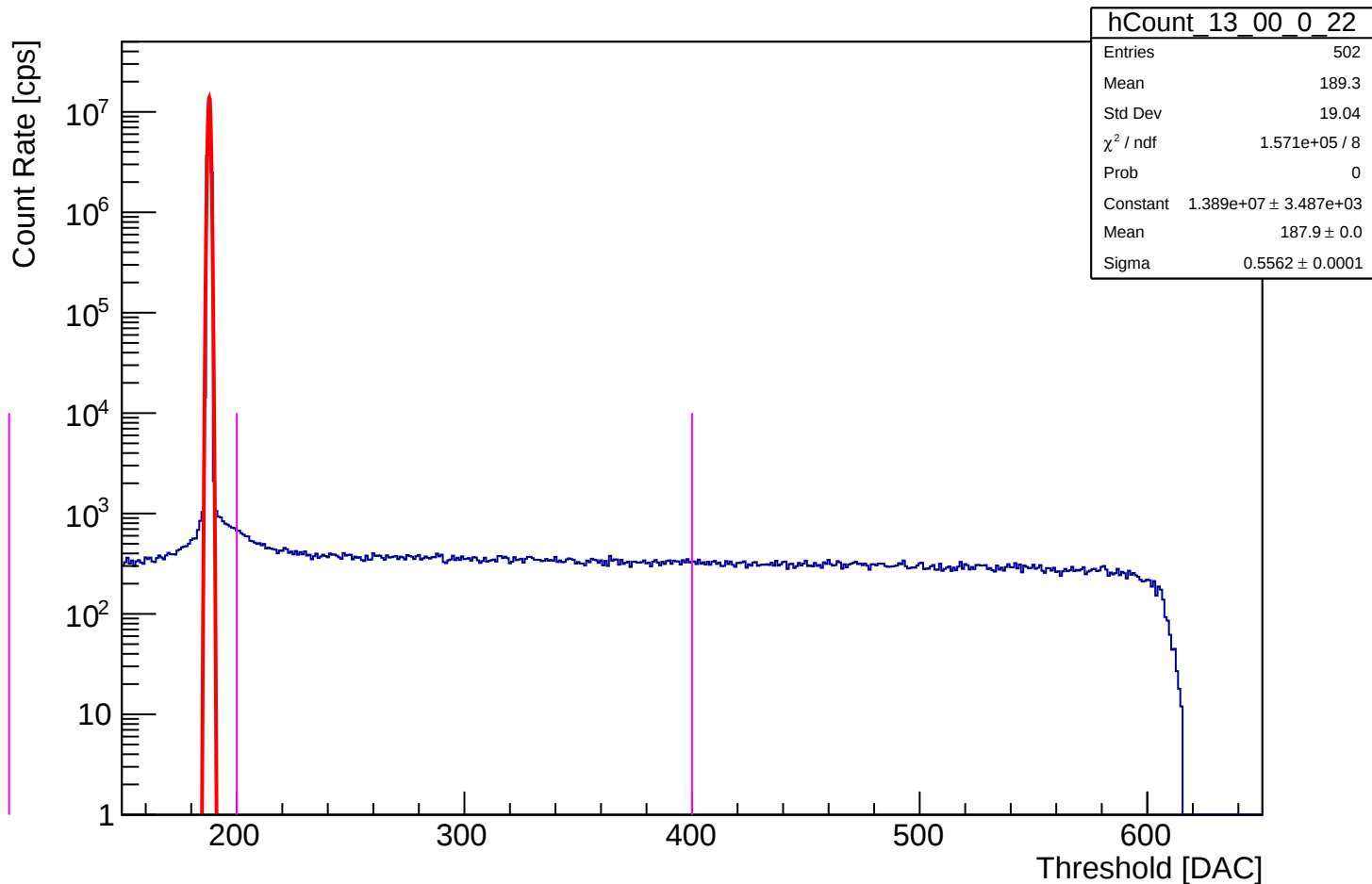
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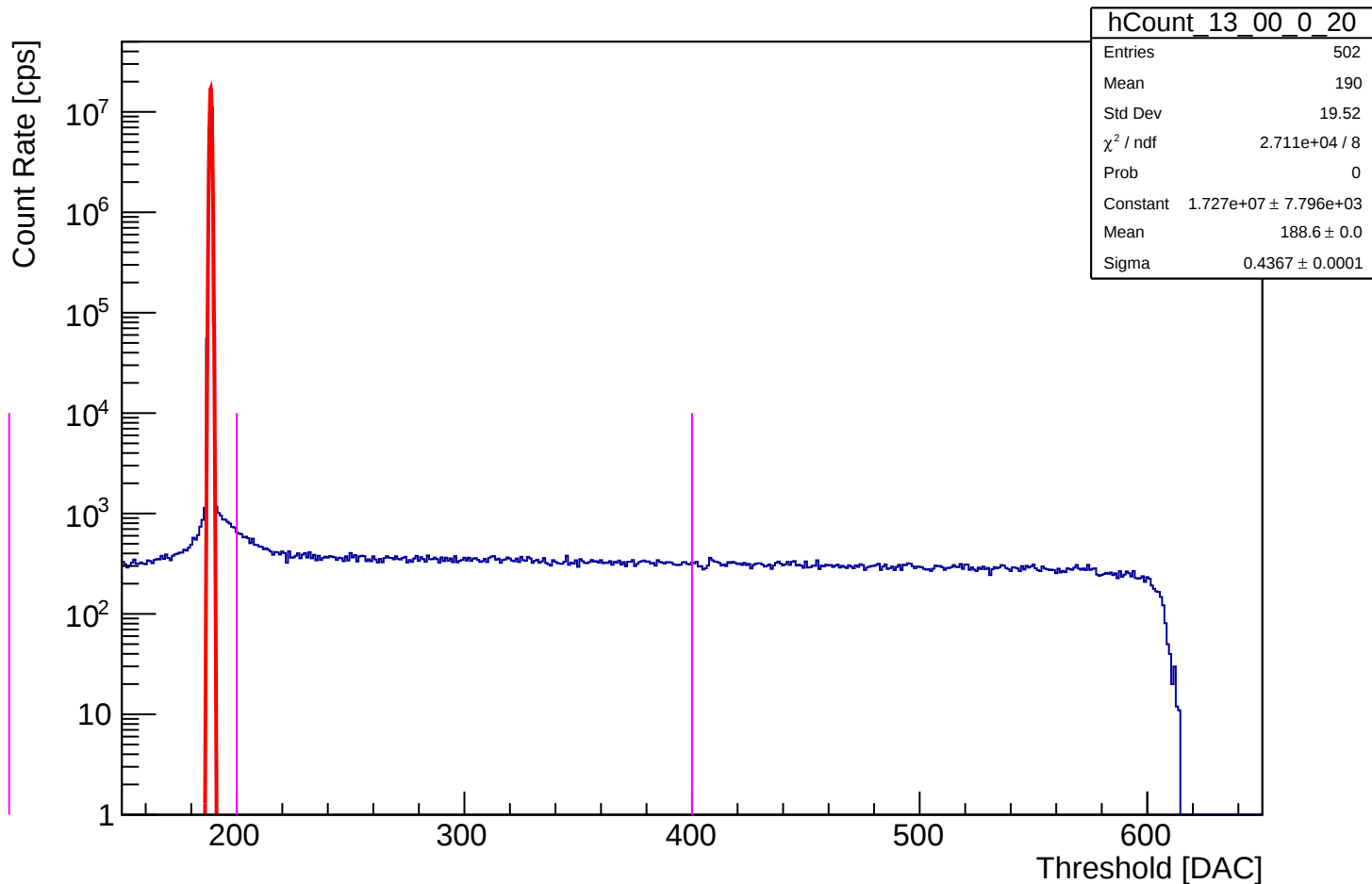
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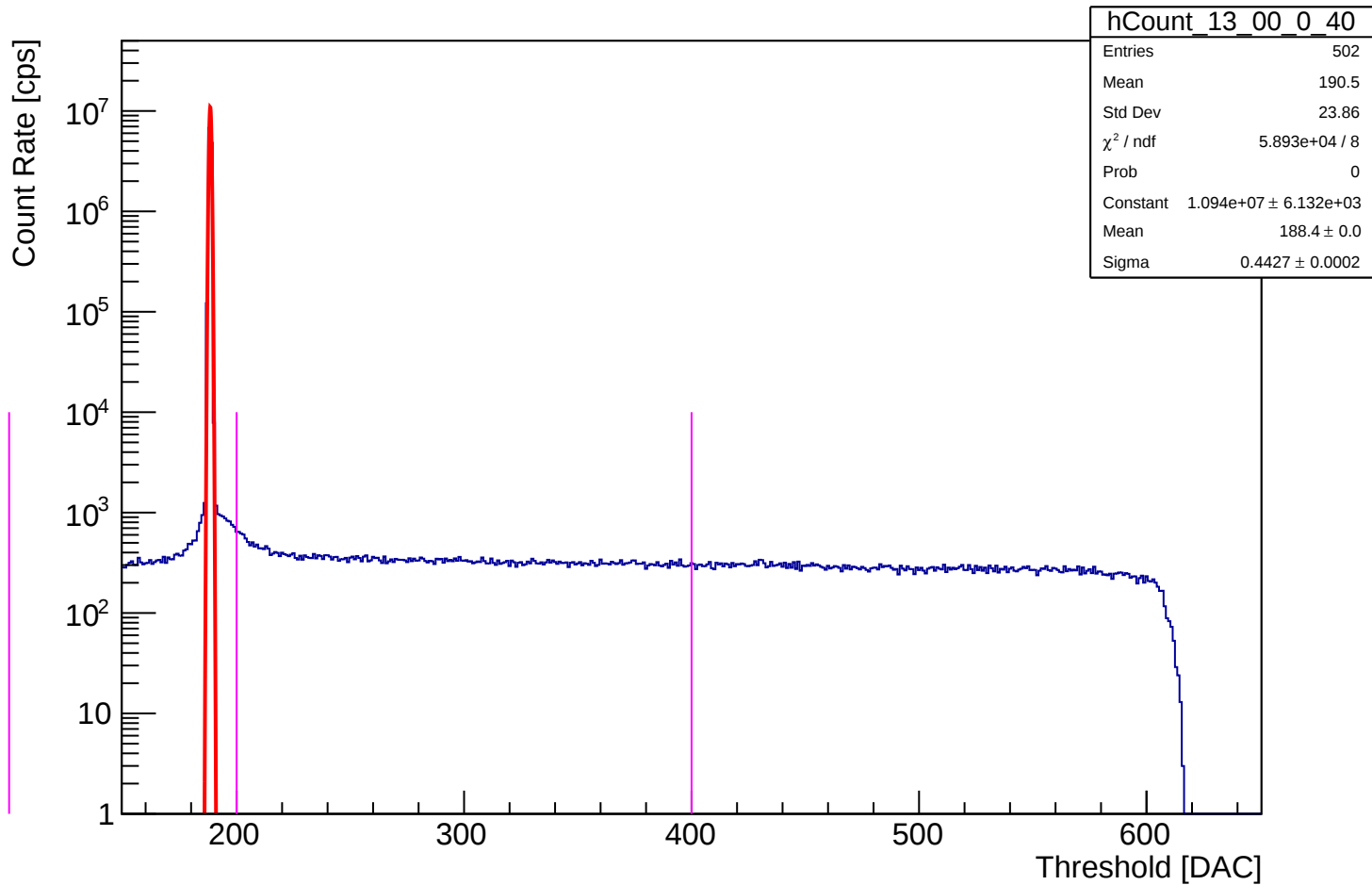
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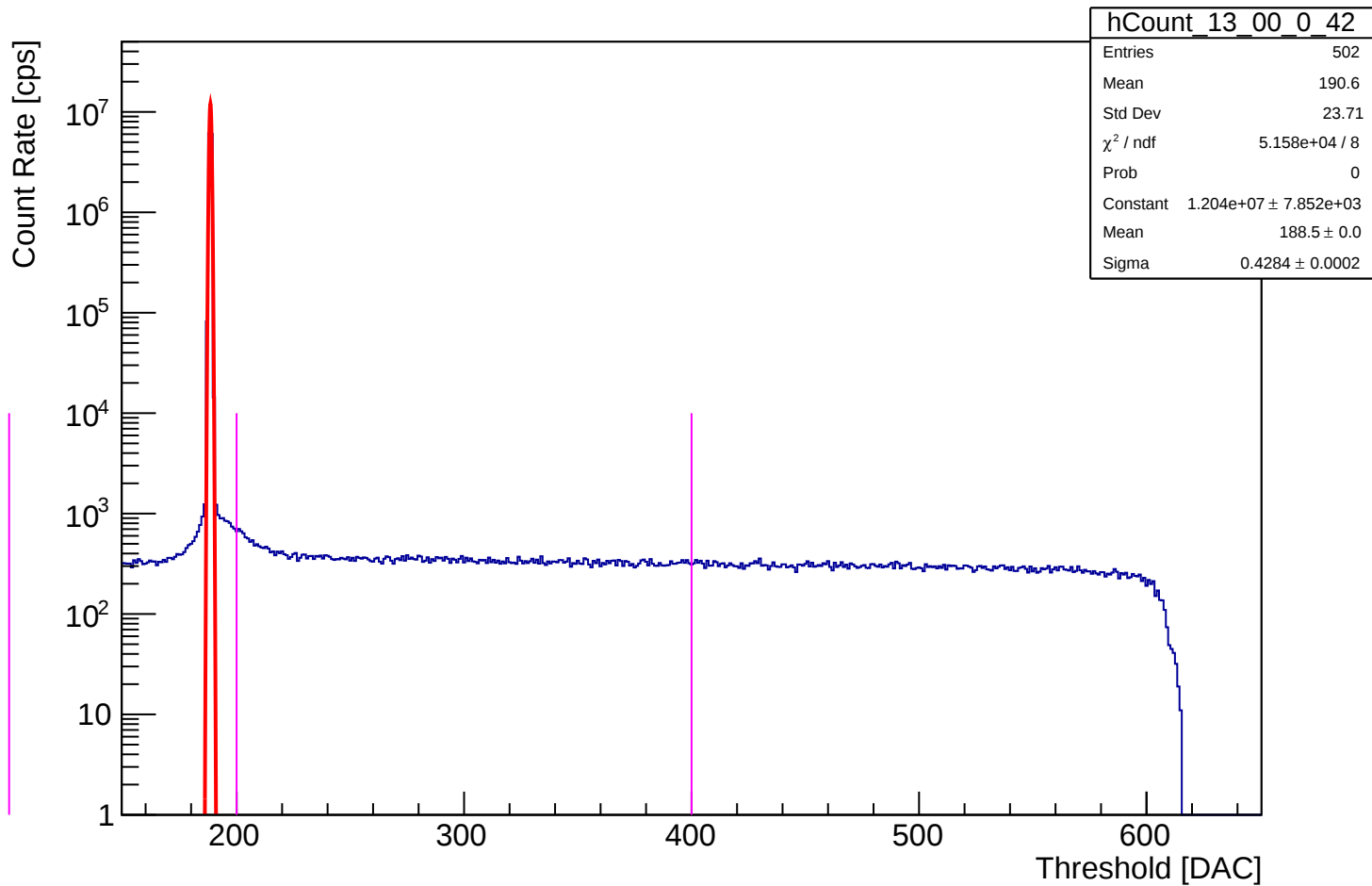
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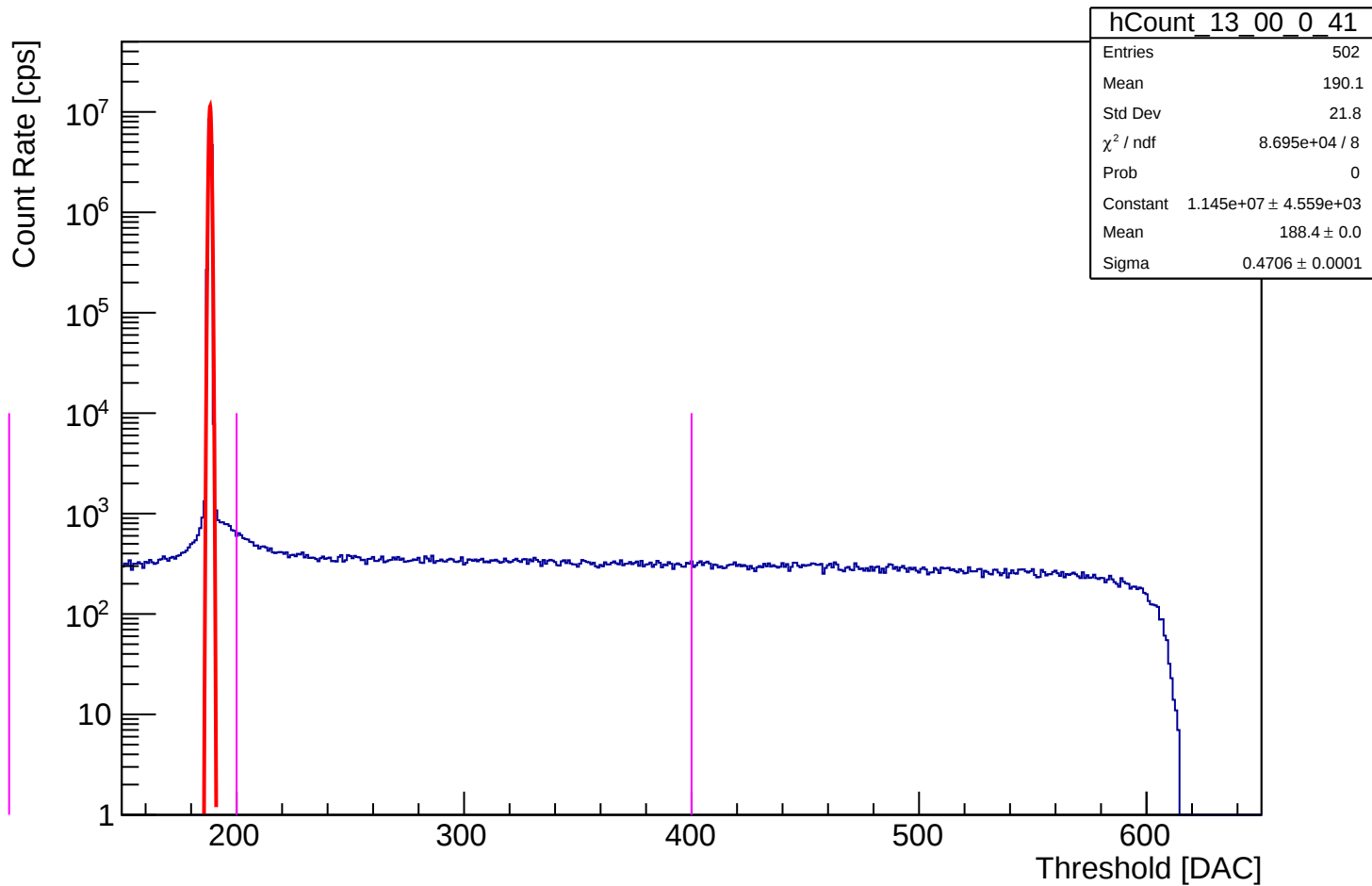
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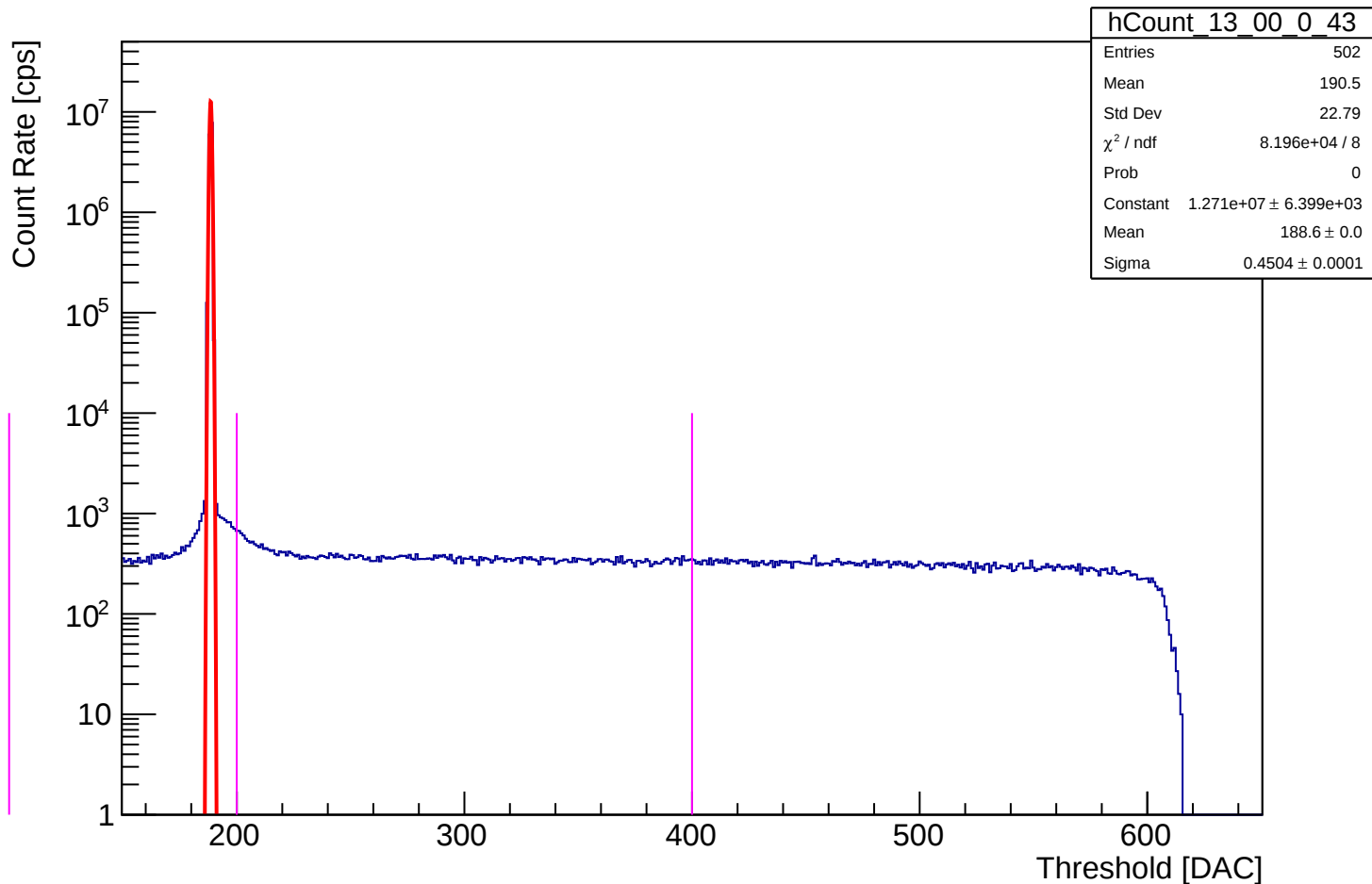
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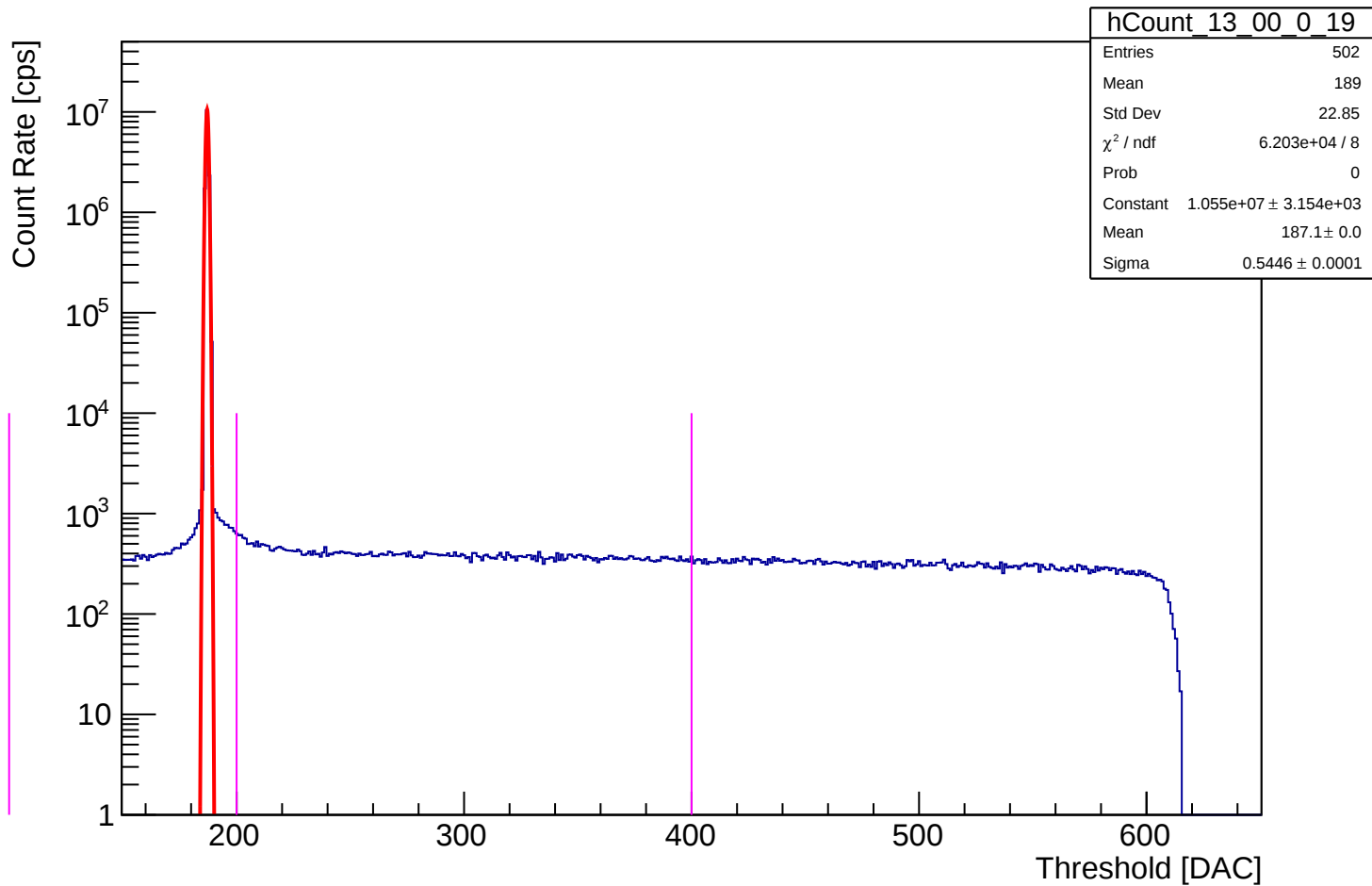
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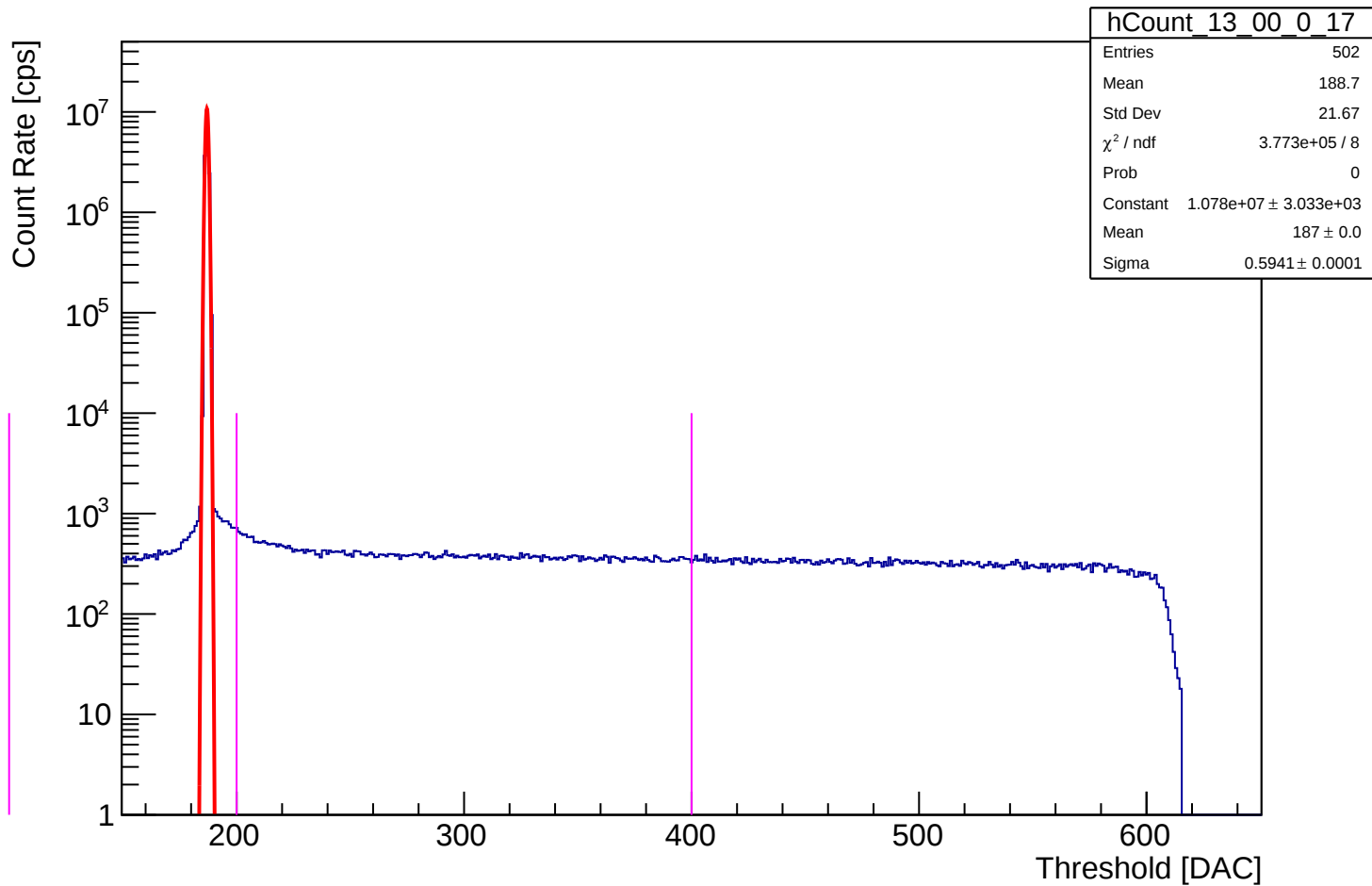
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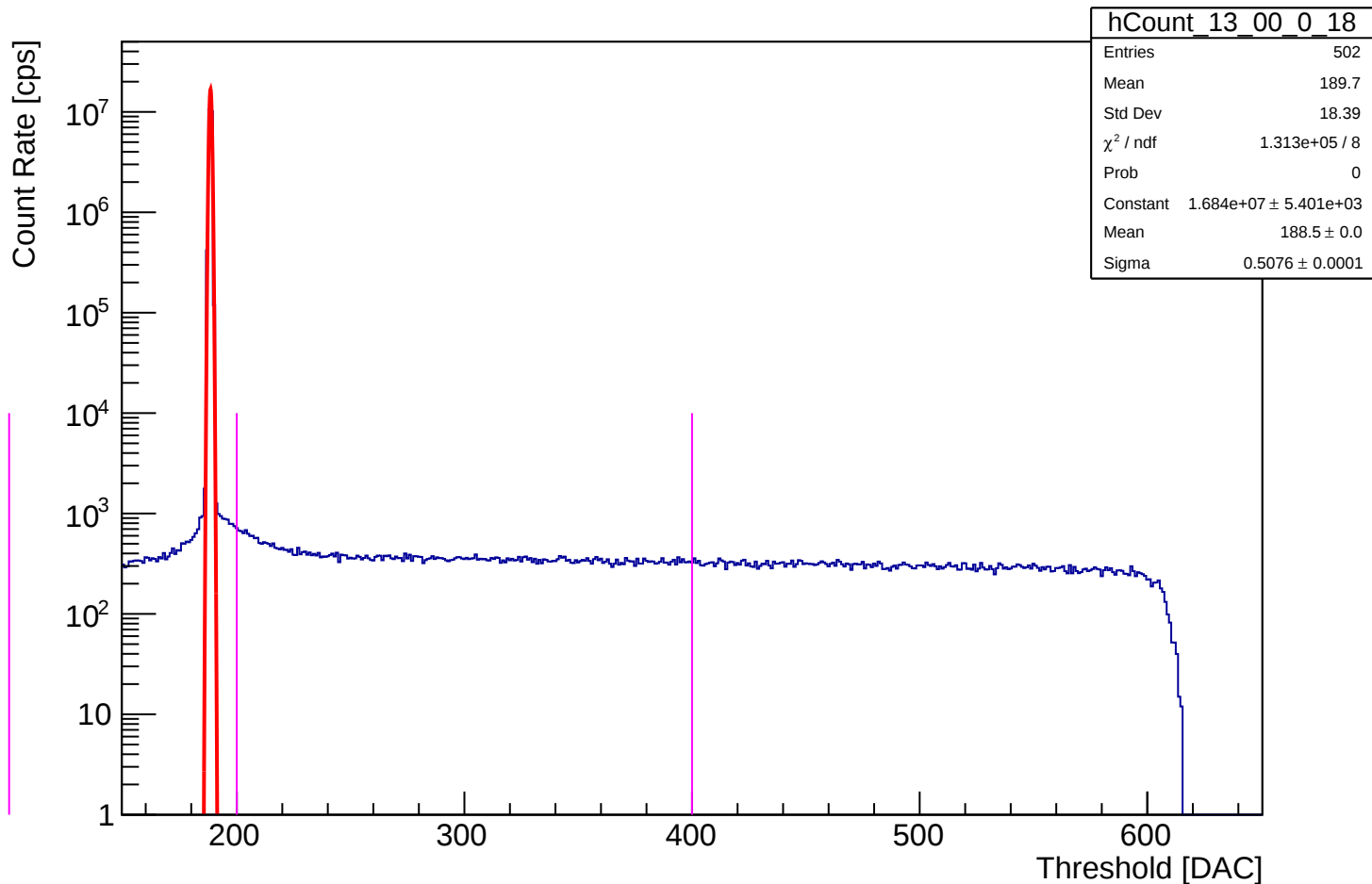
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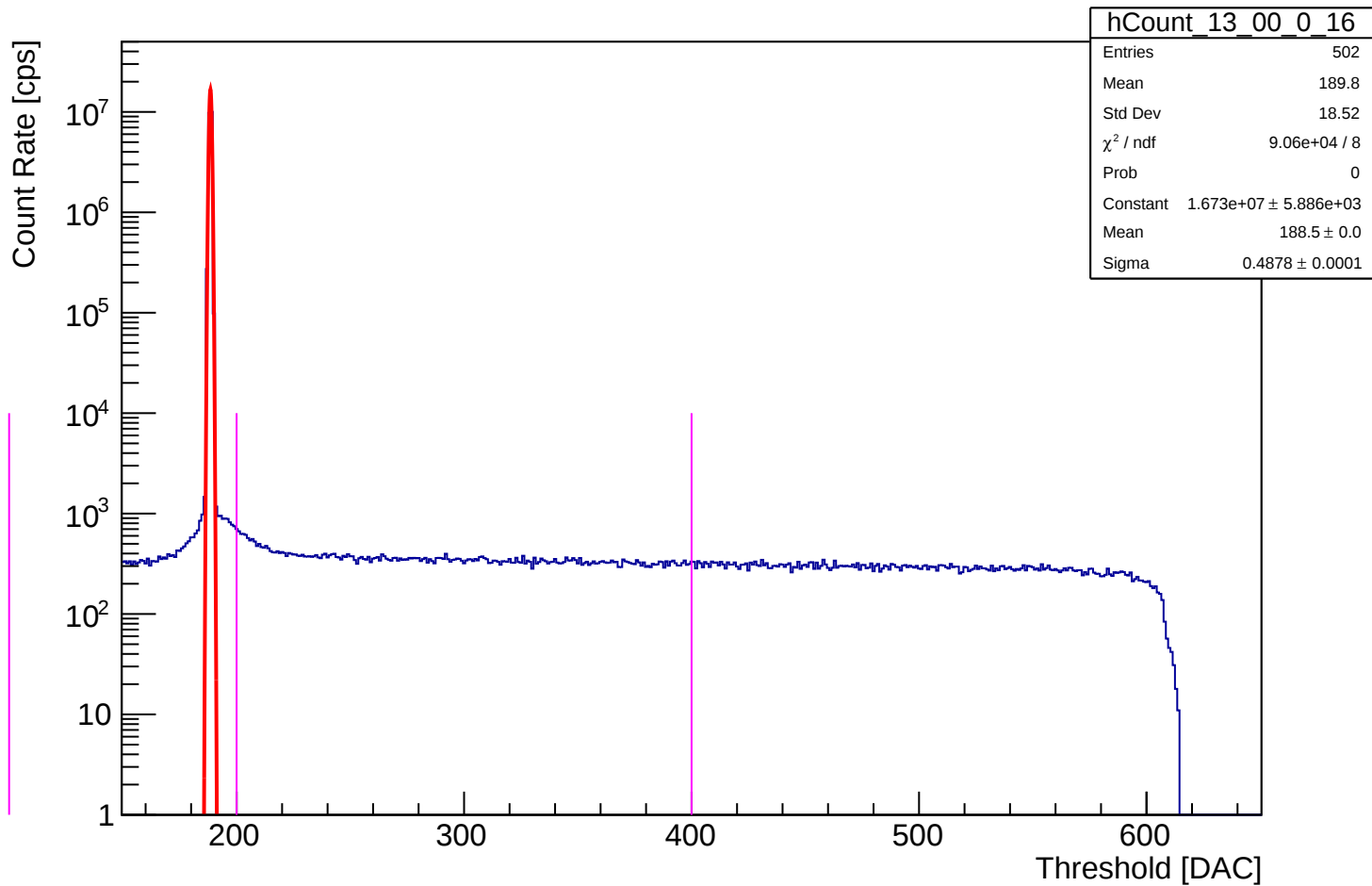
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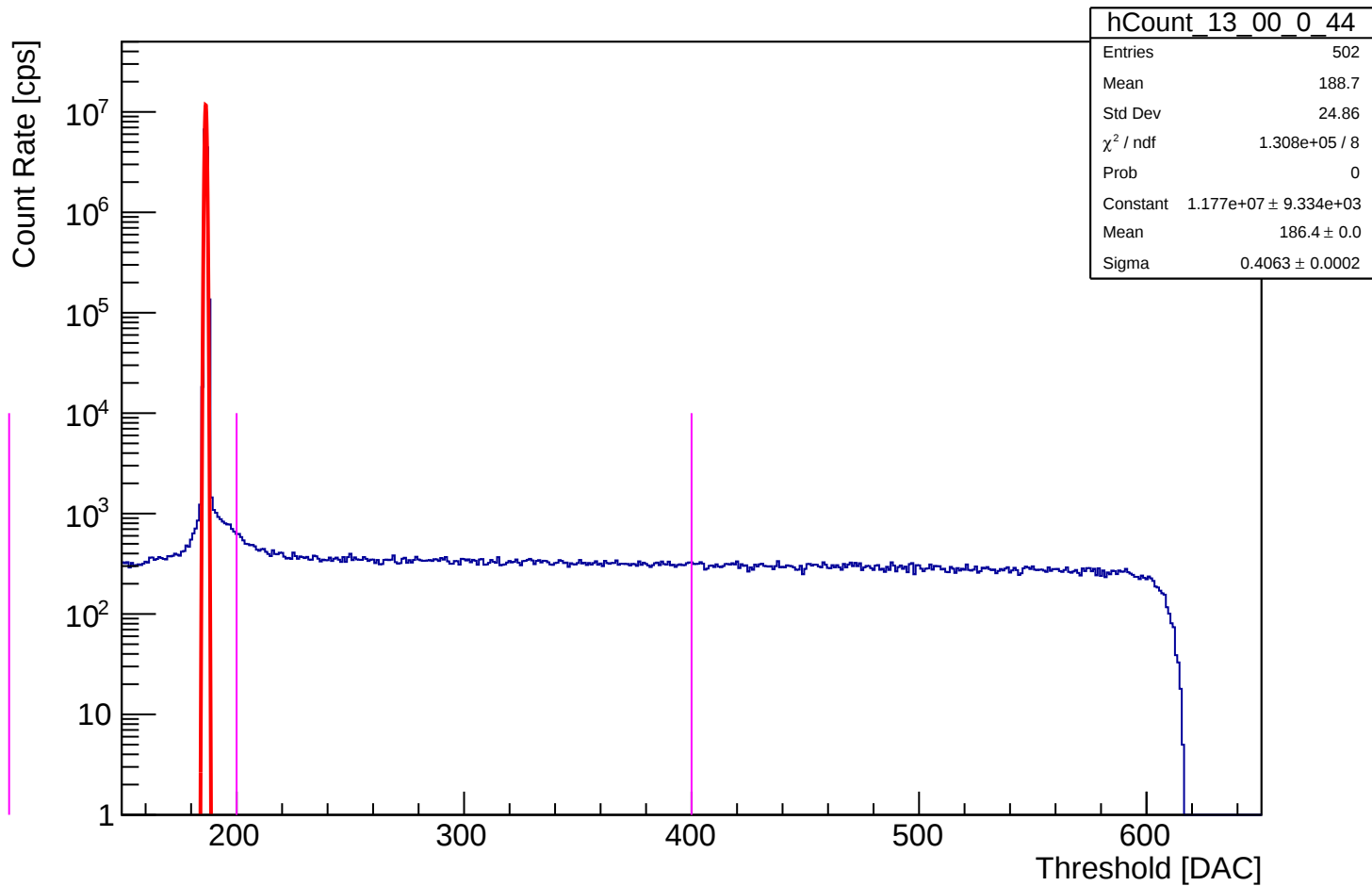
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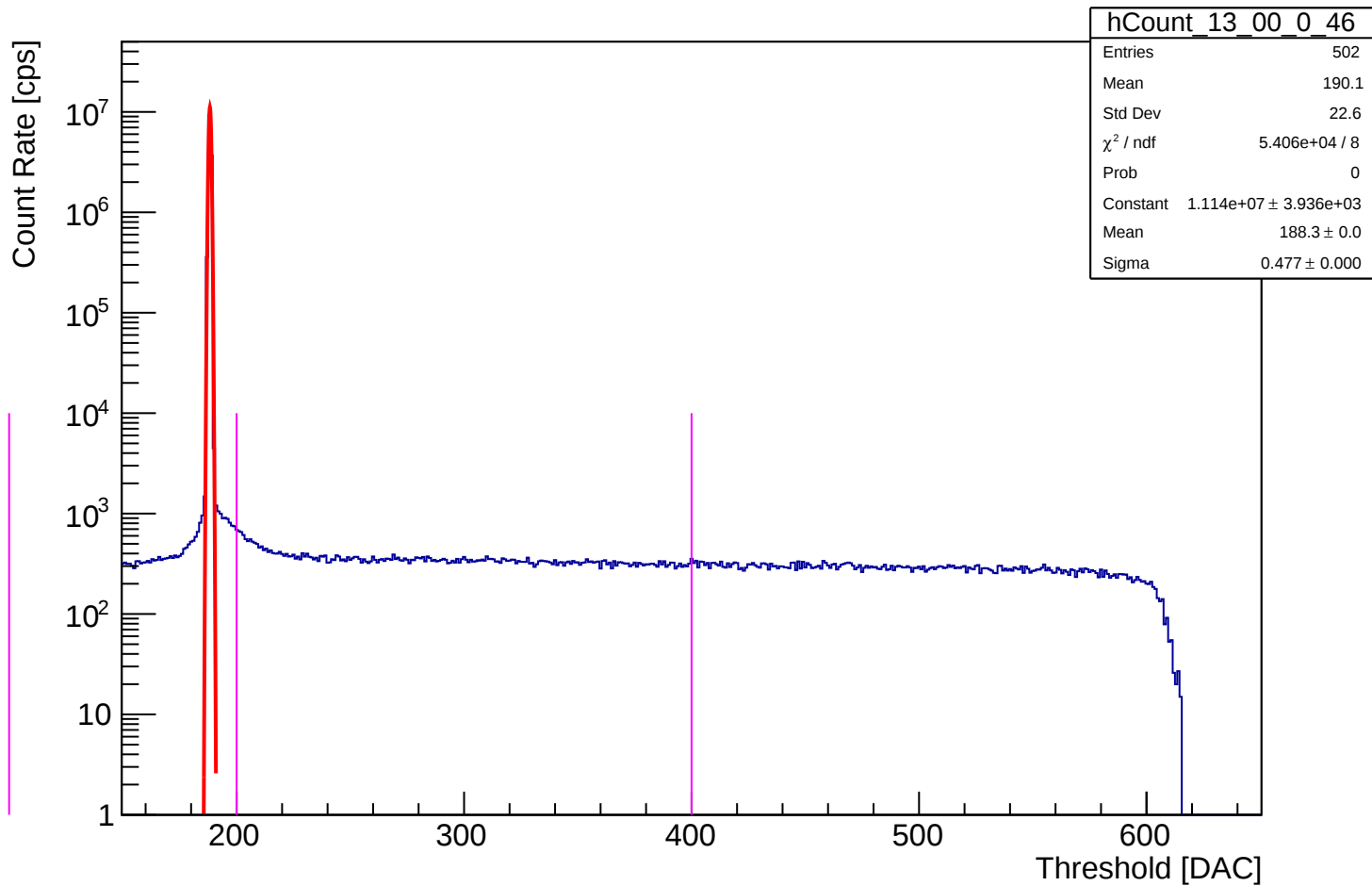
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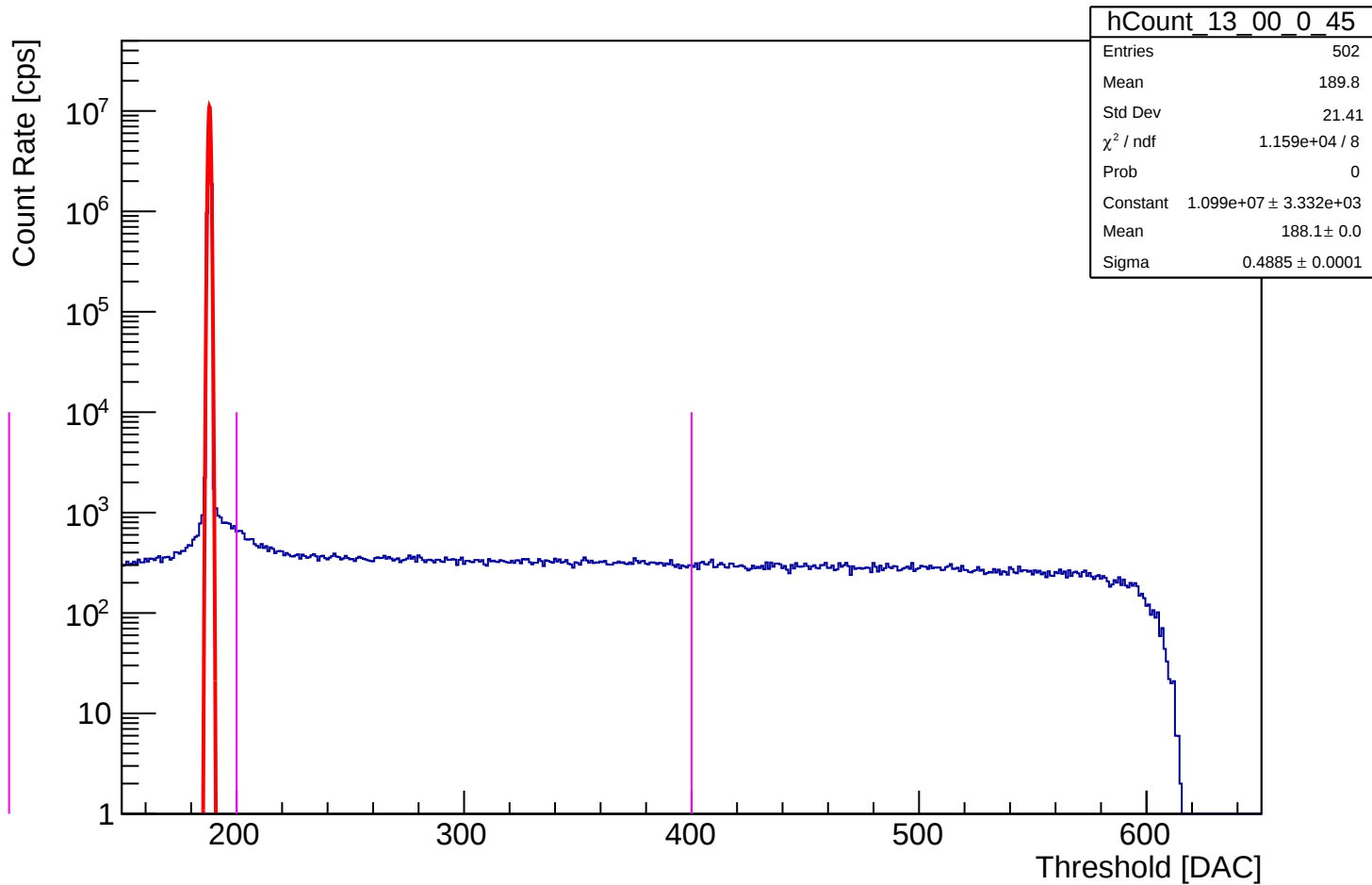
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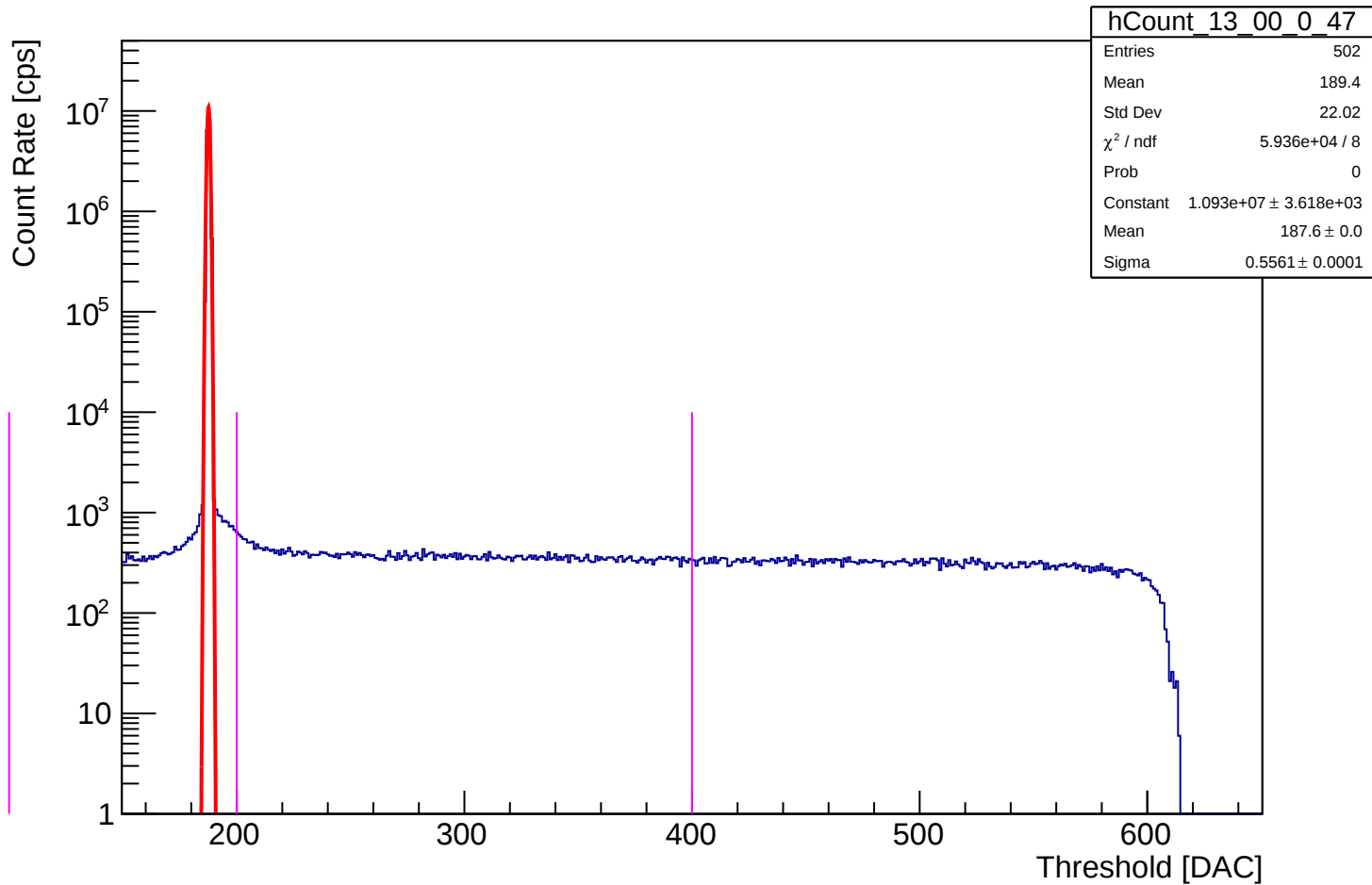
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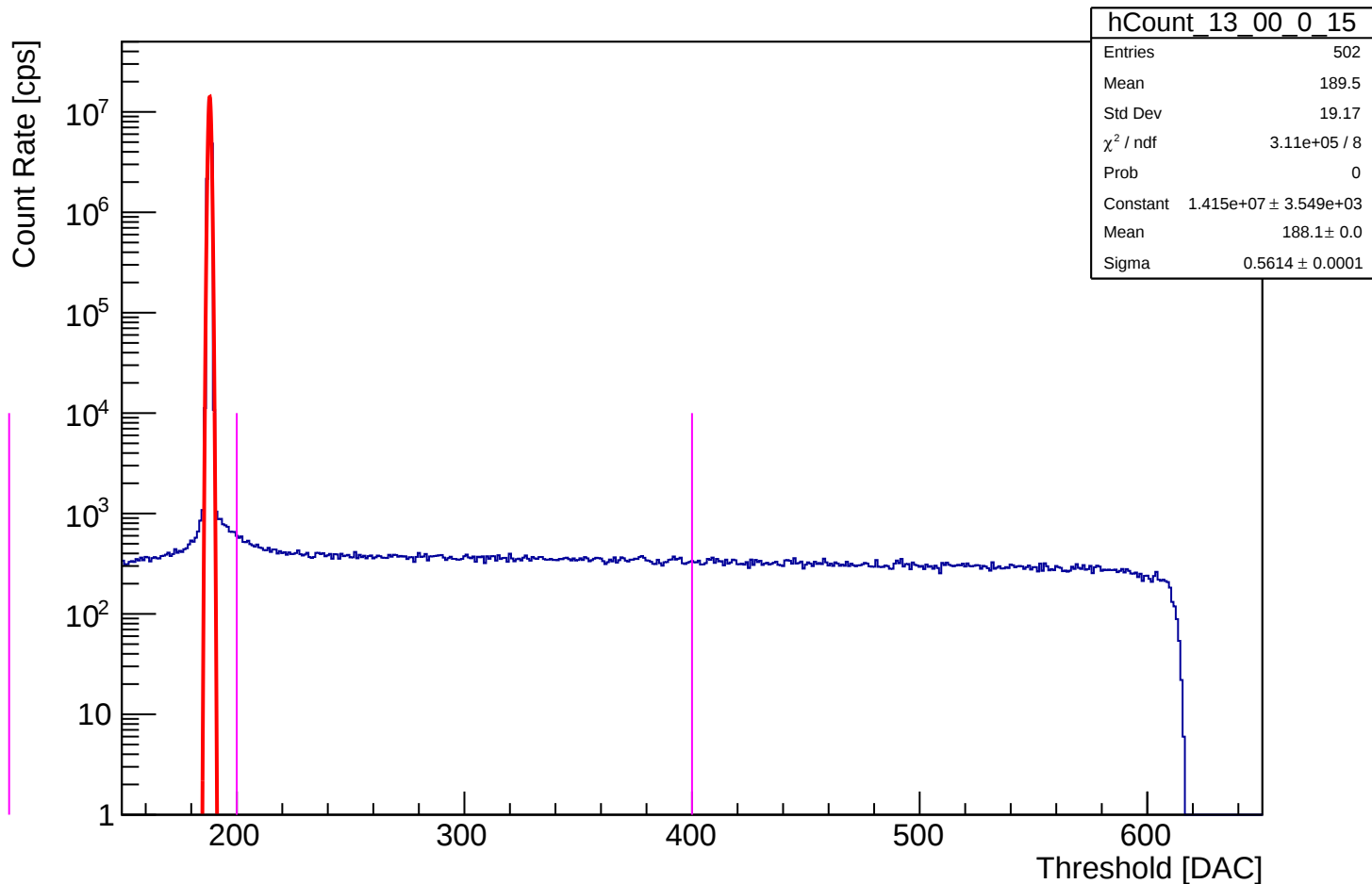
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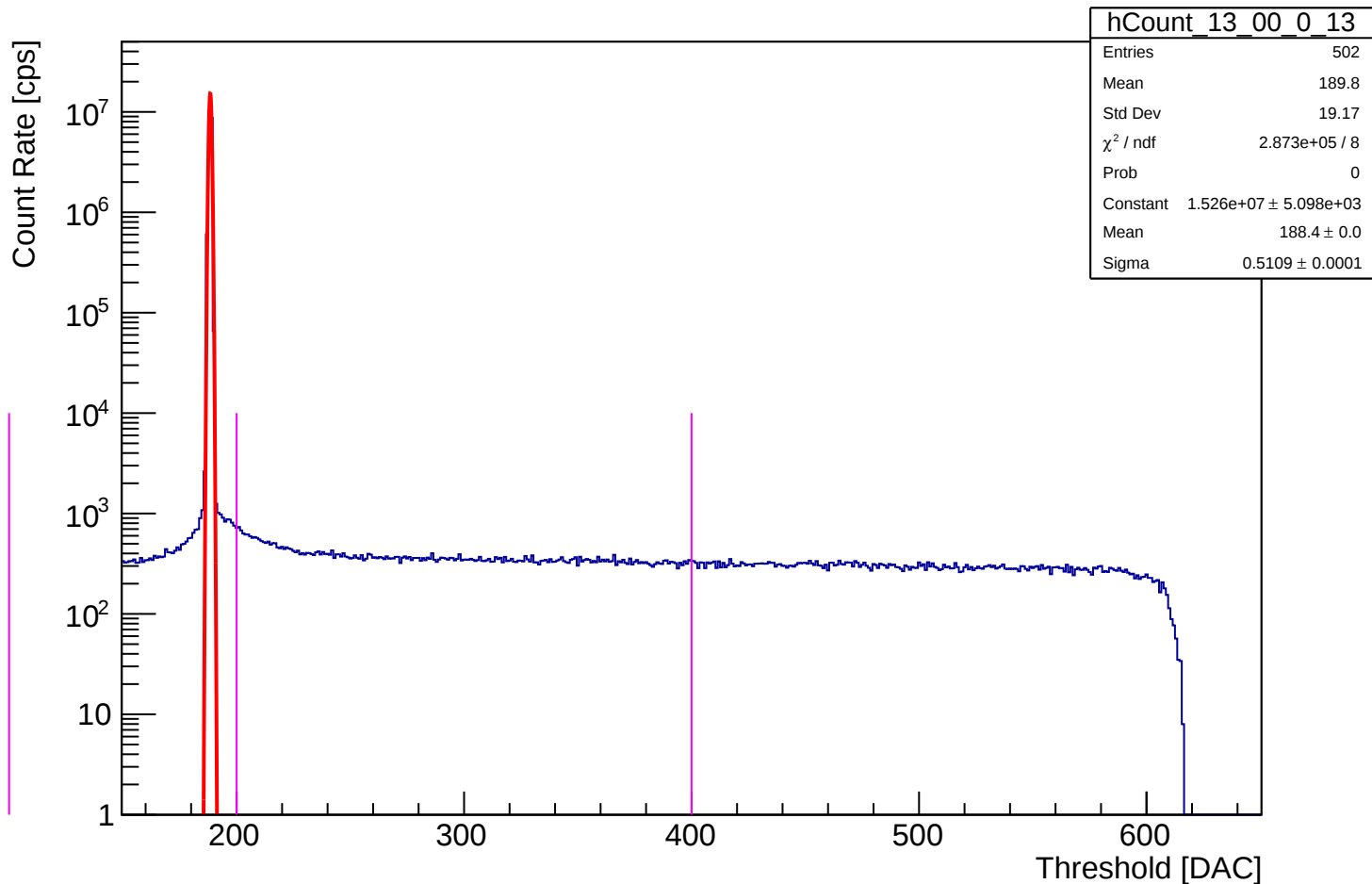
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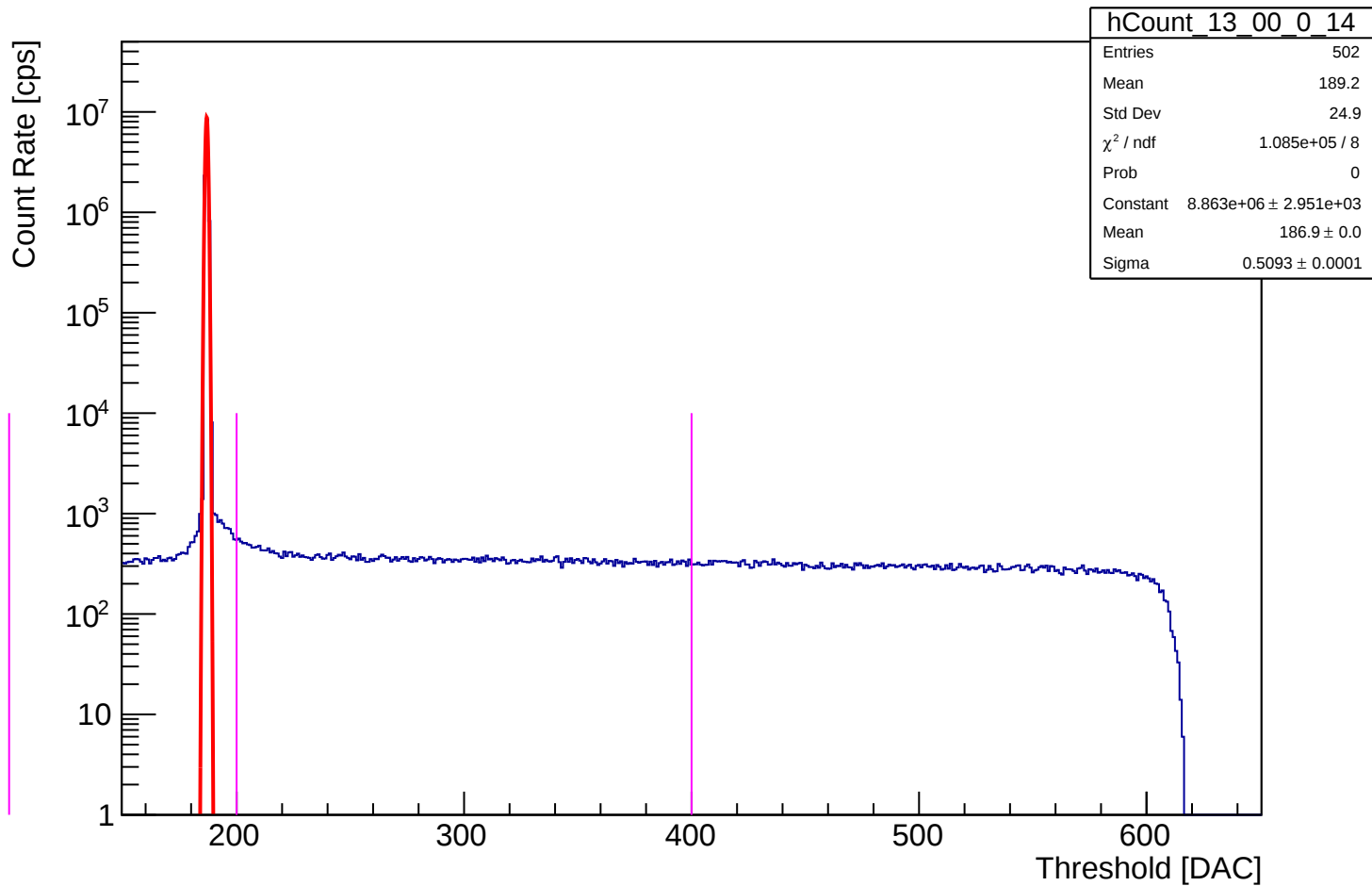
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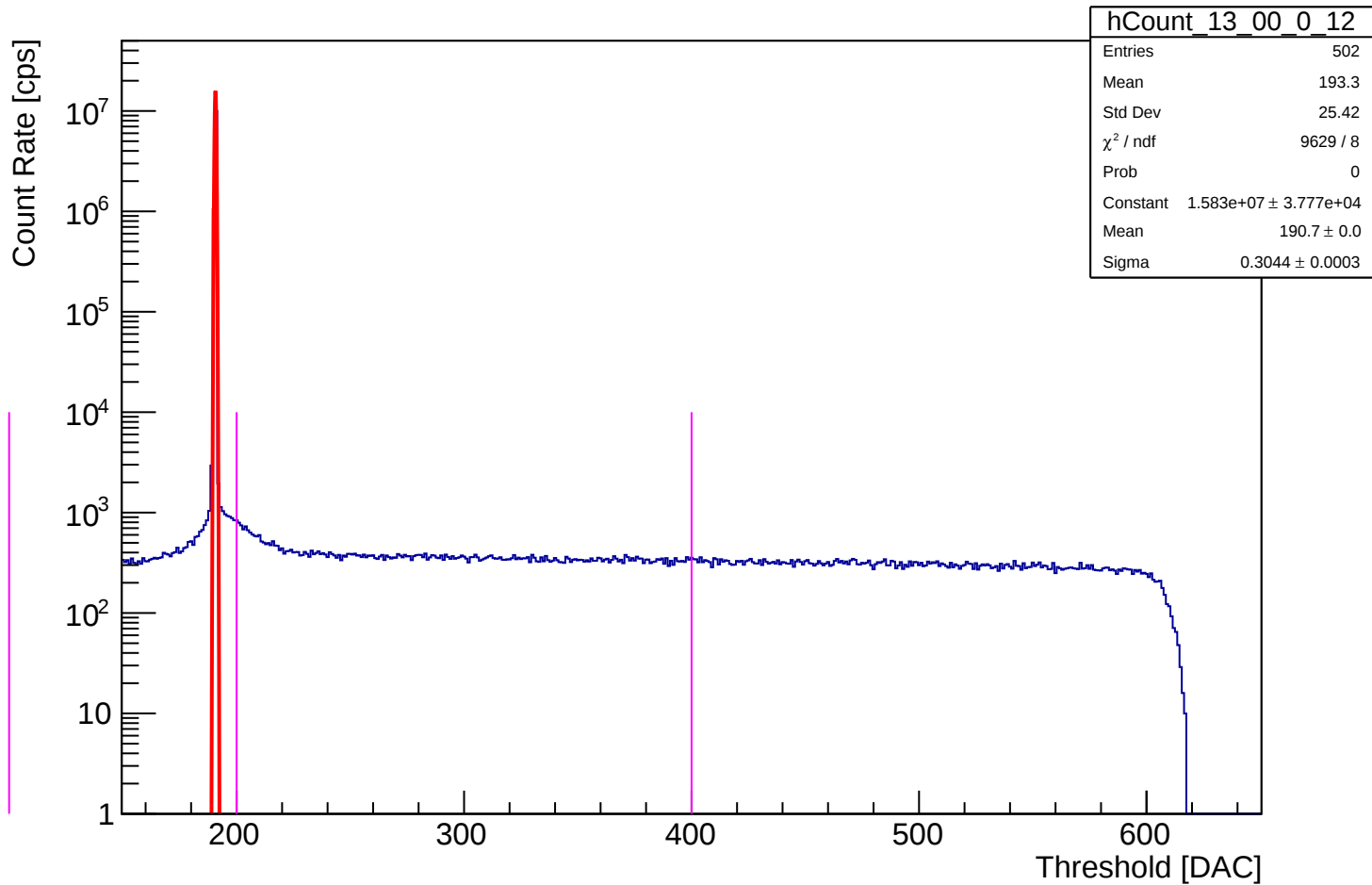
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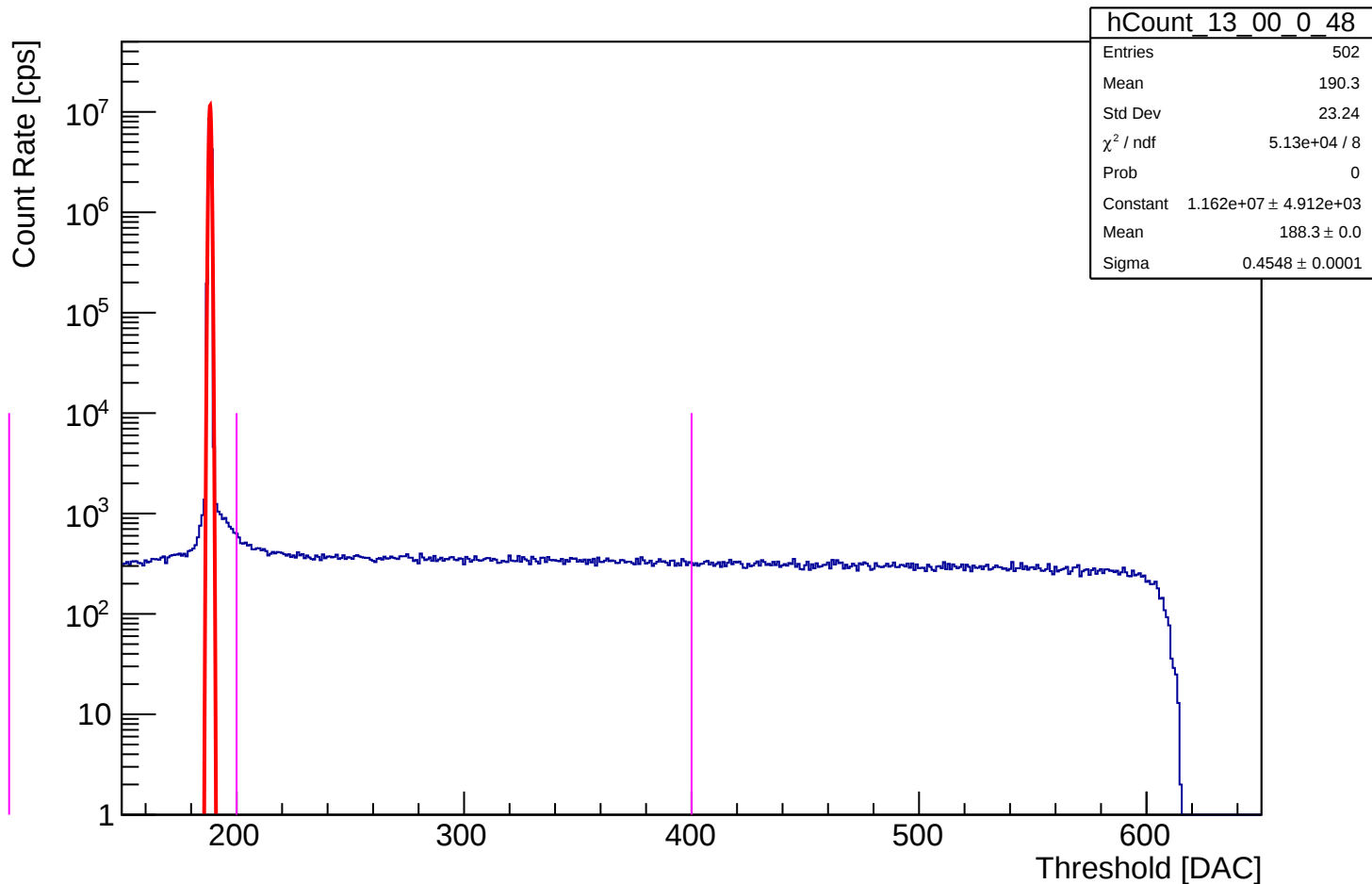
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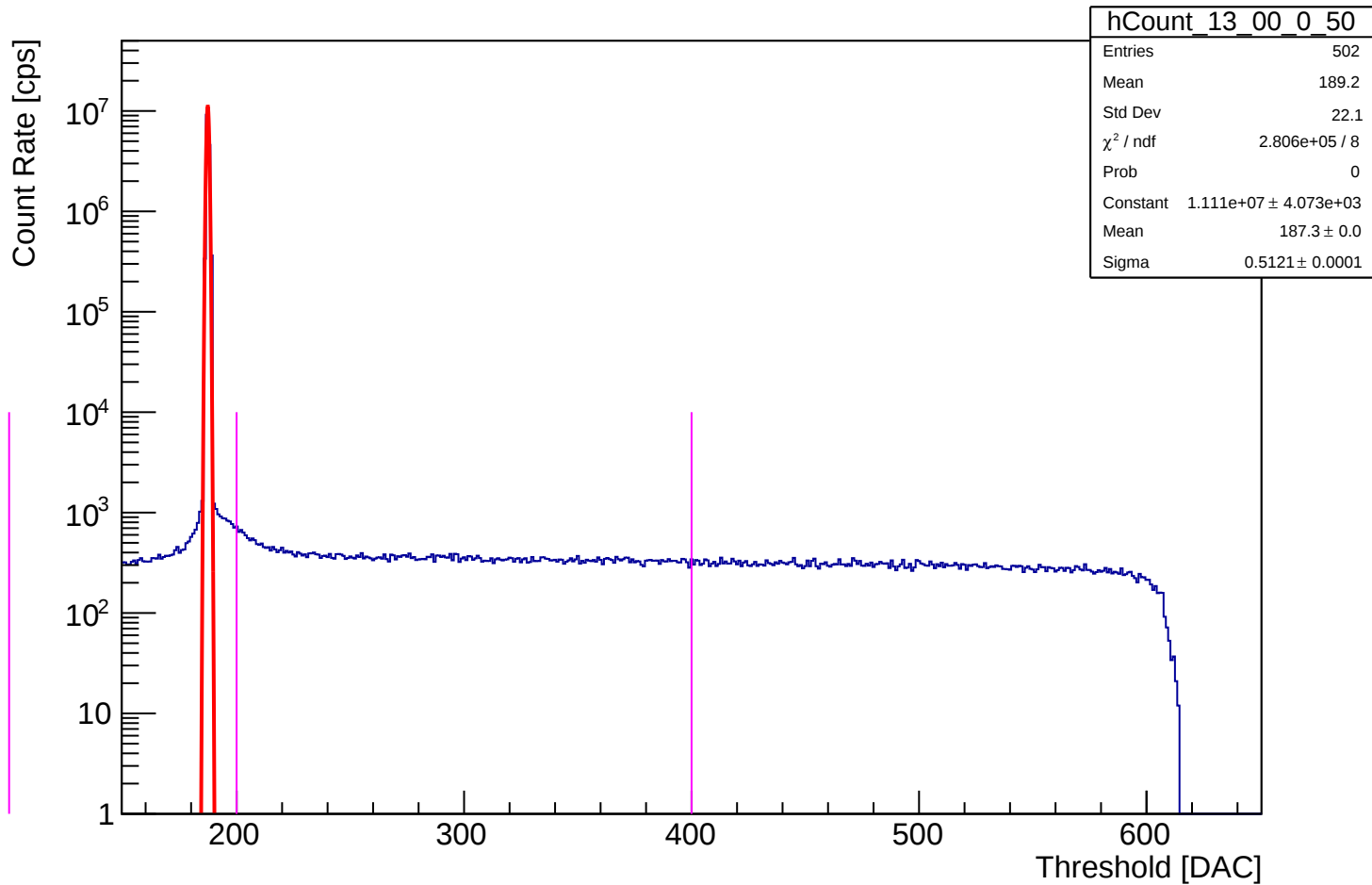
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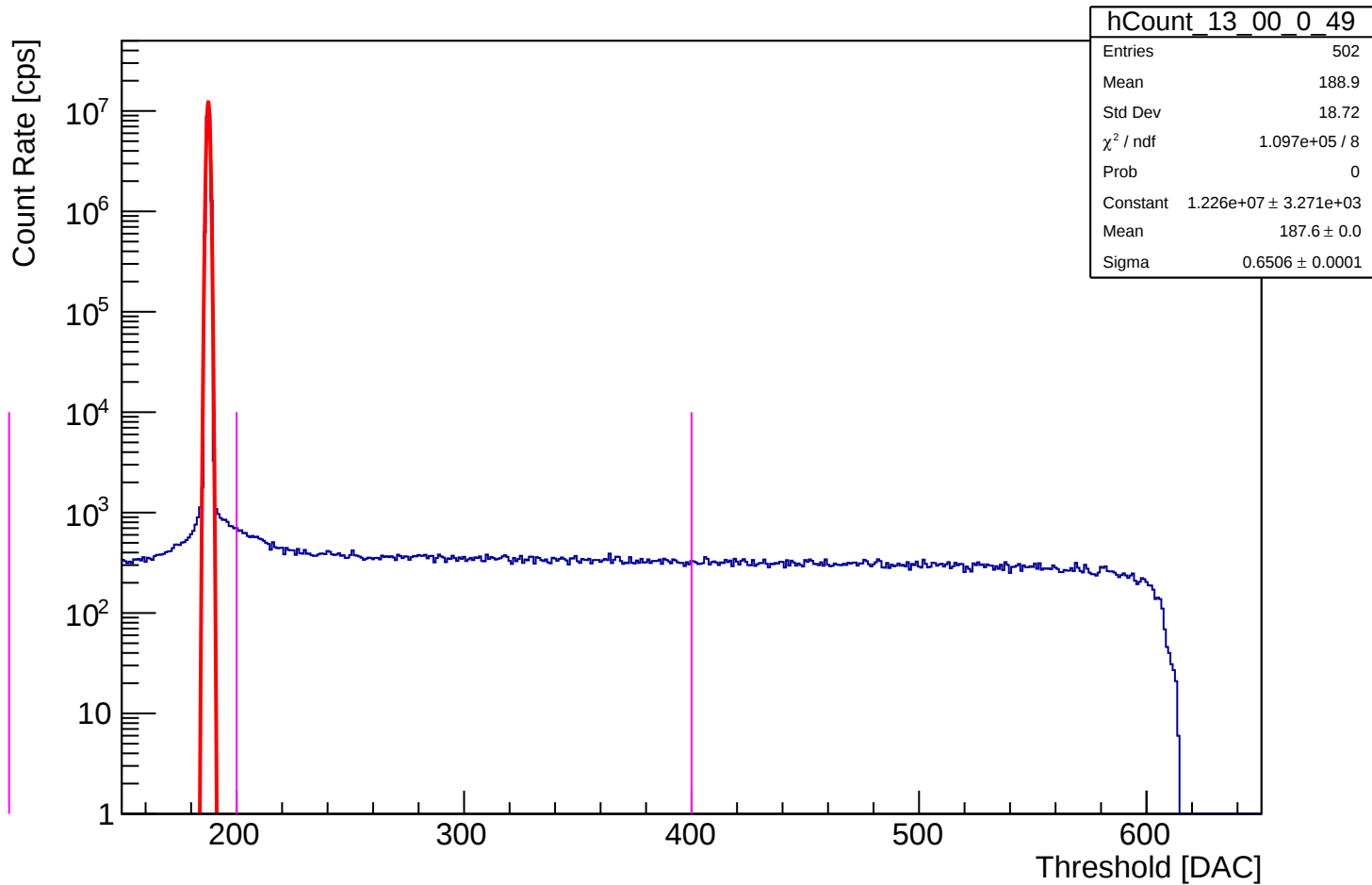
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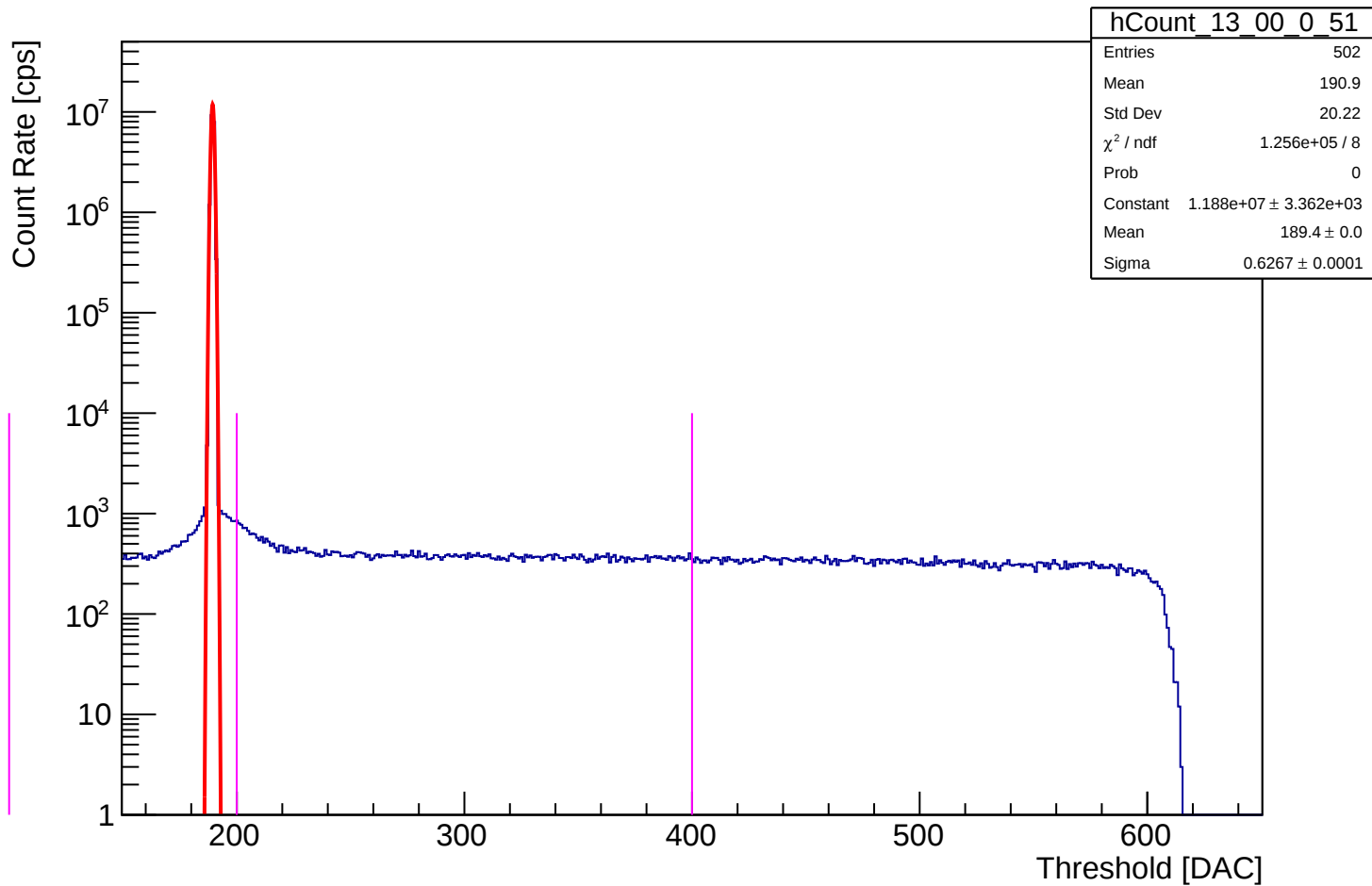
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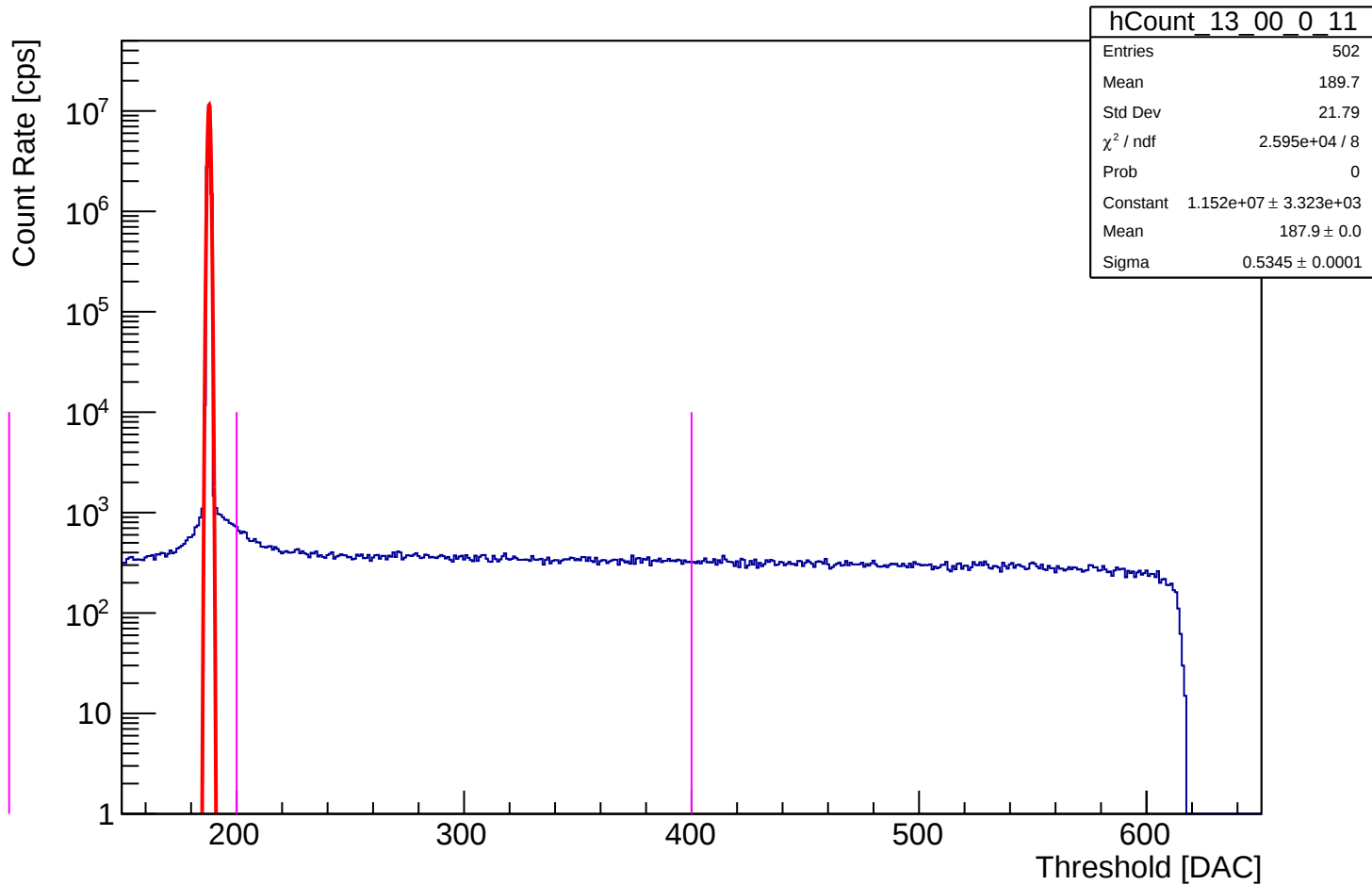
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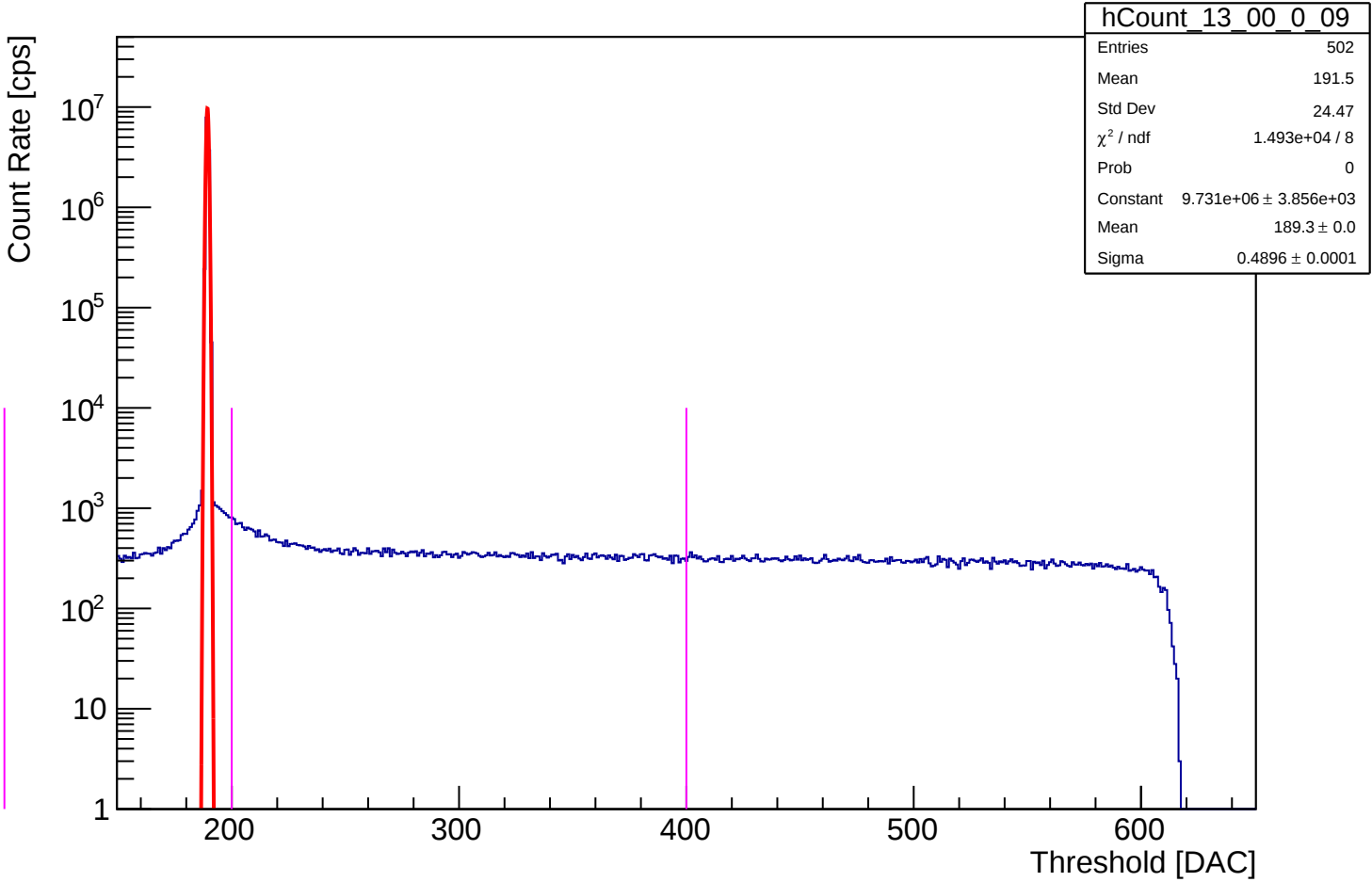


Row 1 Tile 1 Pmt 1 Pixel 40 Slot 13 Fiber 0 Asic 0 Channel 51

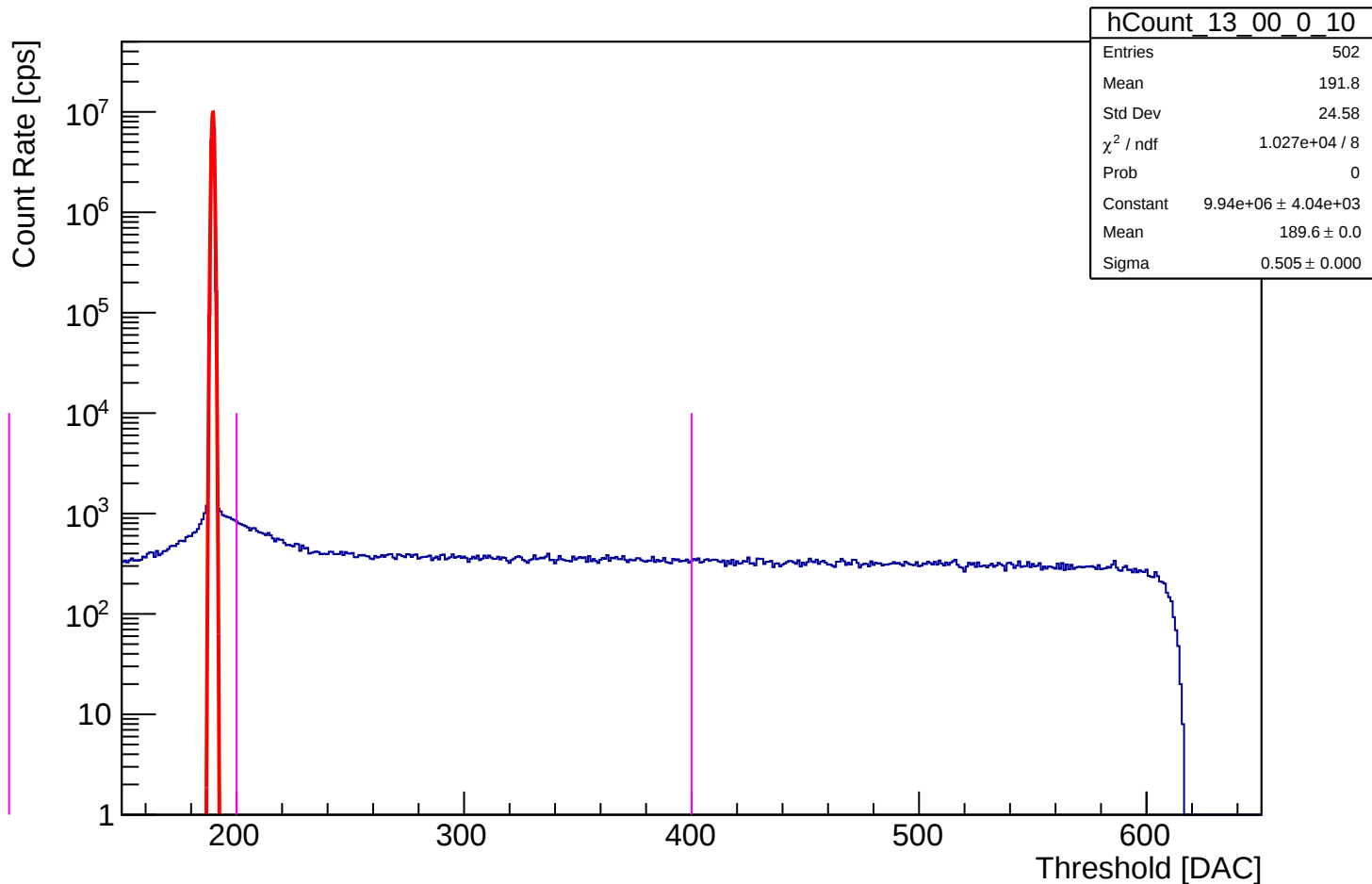


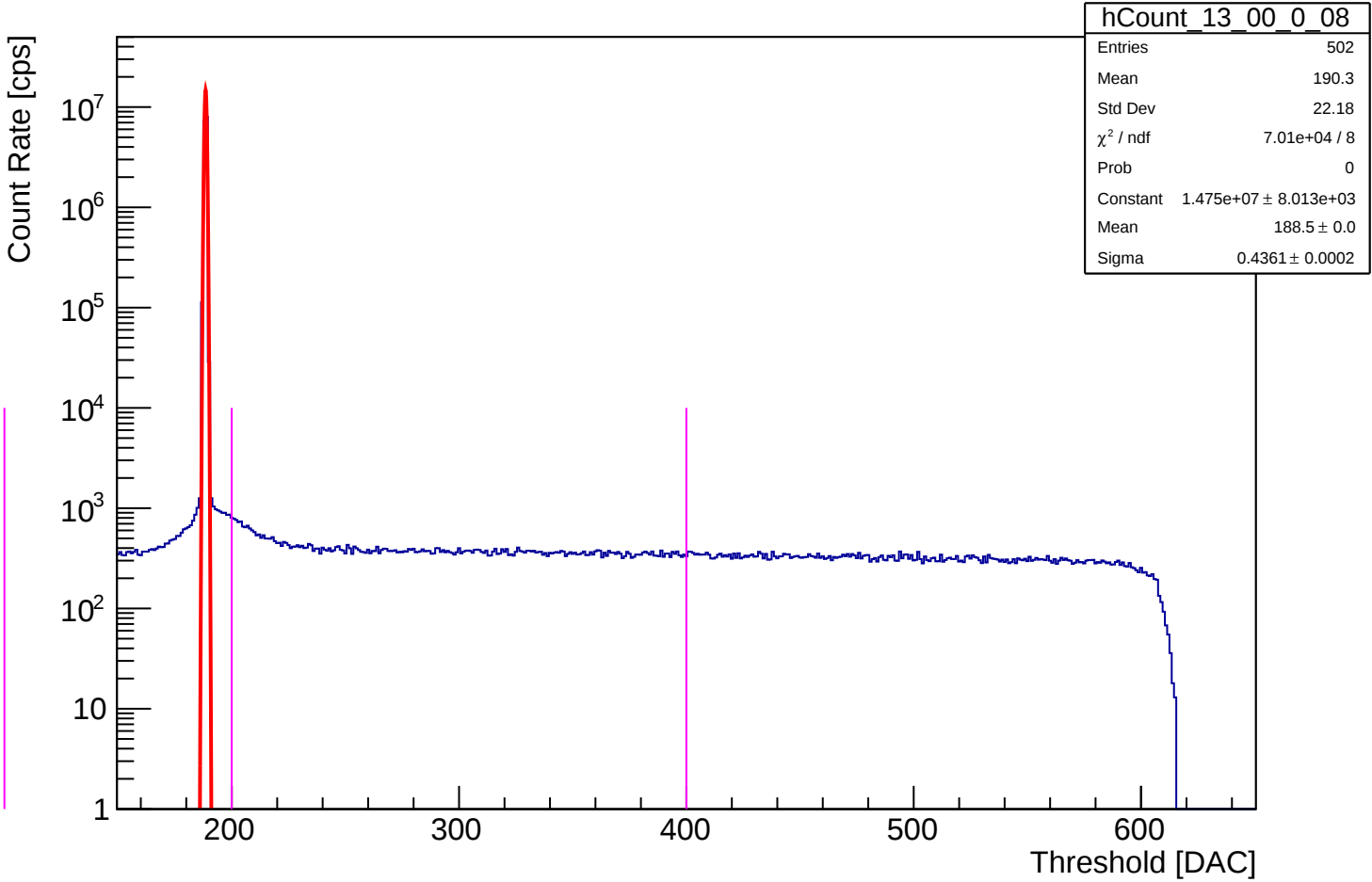
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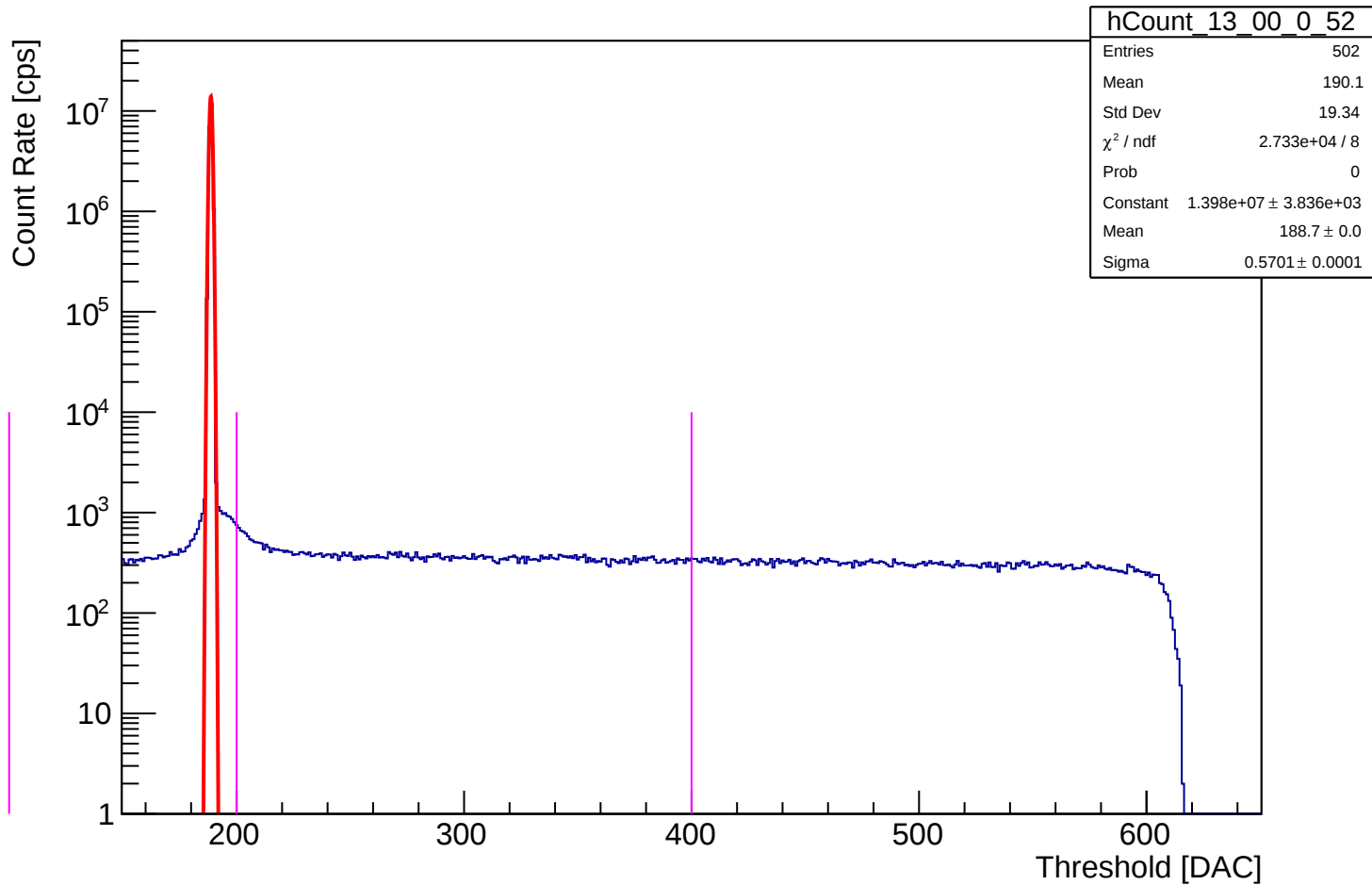


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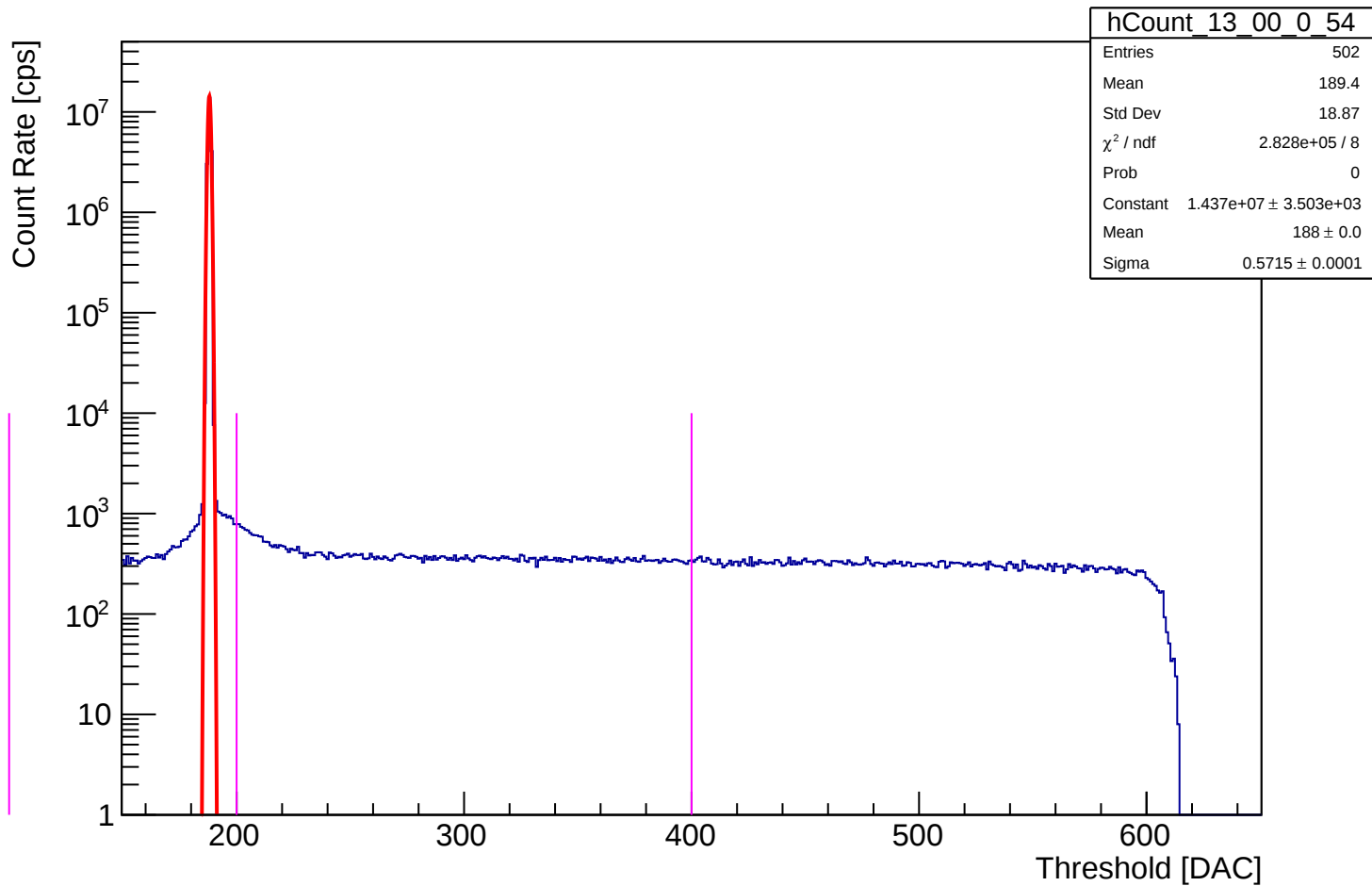




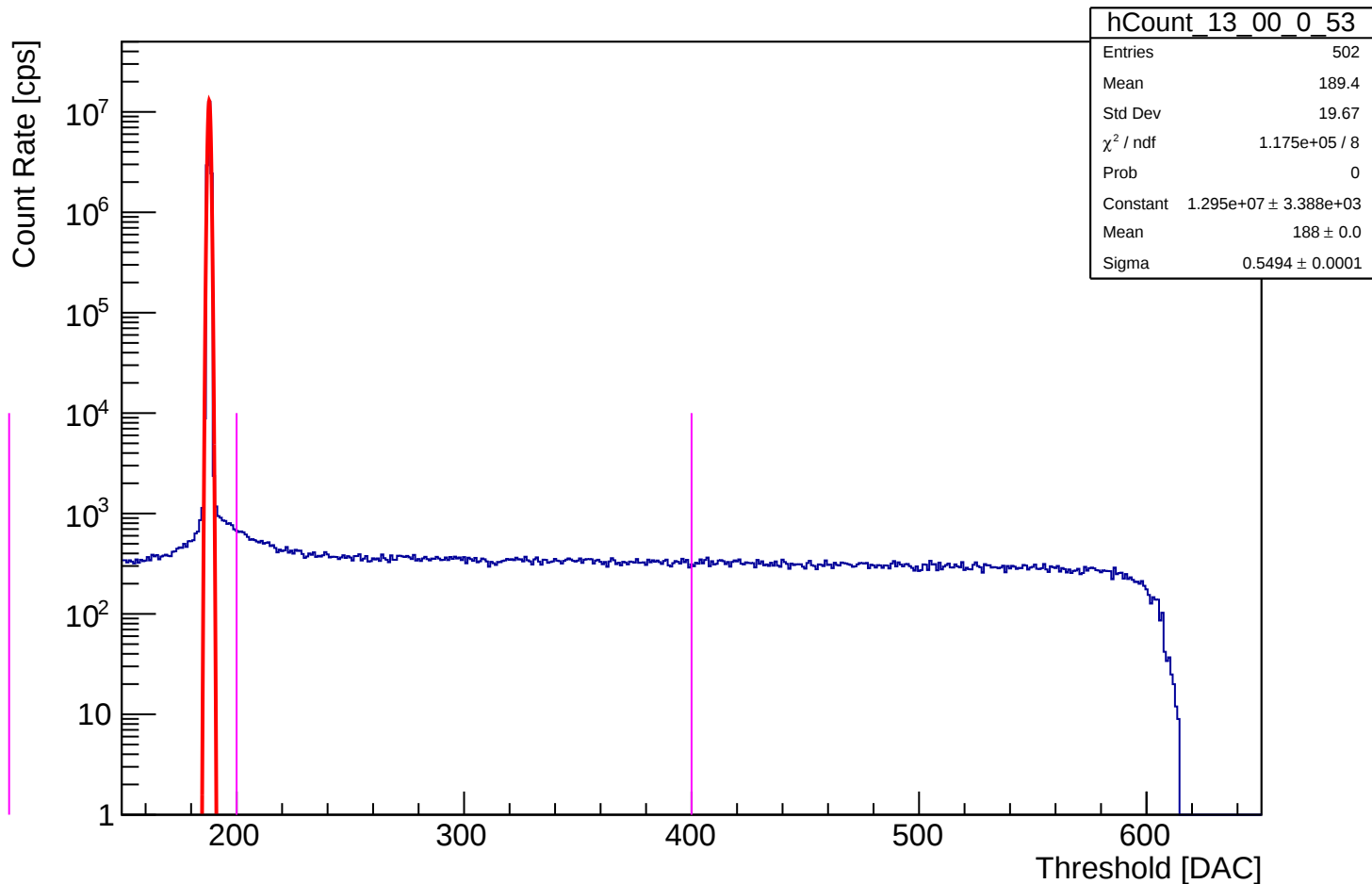
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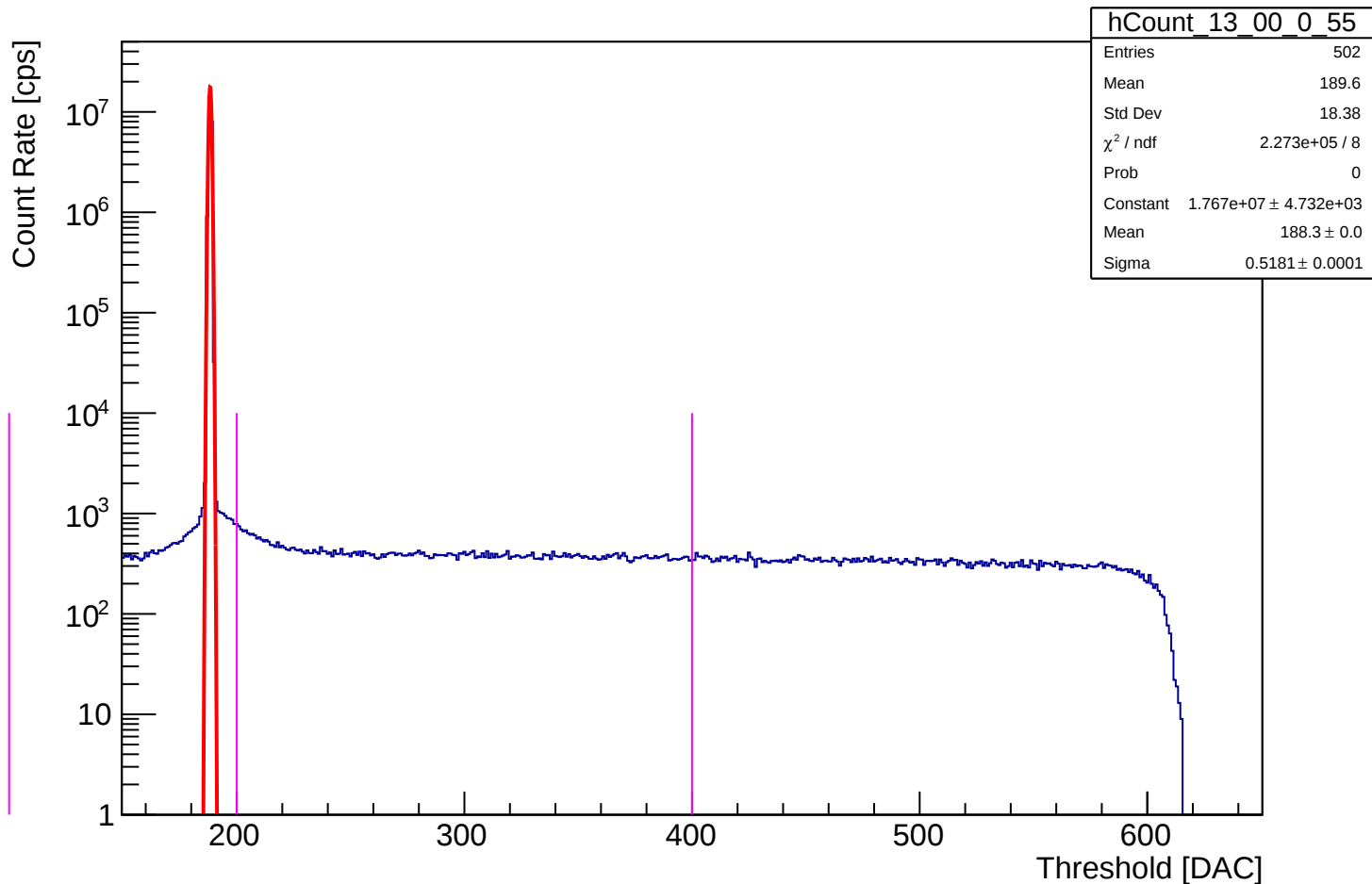
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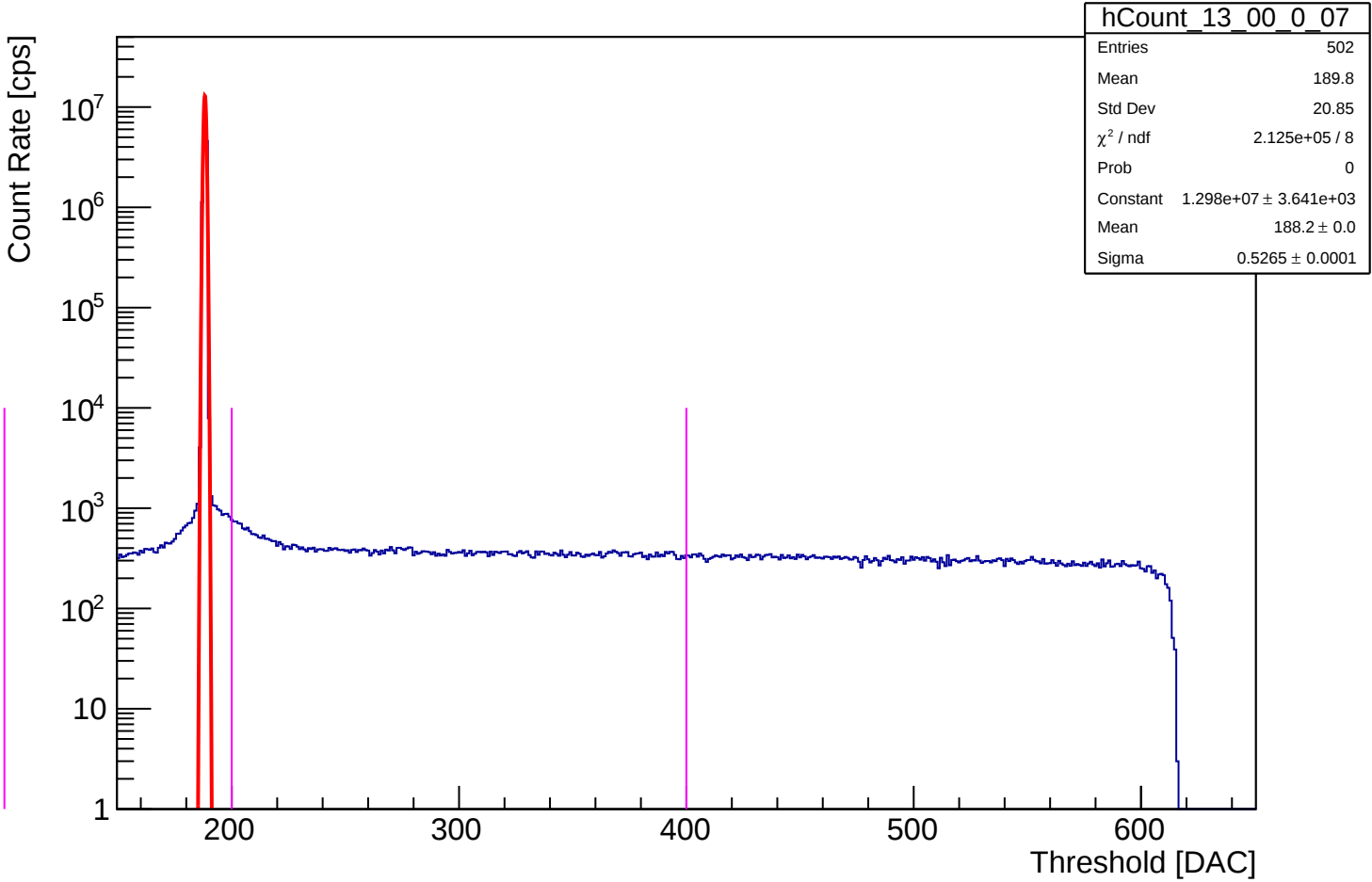


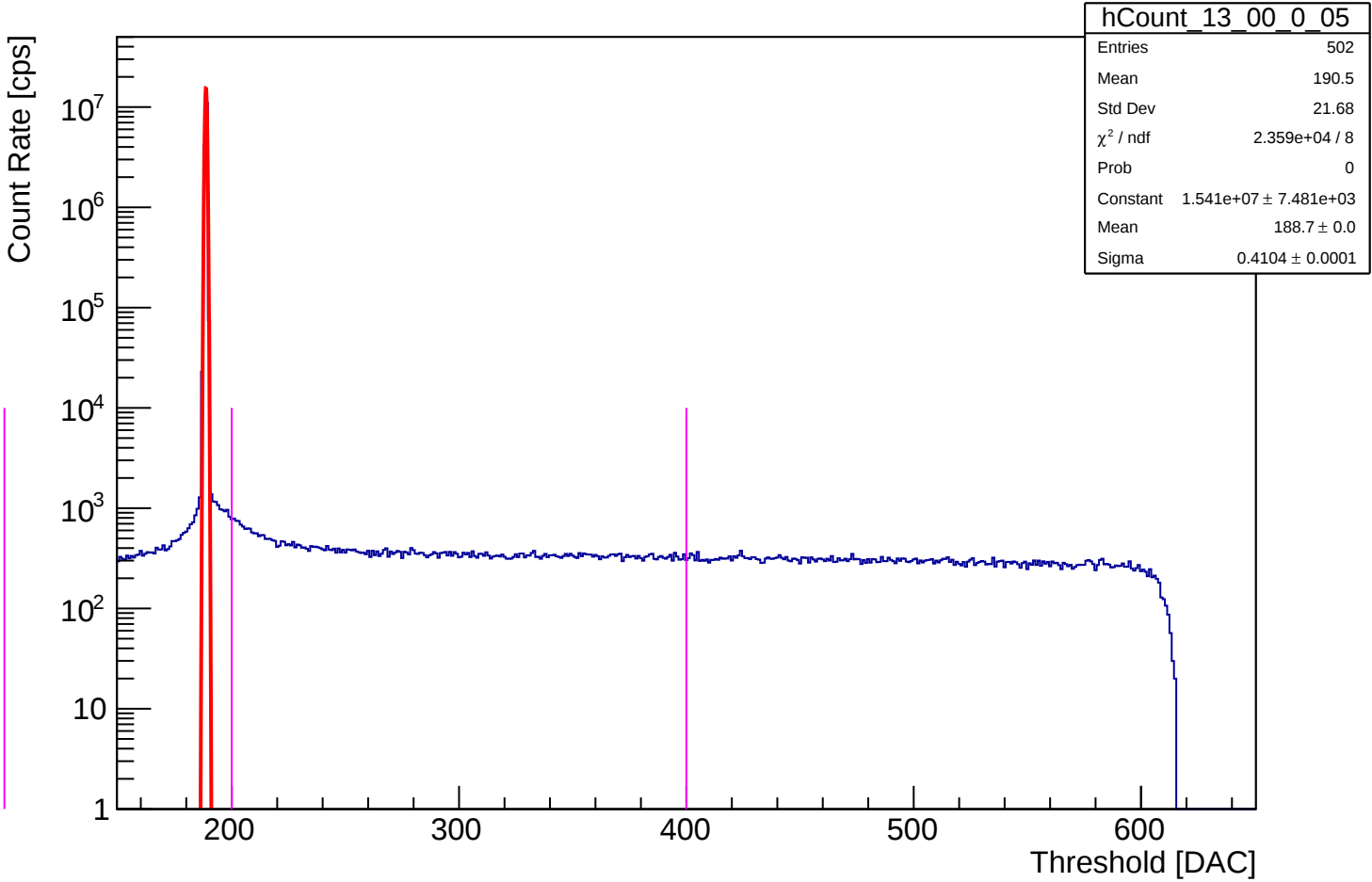
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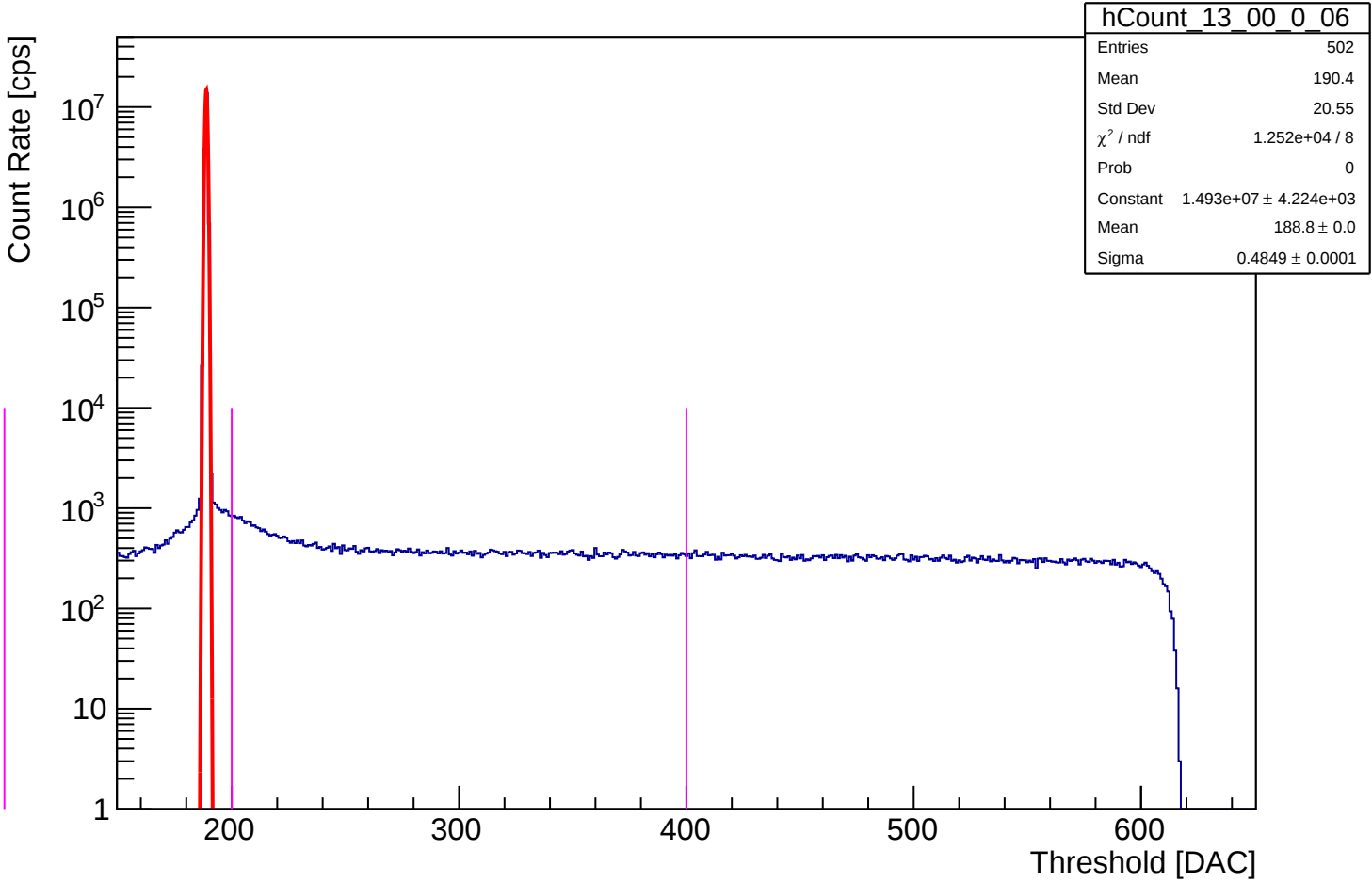


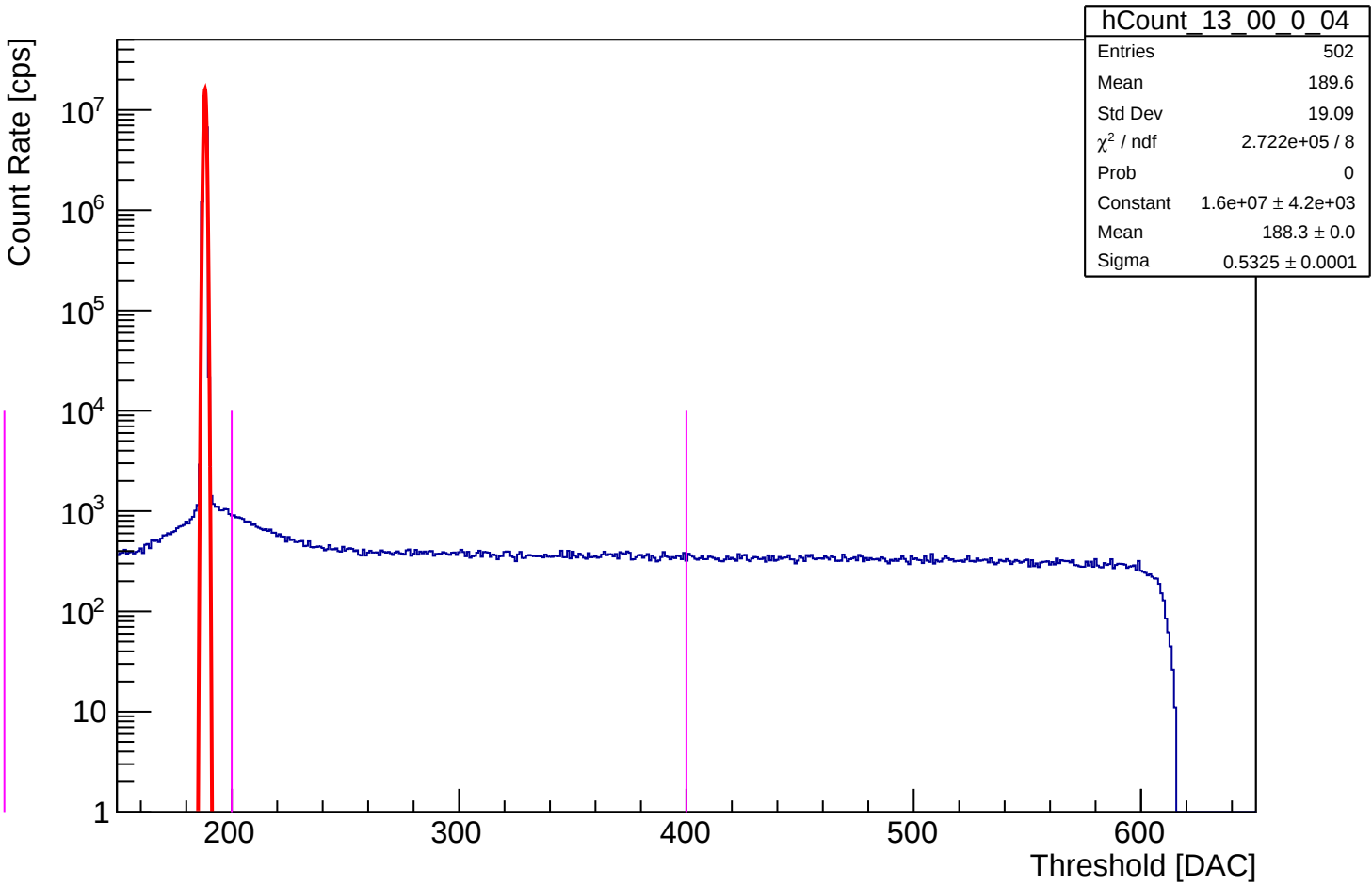
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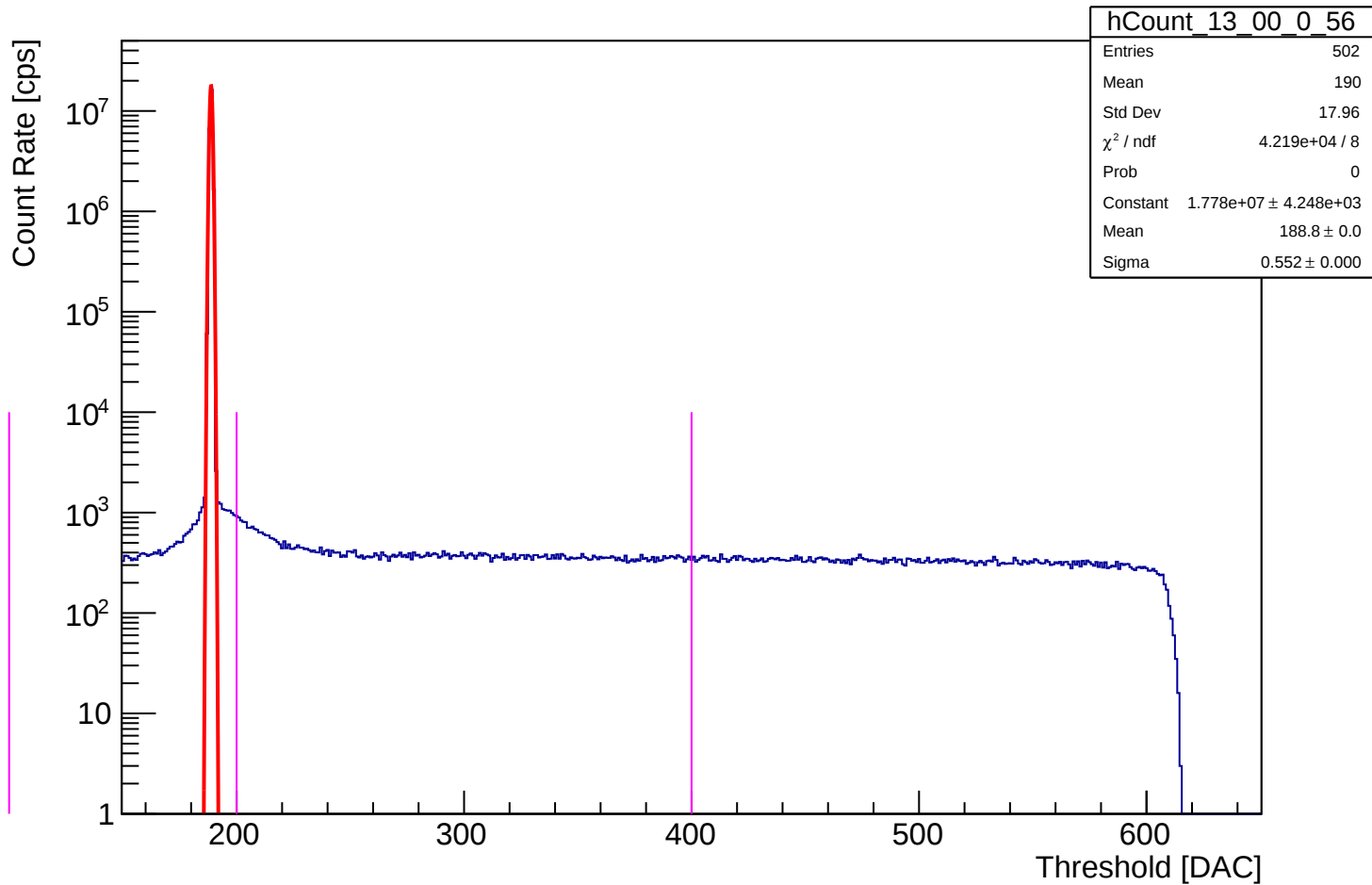




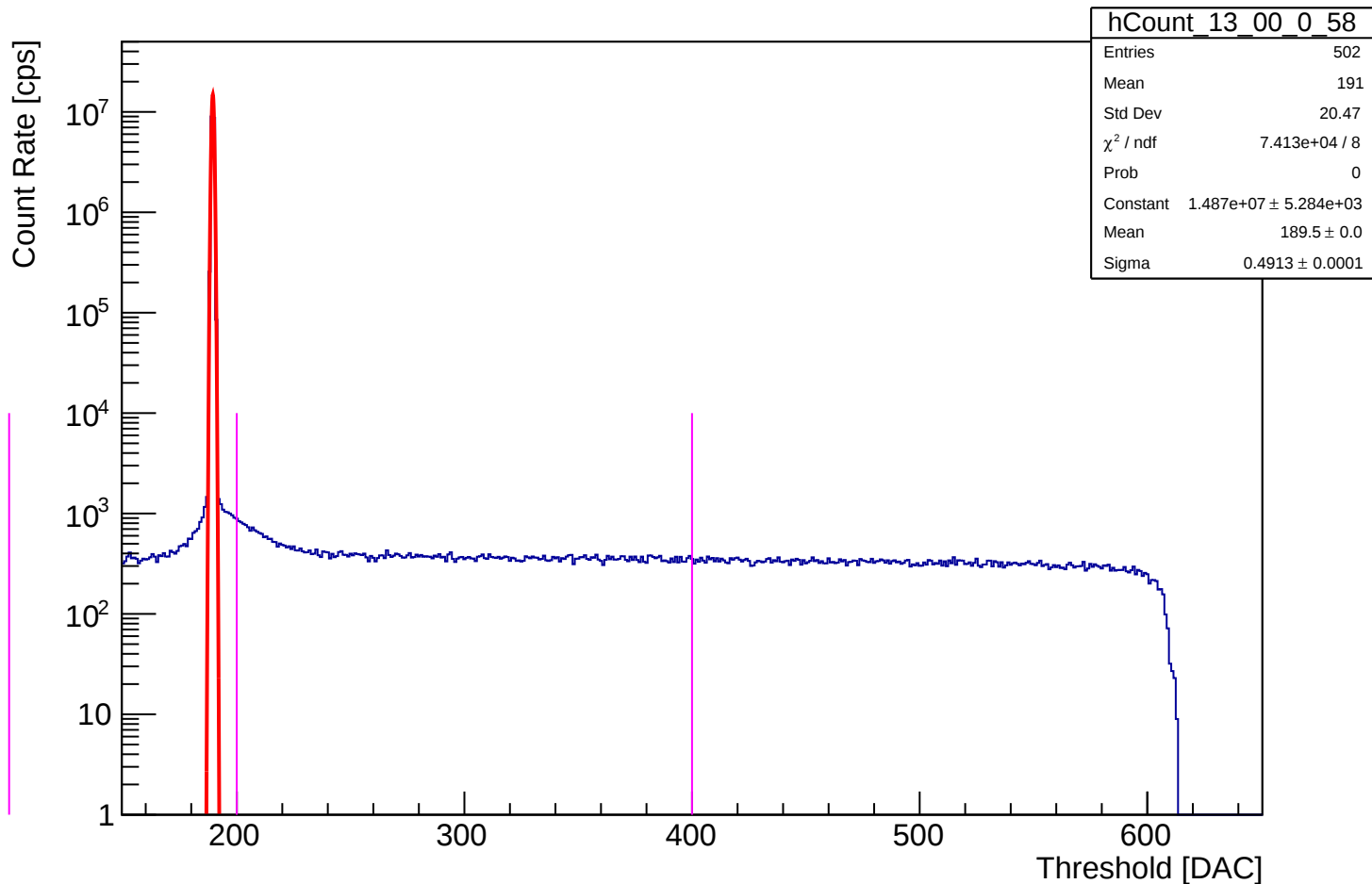




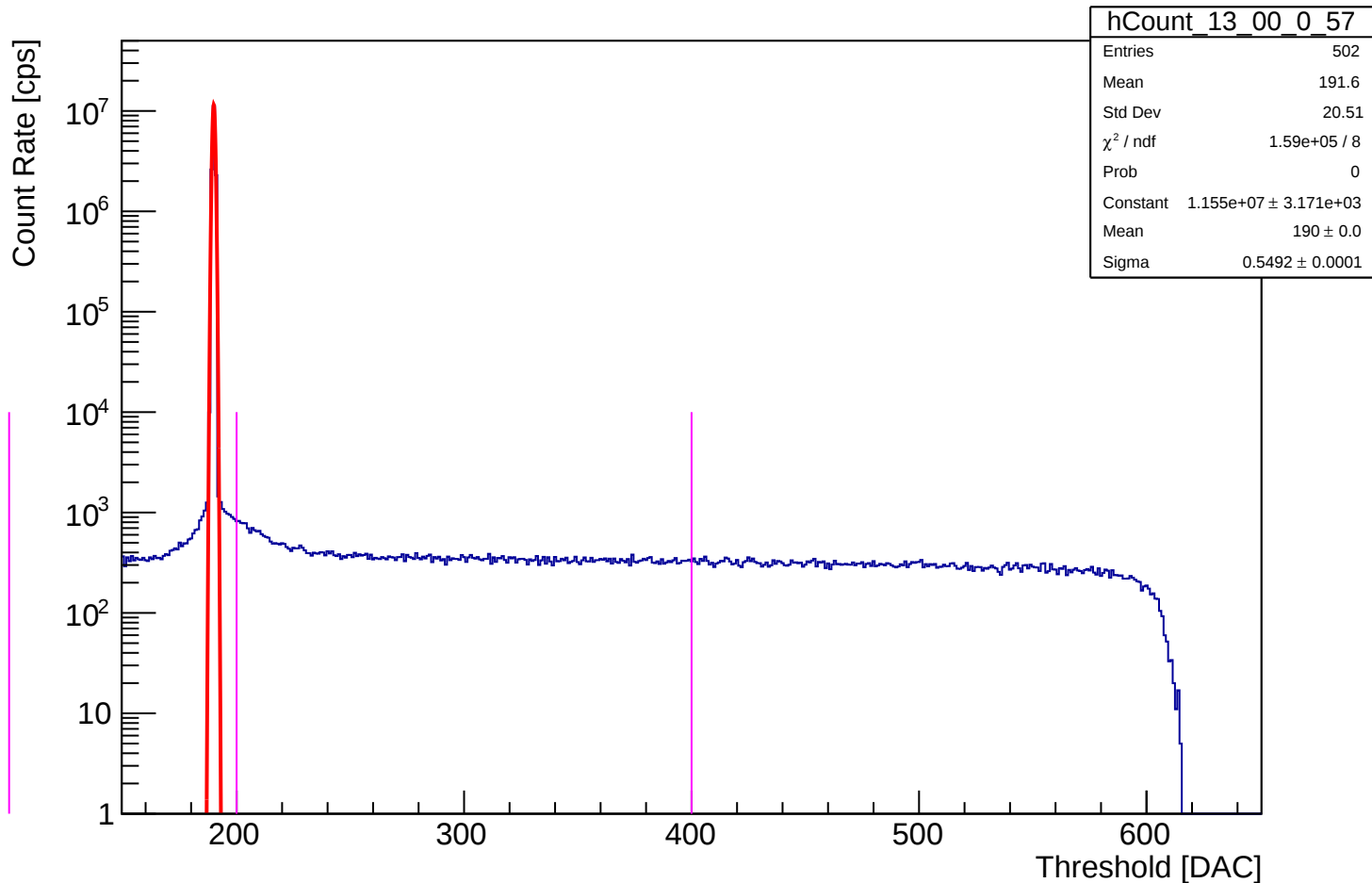
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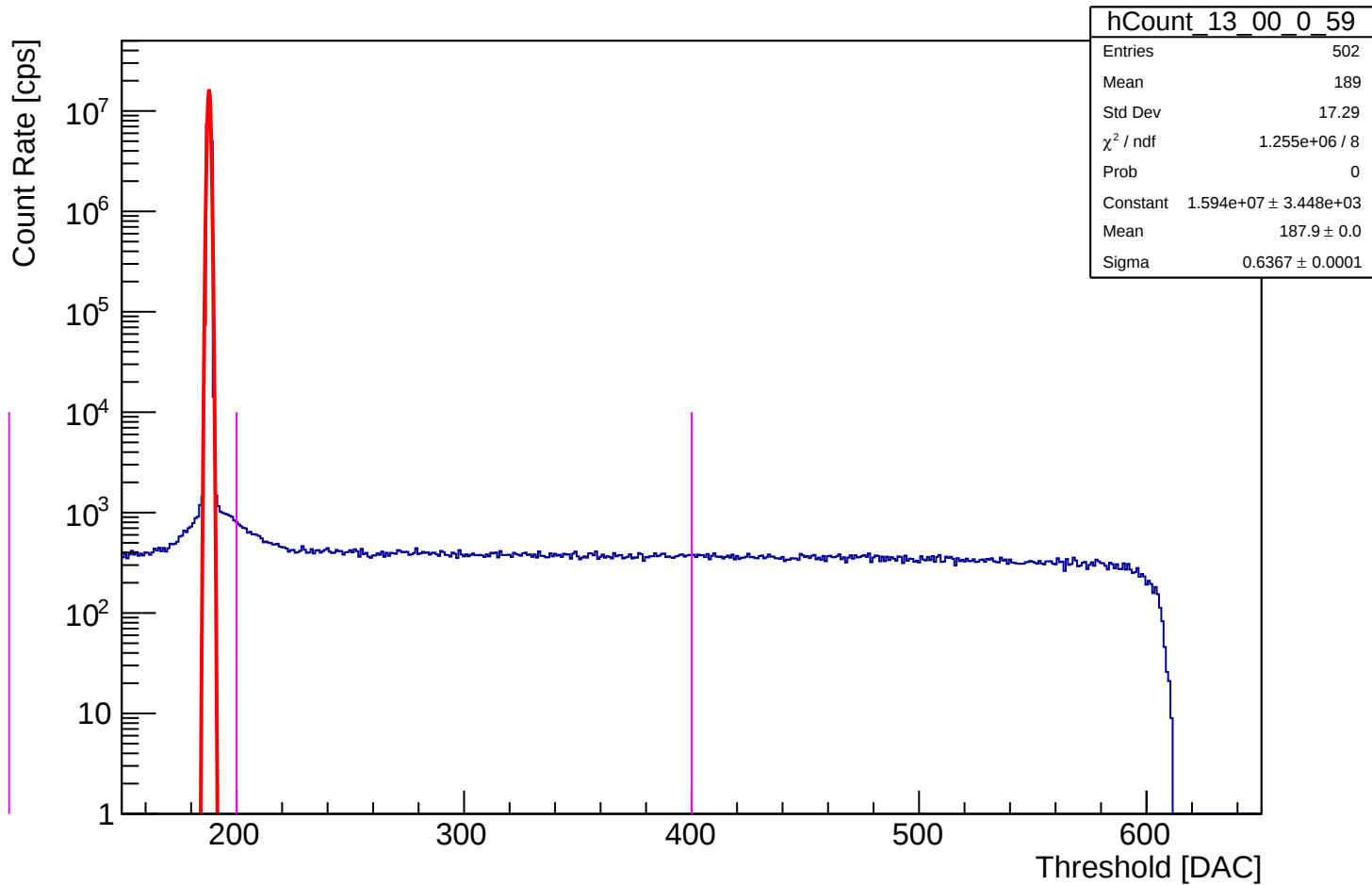
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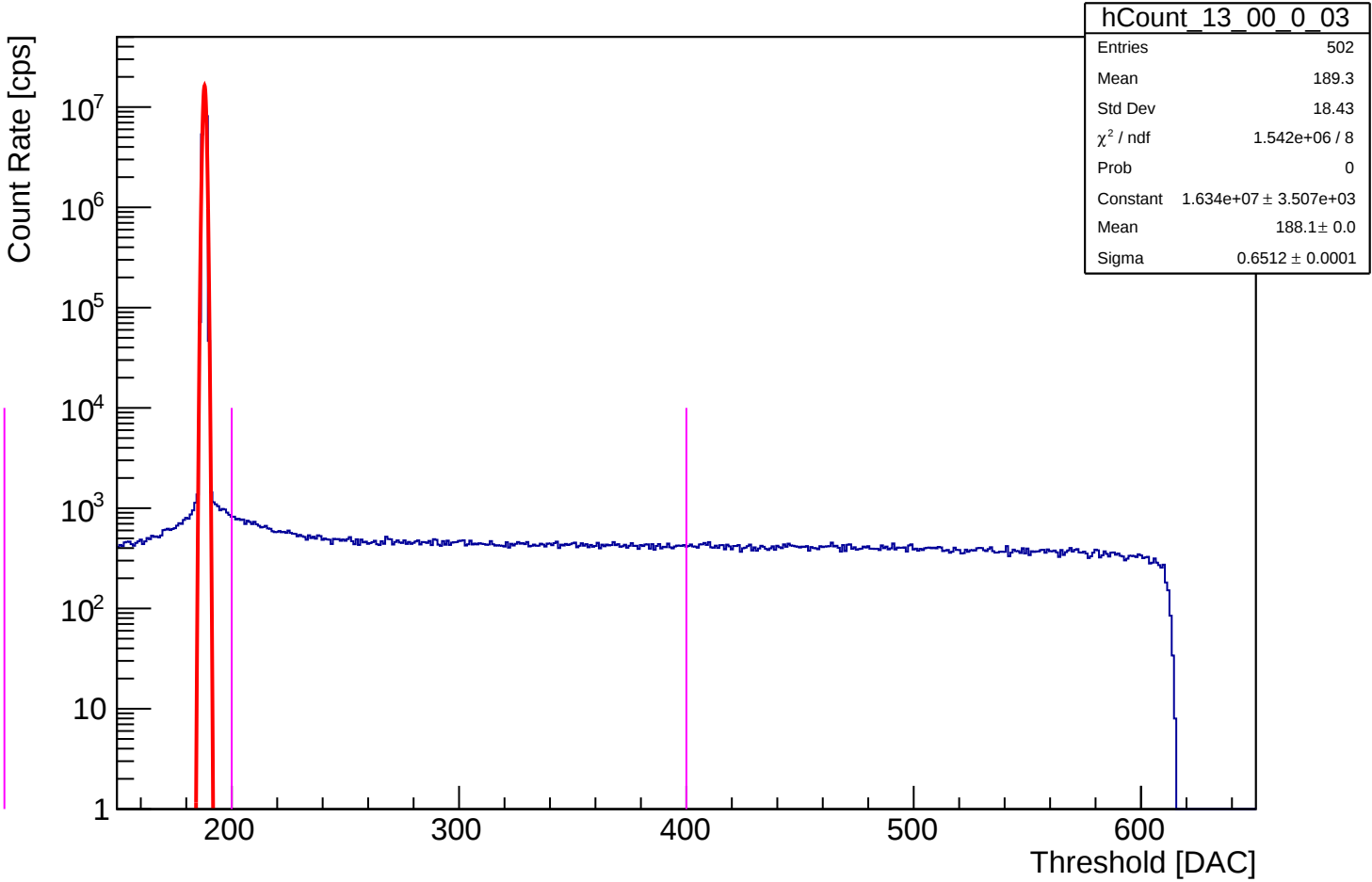


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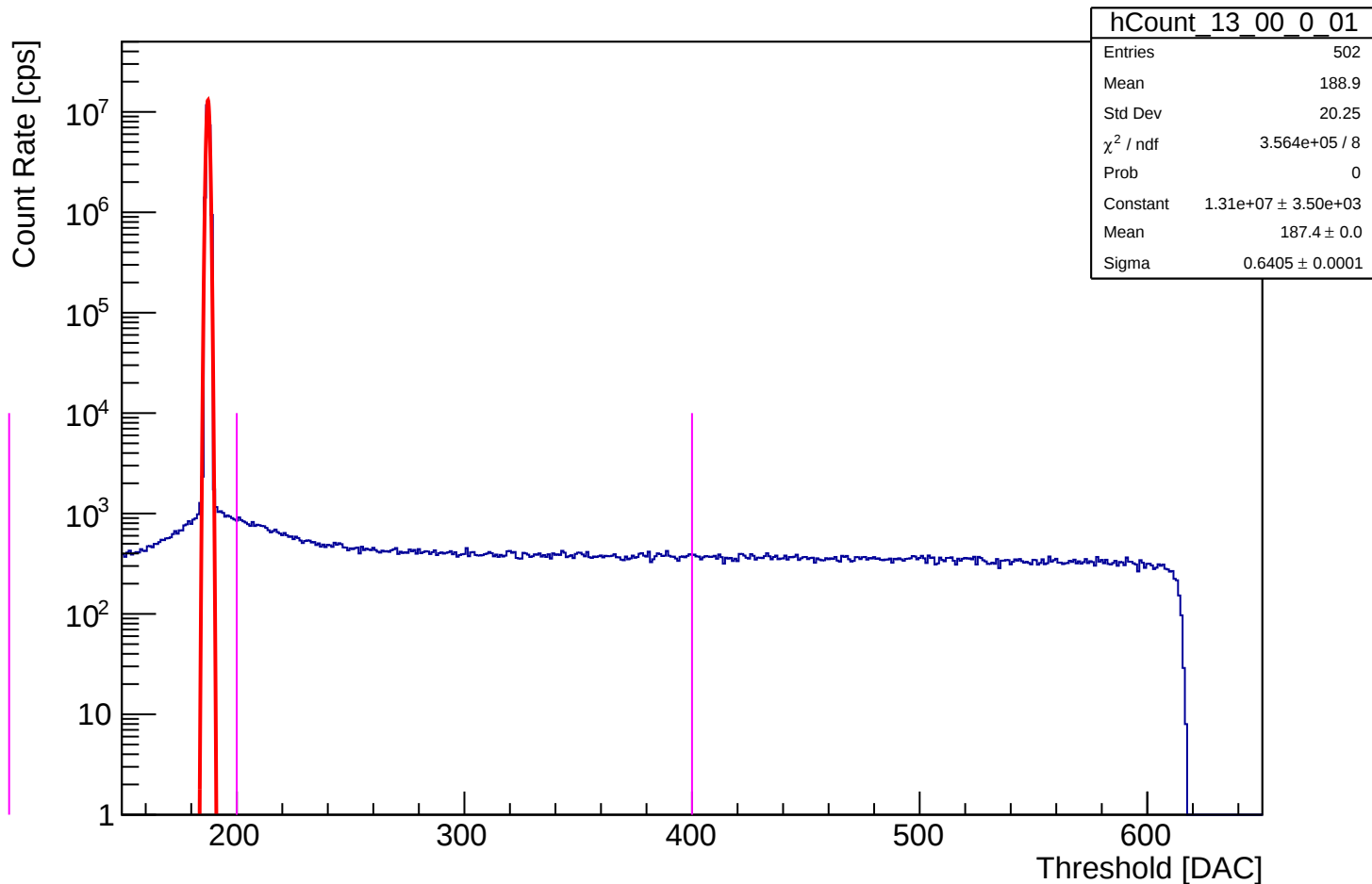


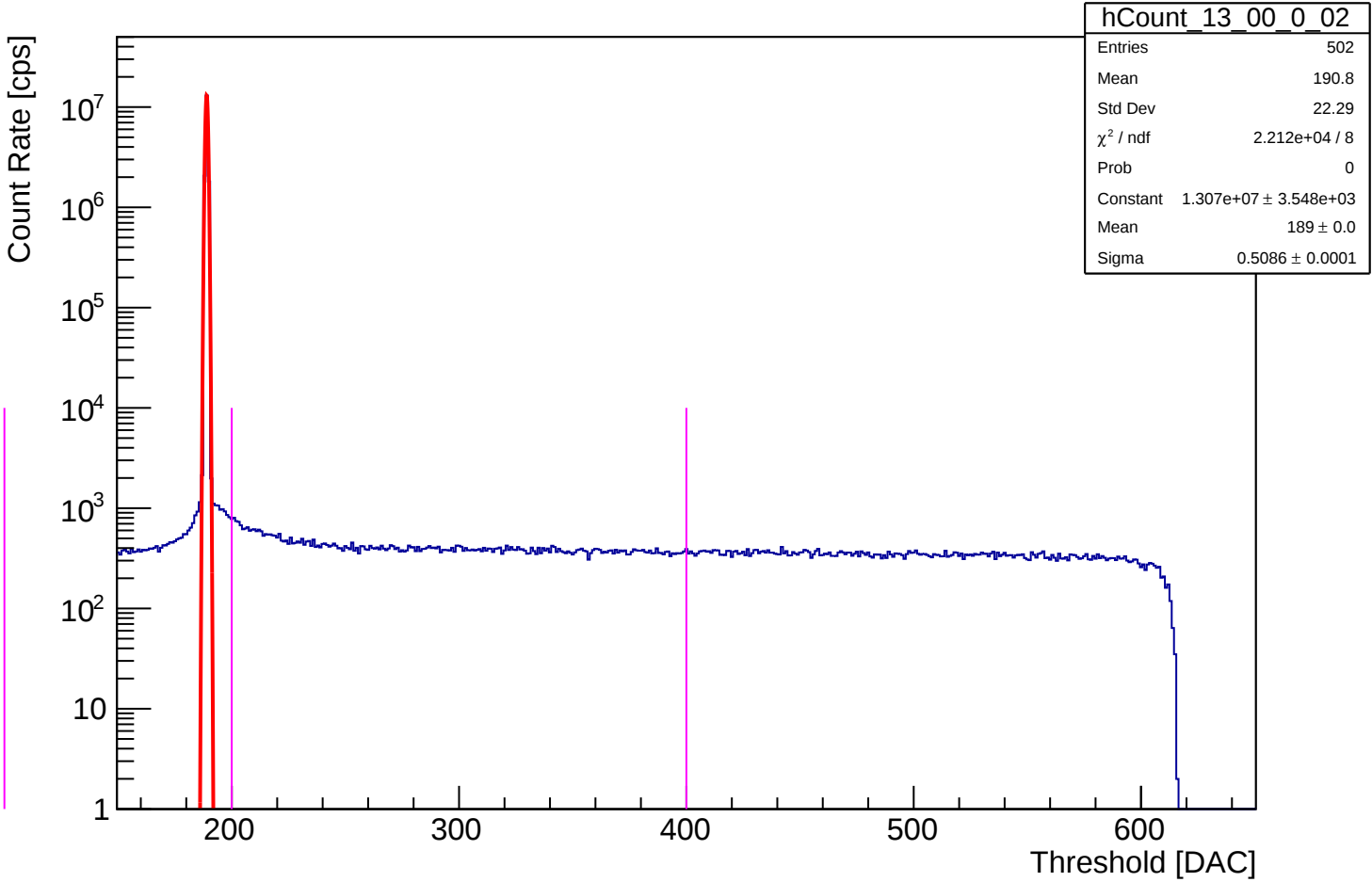
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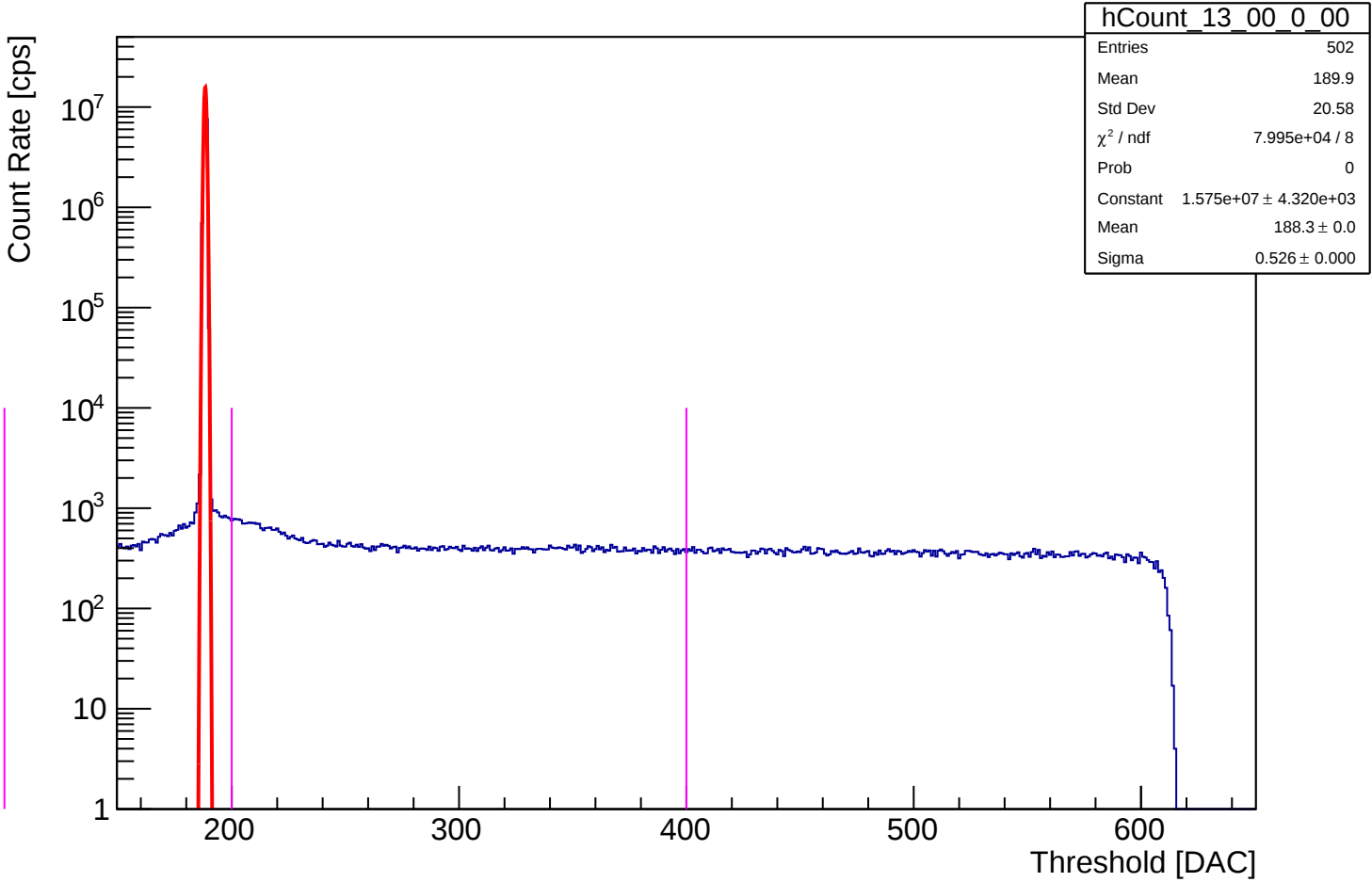




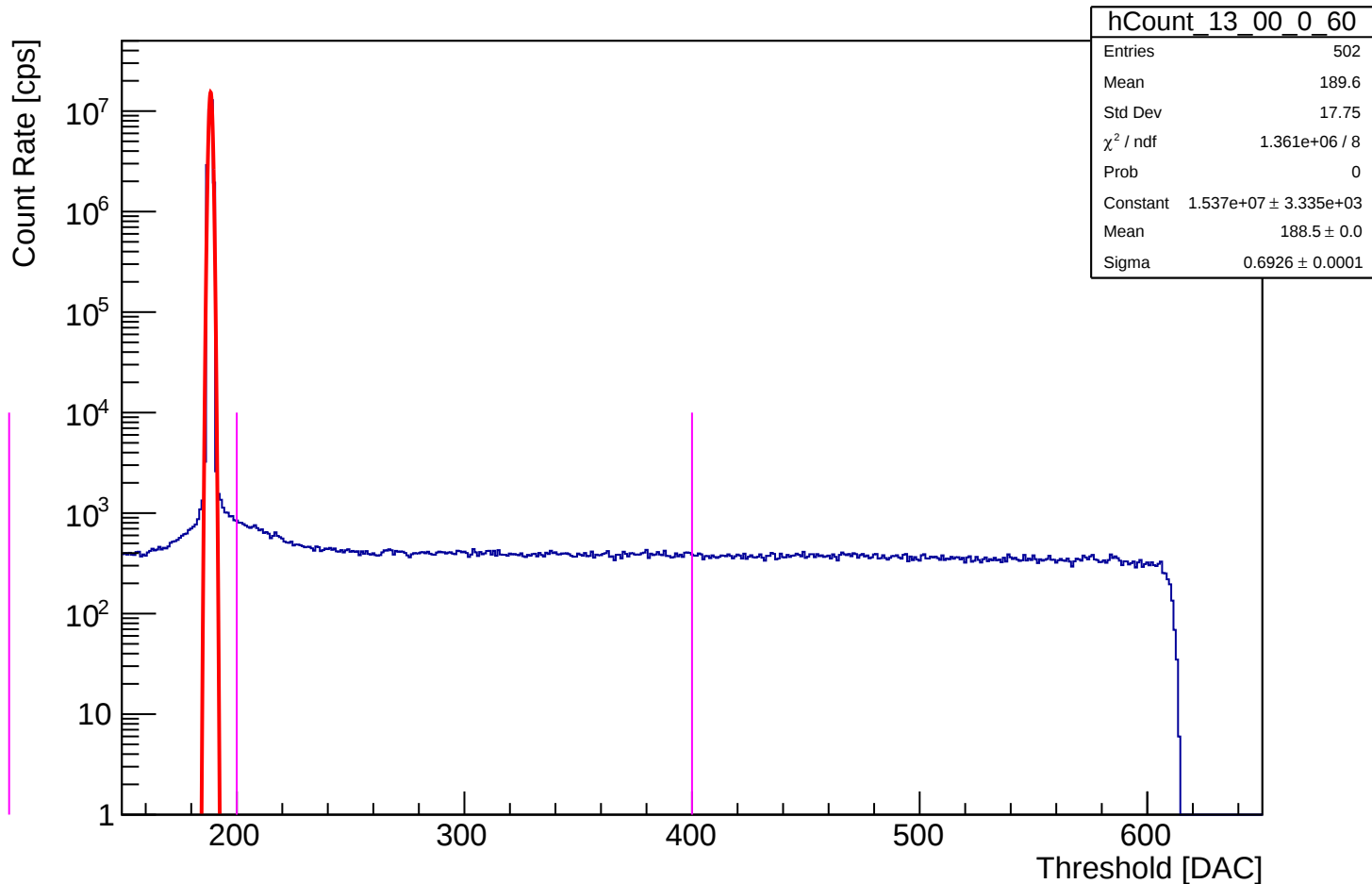
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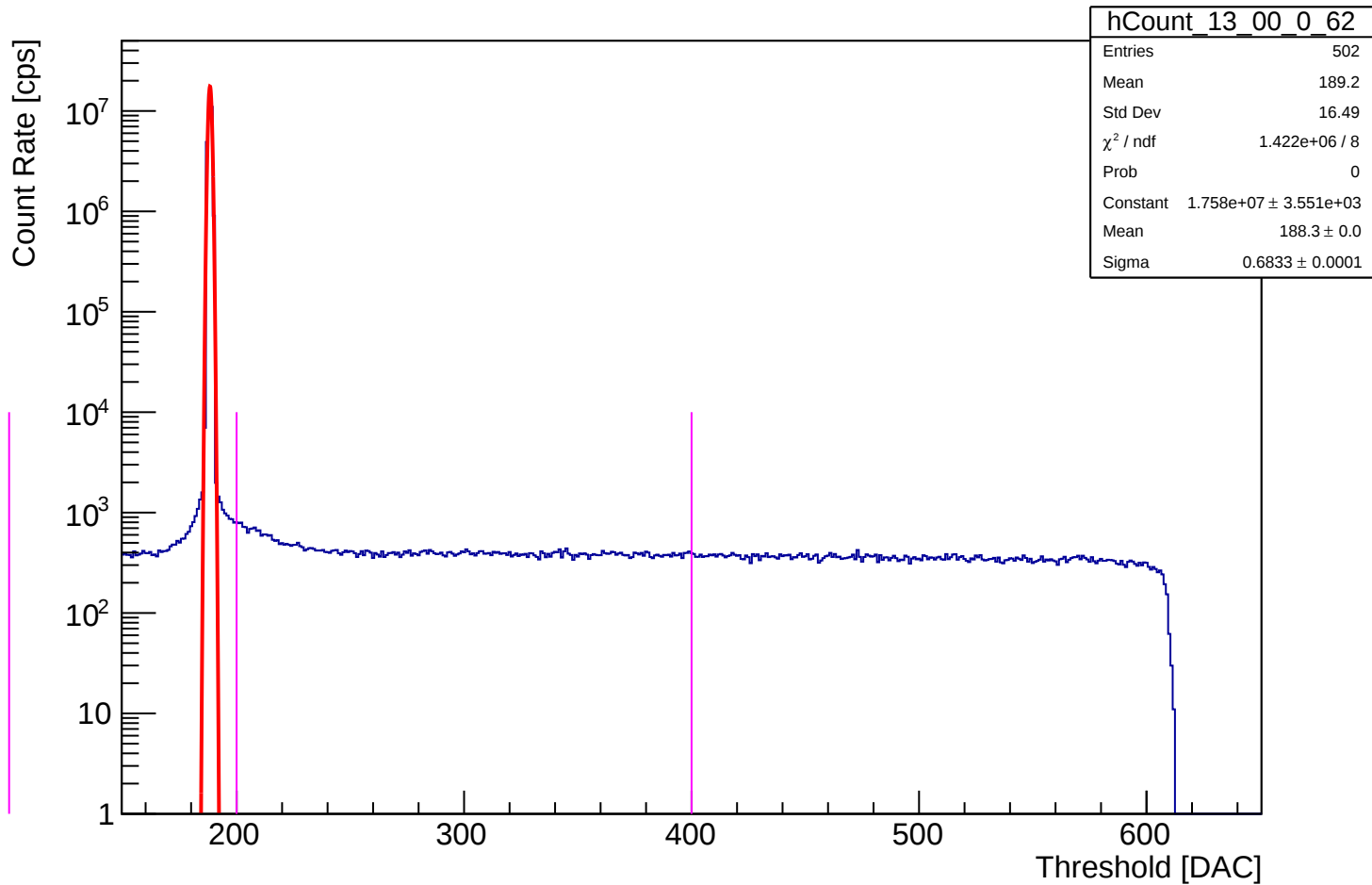


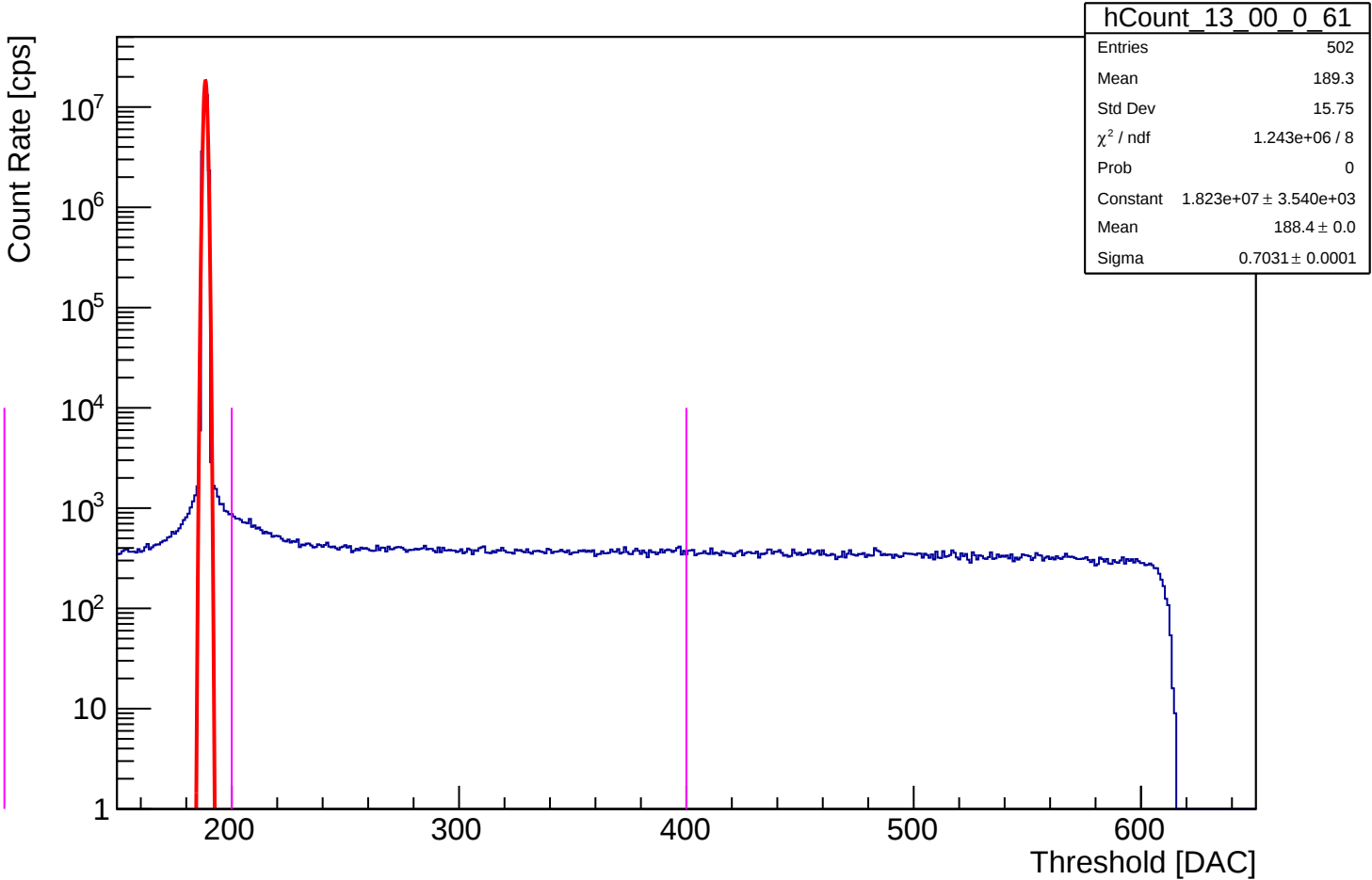


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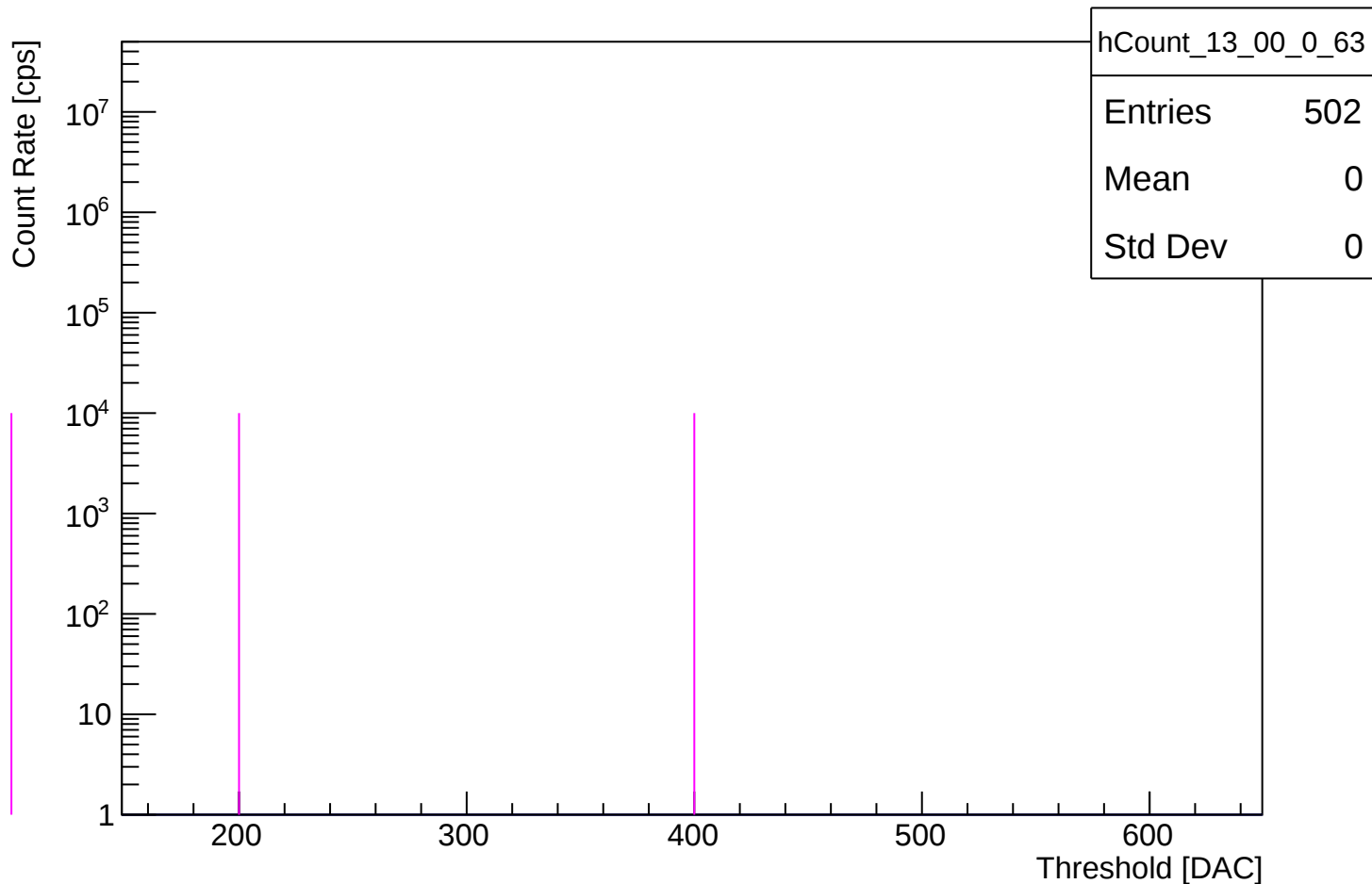


Row 1 Tile 1 Pmt 1 Pixel 62 Slot 13 Fiber 0 Asic 0 Channel 62

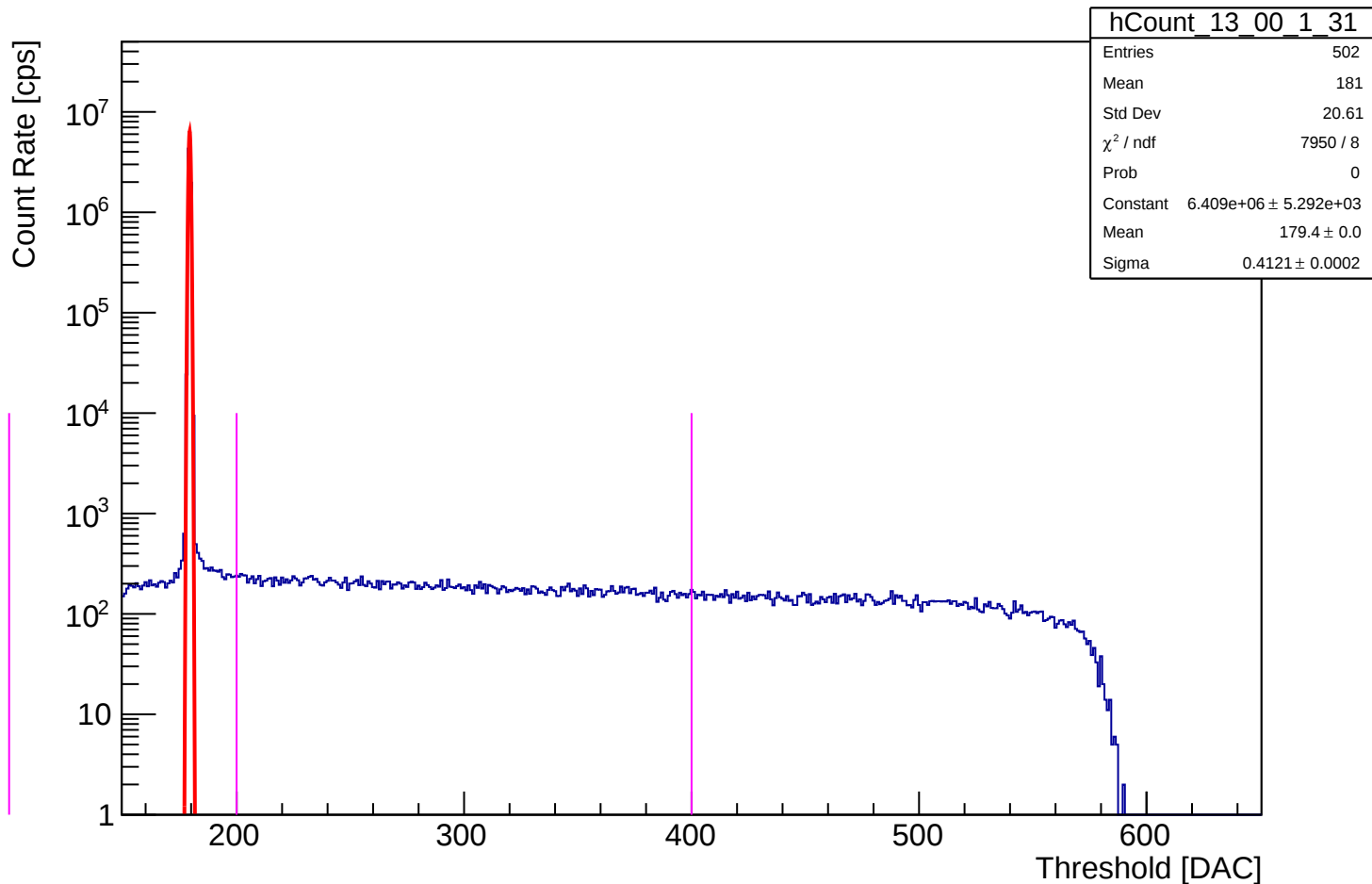




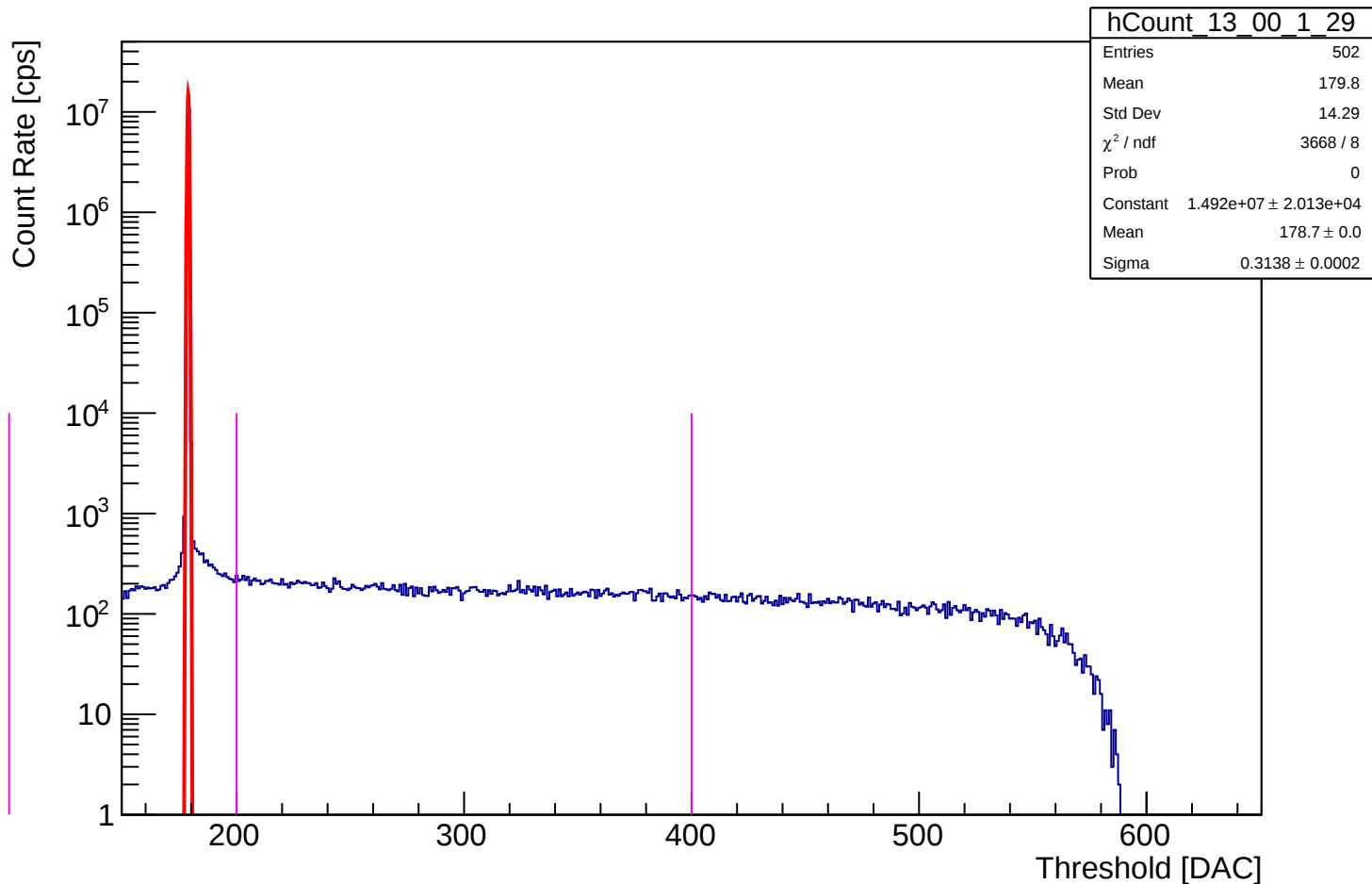
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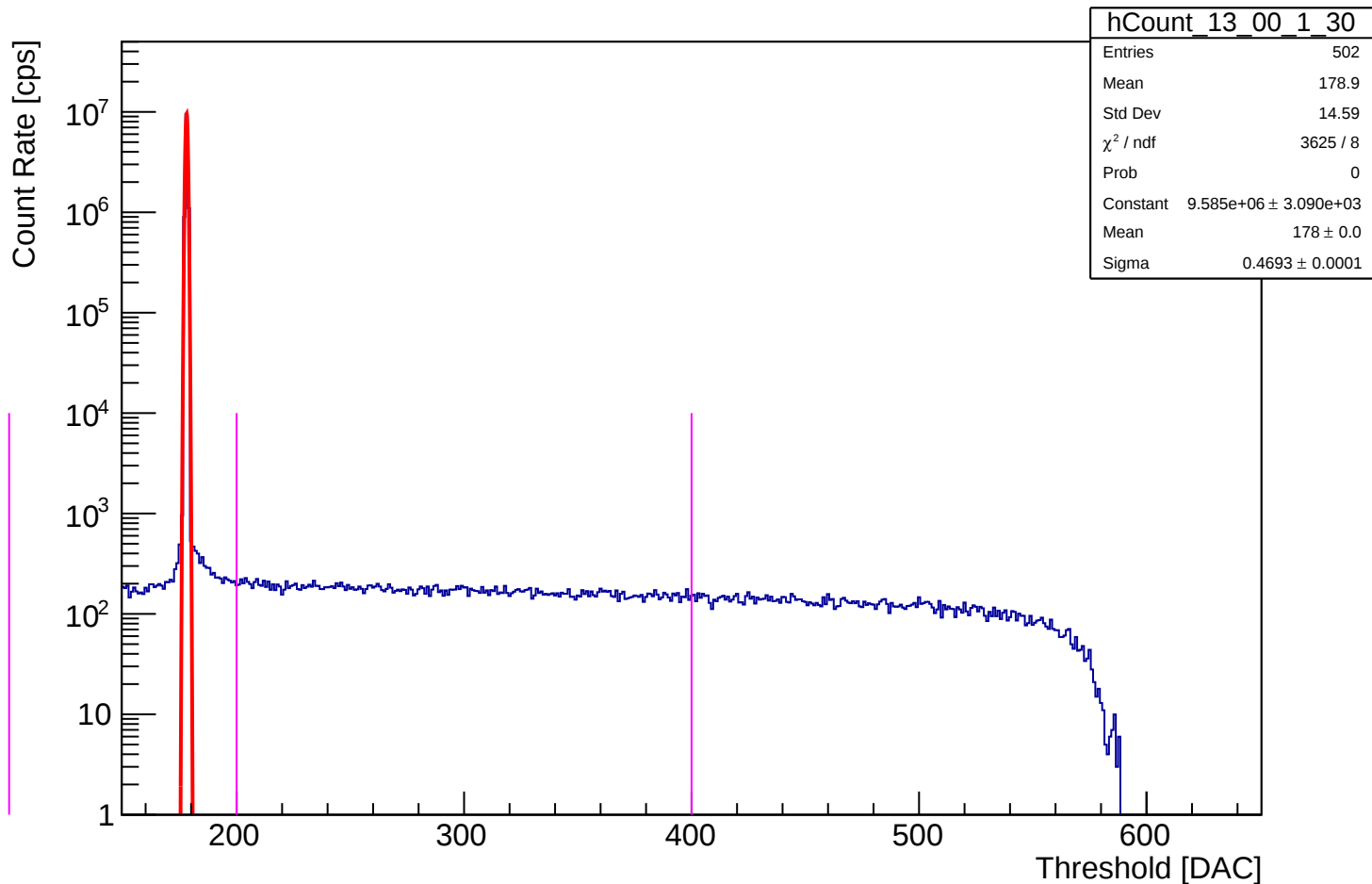
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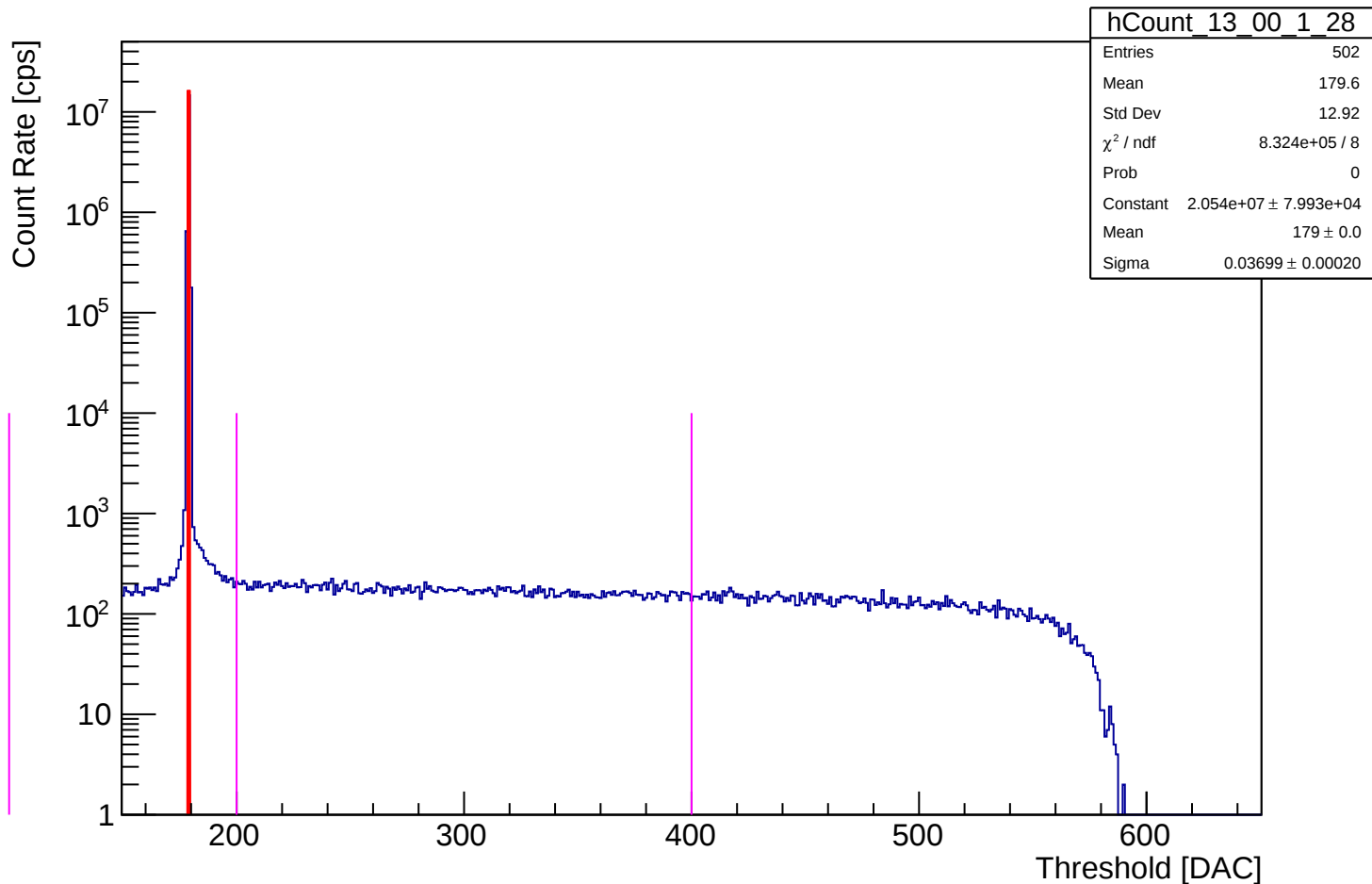
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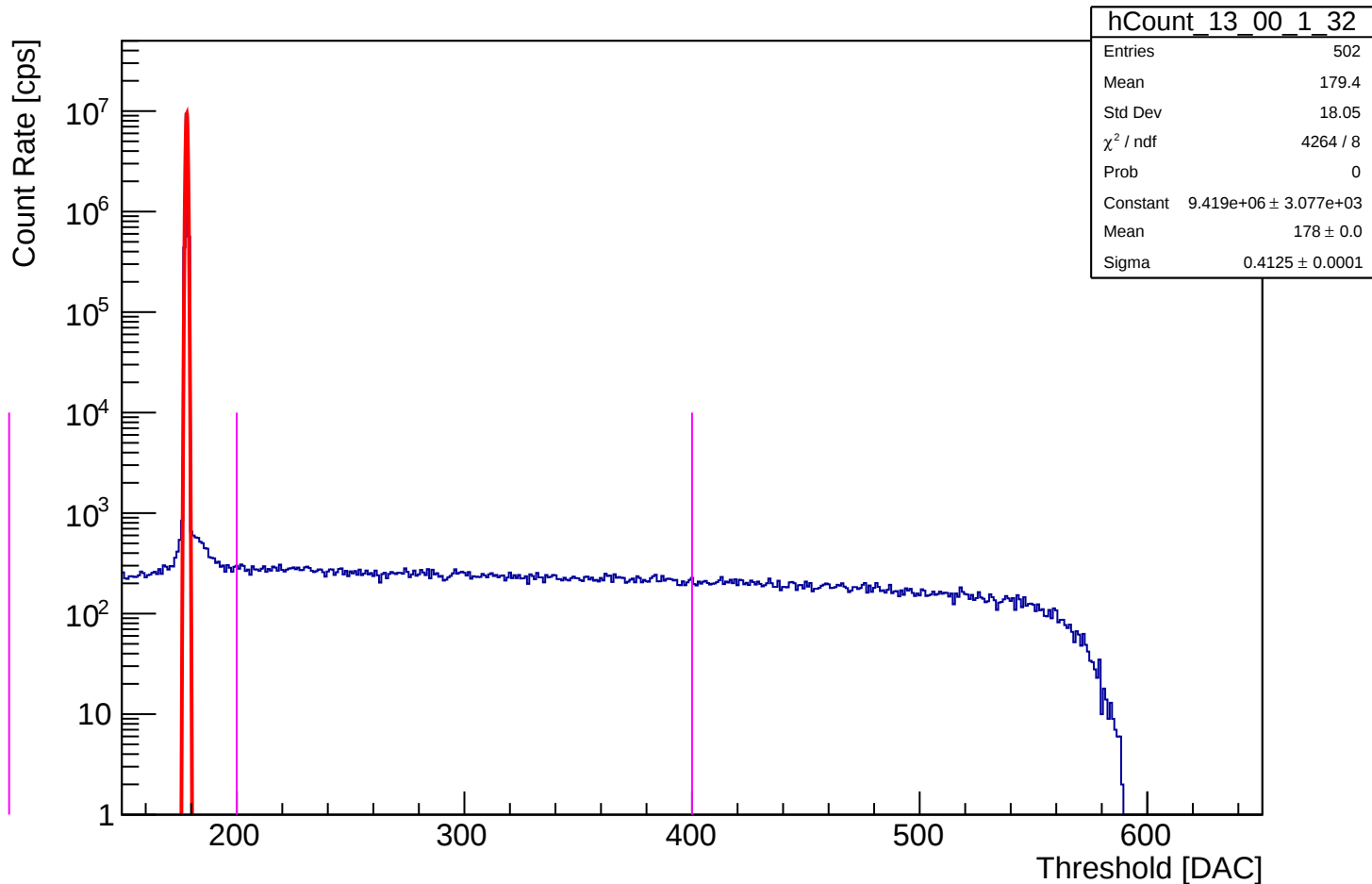
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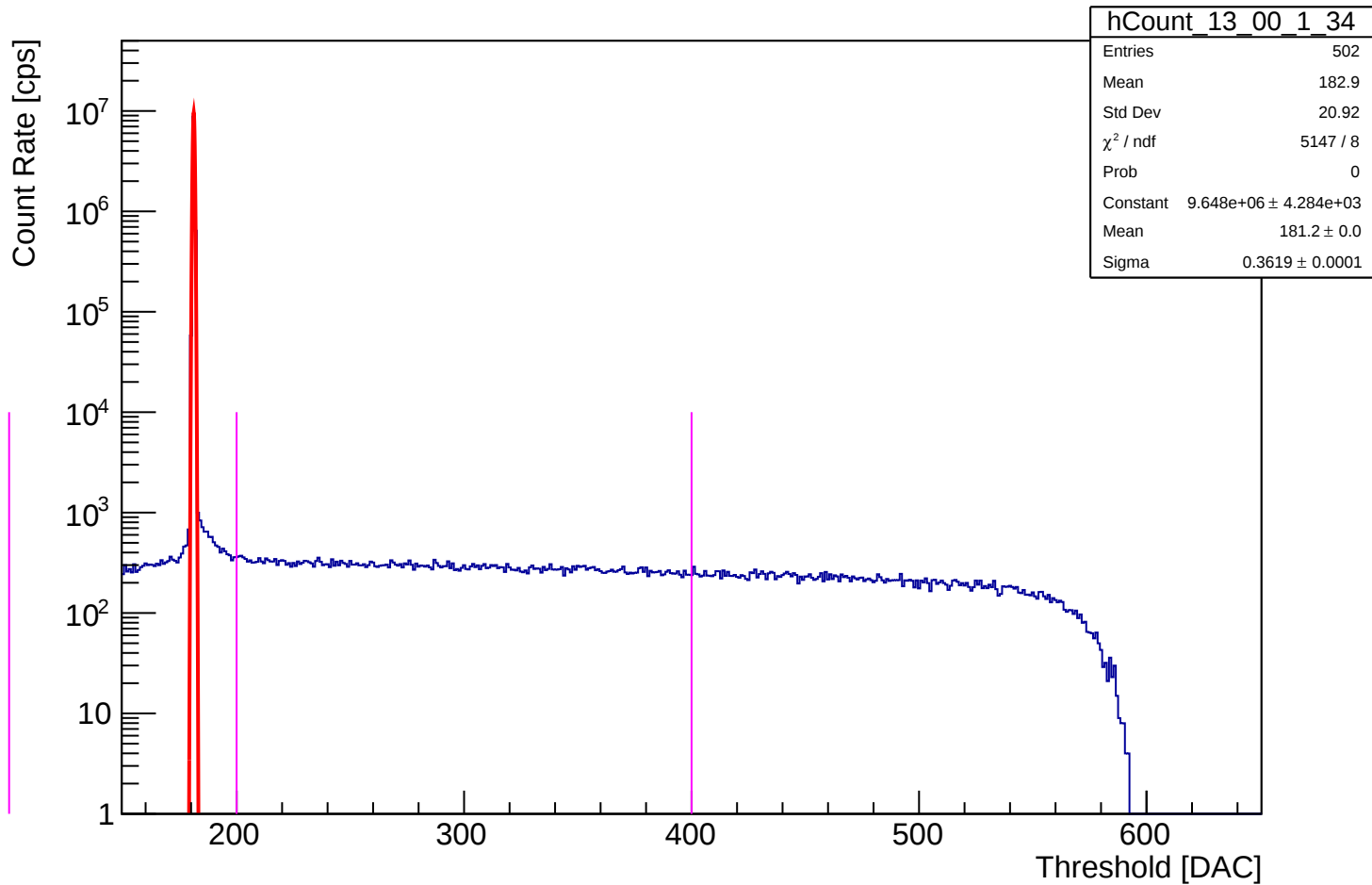
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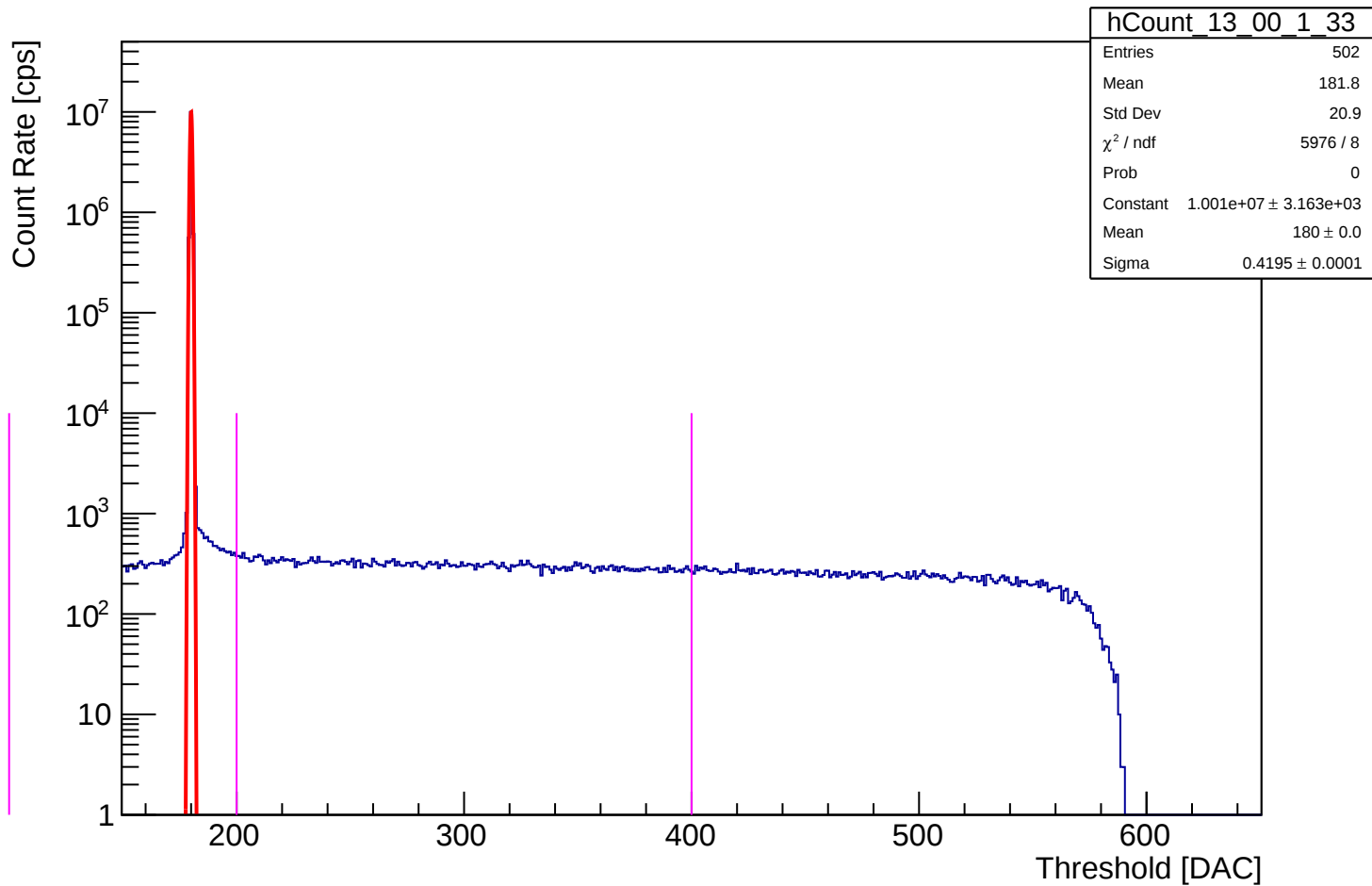
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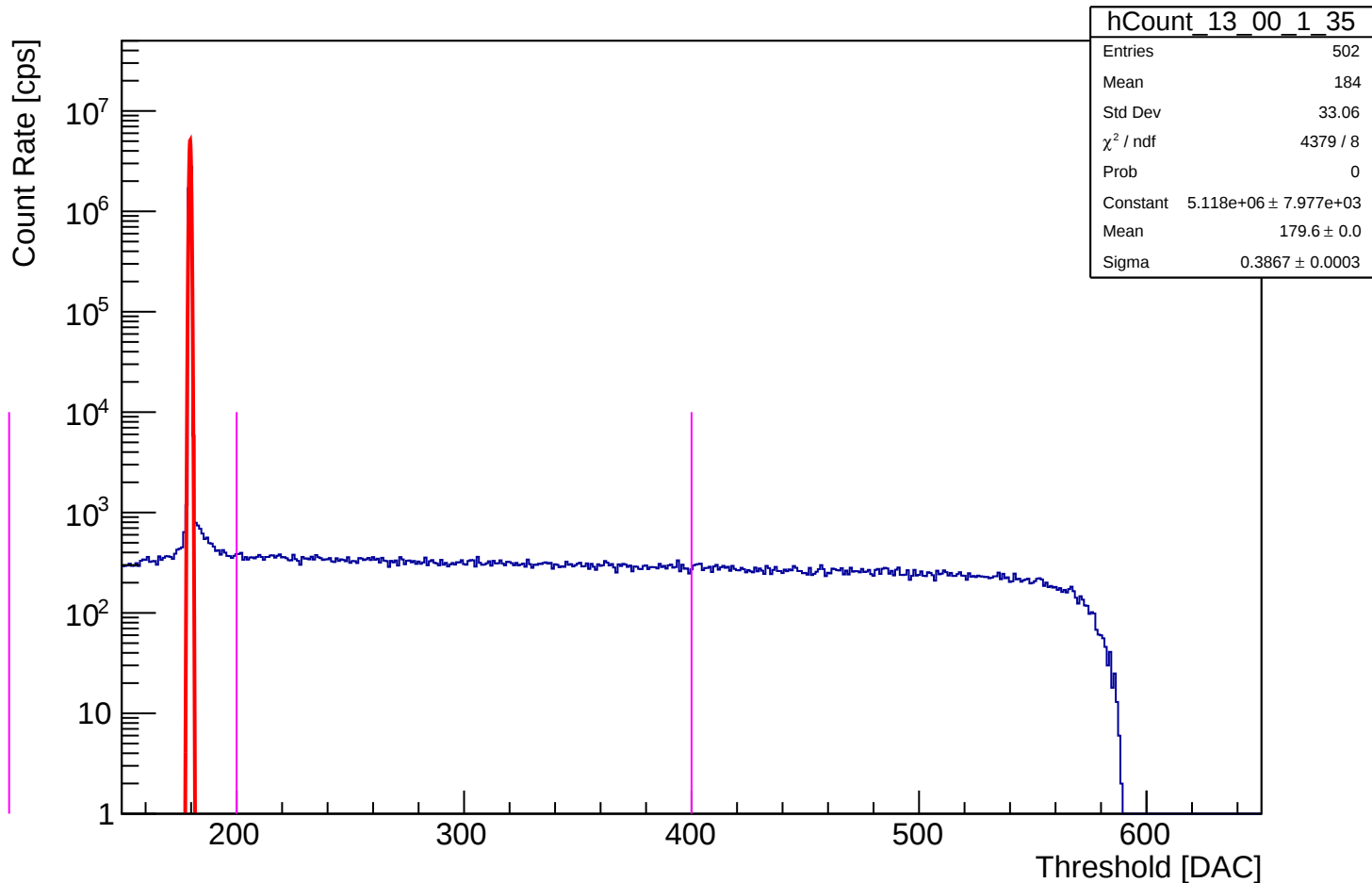
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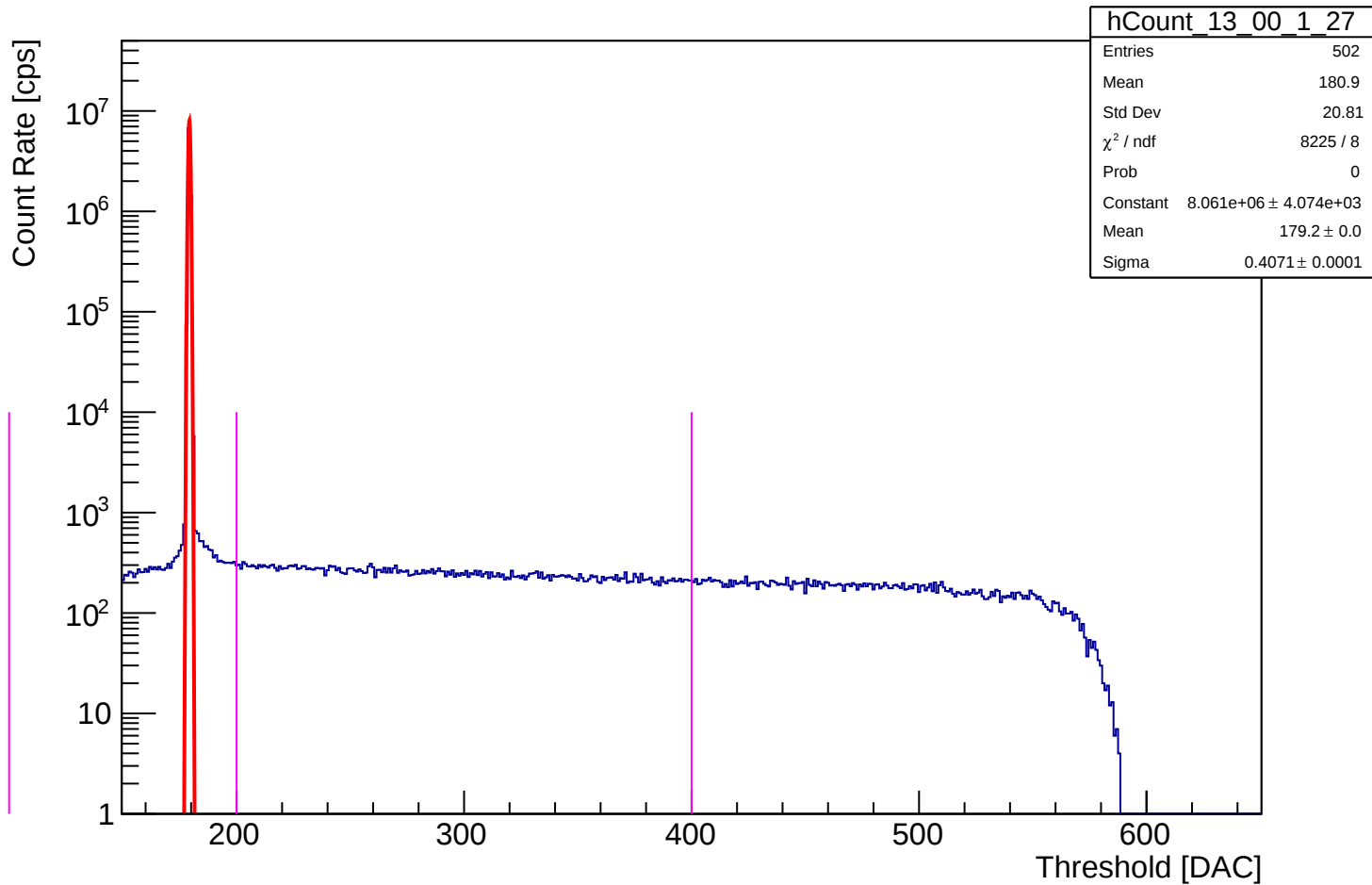
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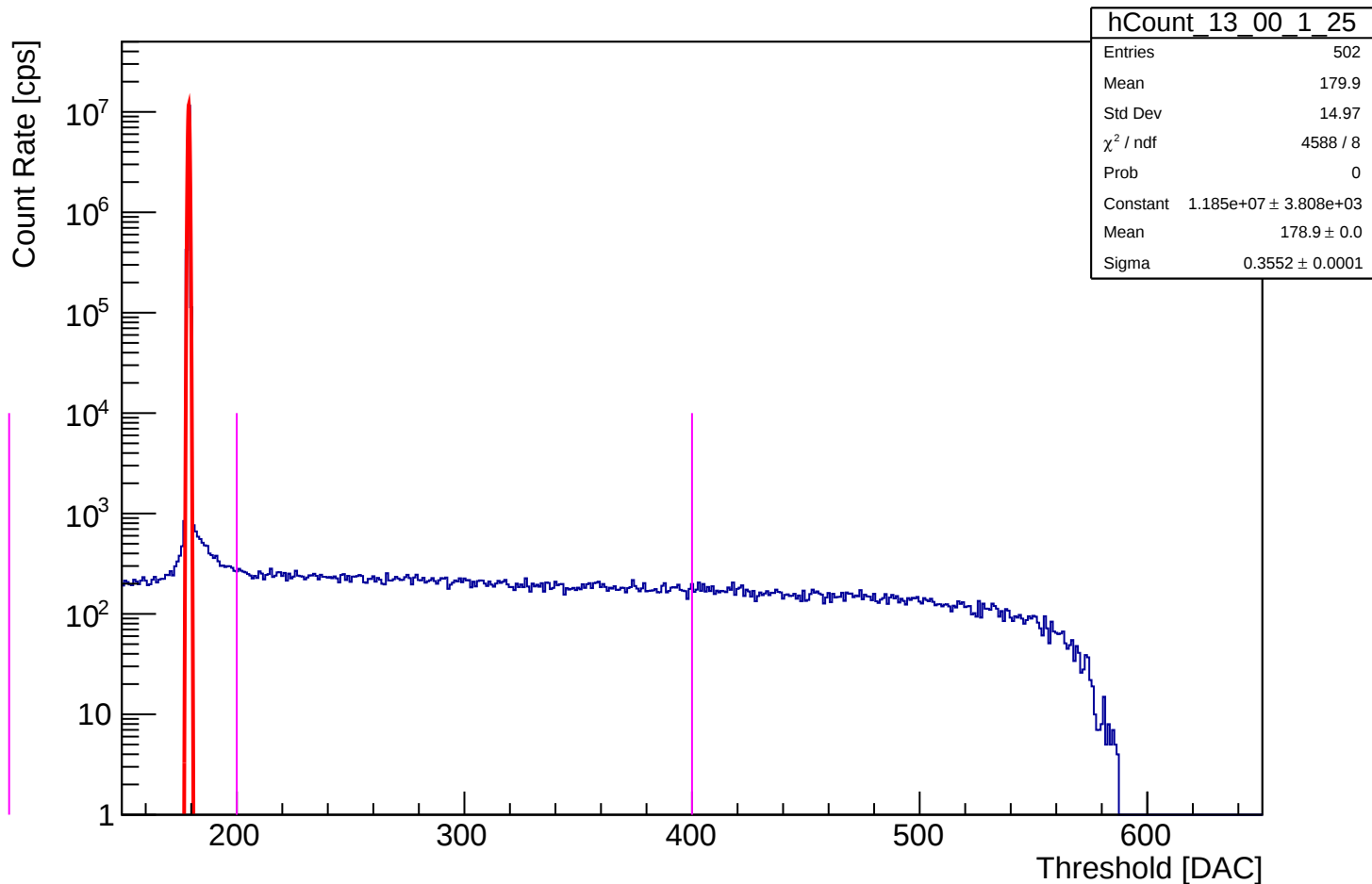
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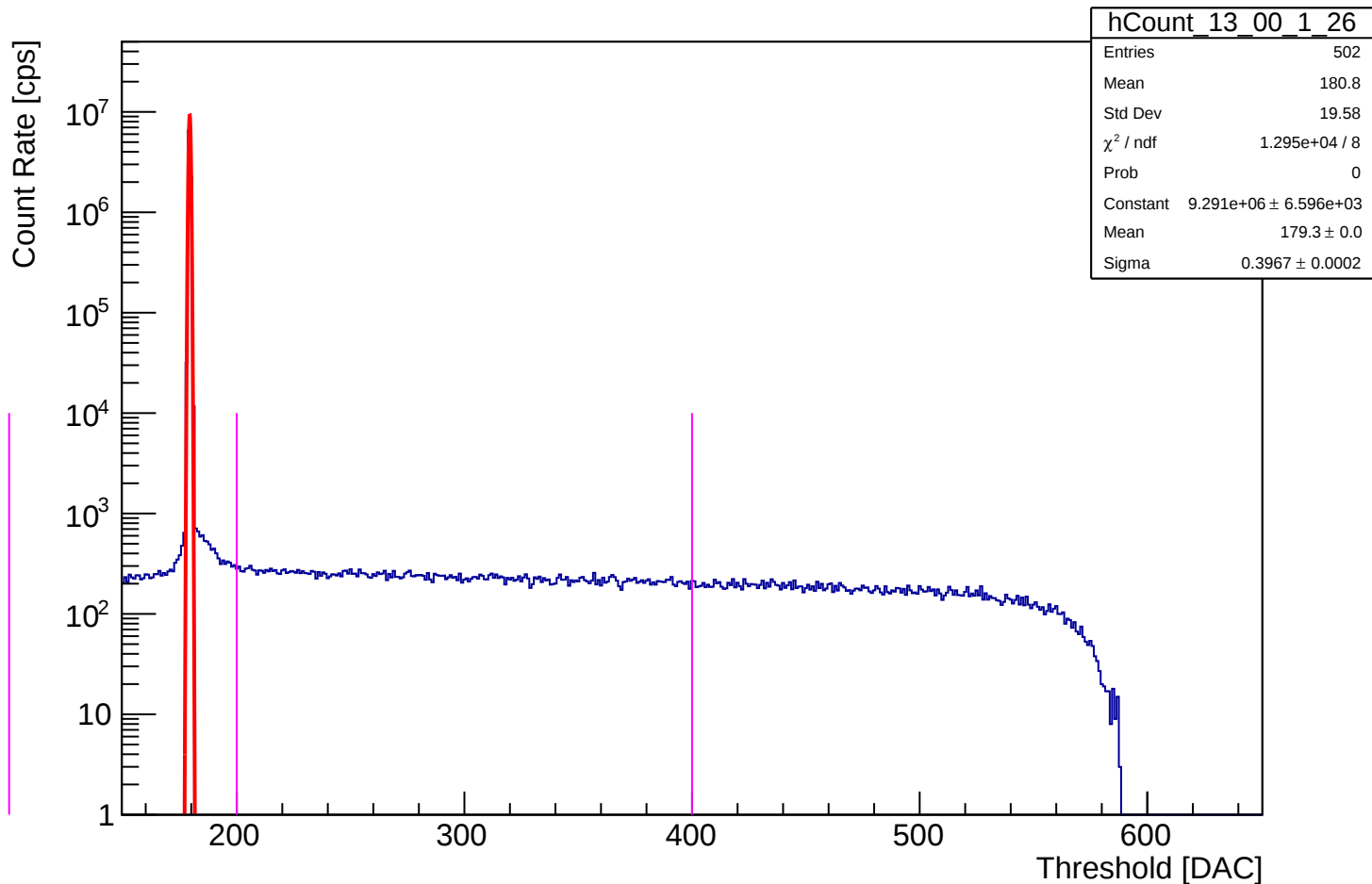
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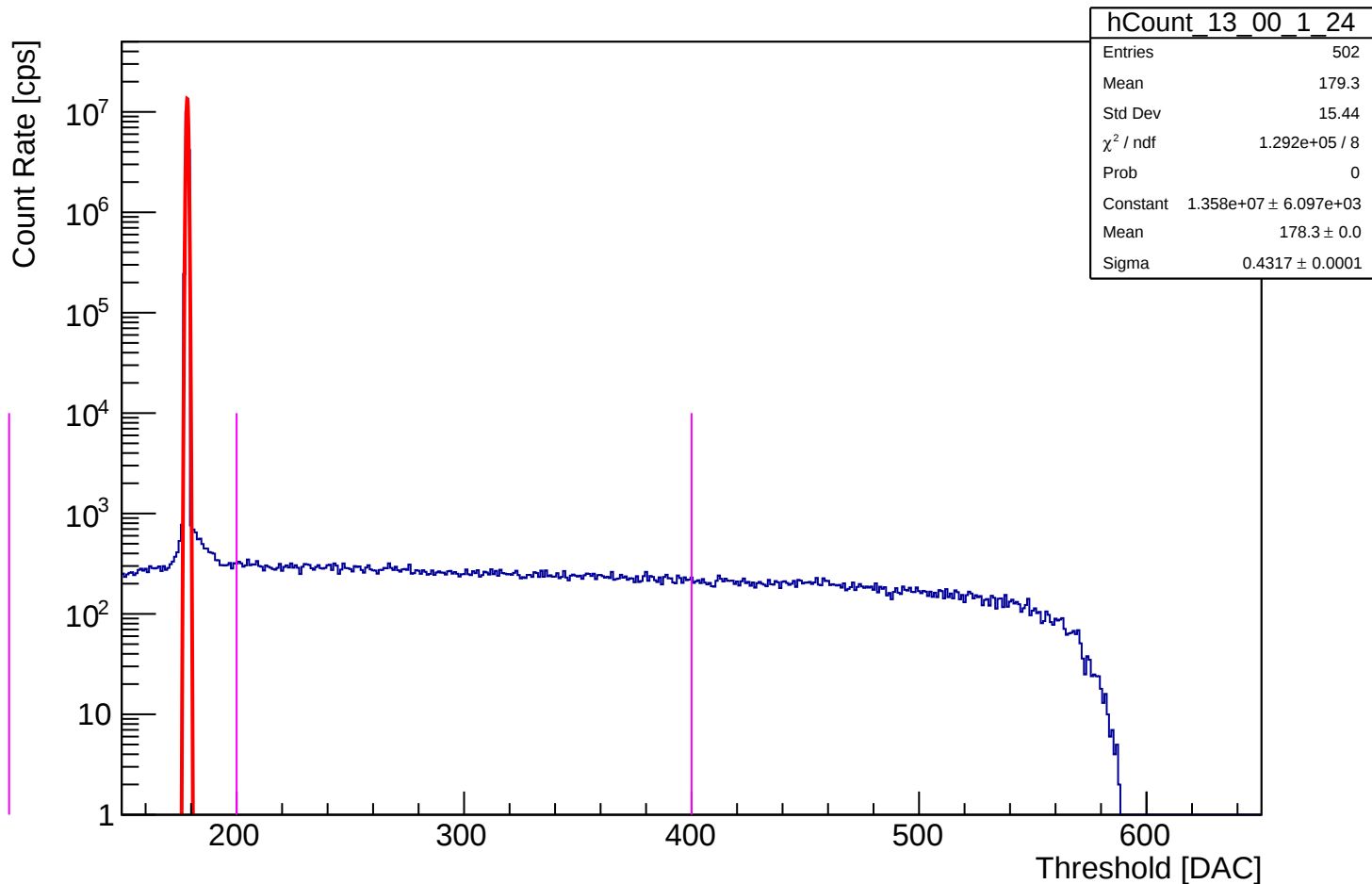
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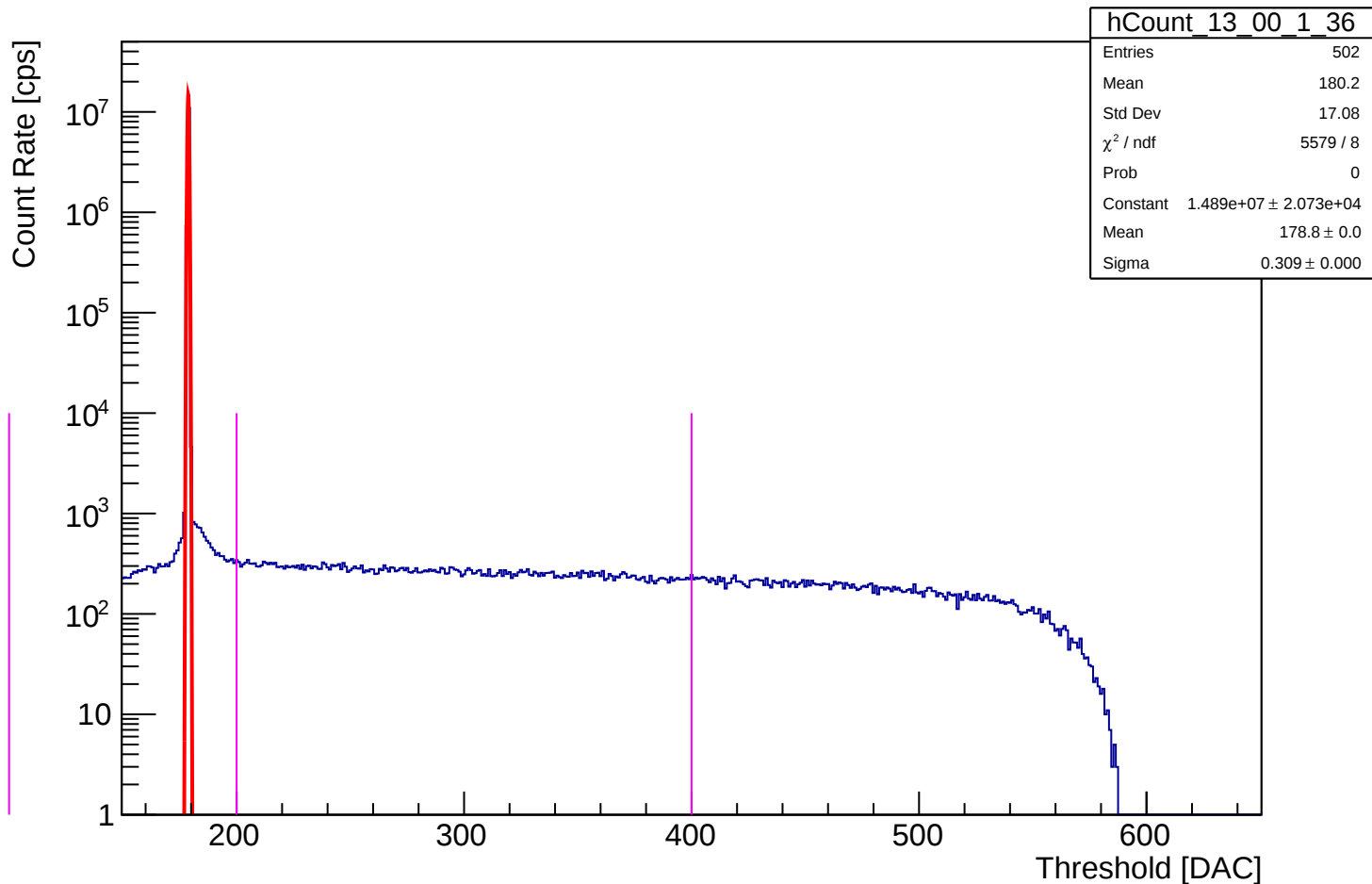
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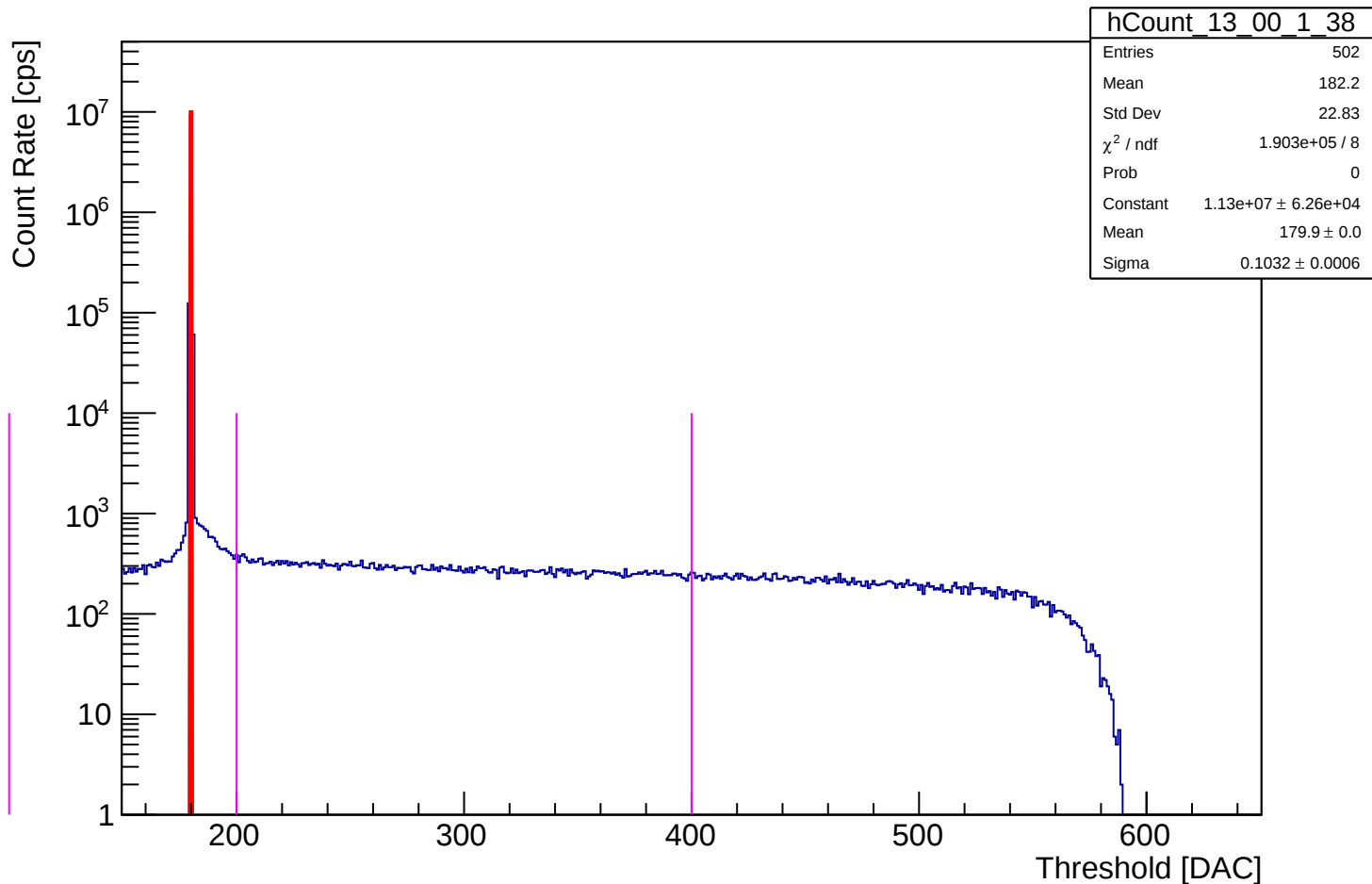
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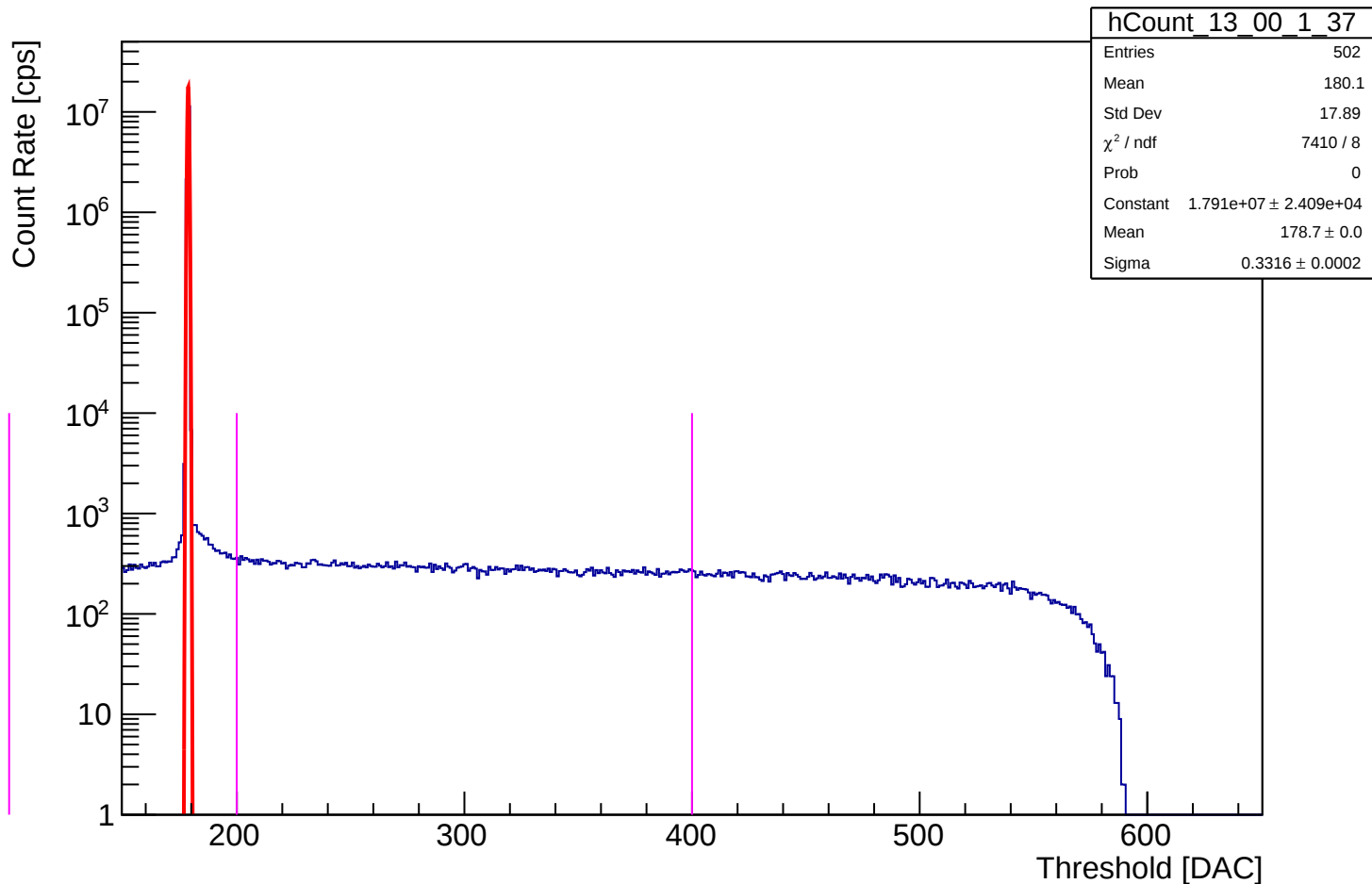
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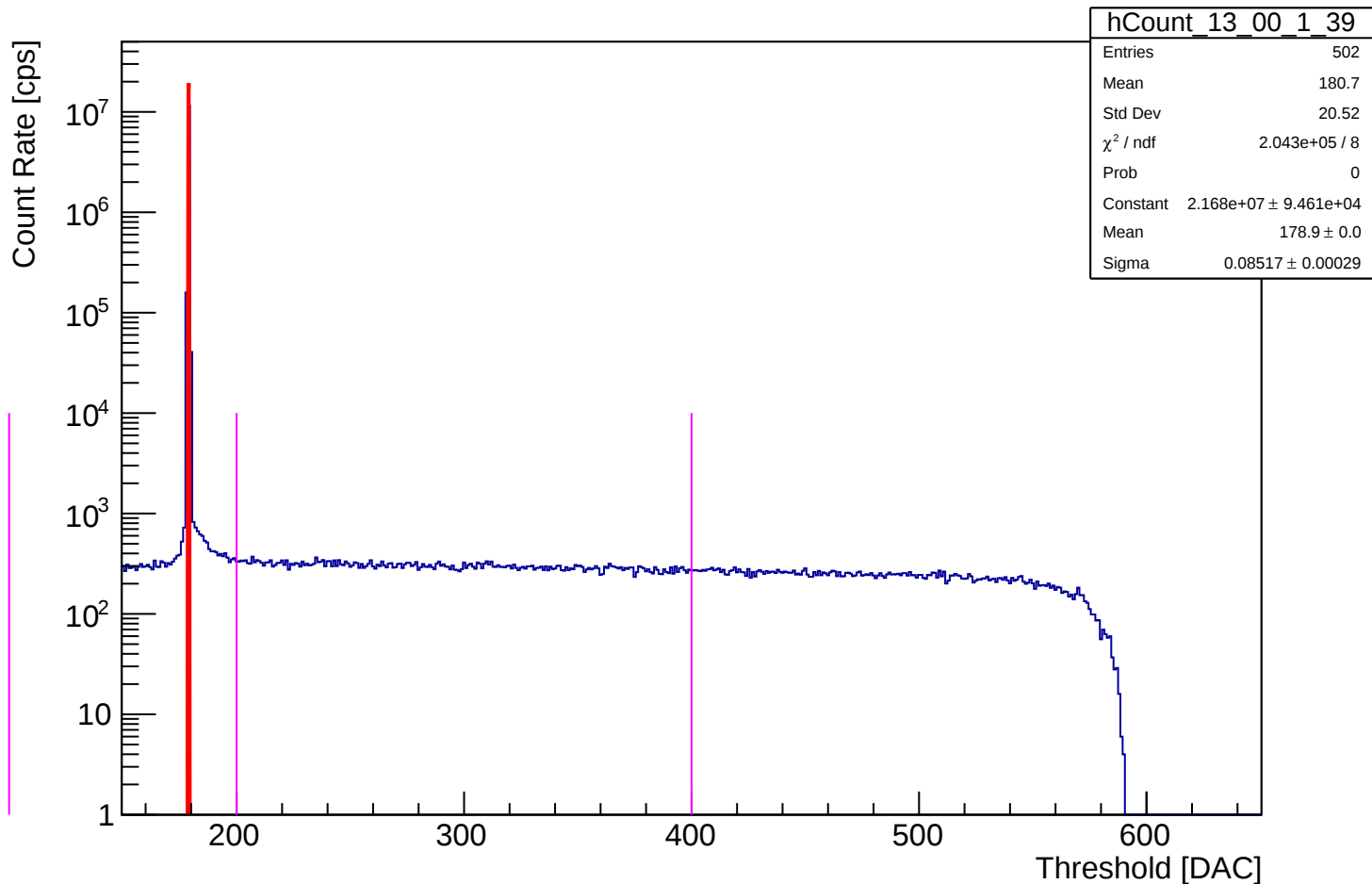
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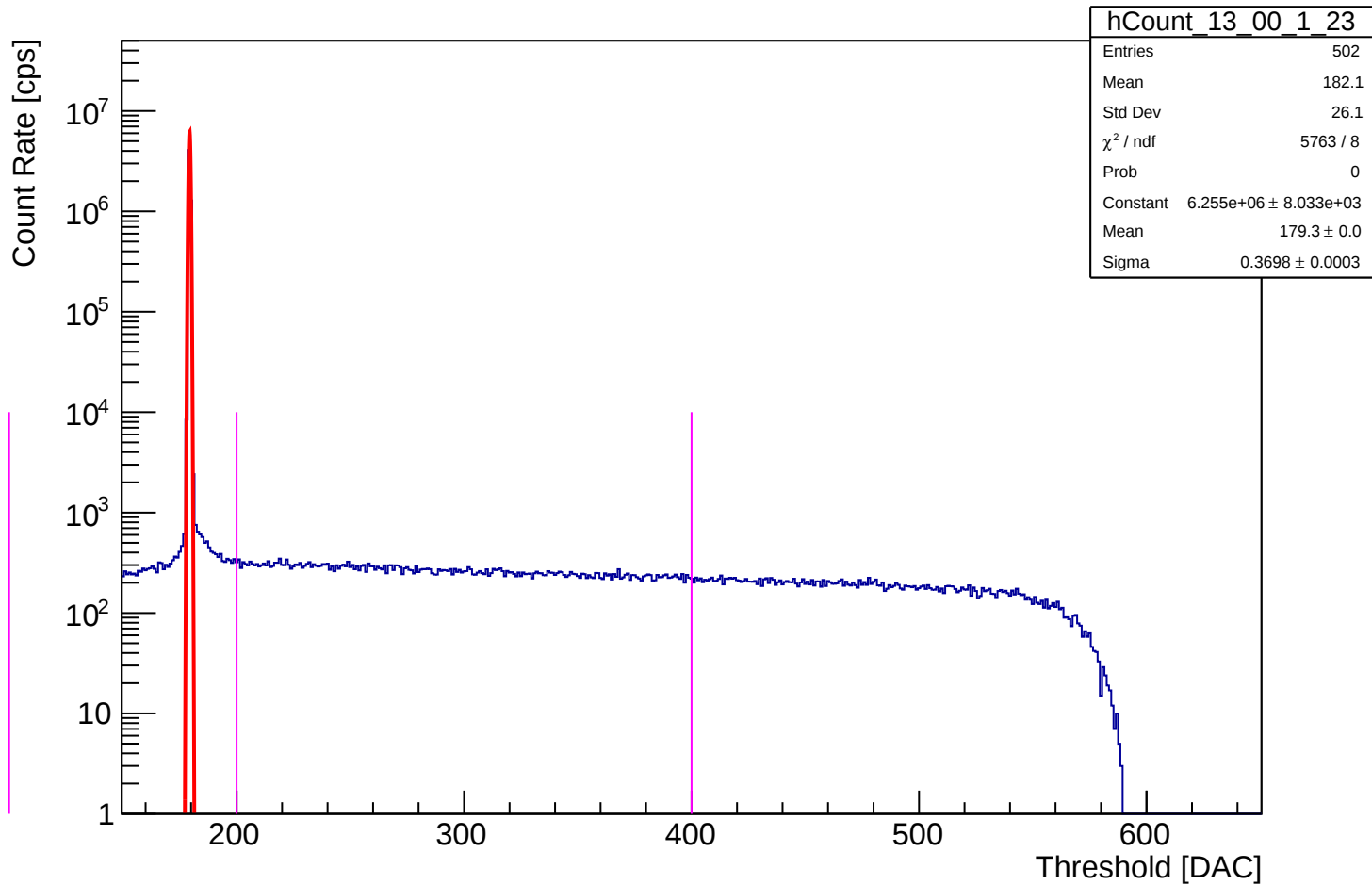
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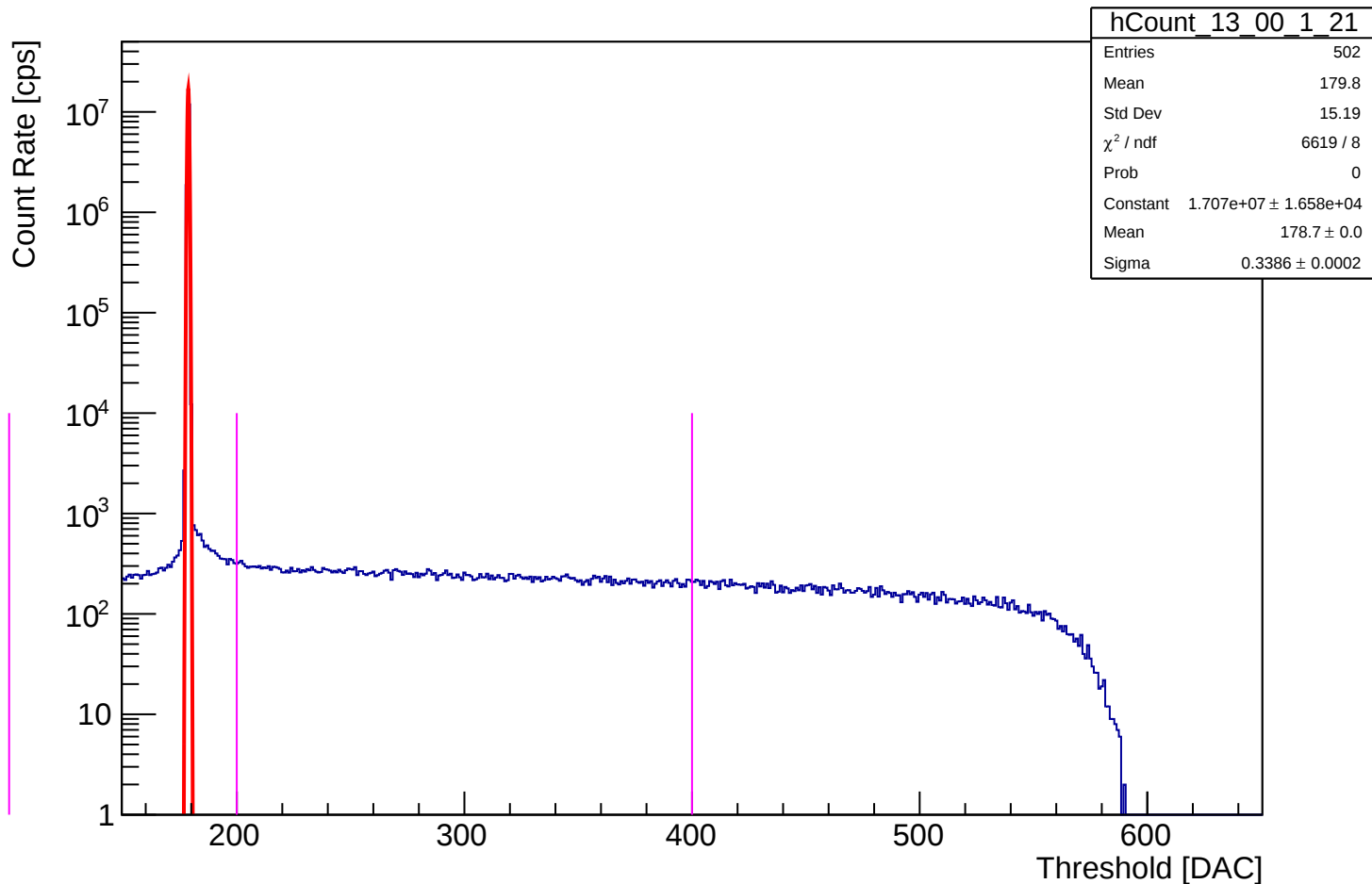
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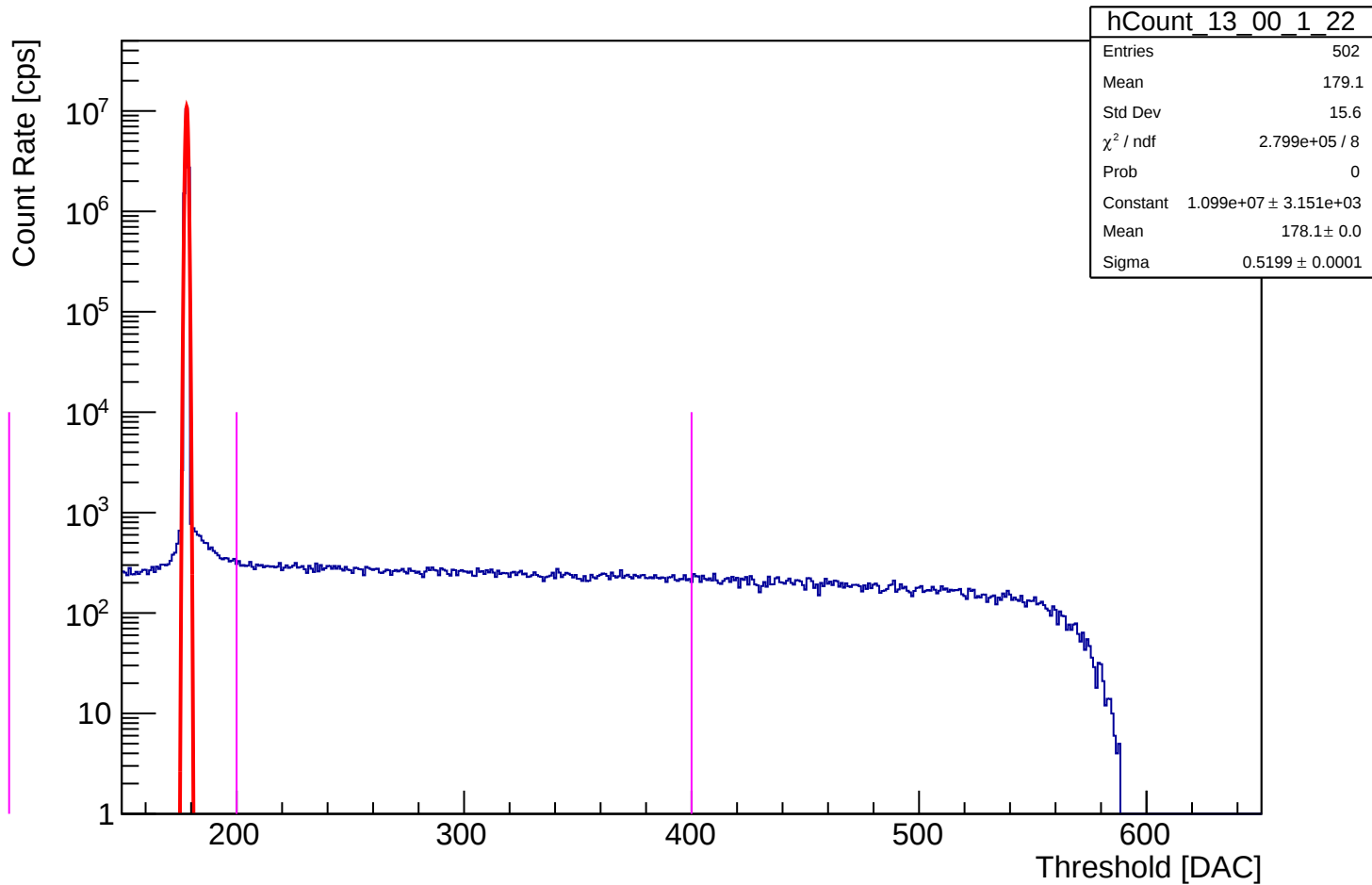
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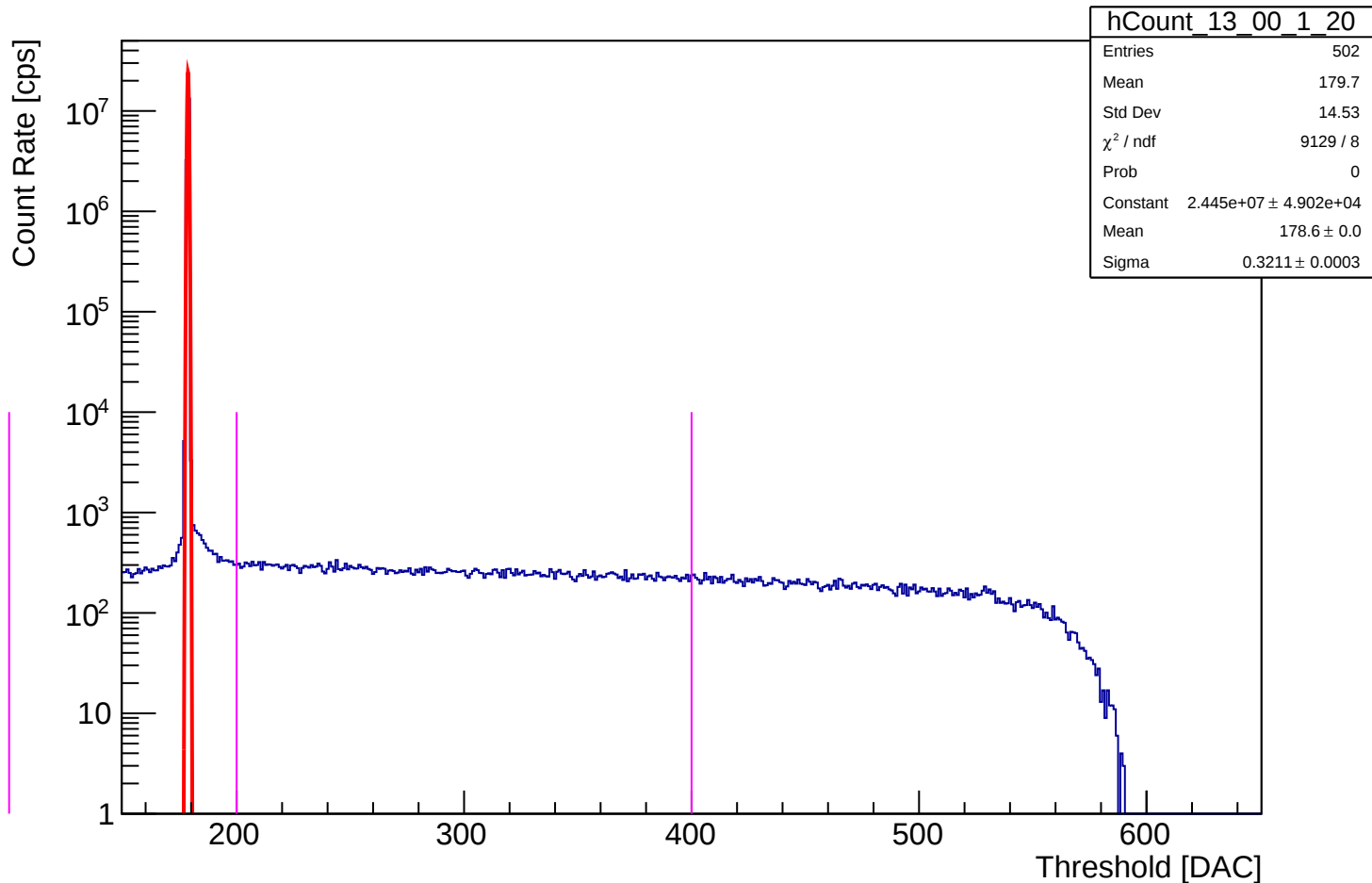
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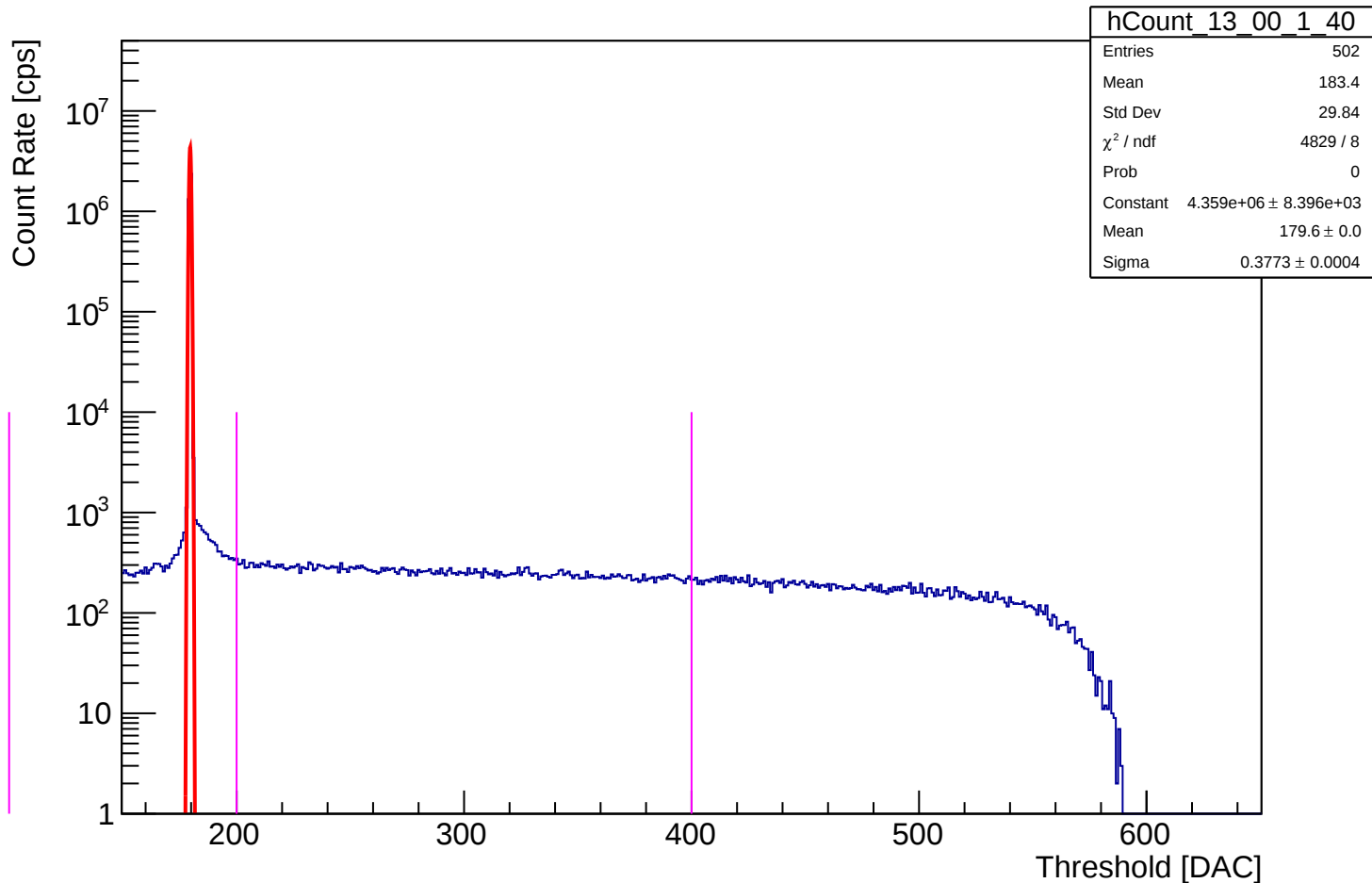
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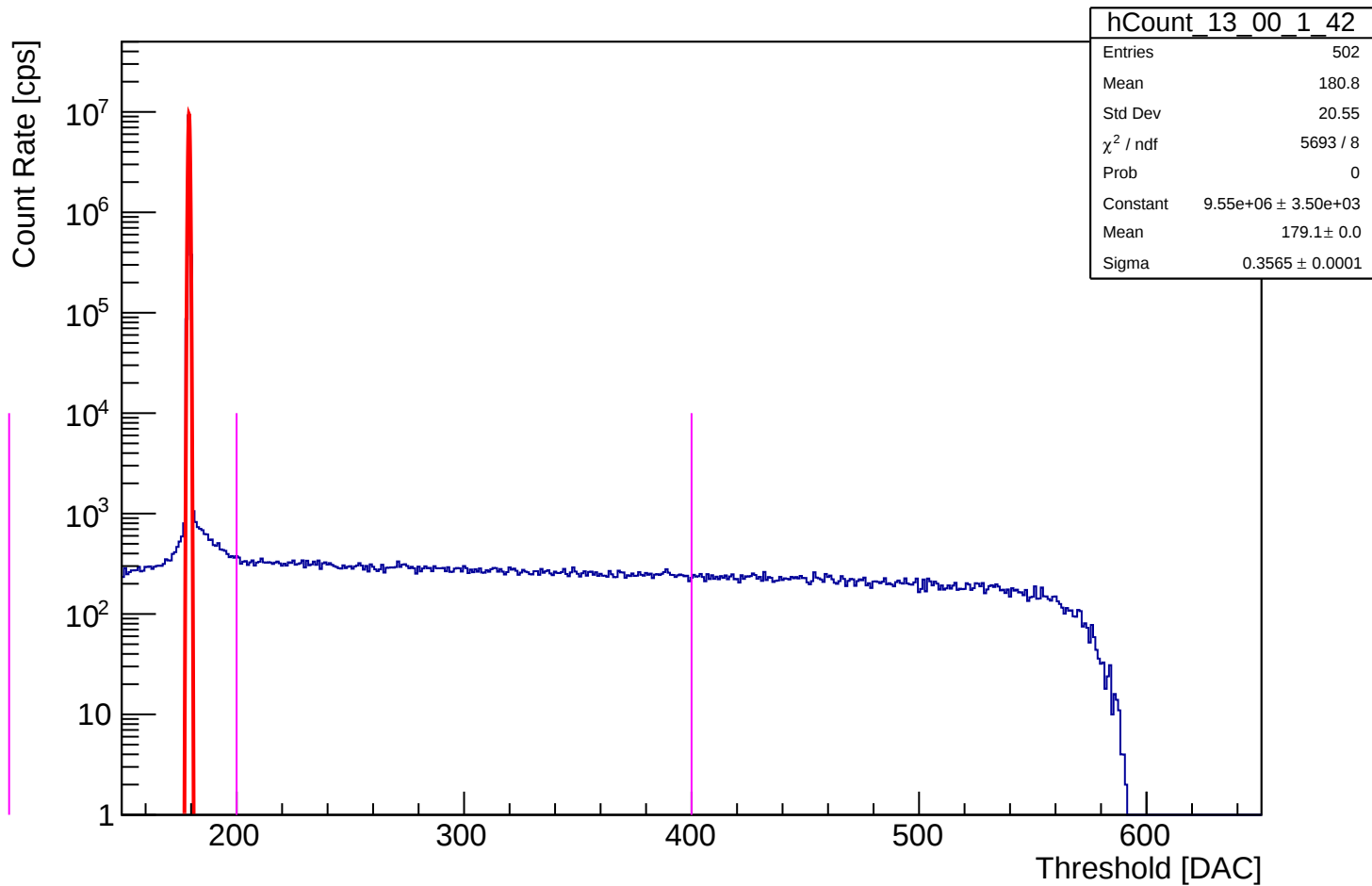
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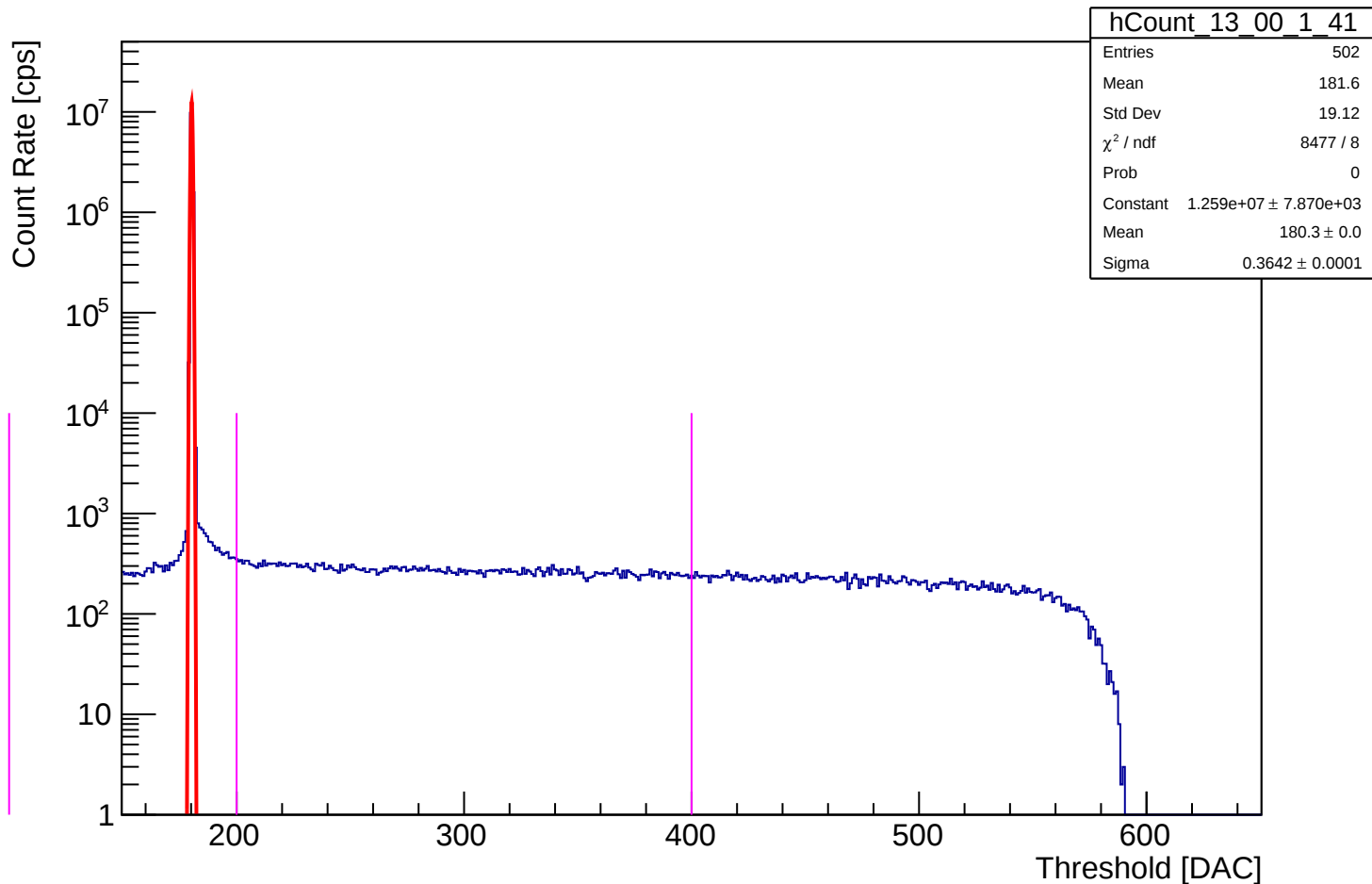
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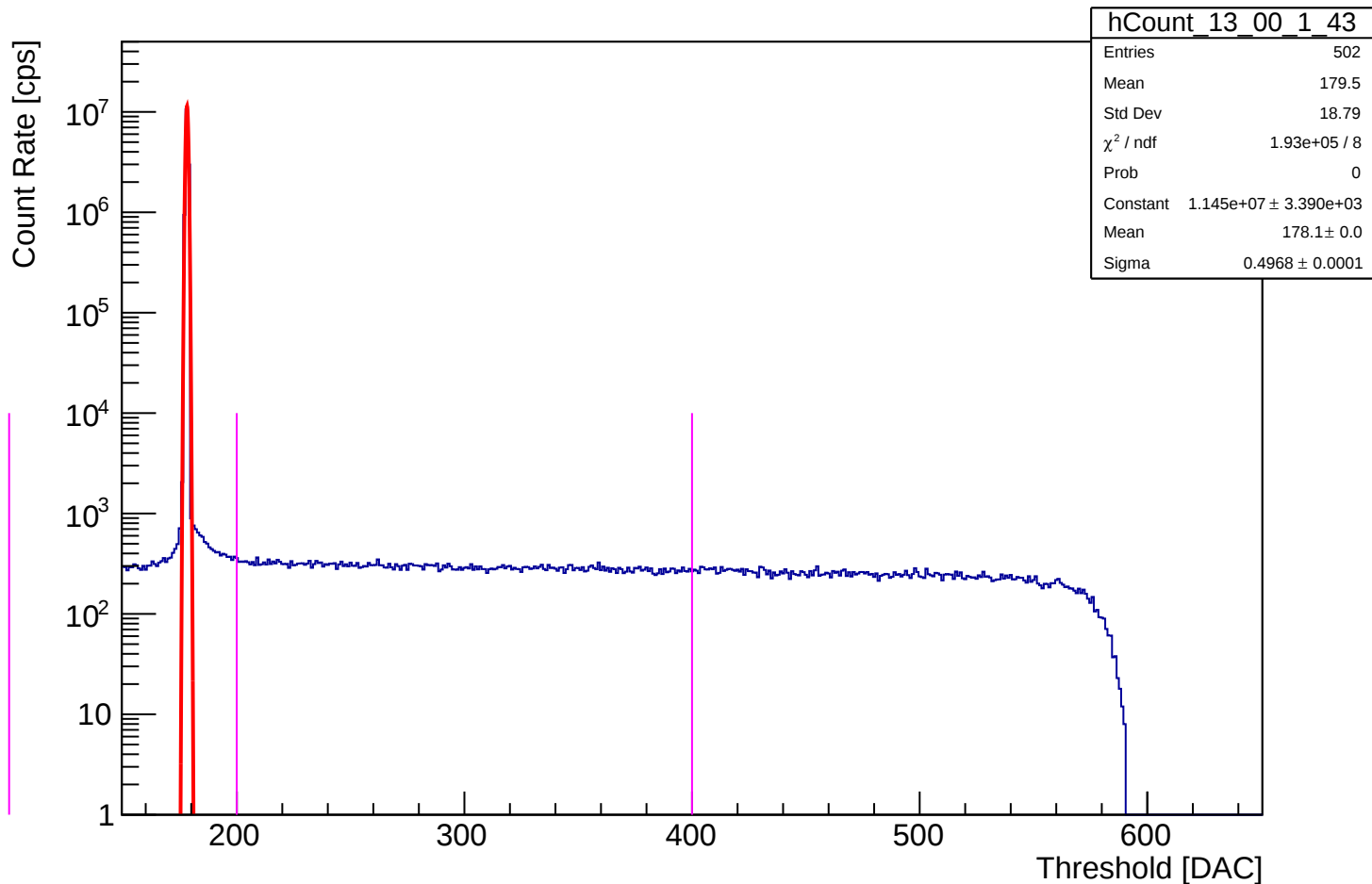
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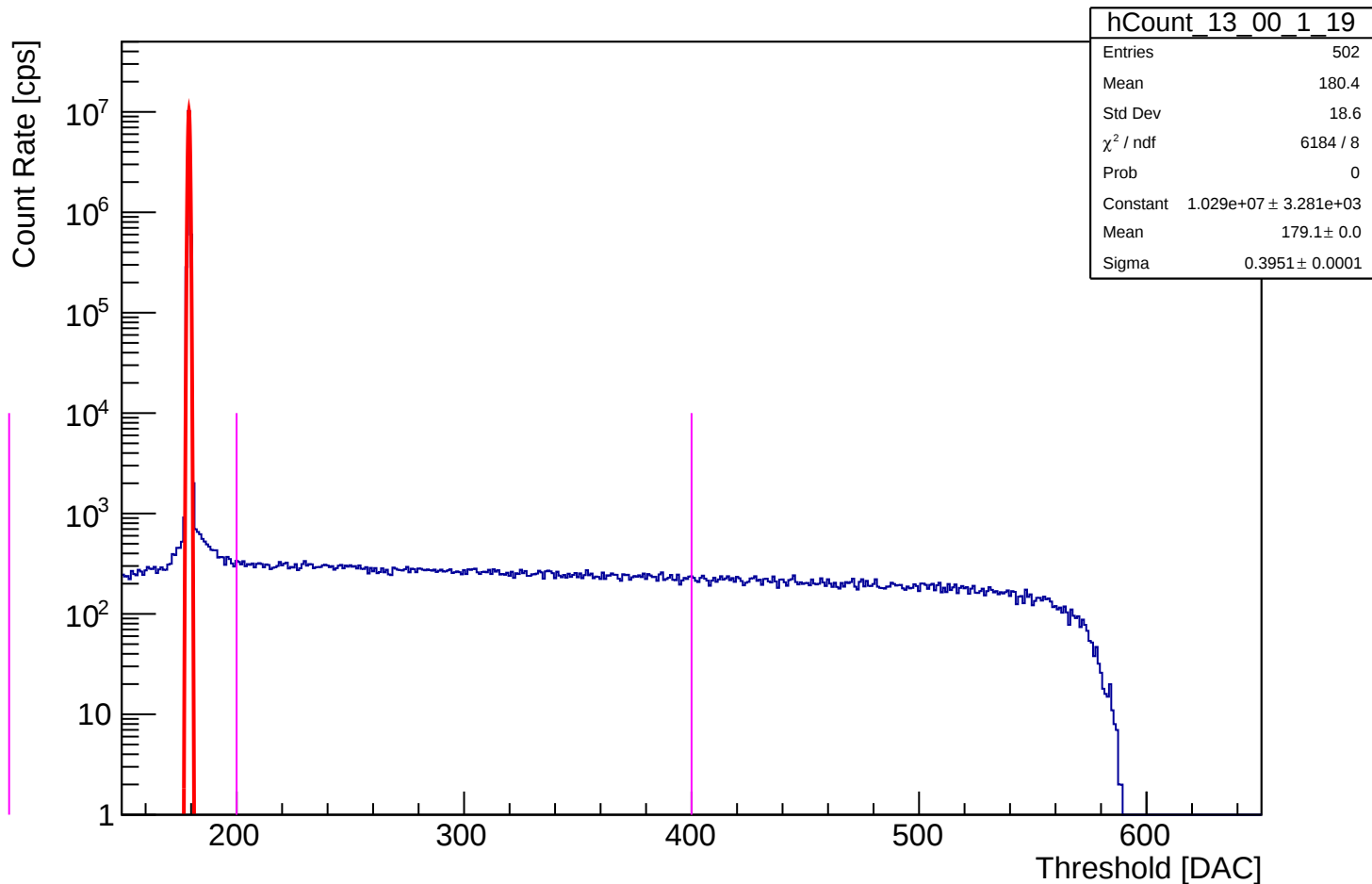
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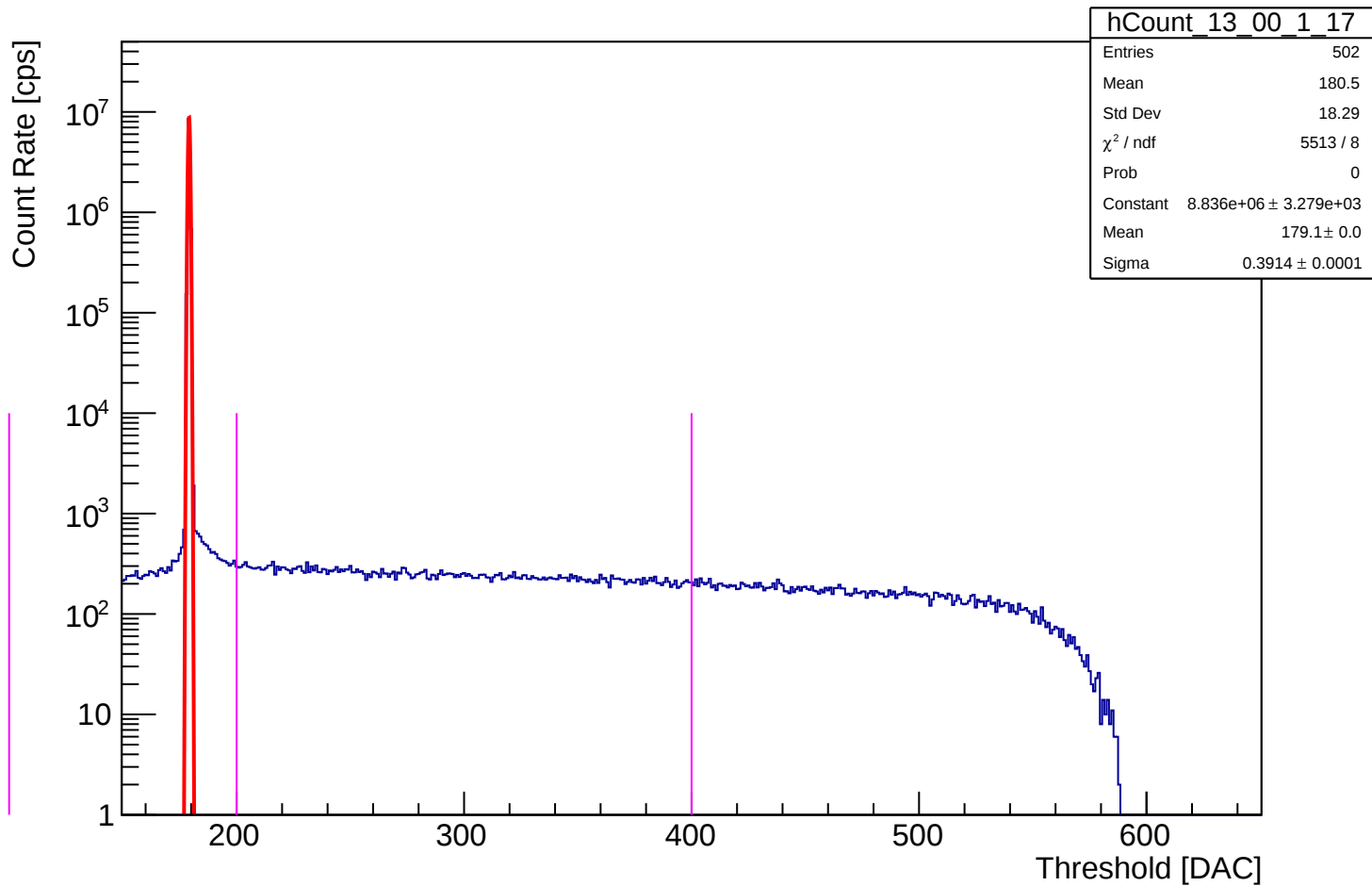
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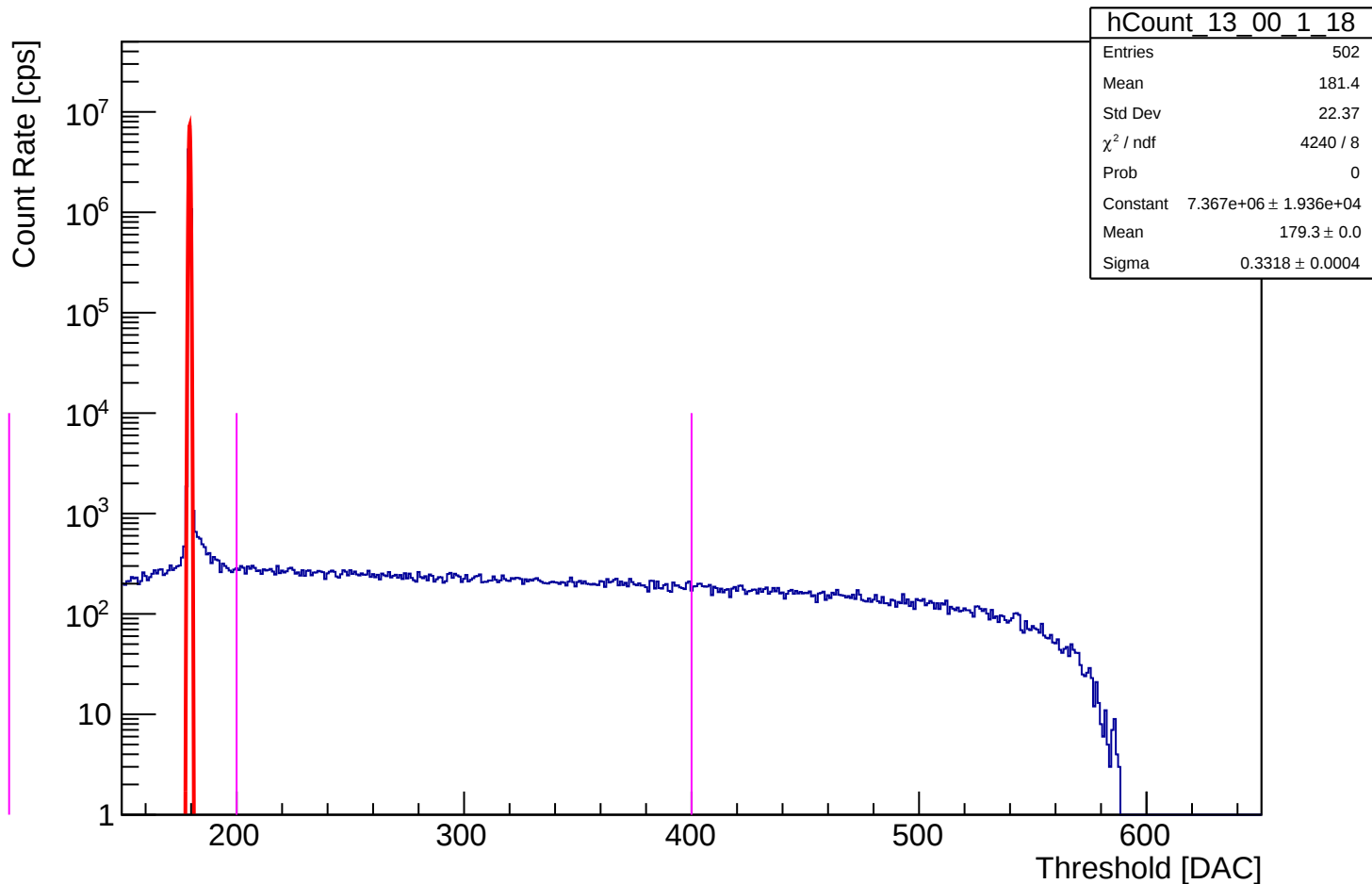
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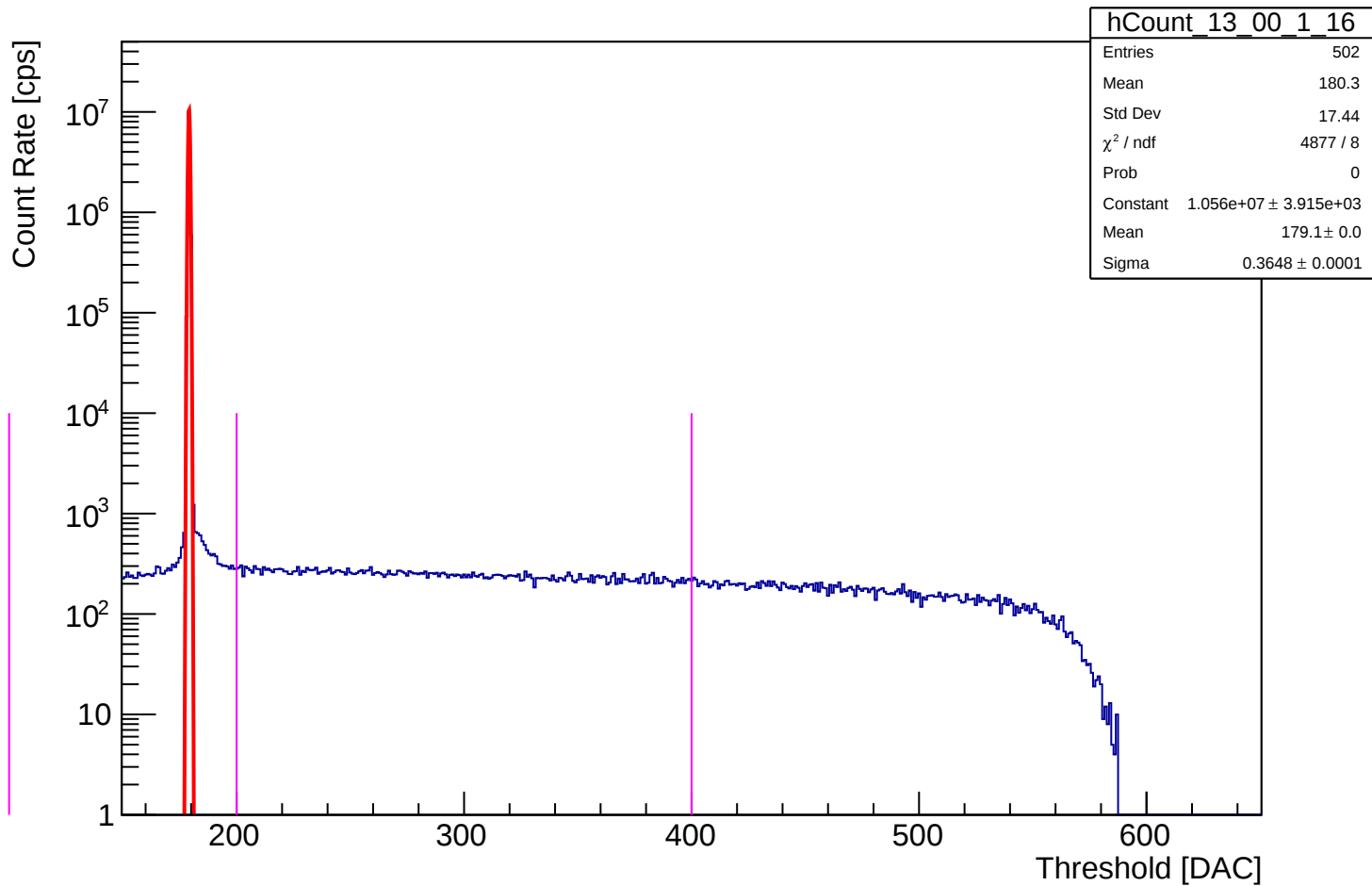
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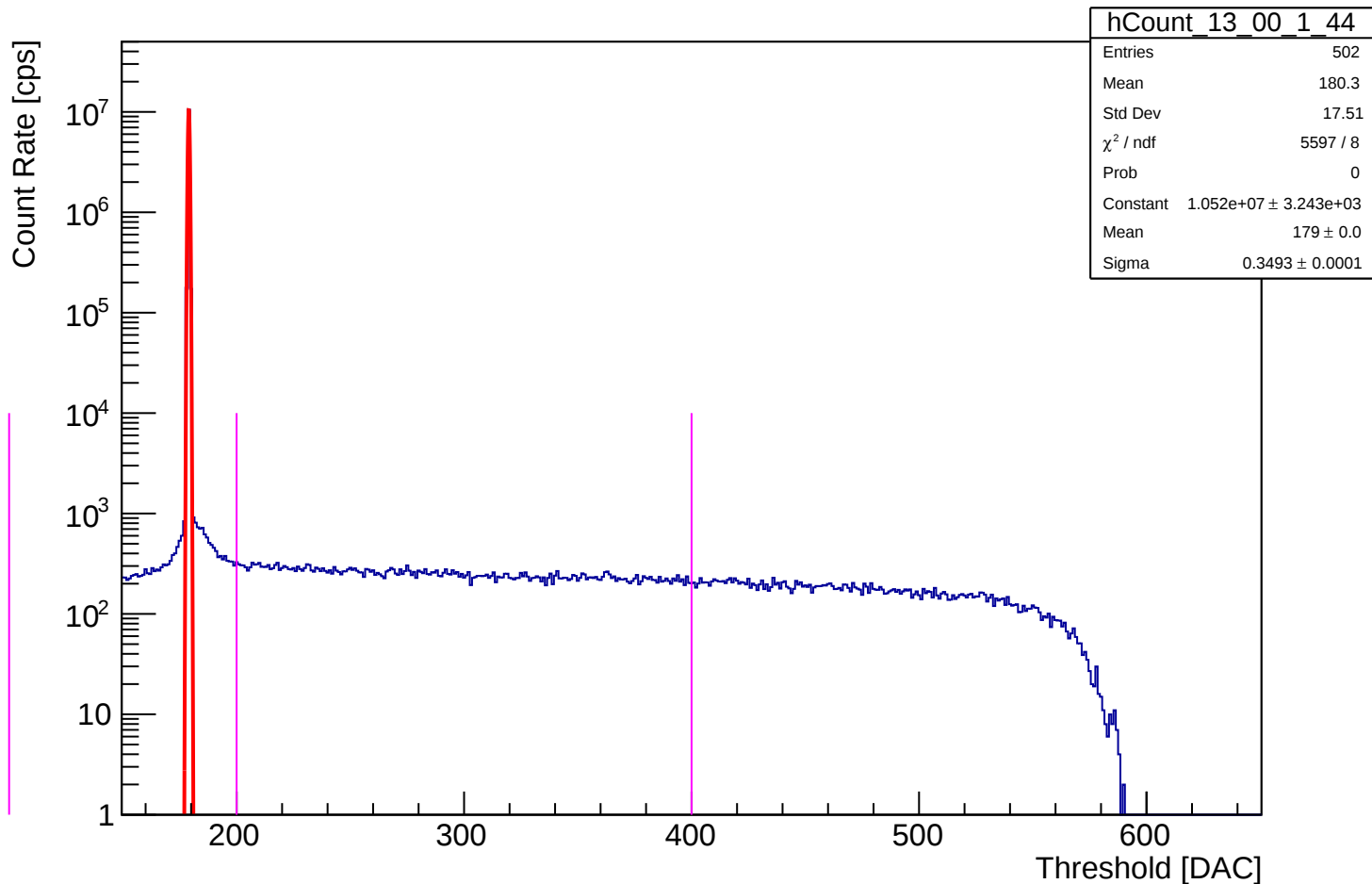
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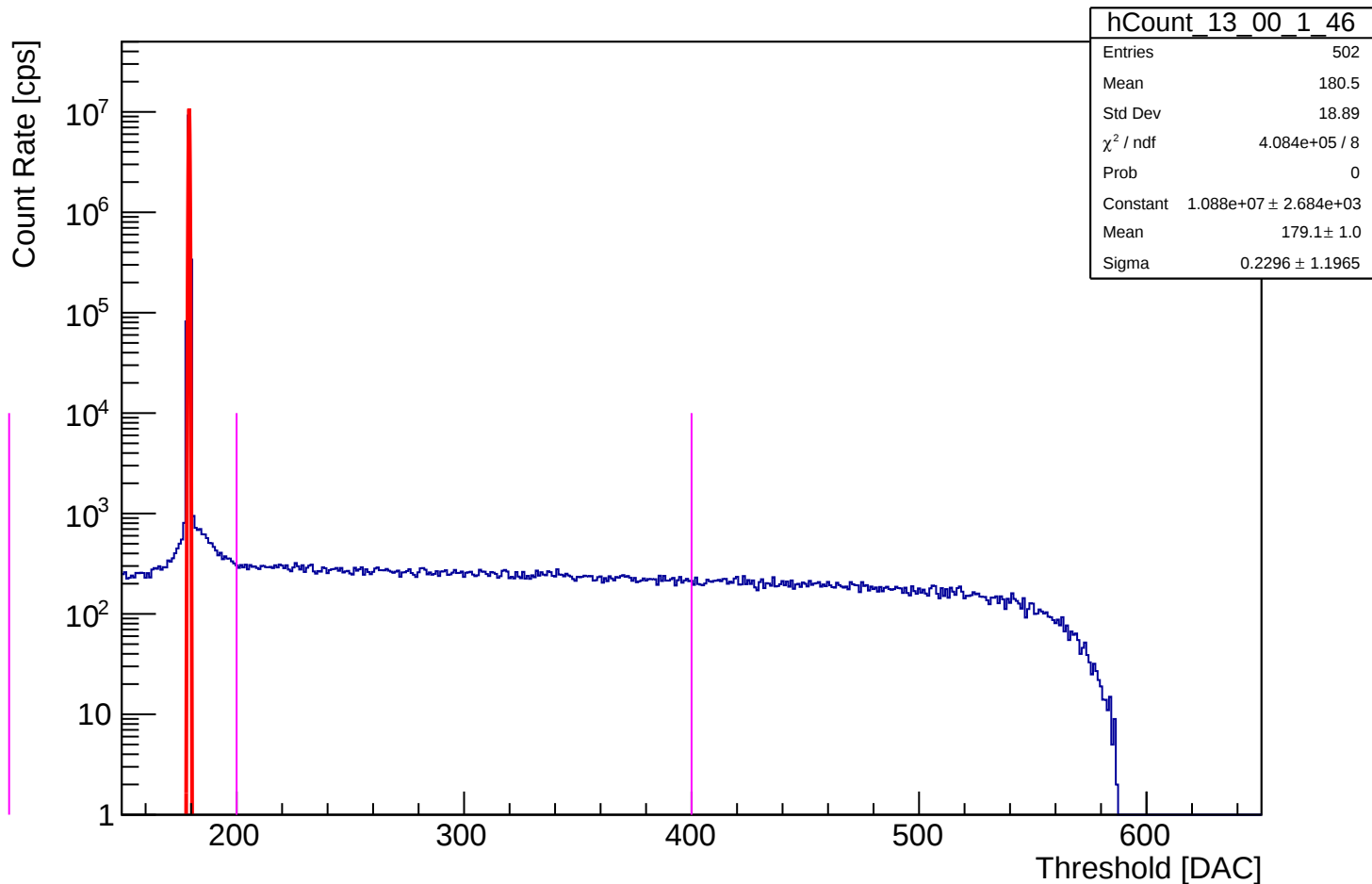
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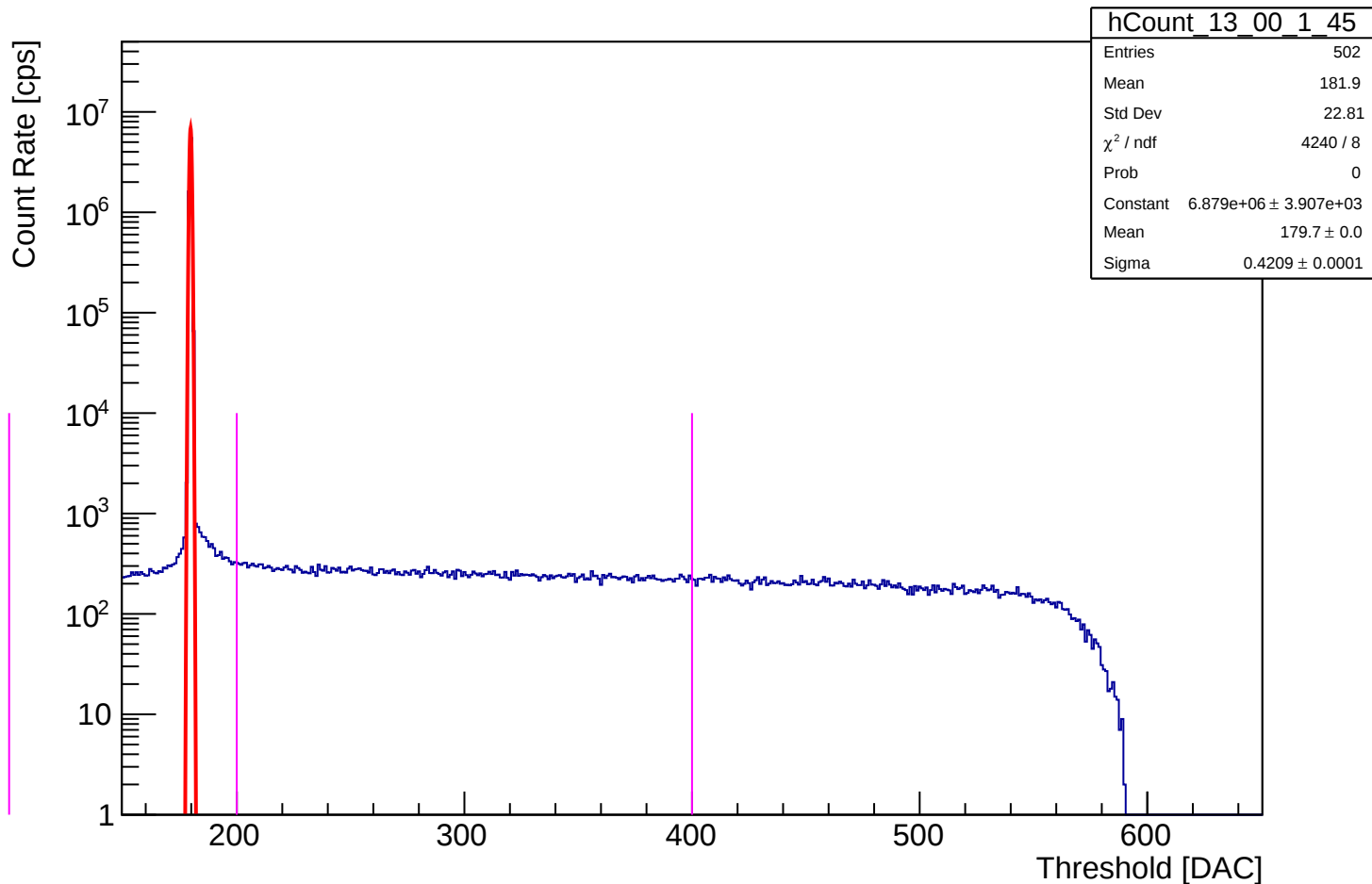
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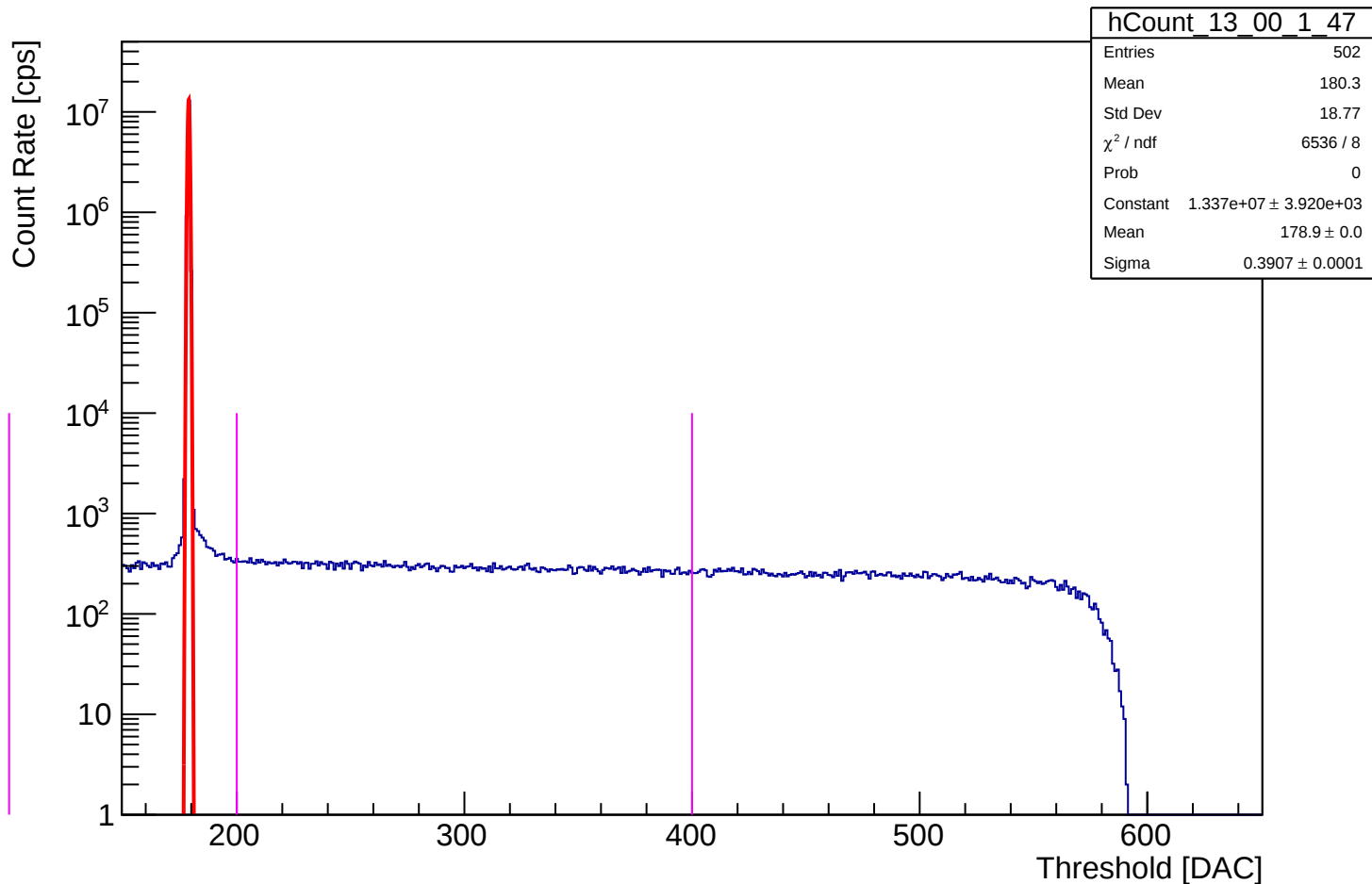
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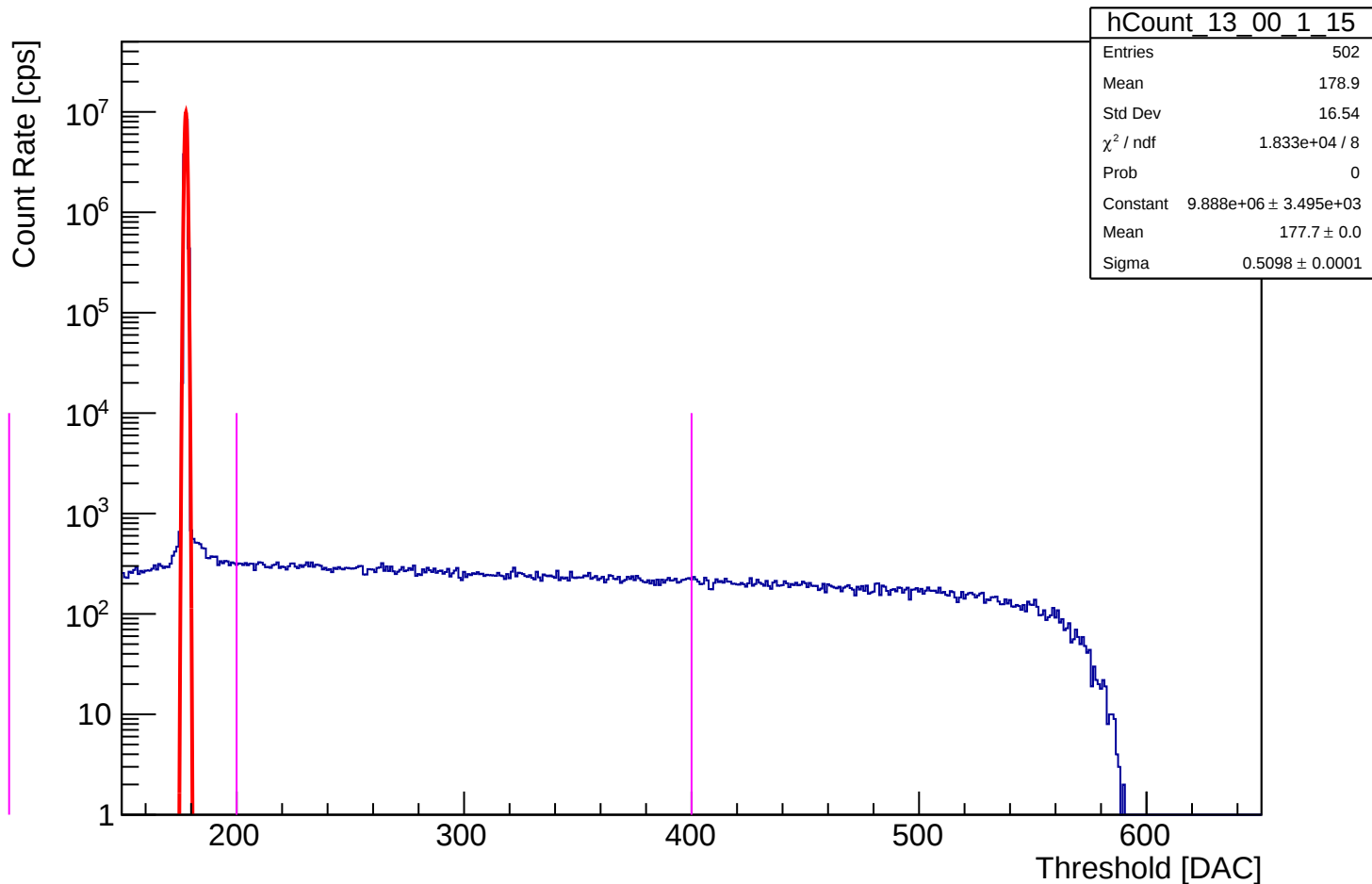
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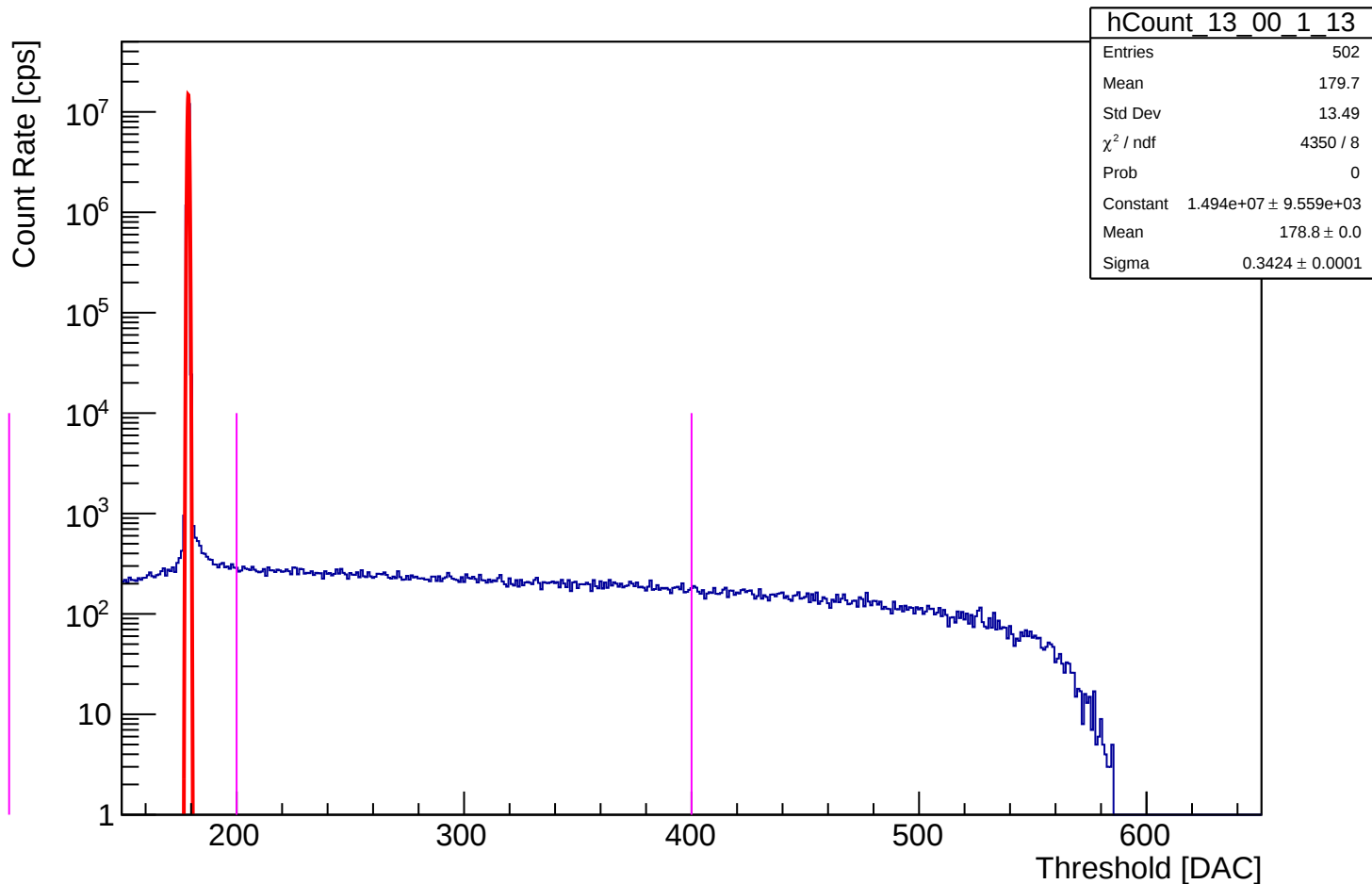
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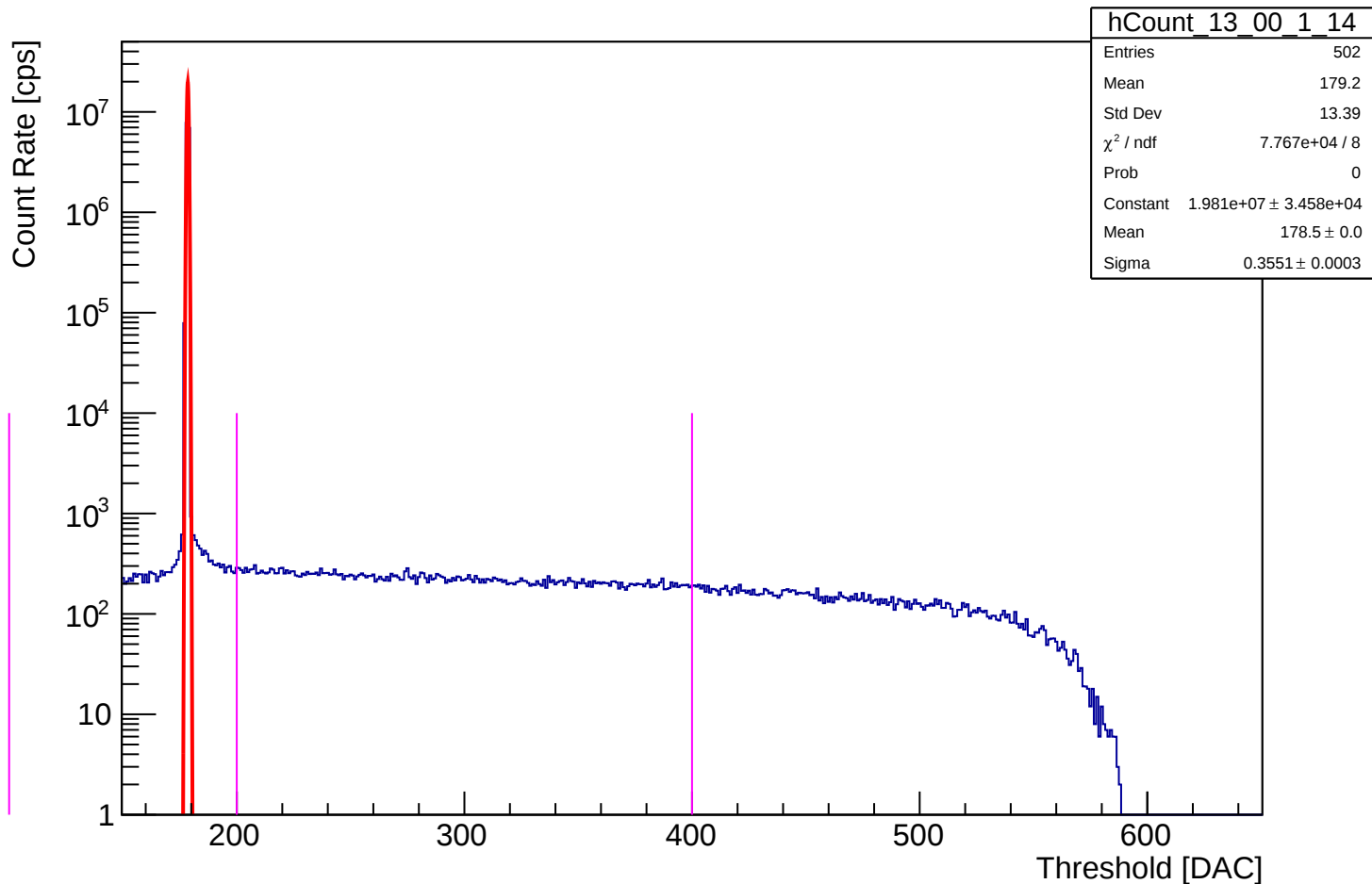
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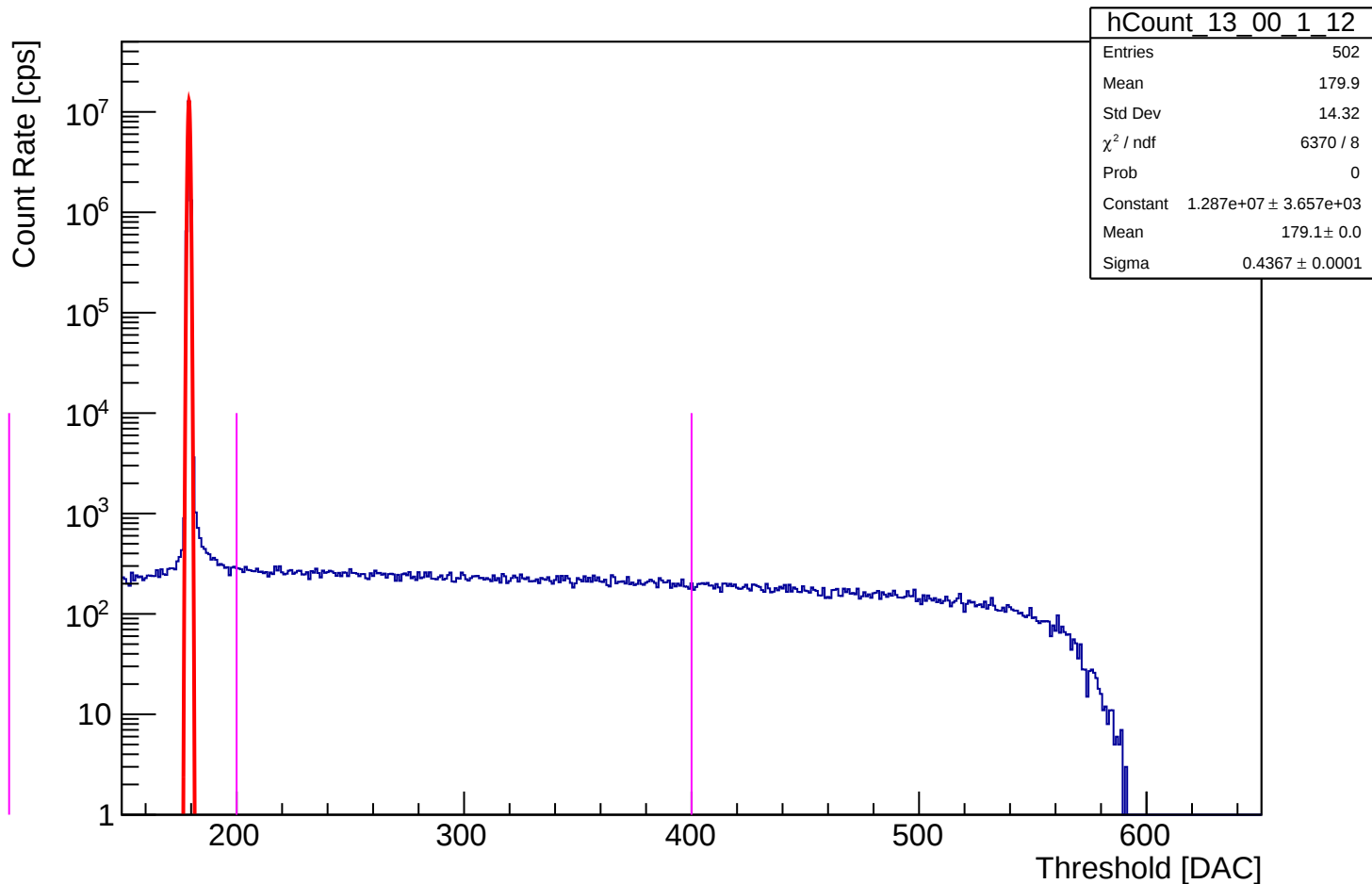
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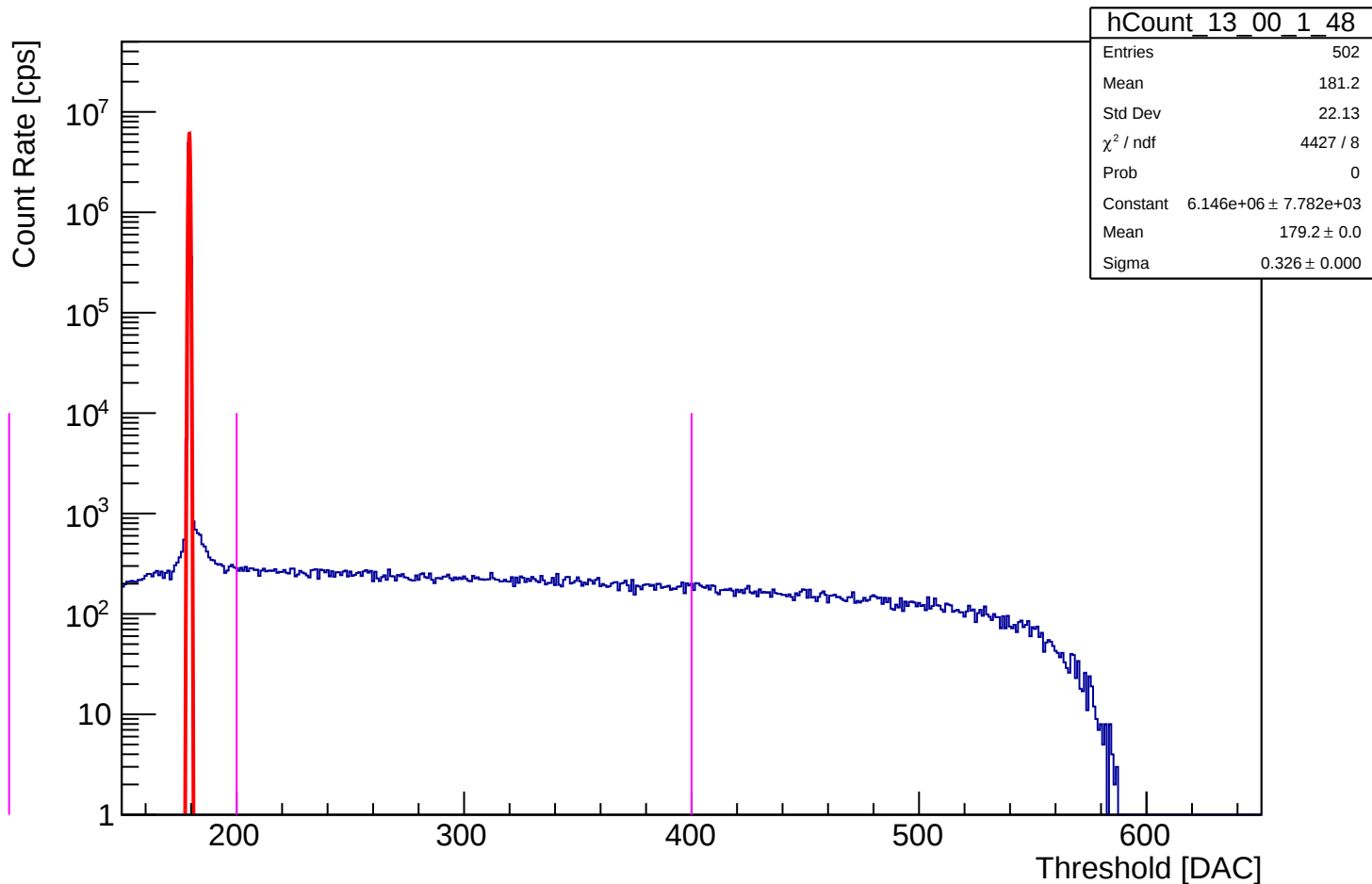
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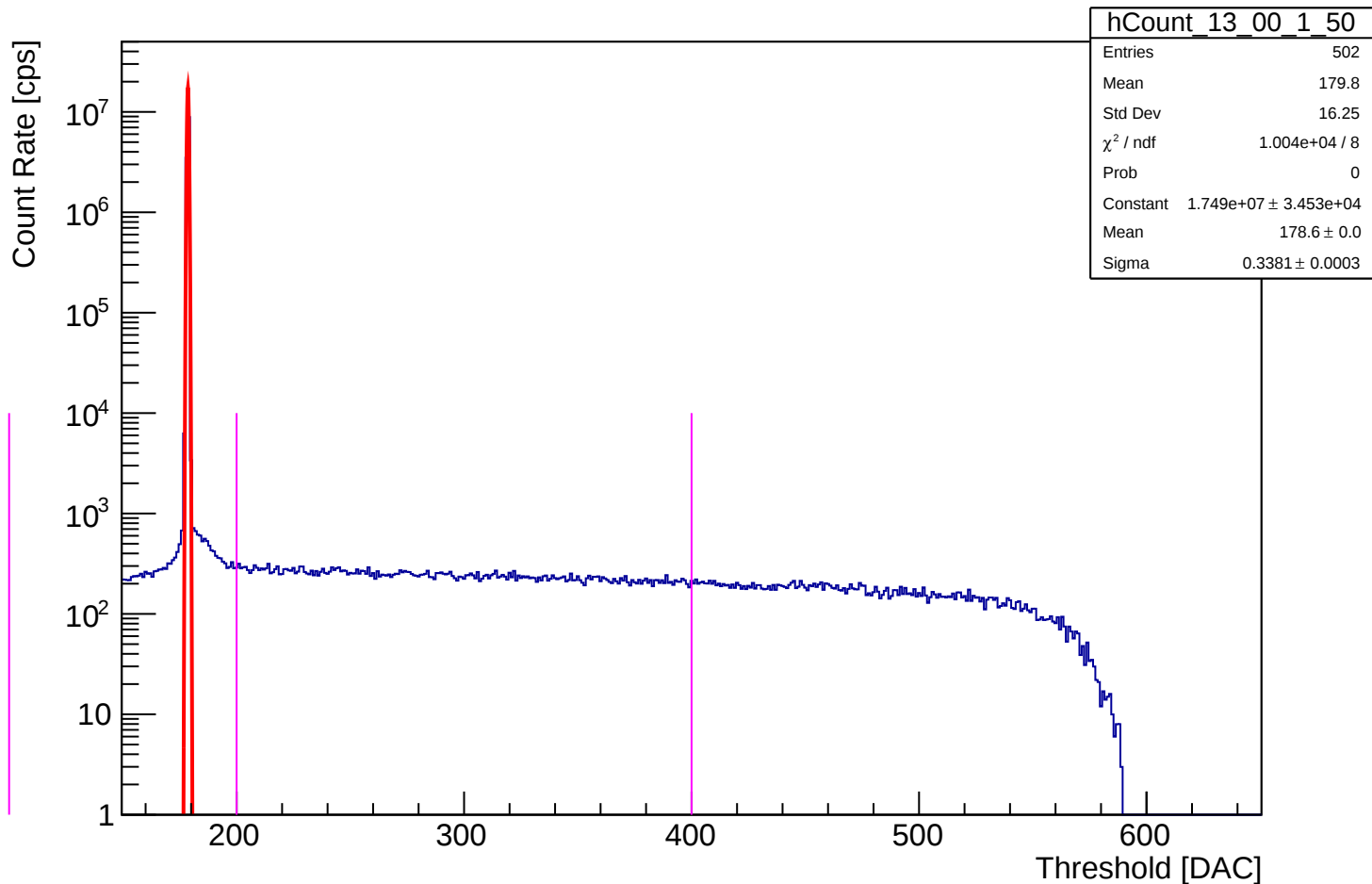
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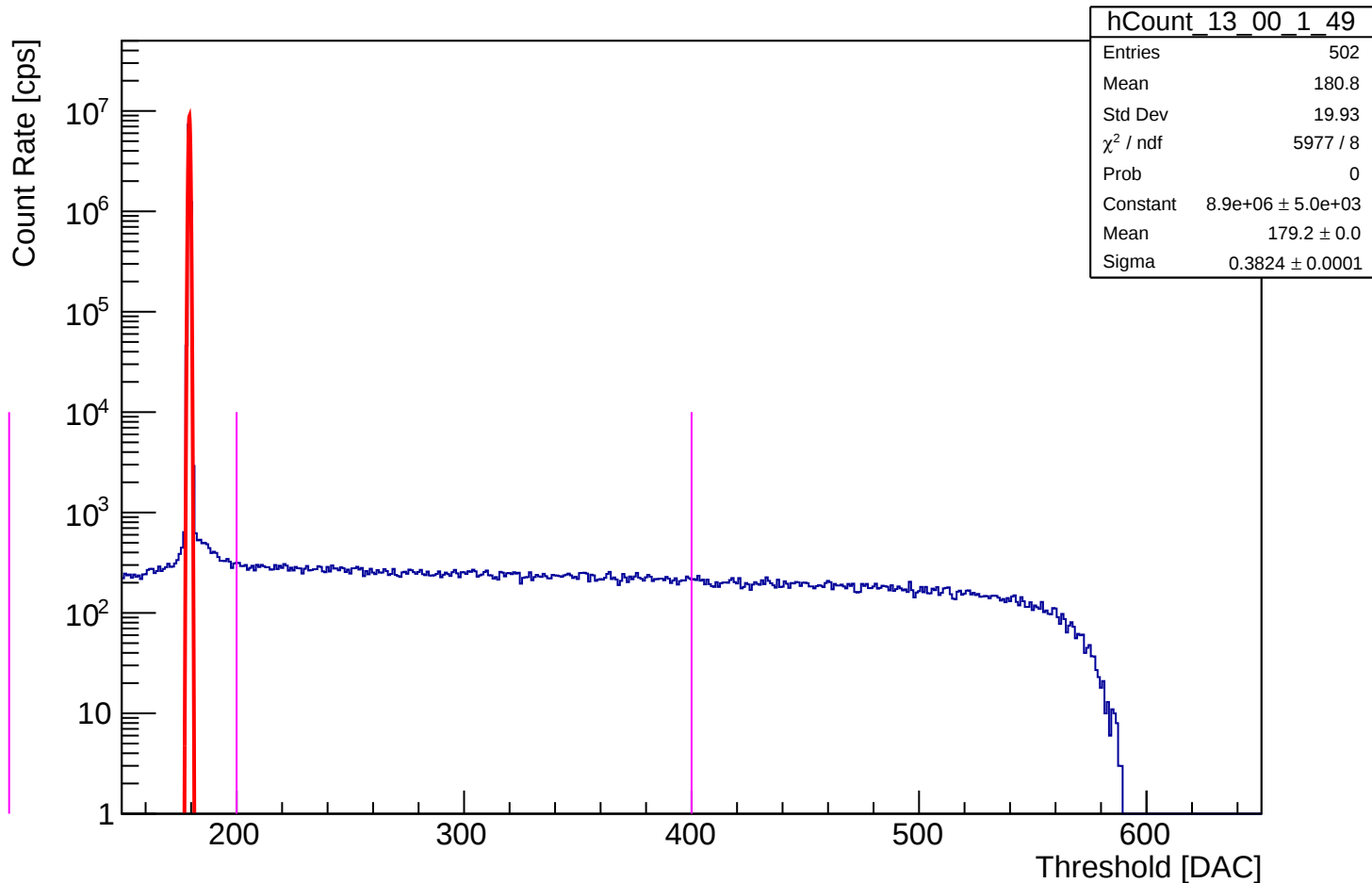
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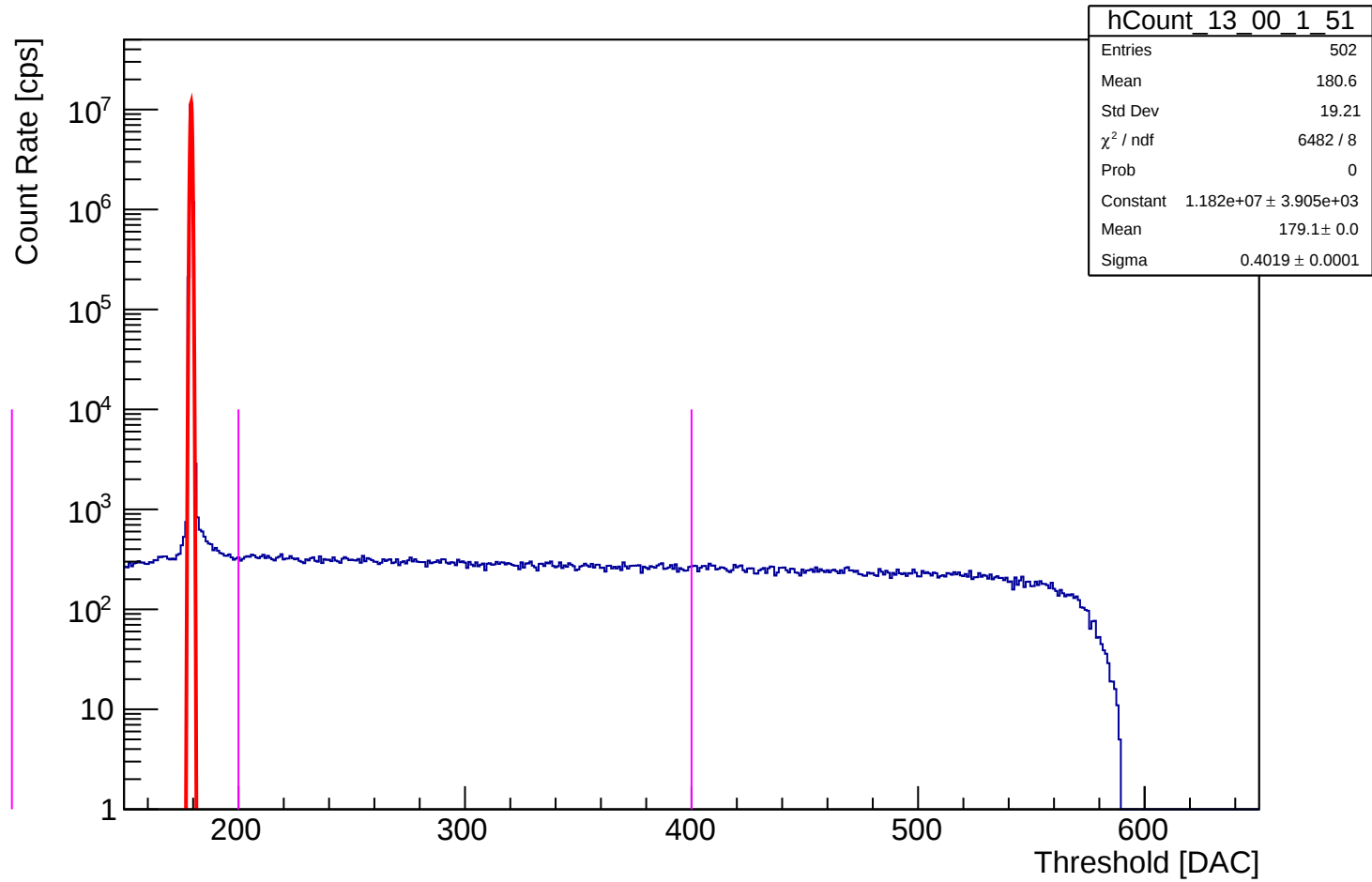


Row 1 Tile 1 Pmt 2 Pixel 38 Slot 13 Fiber 0 Asic 1 Channel 50

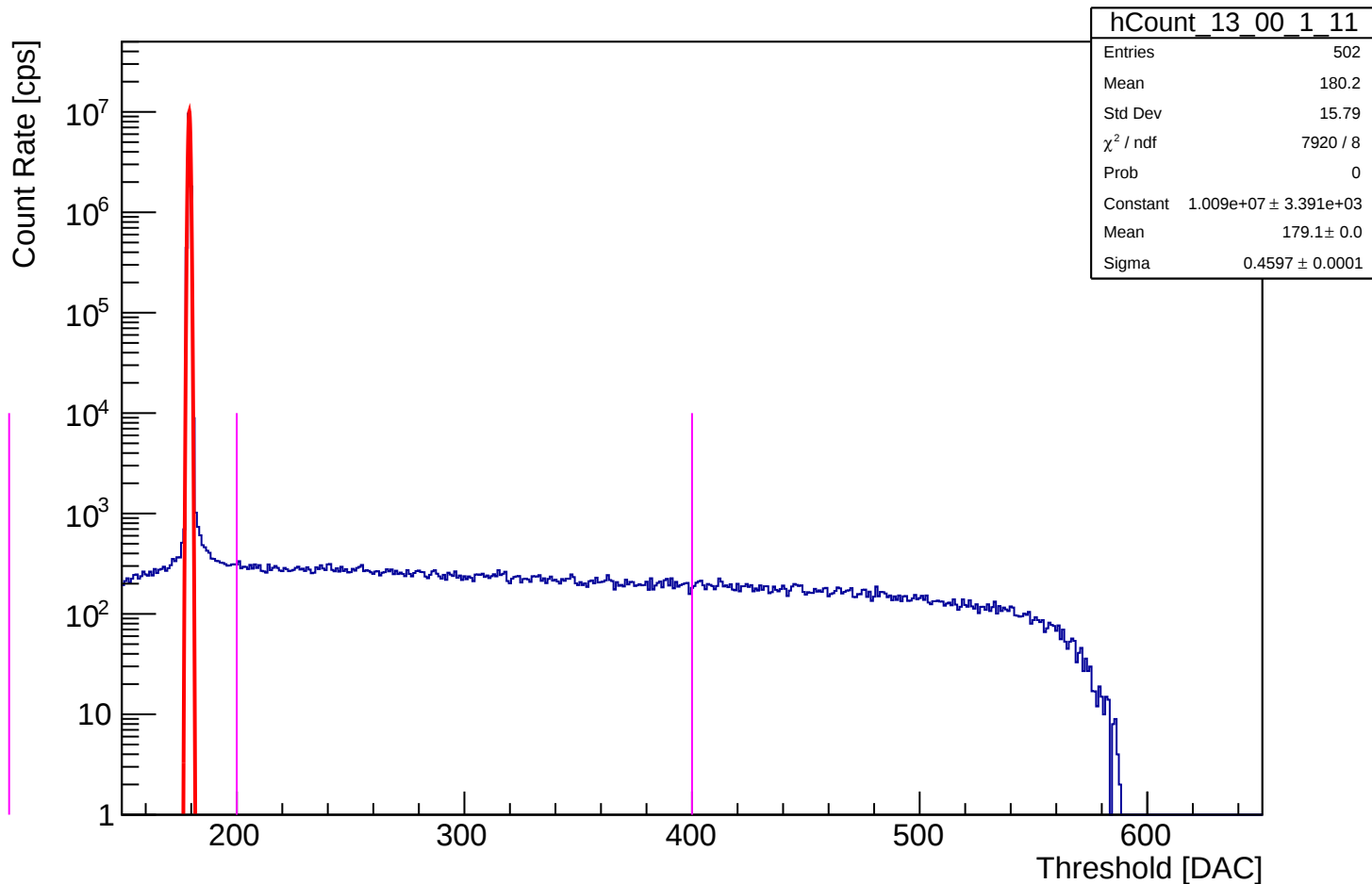


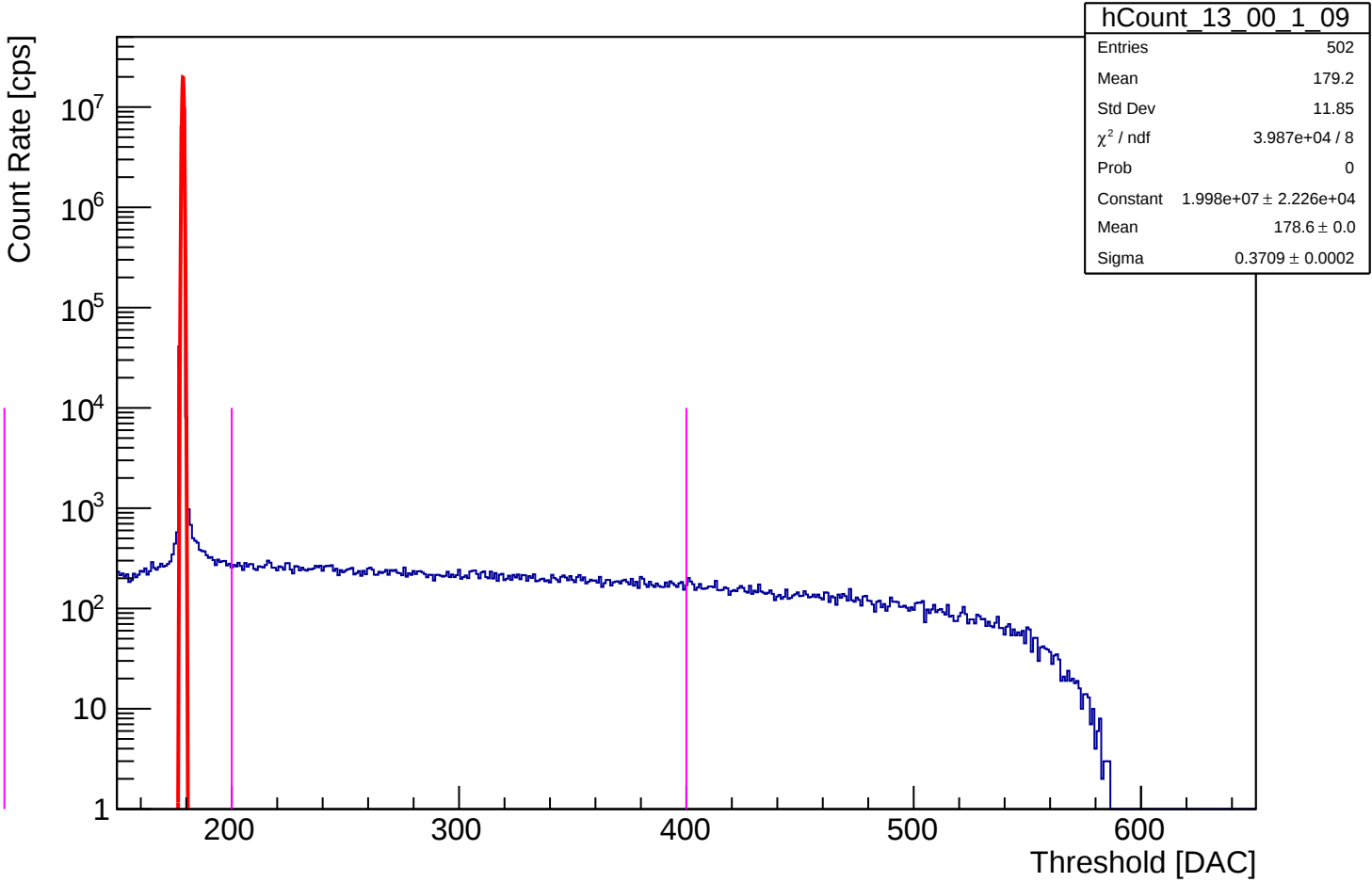
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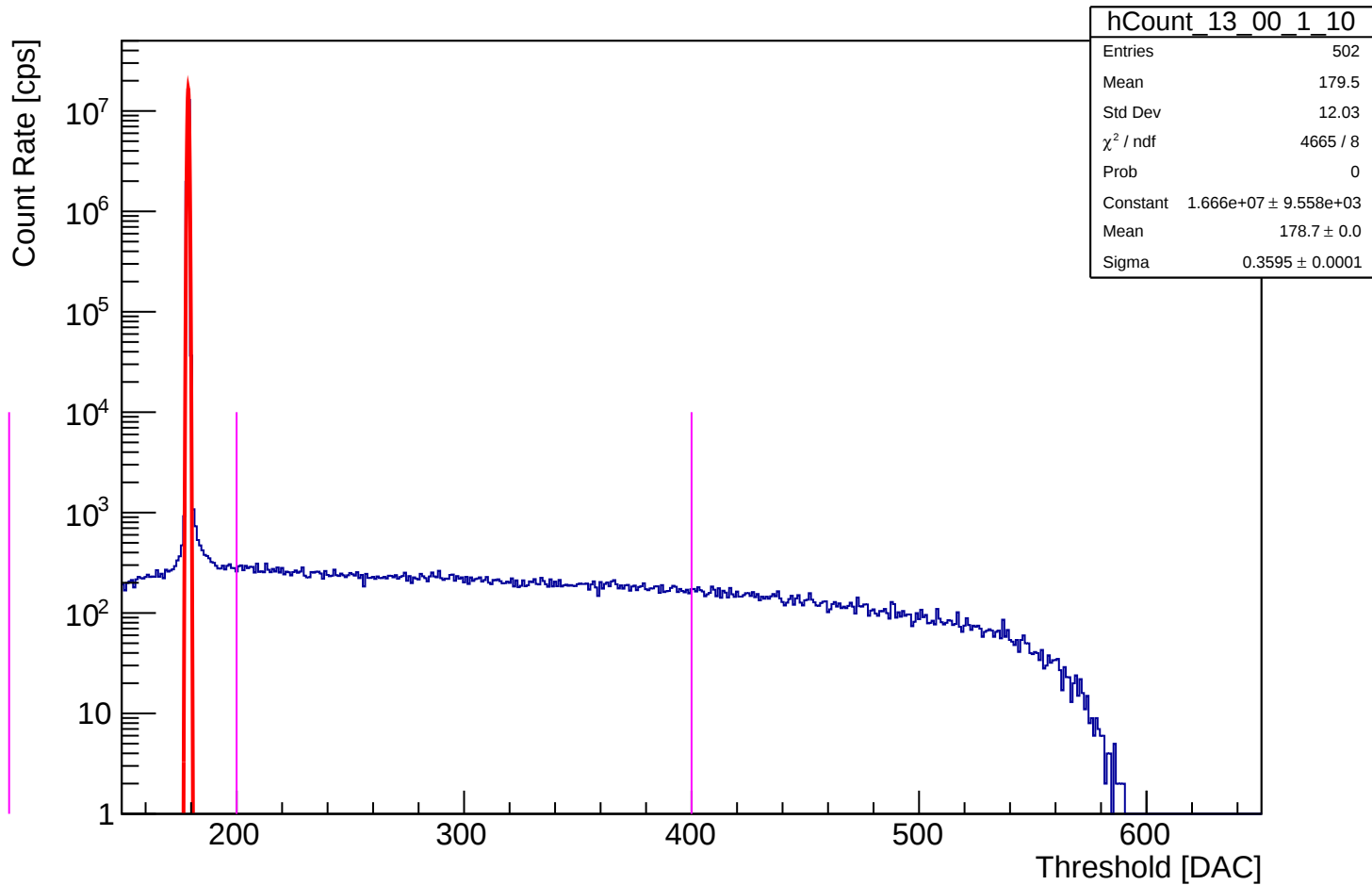


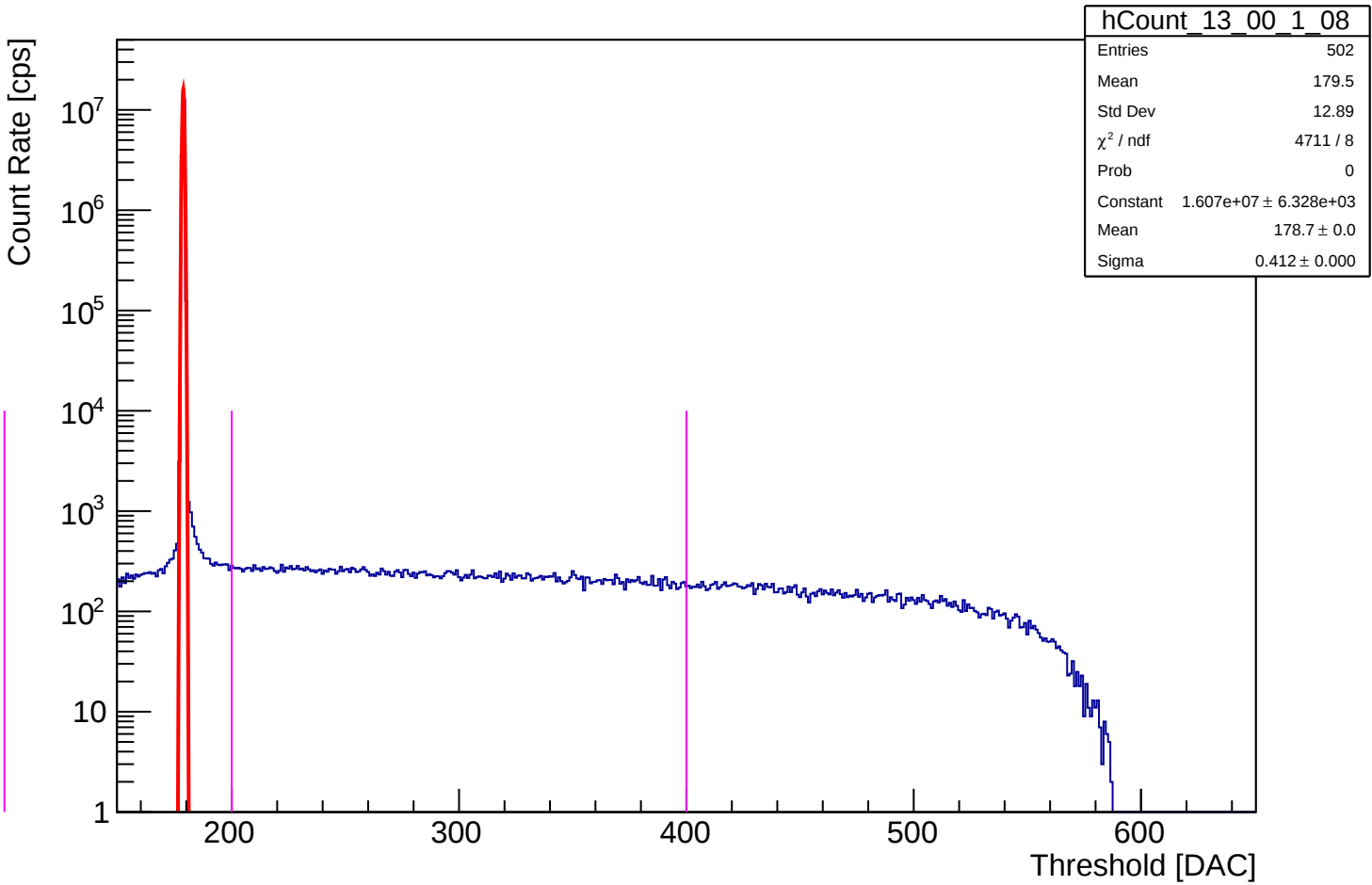
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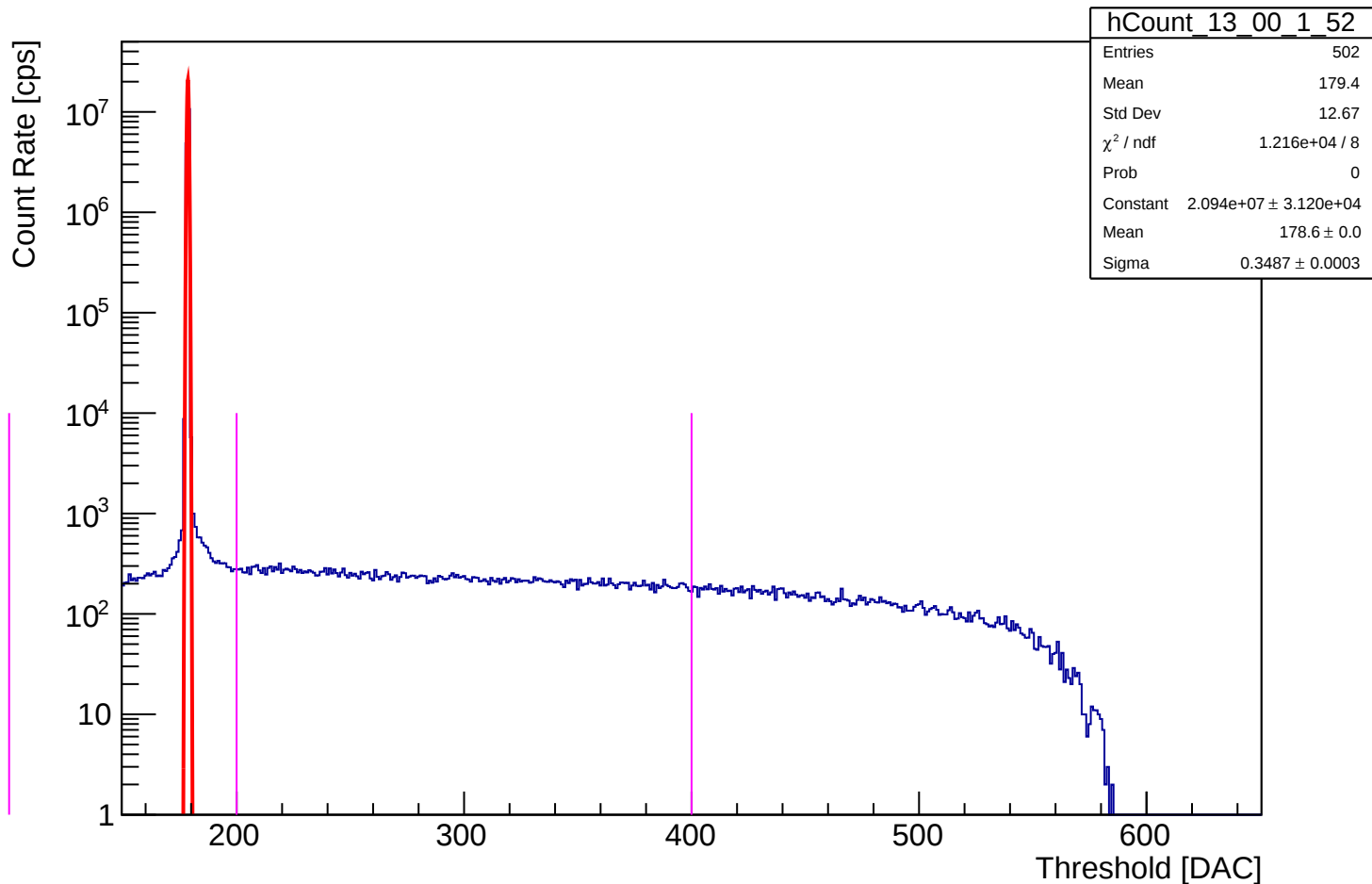


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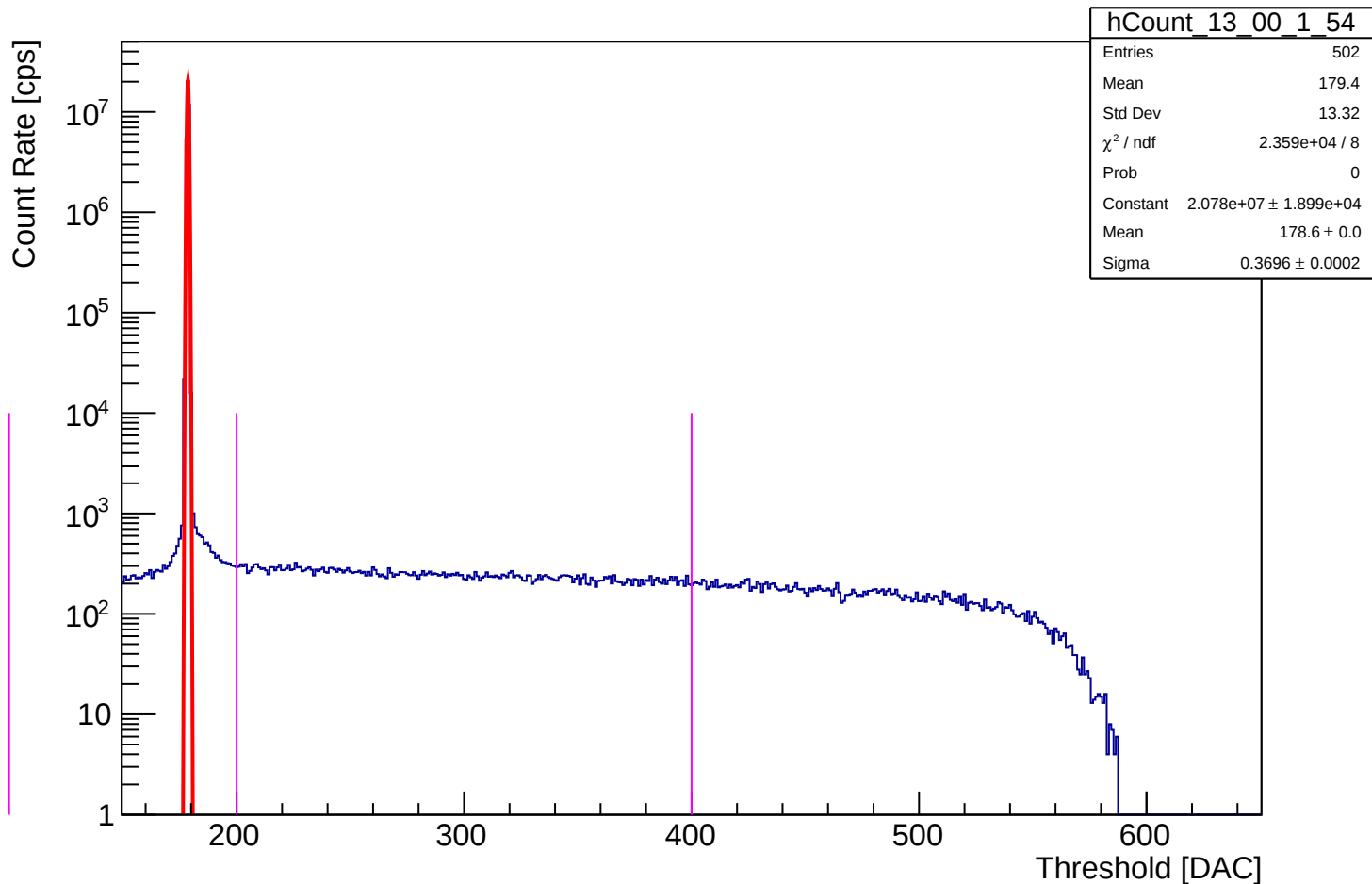




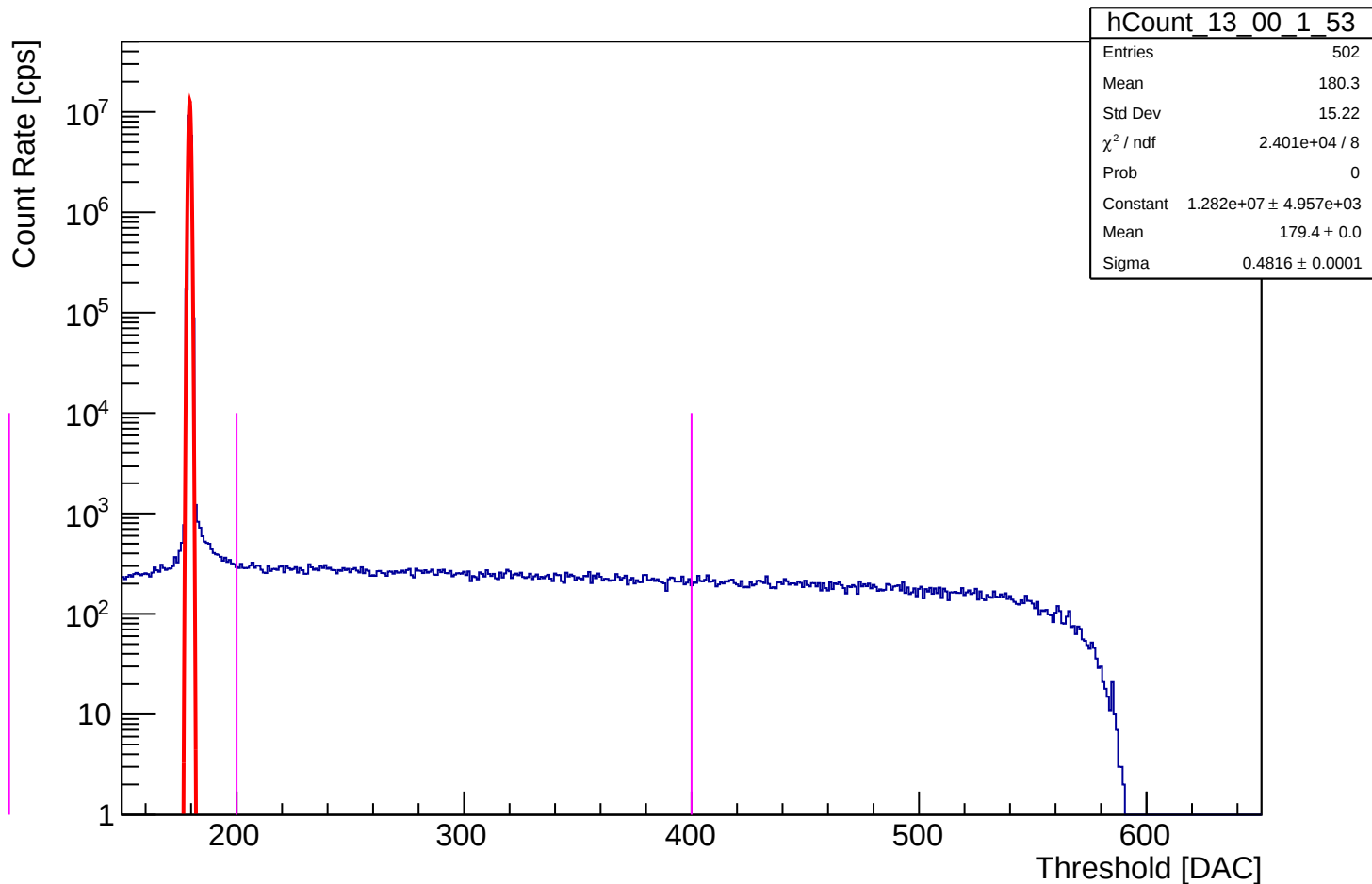
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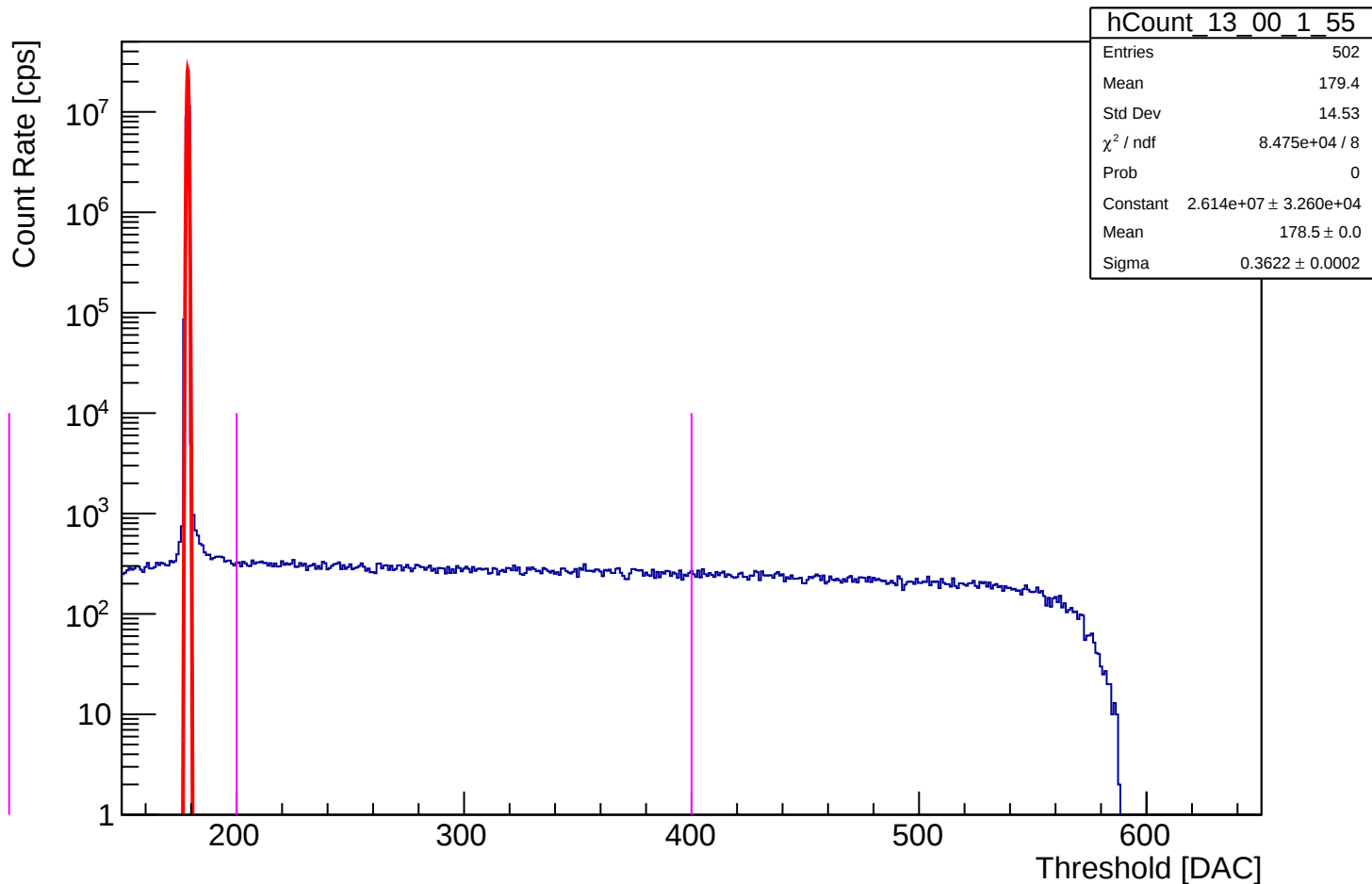
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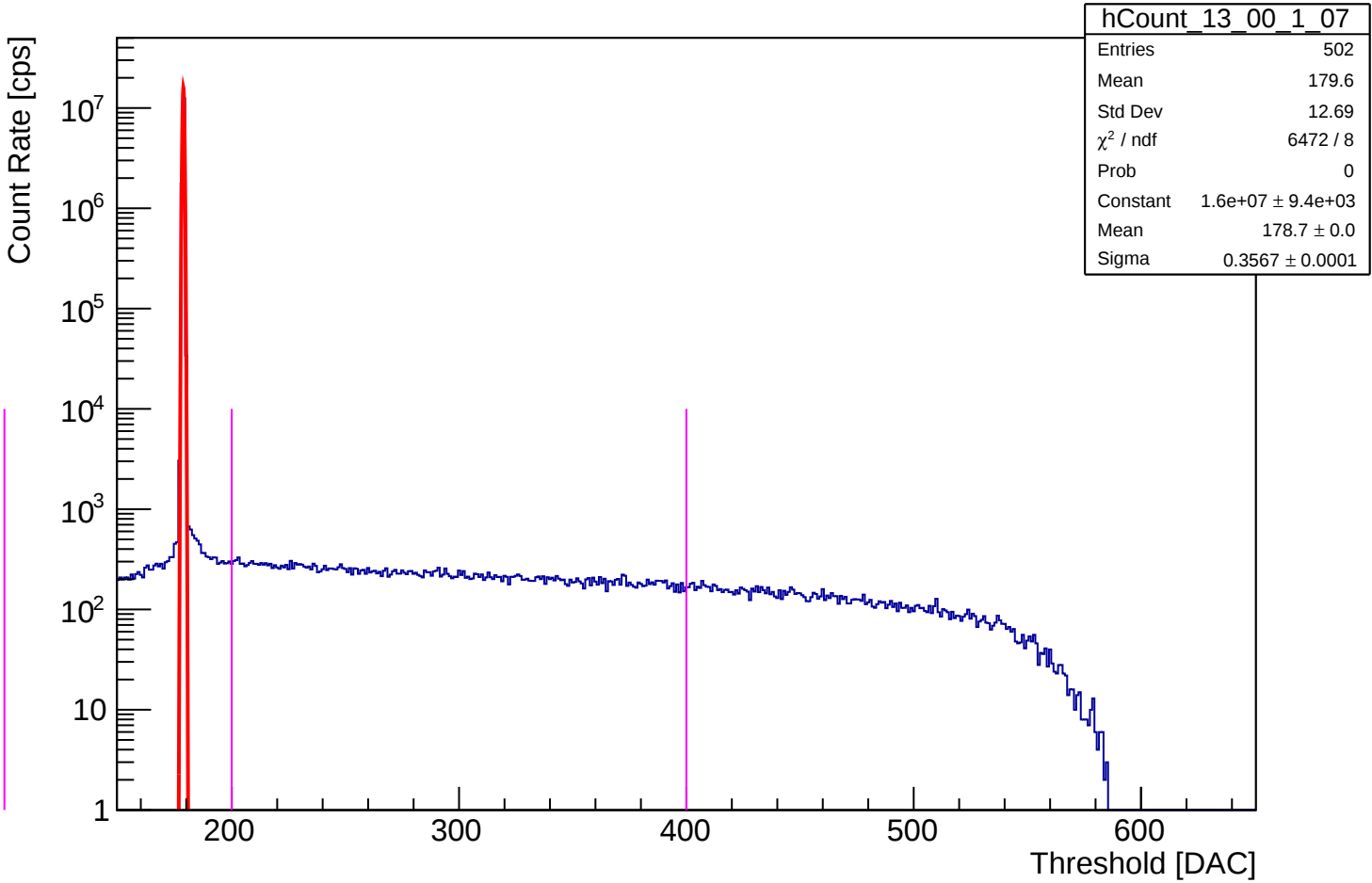


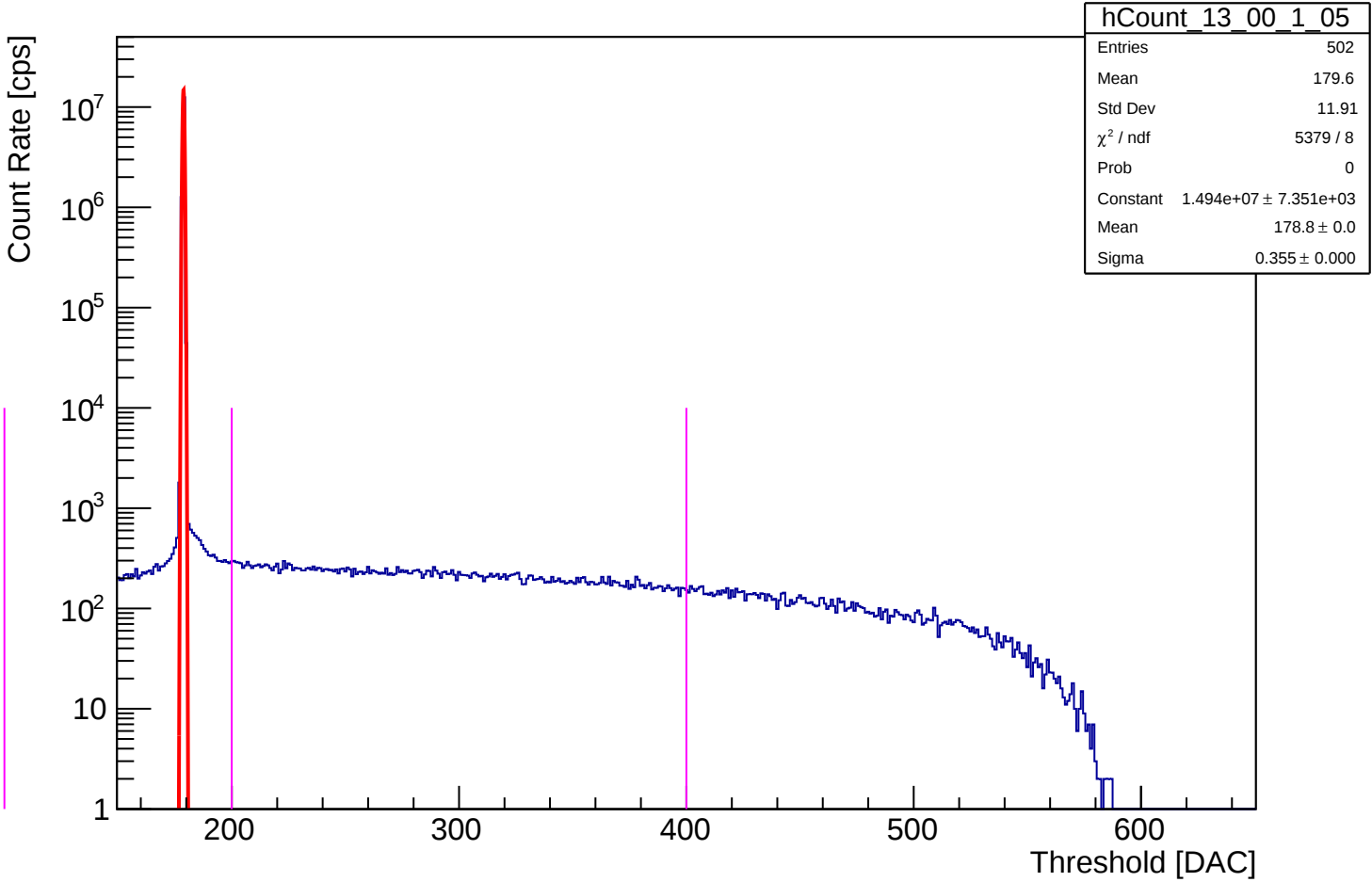
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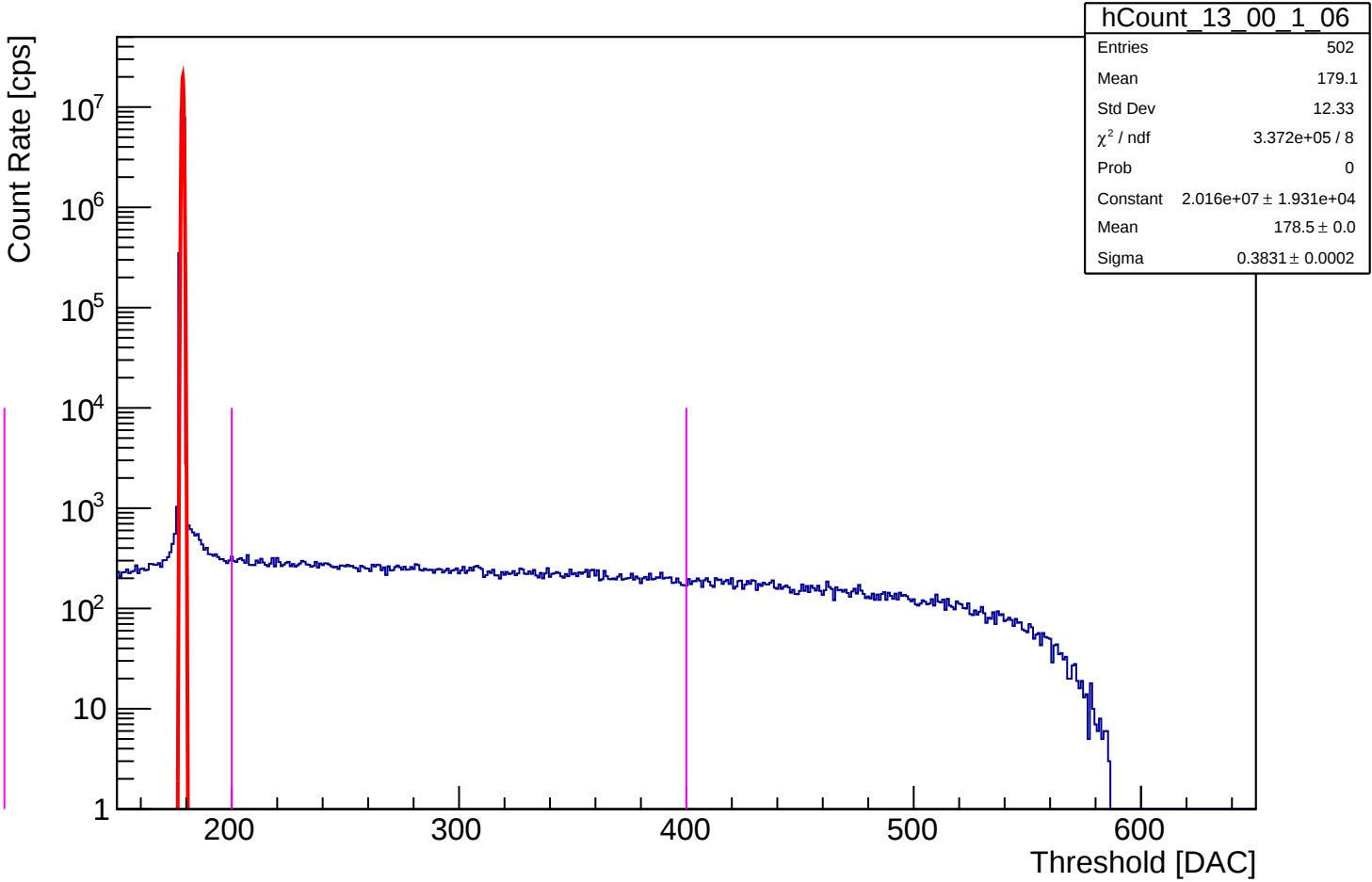


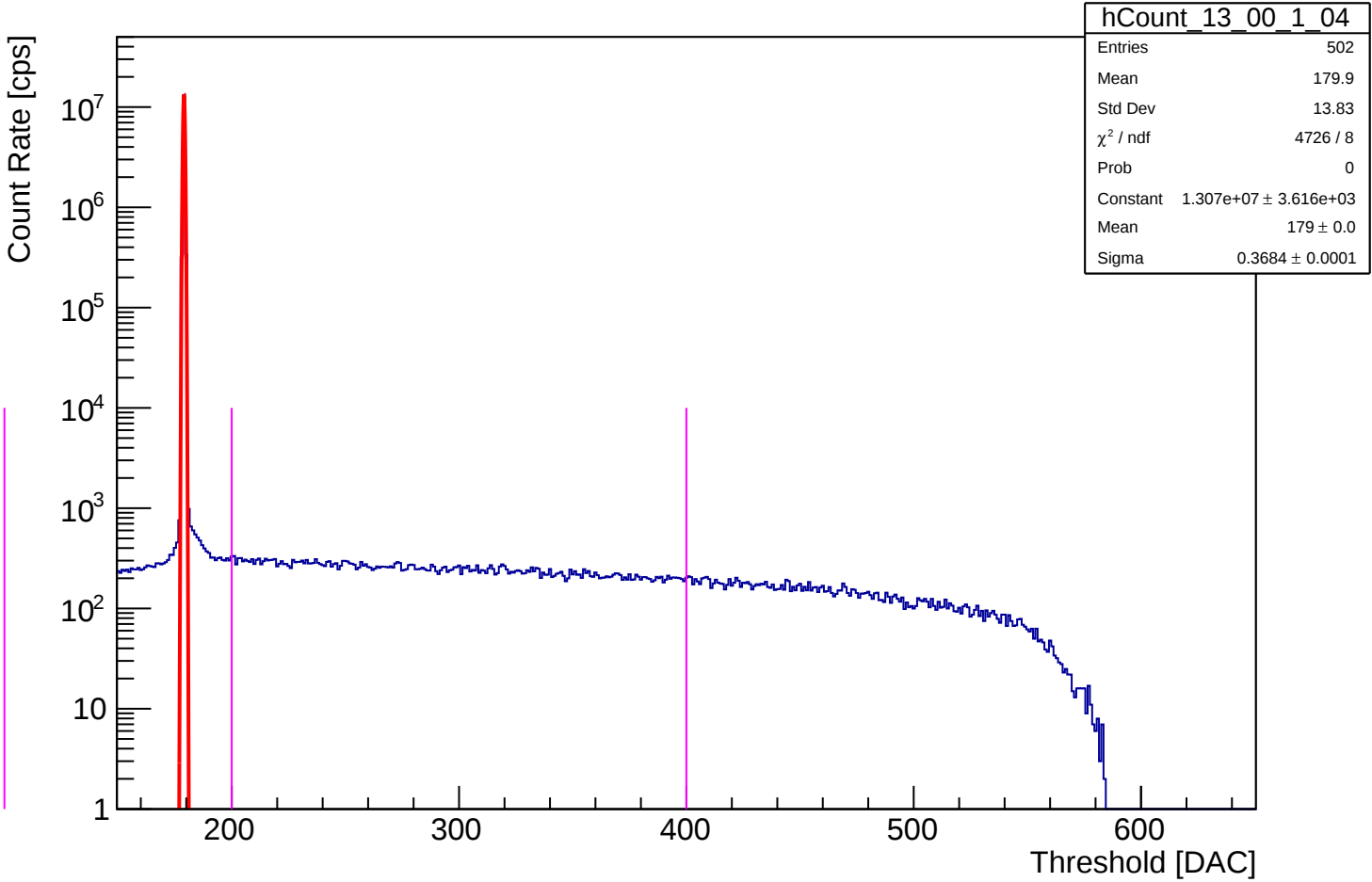
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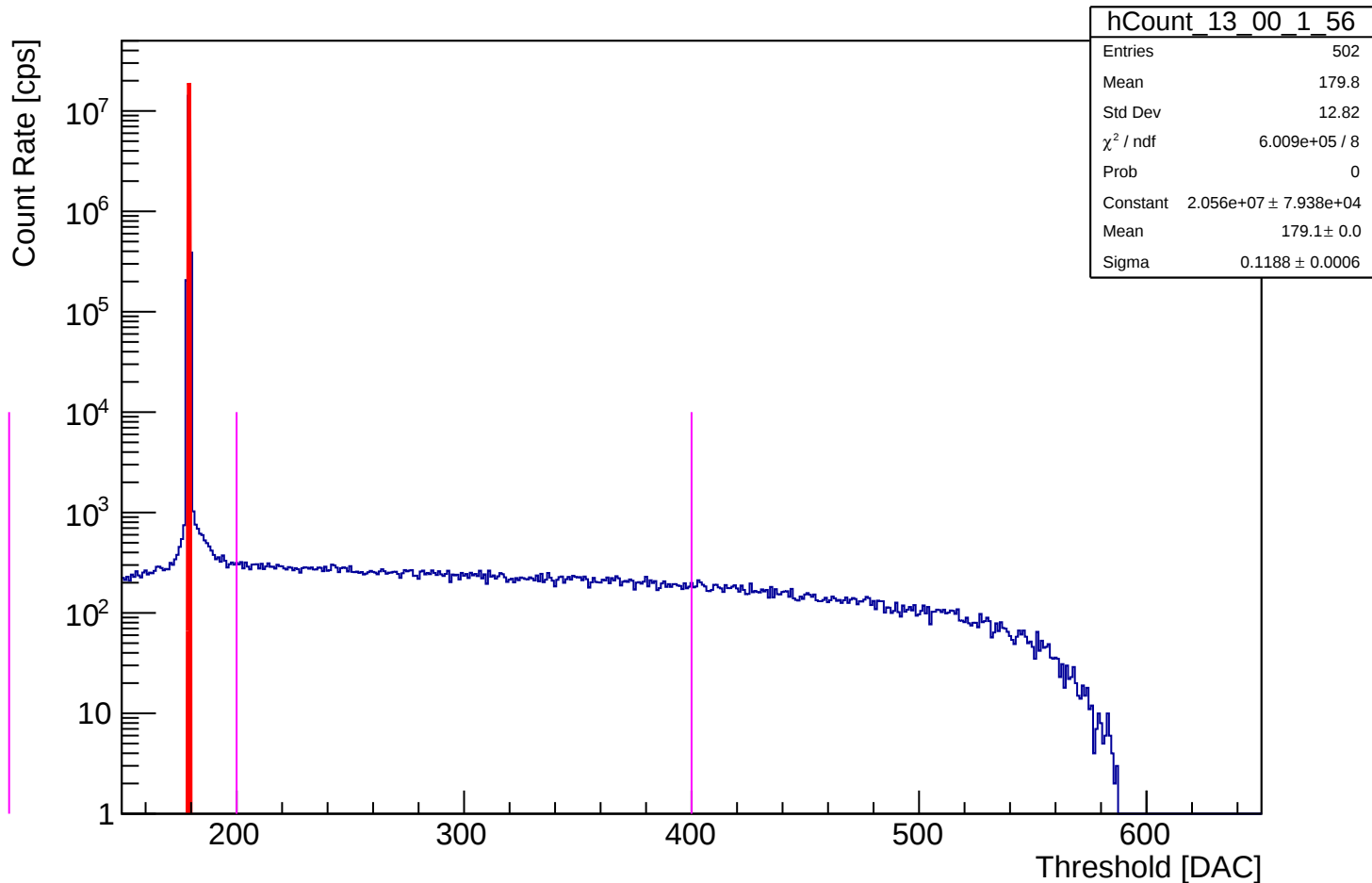




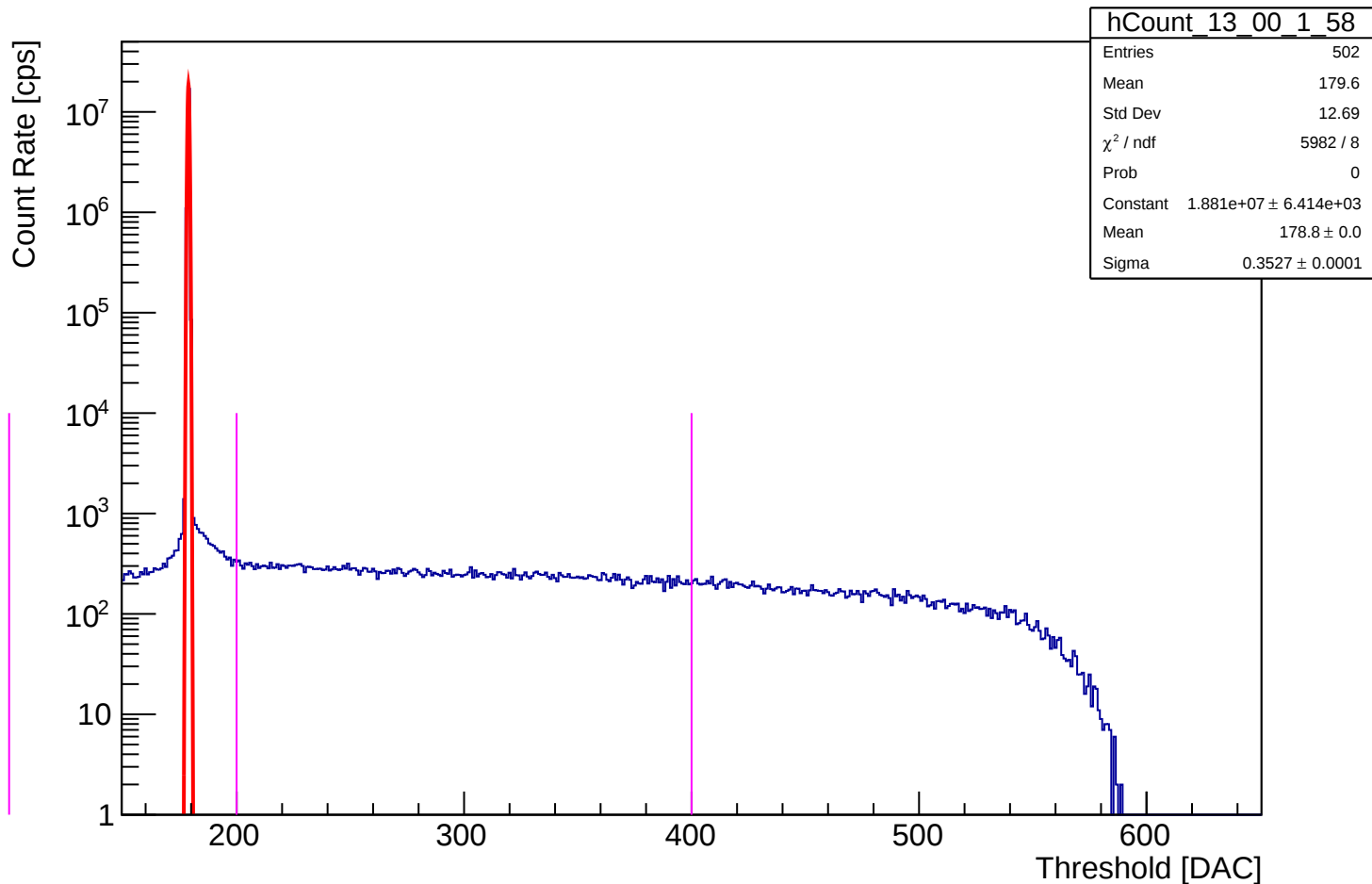




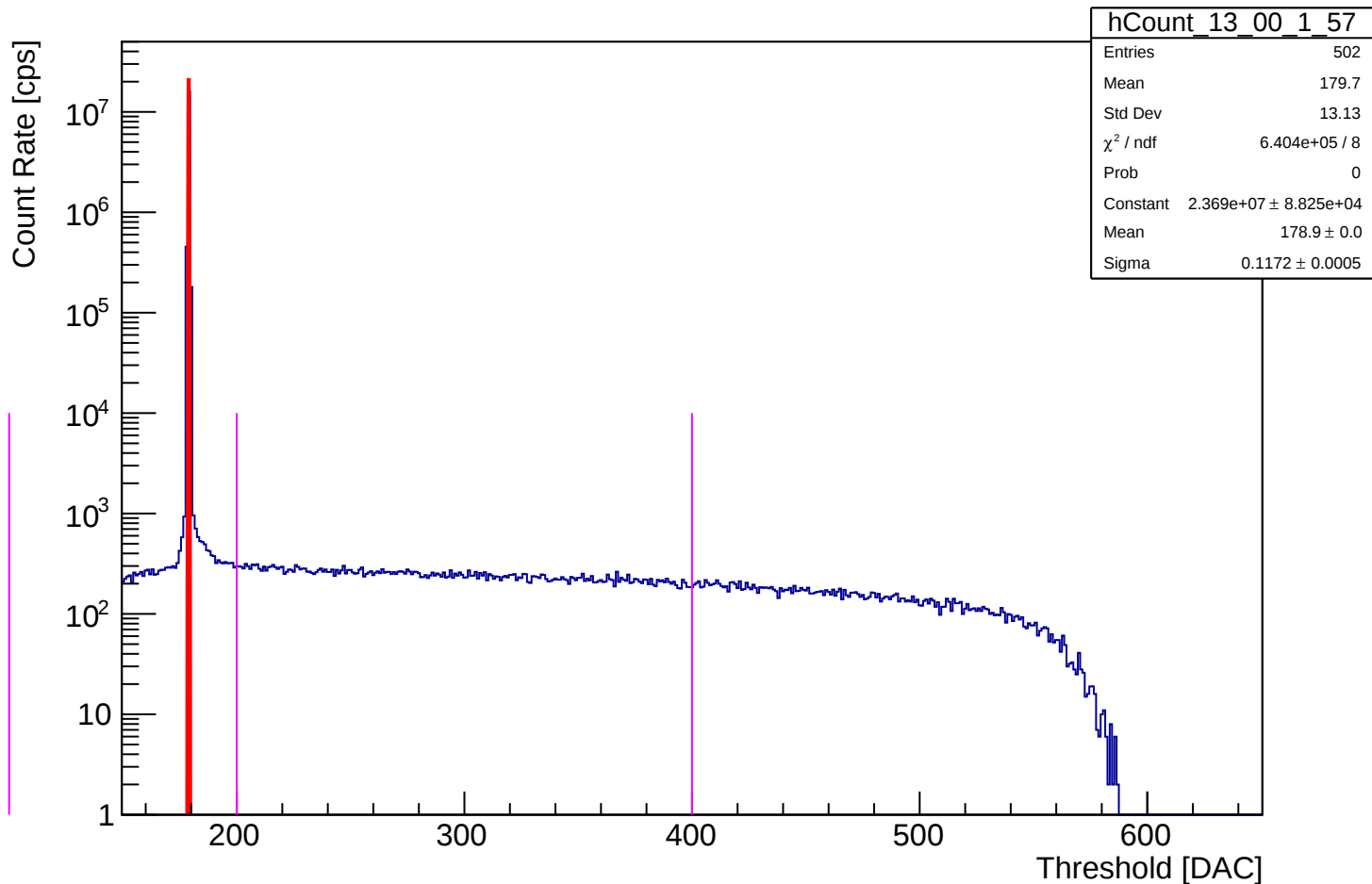
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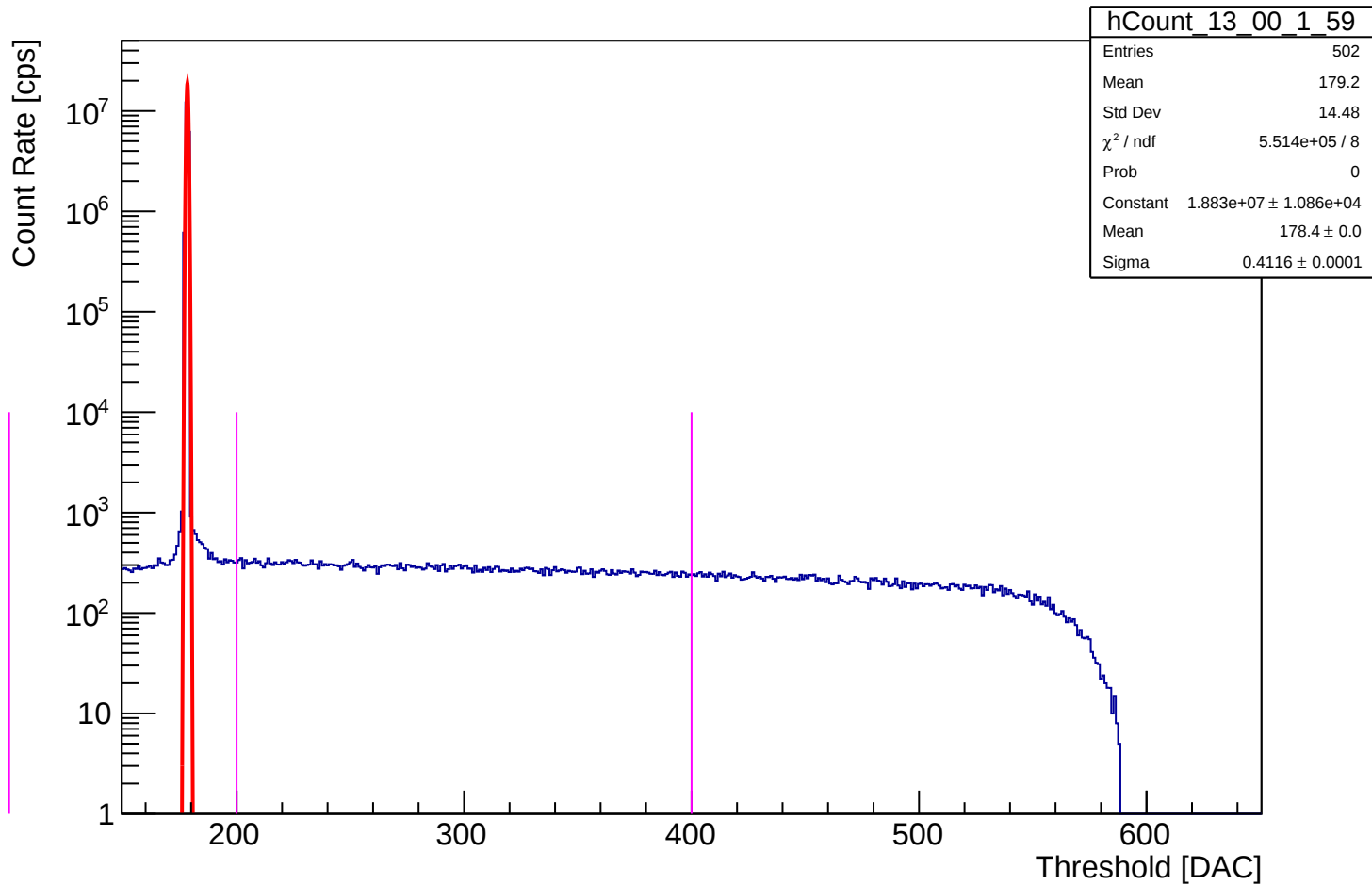
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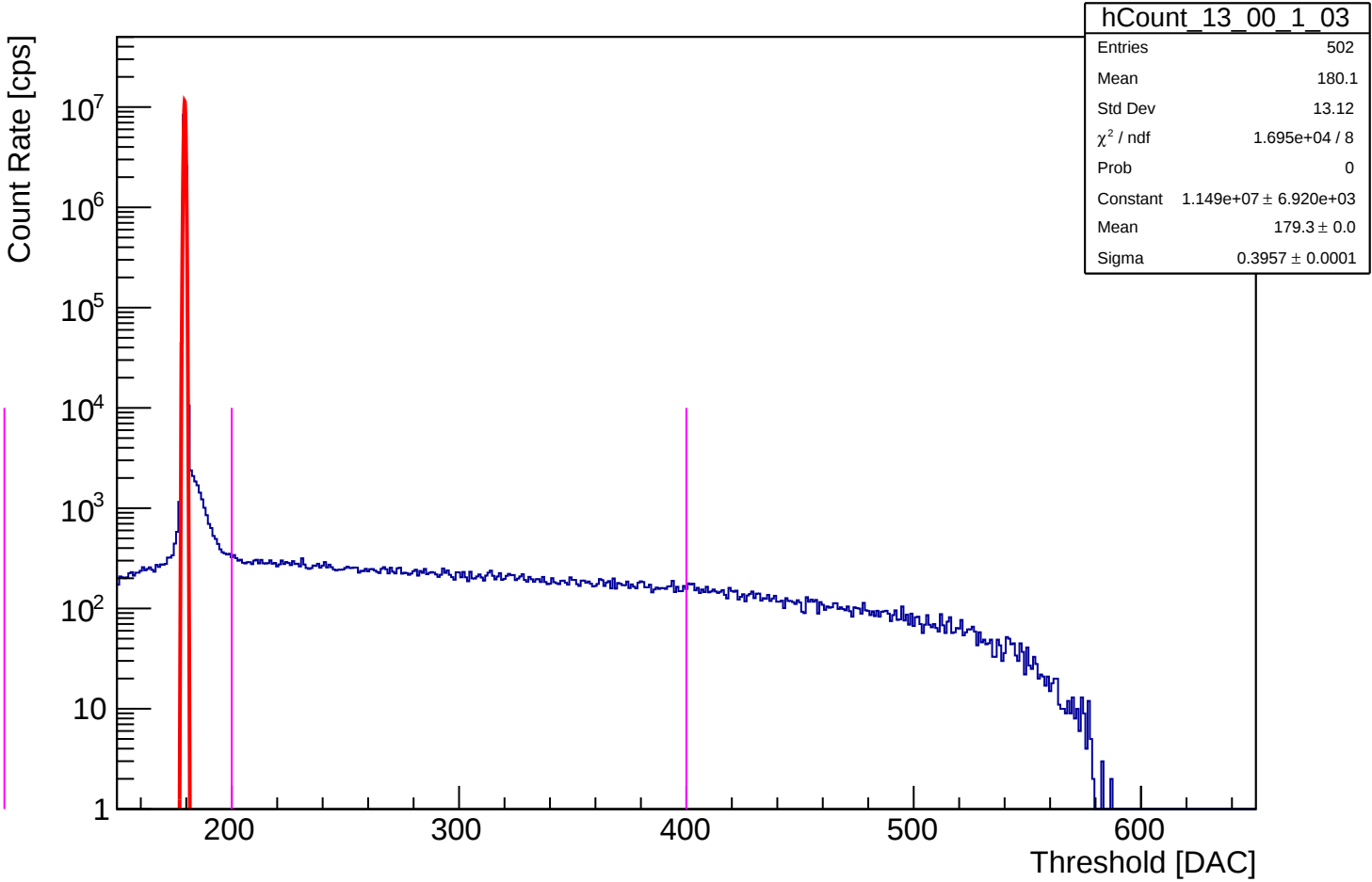


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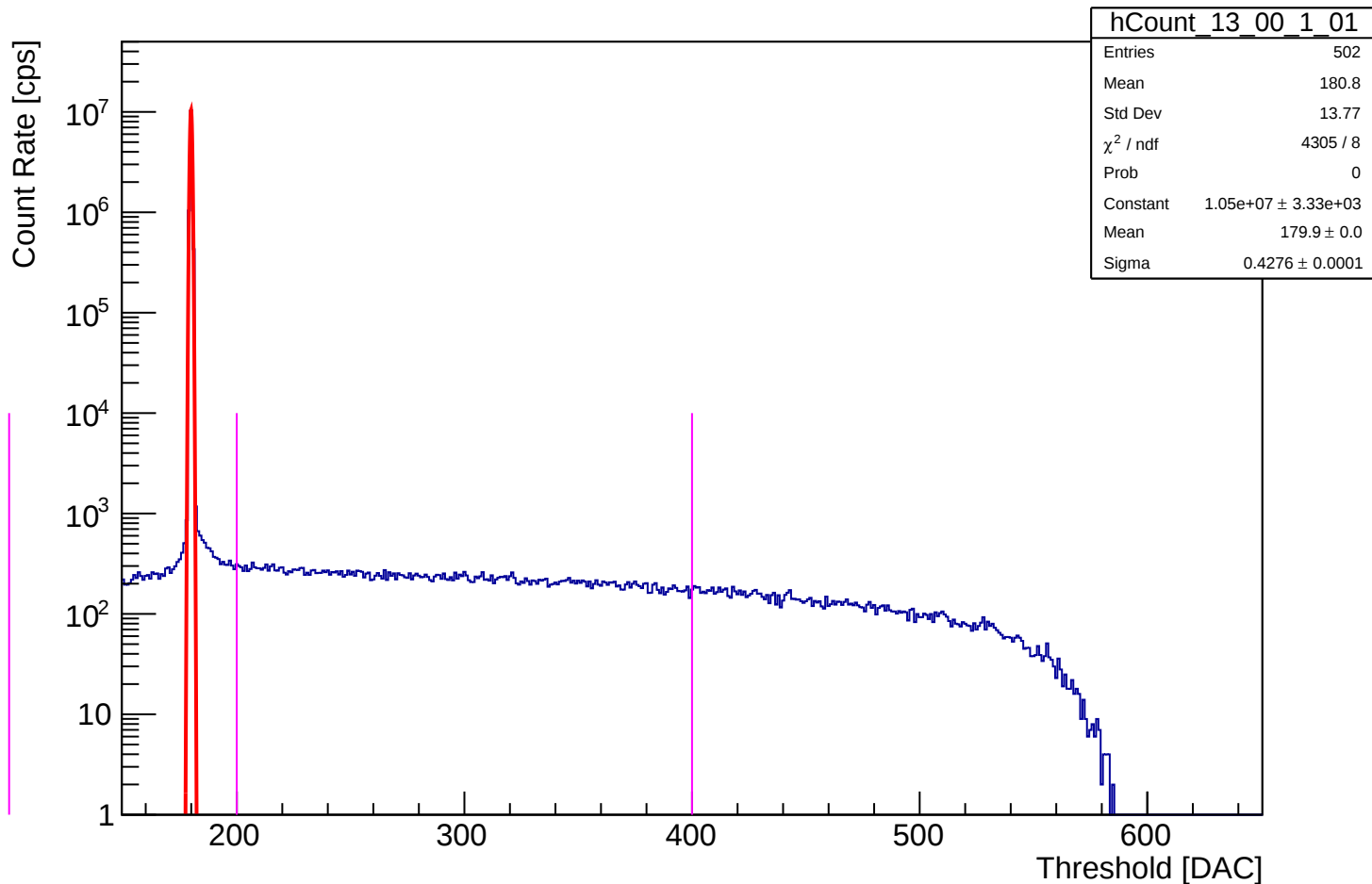


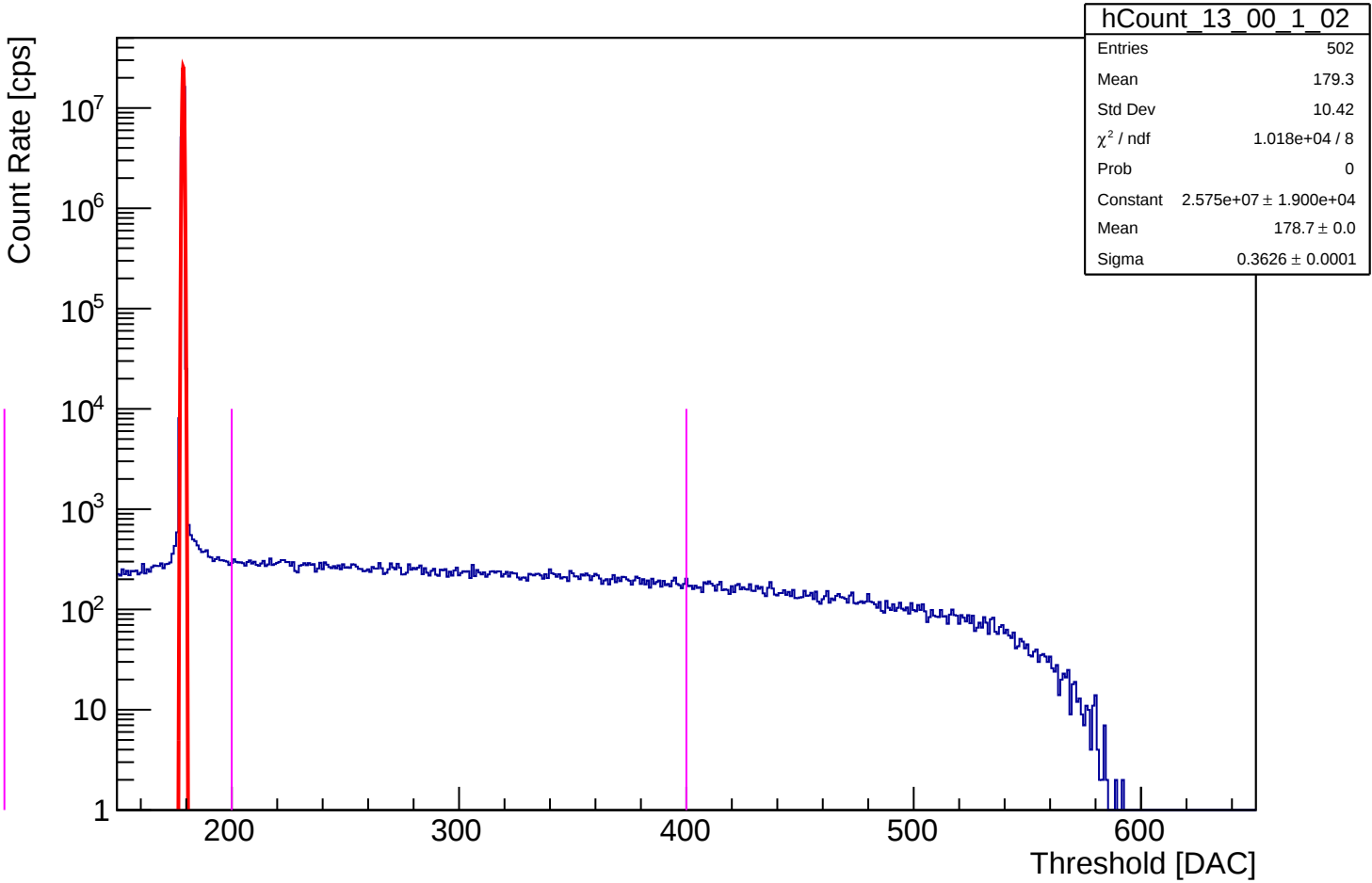
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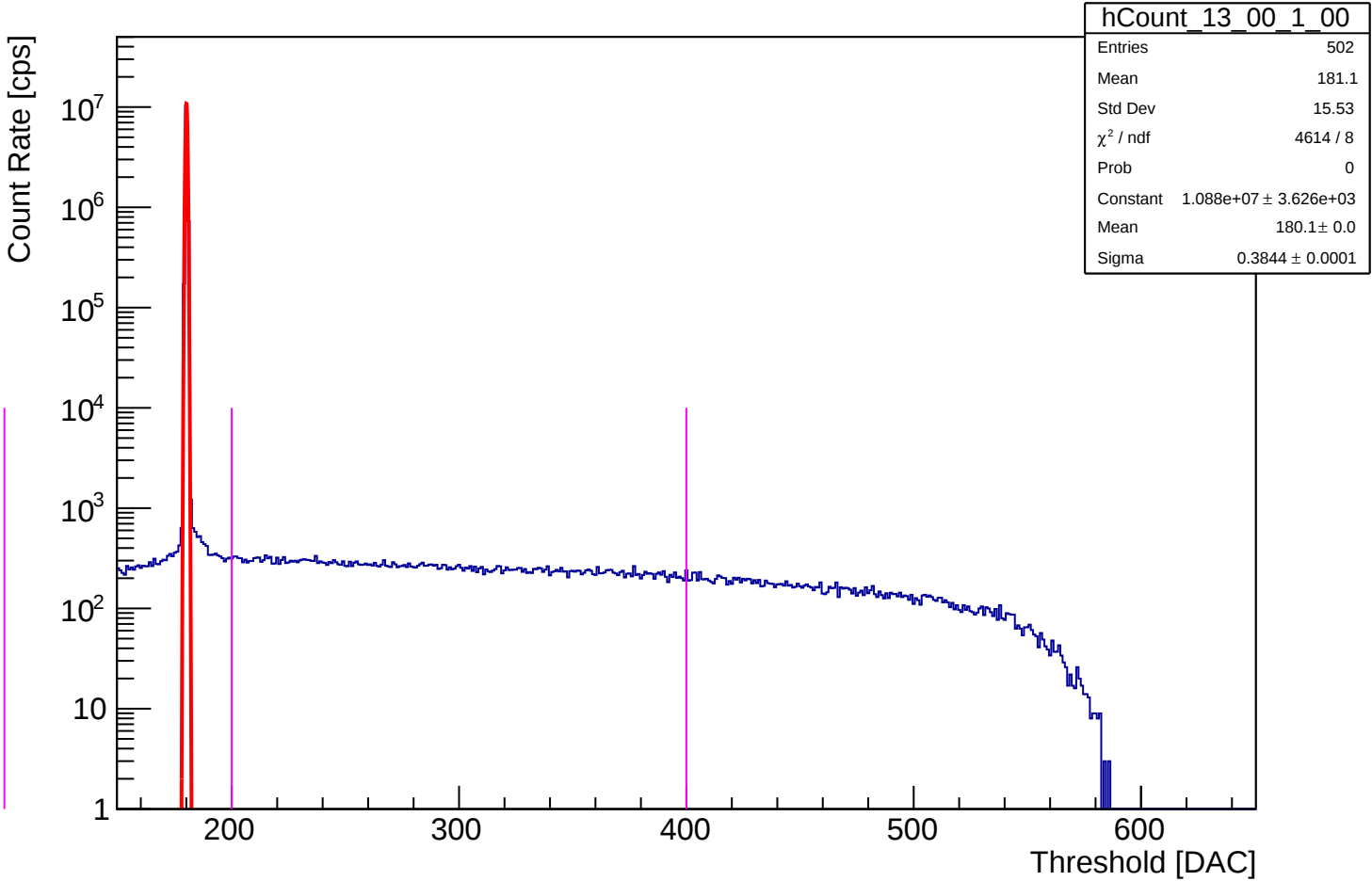




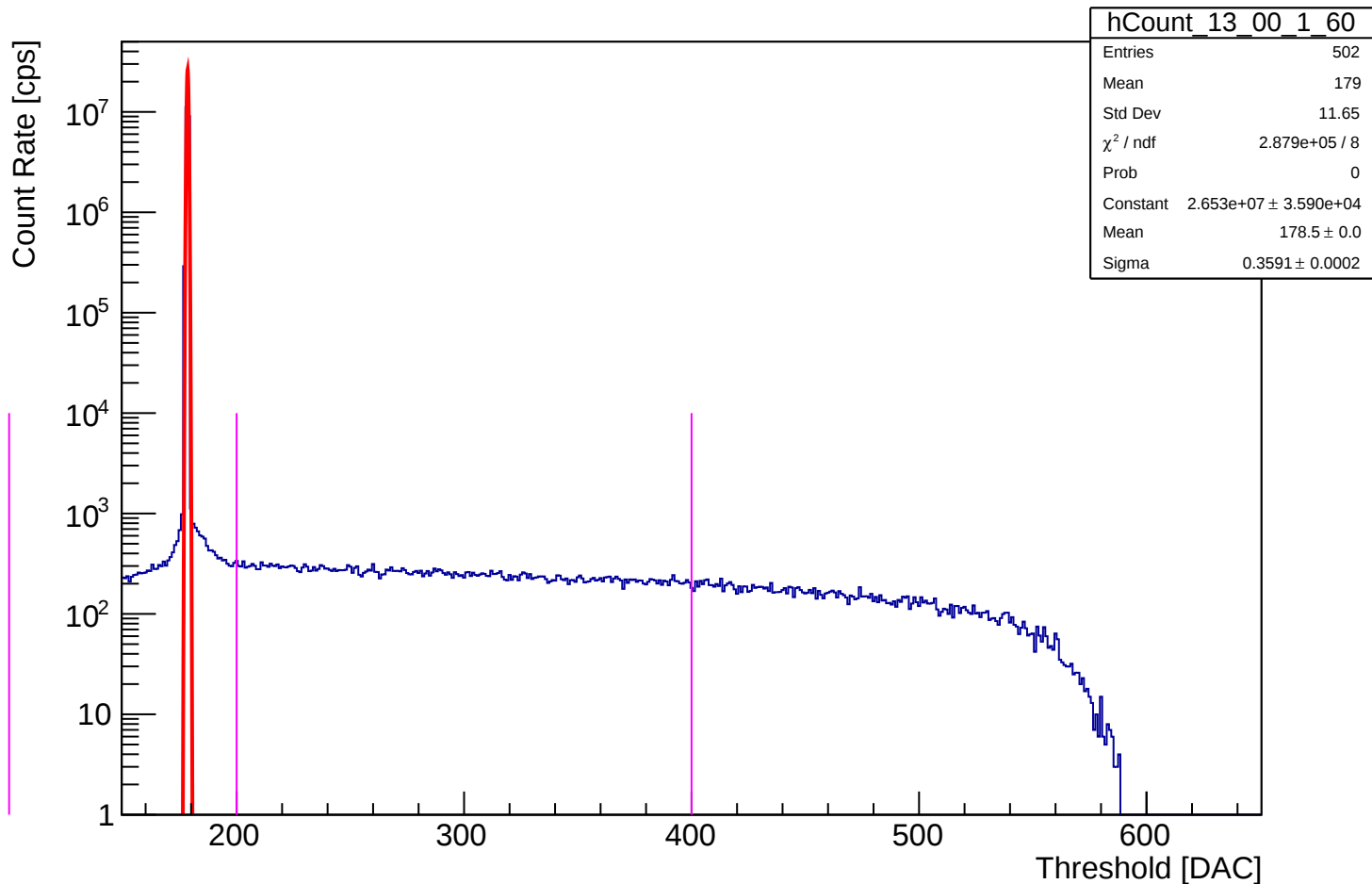
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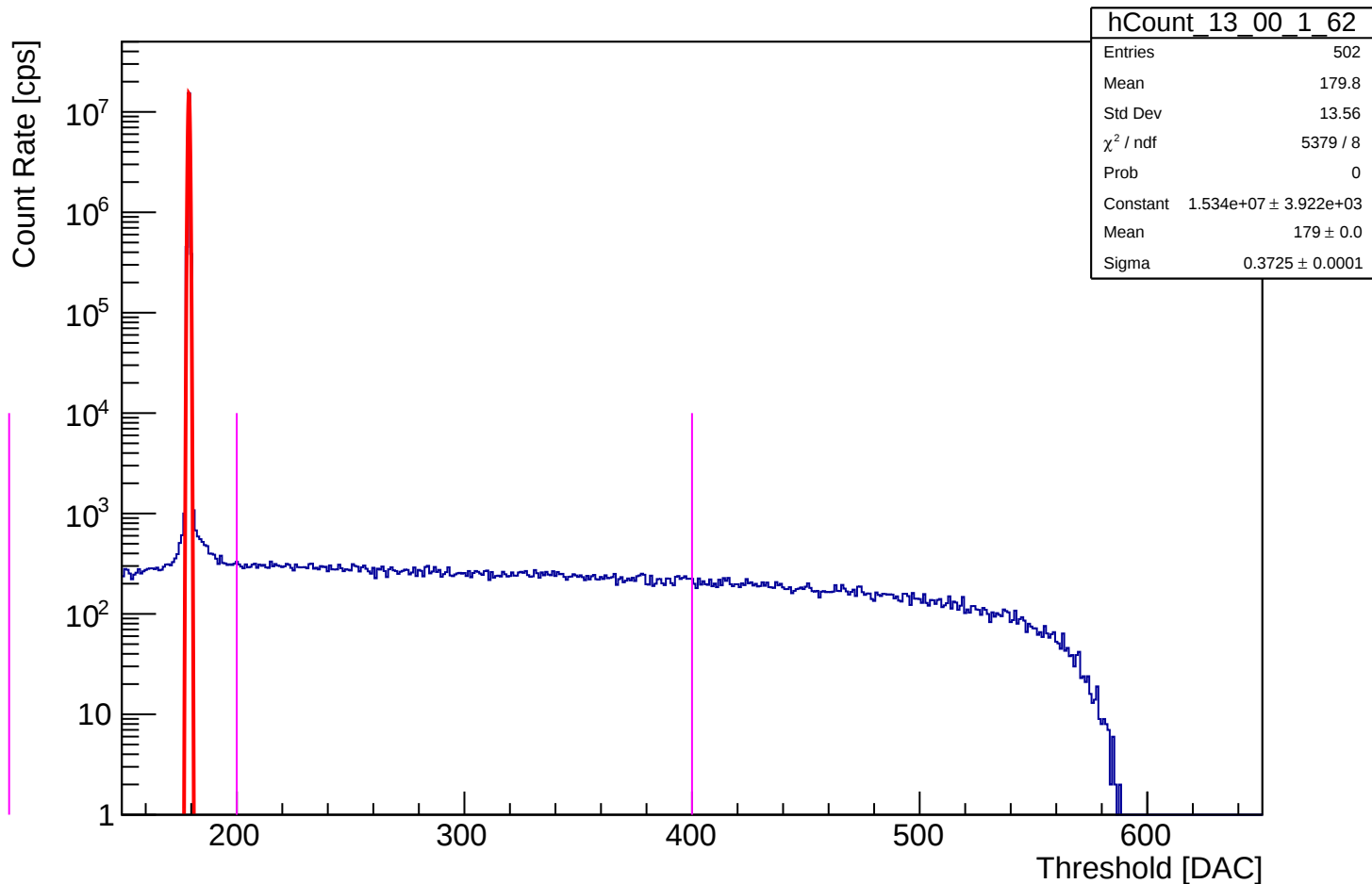


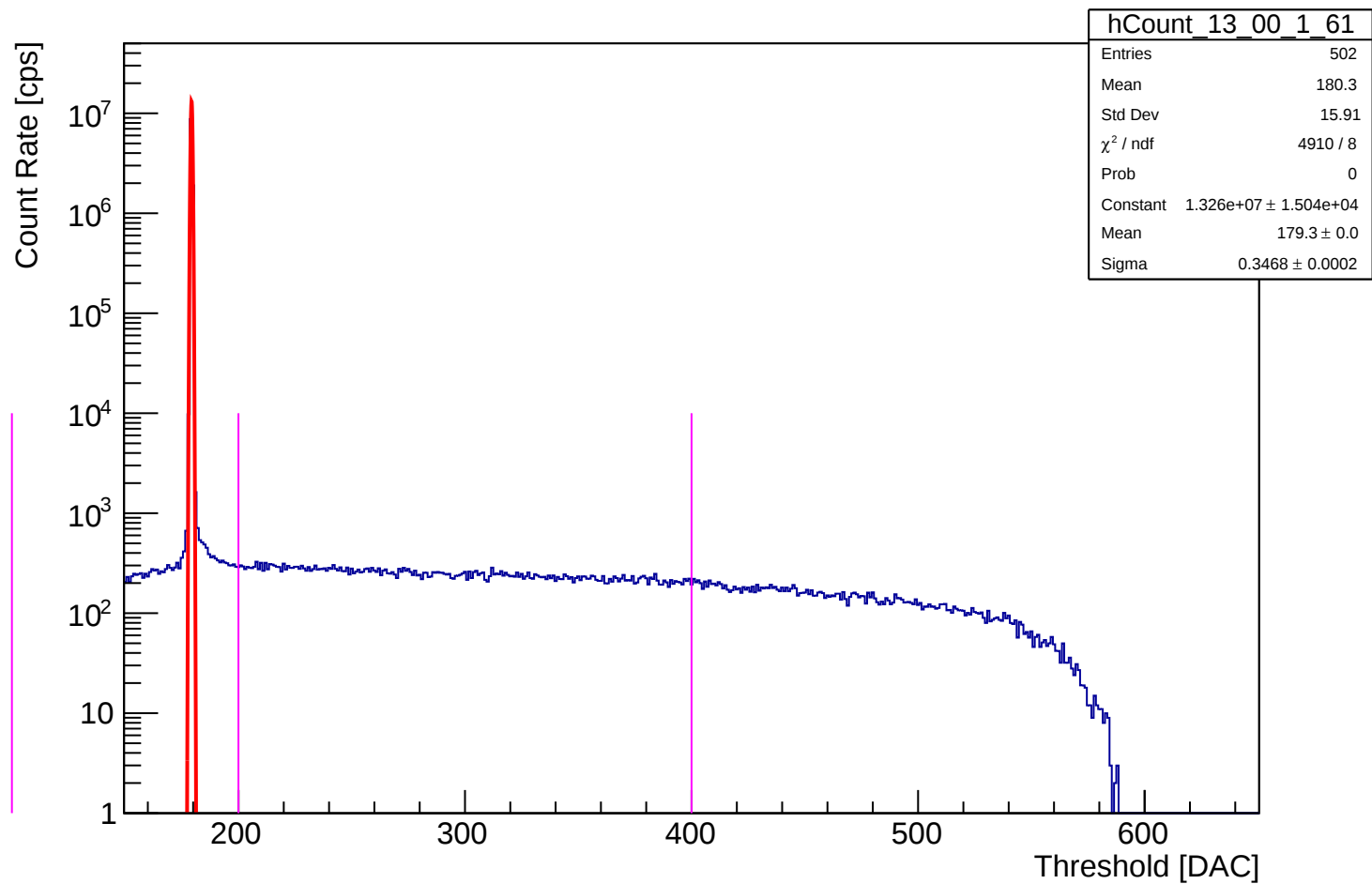


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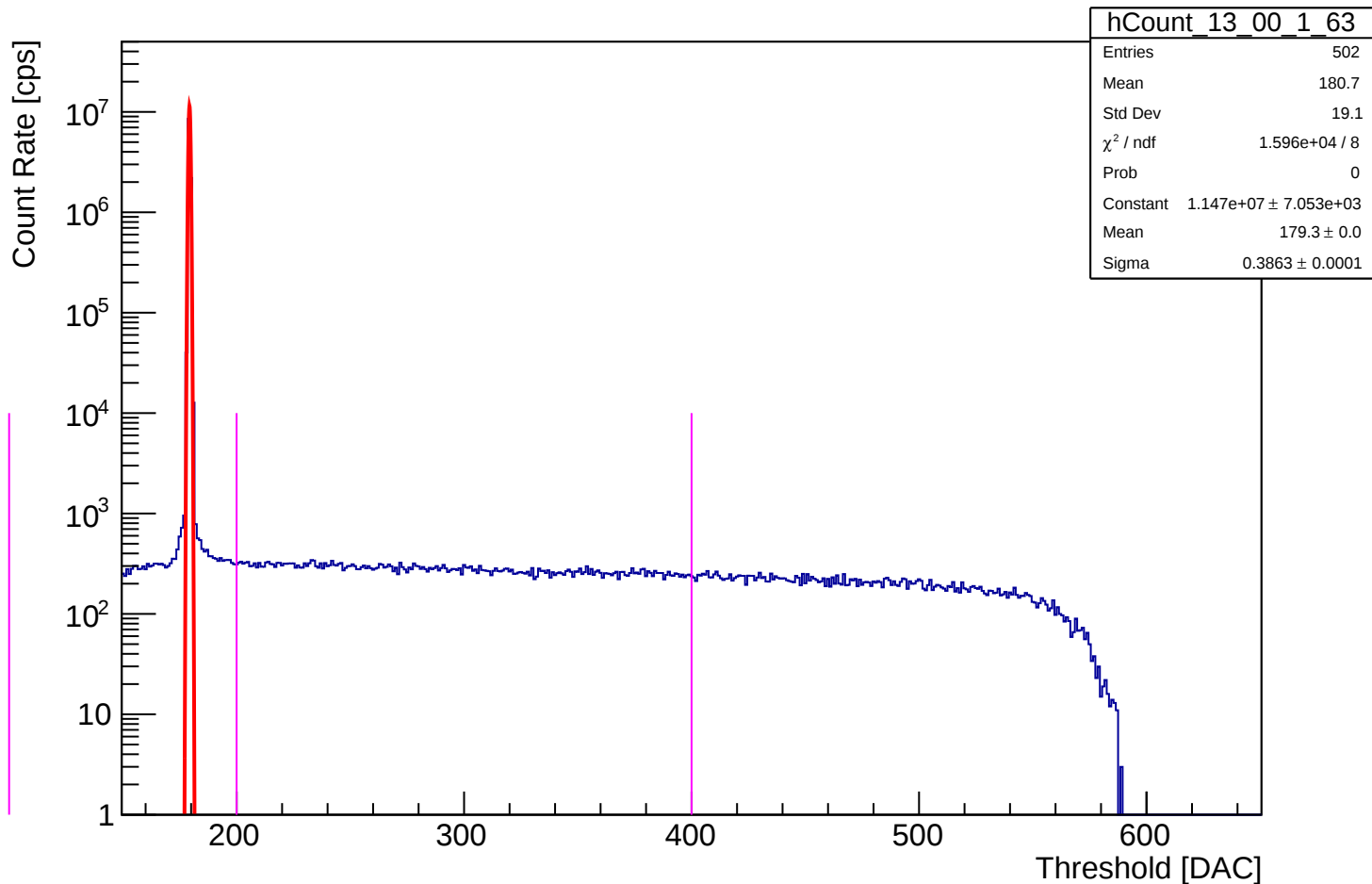


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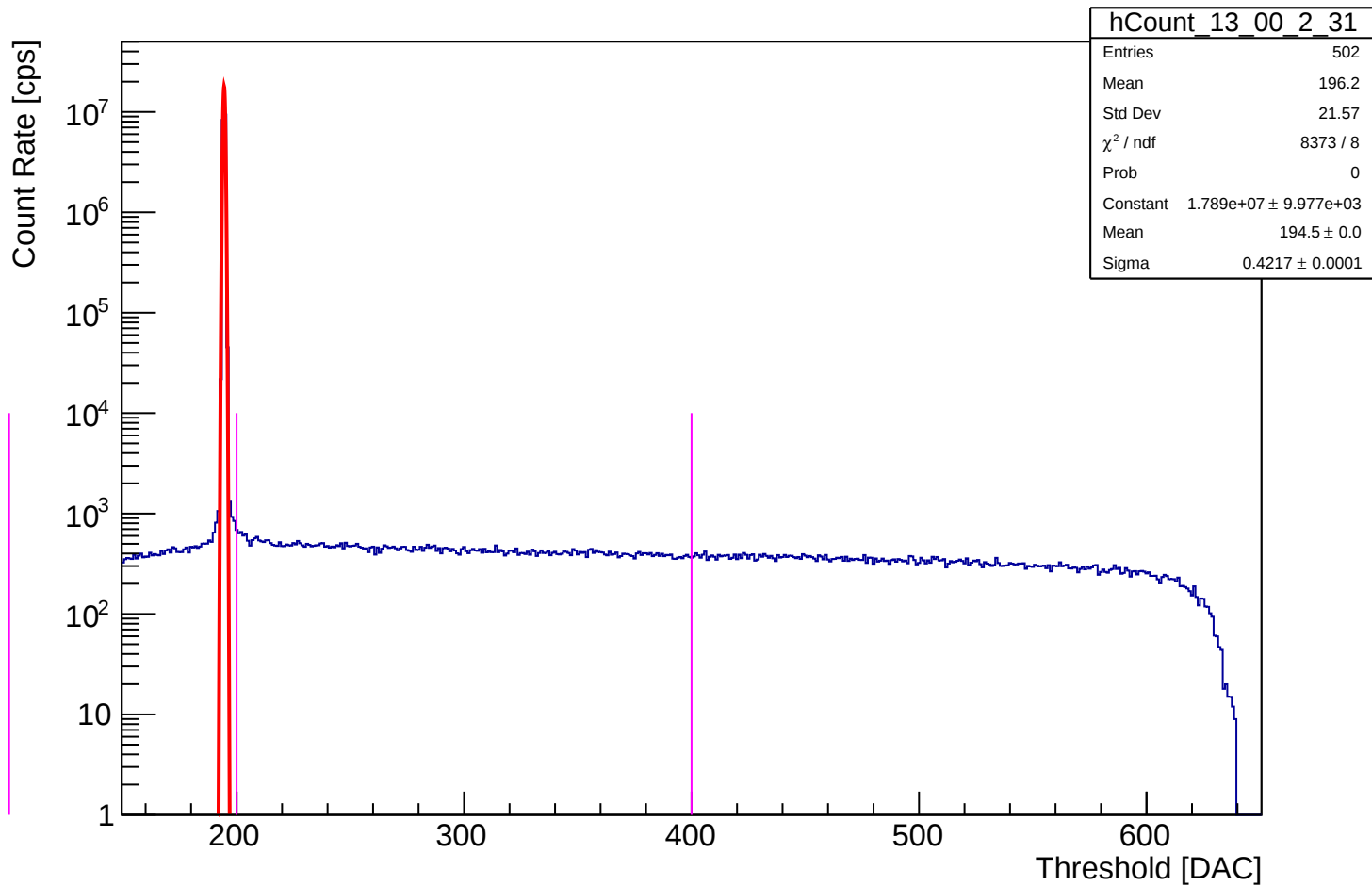




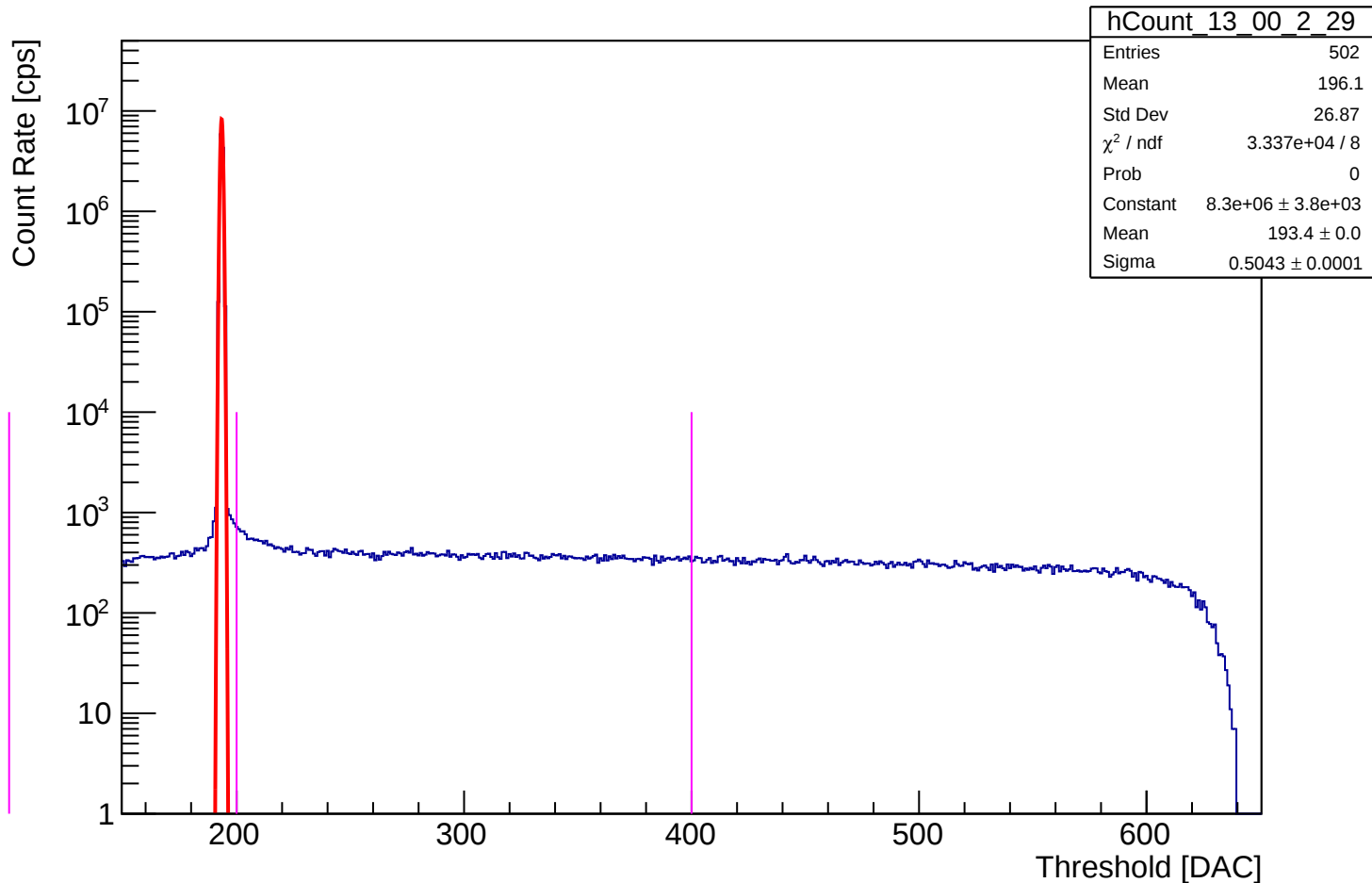
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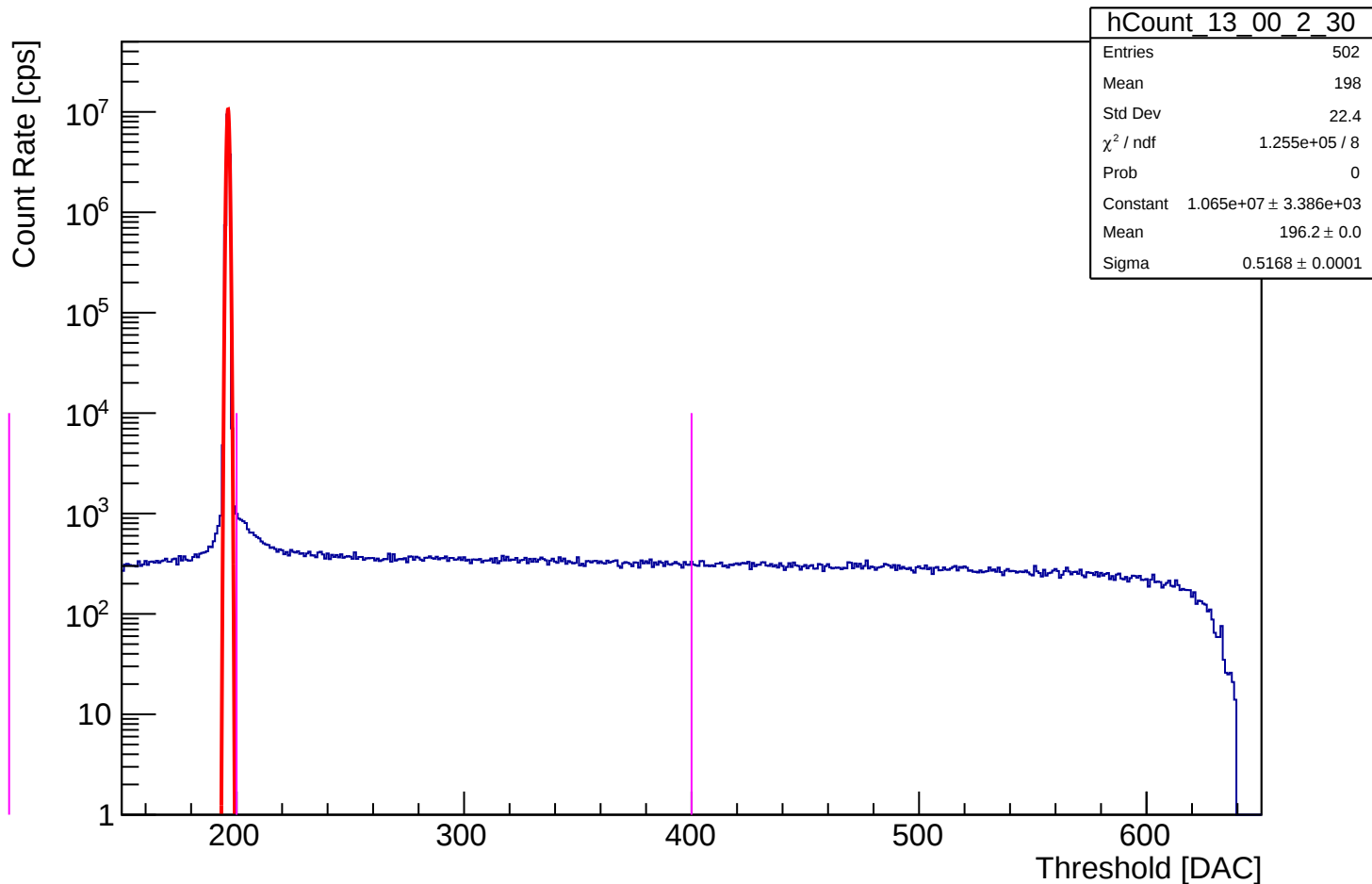
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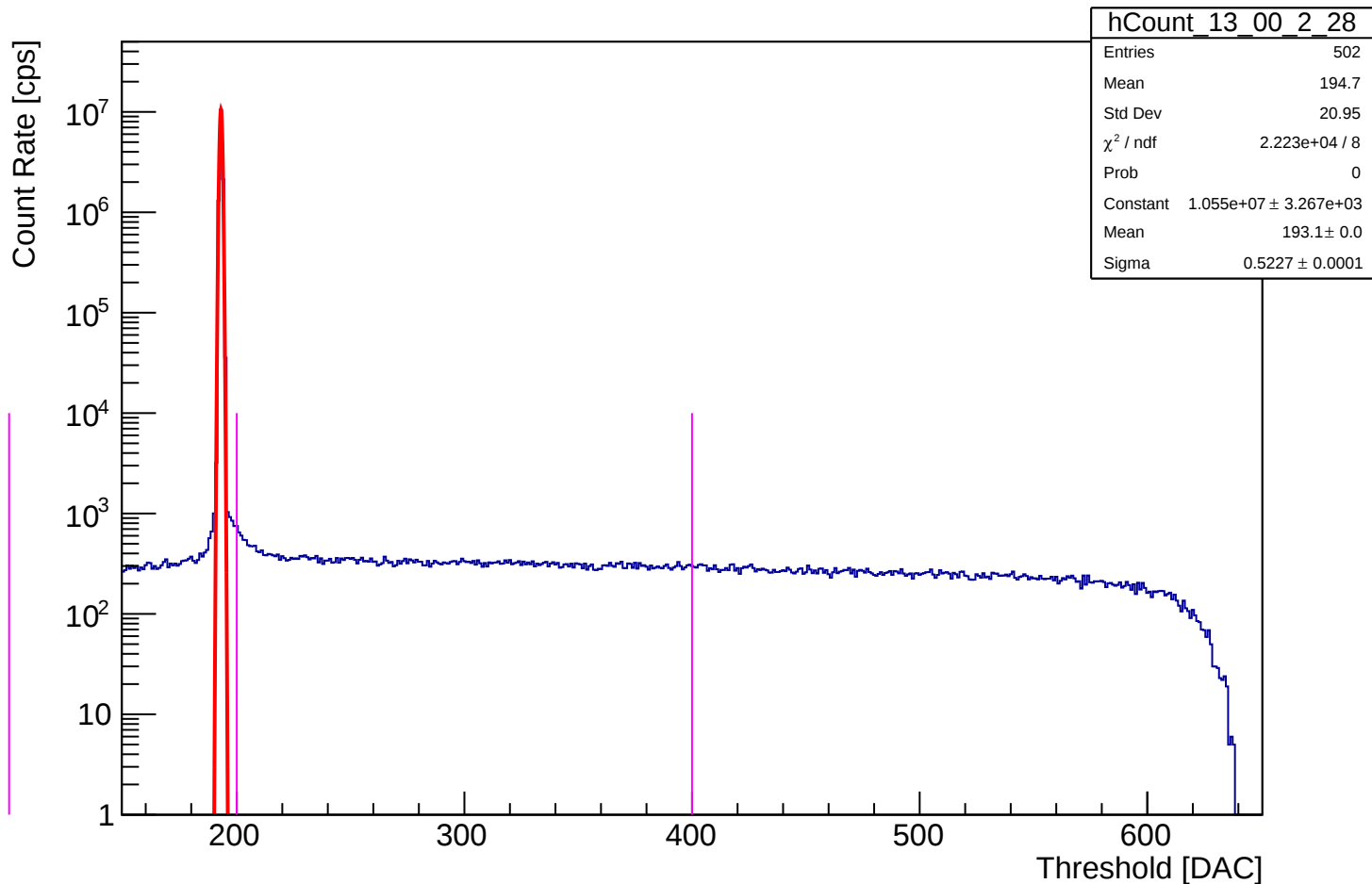
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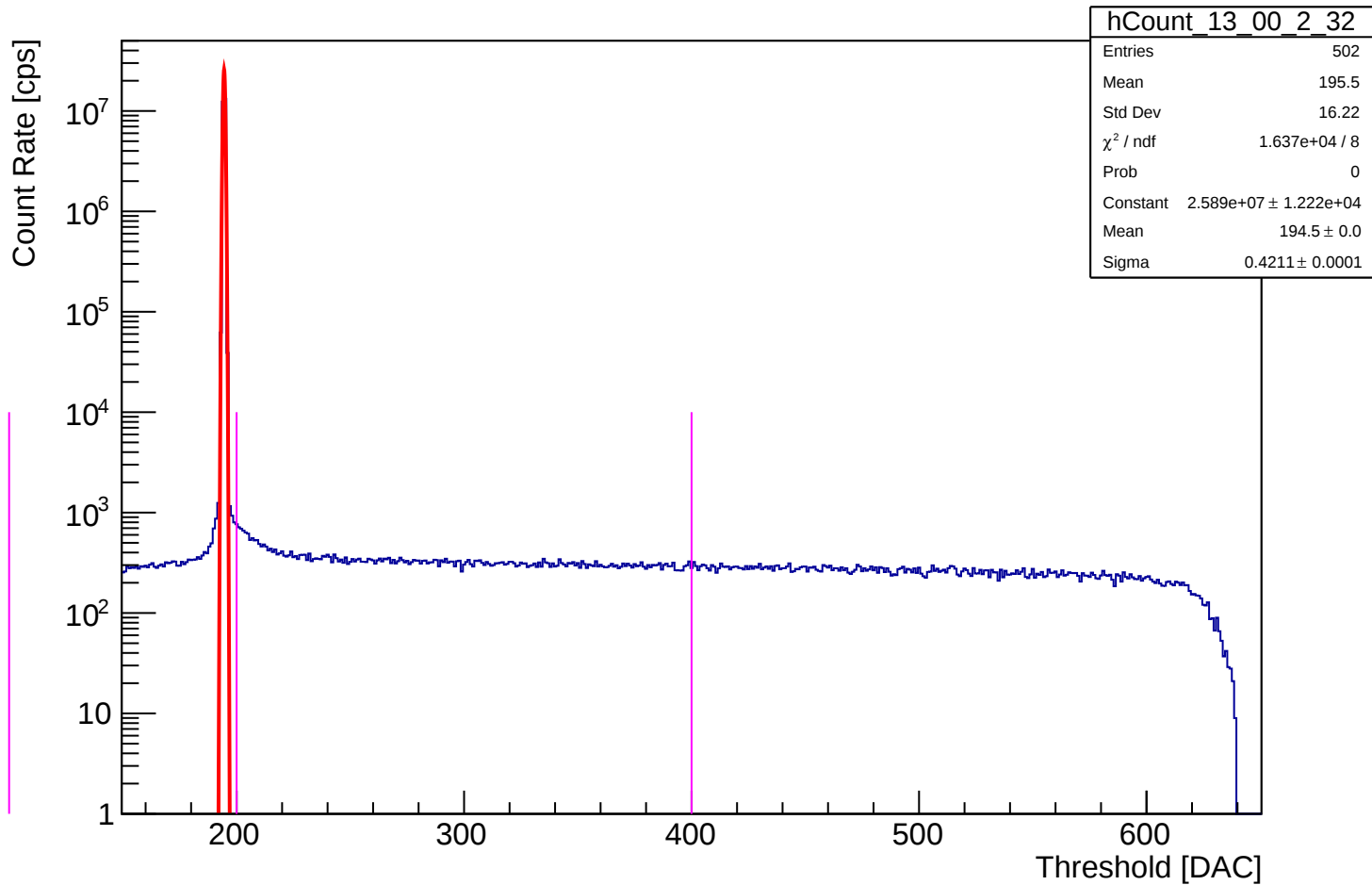
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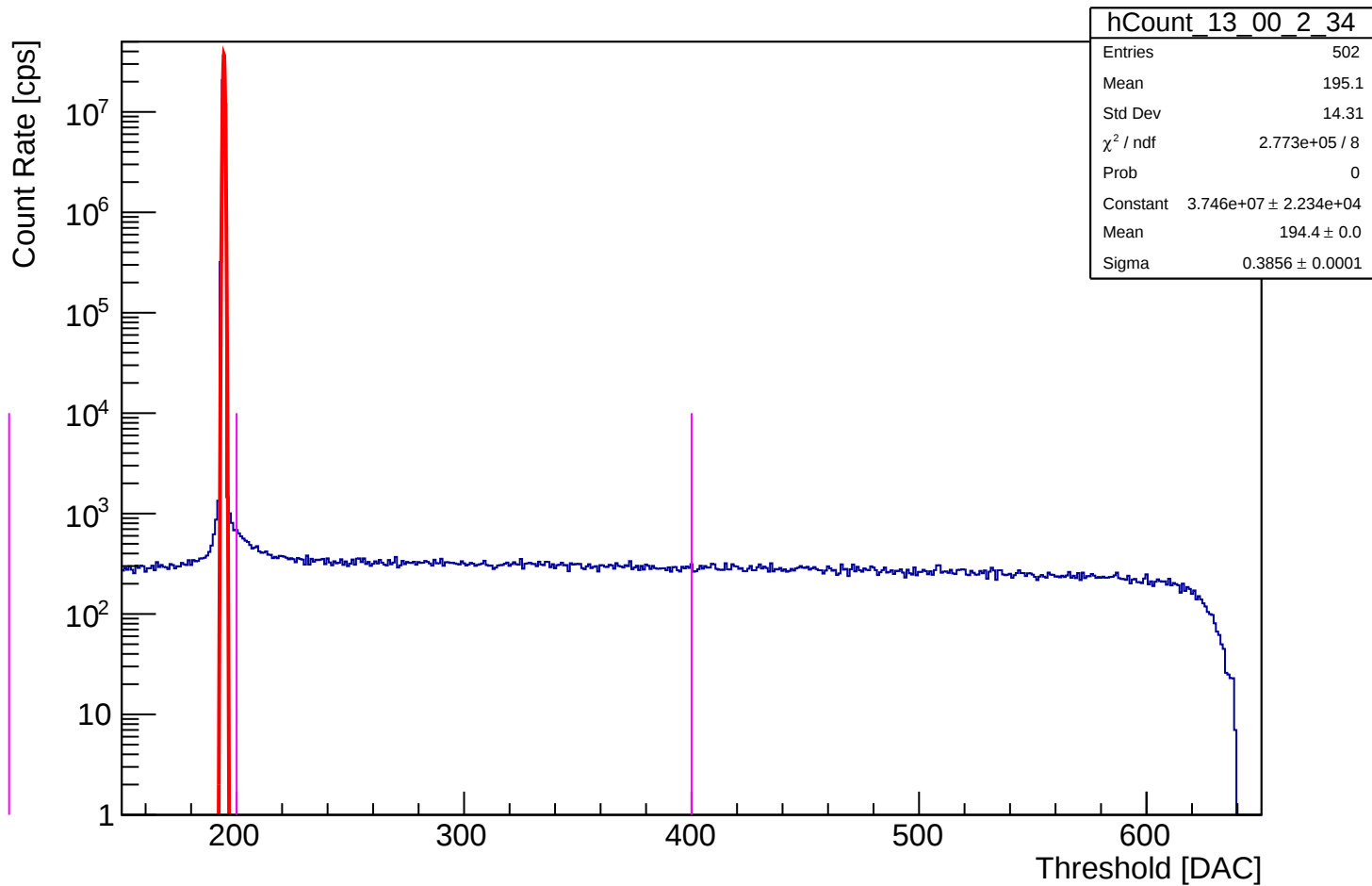
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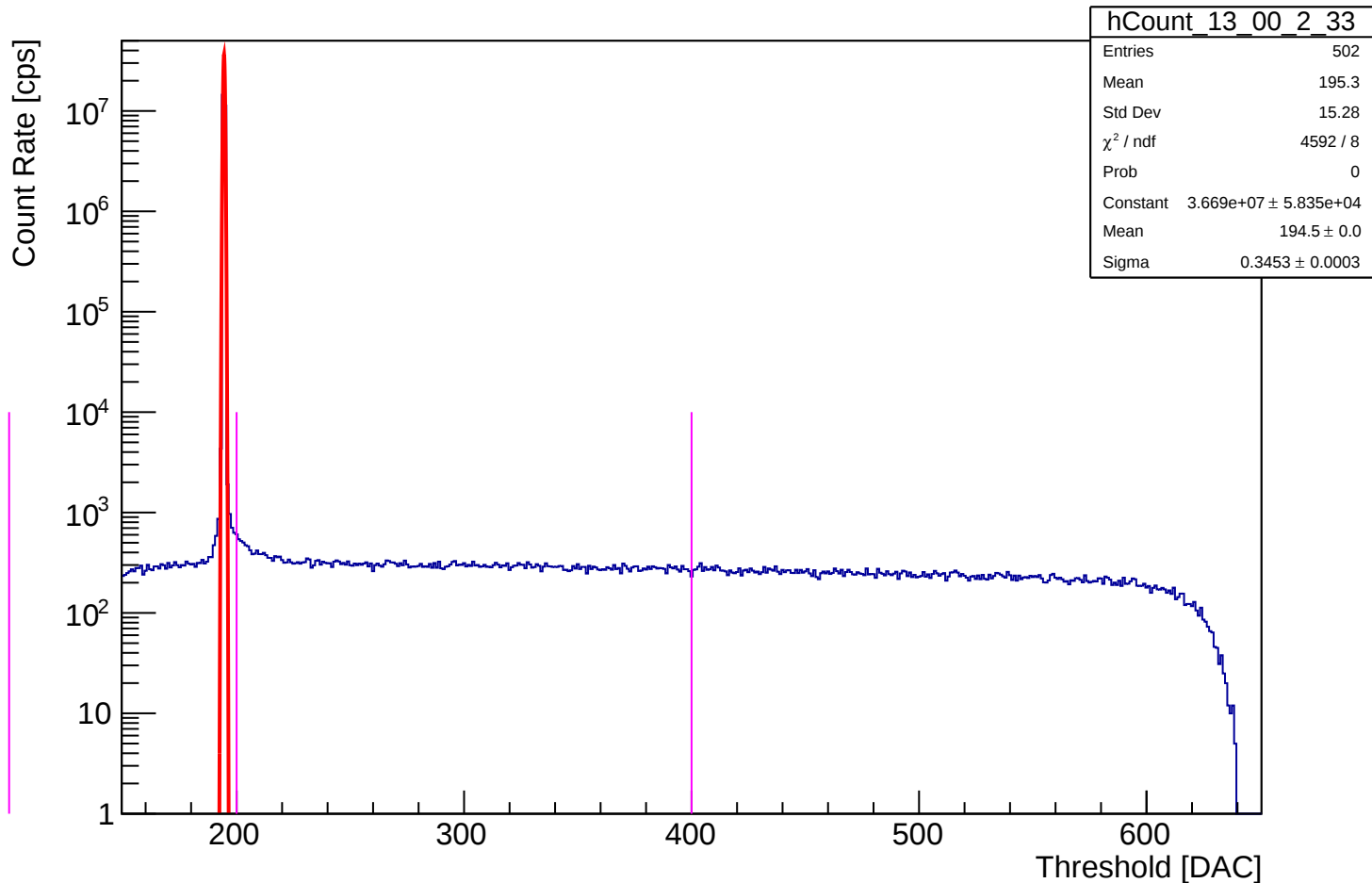
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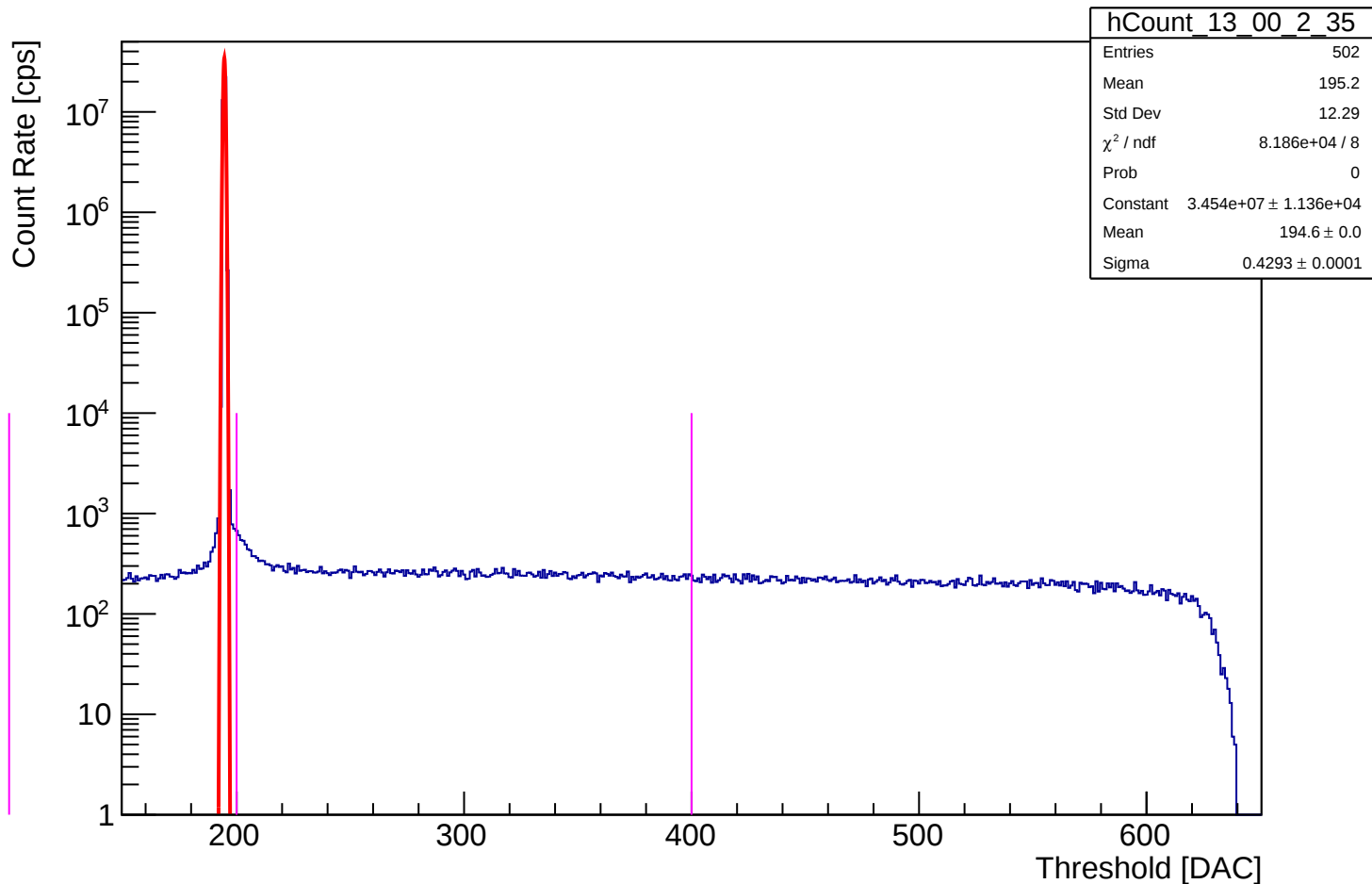
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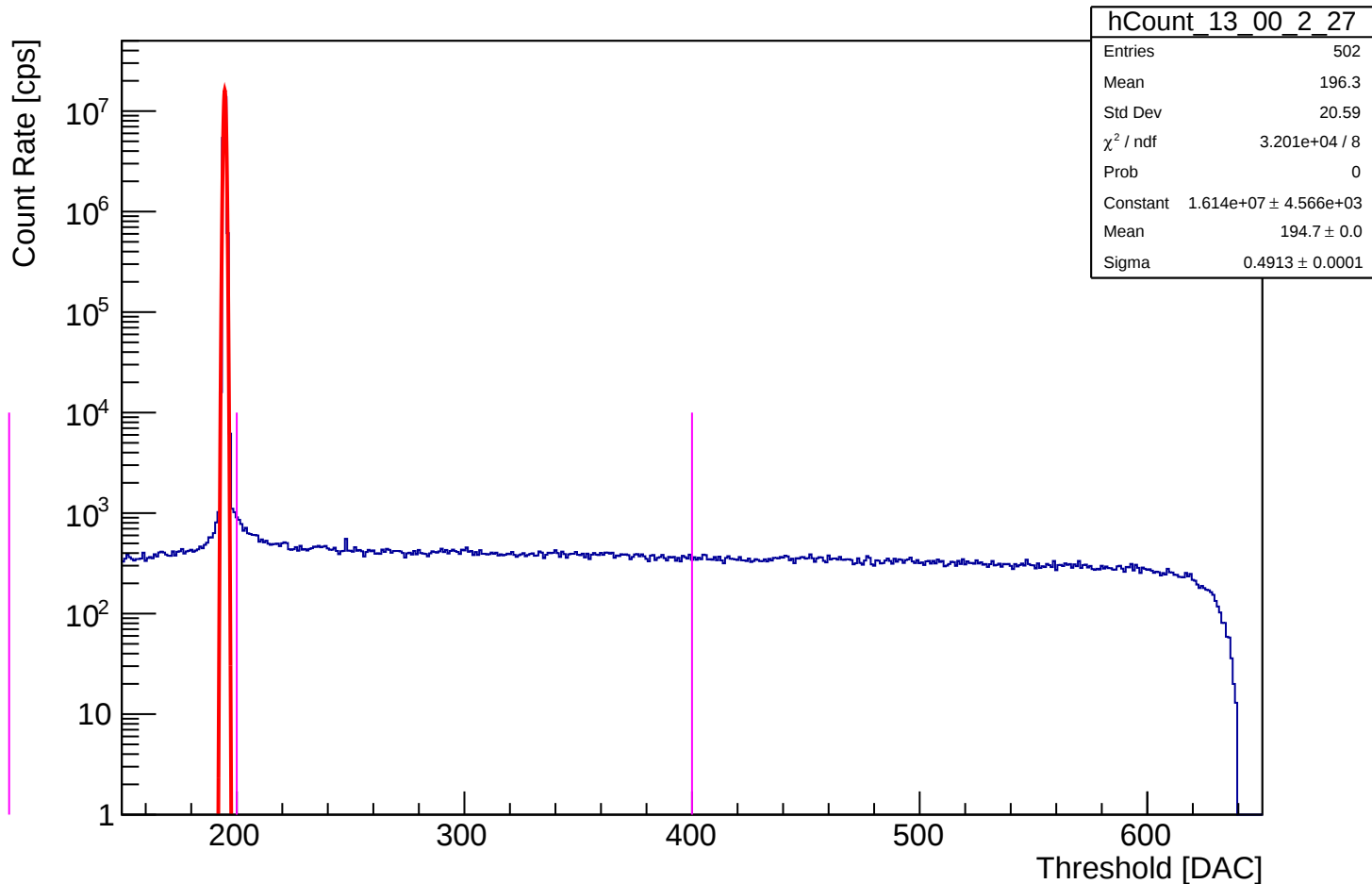
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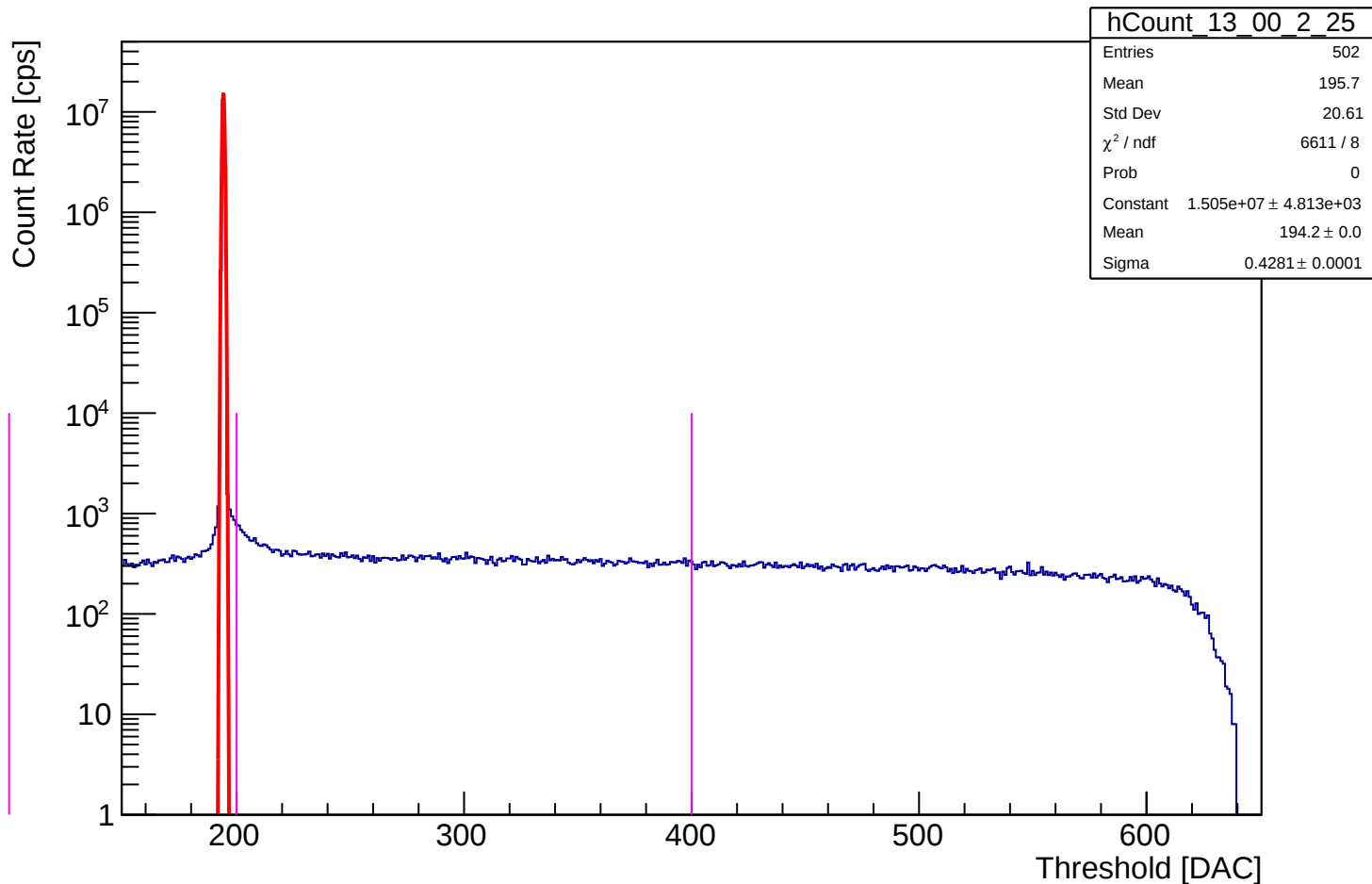
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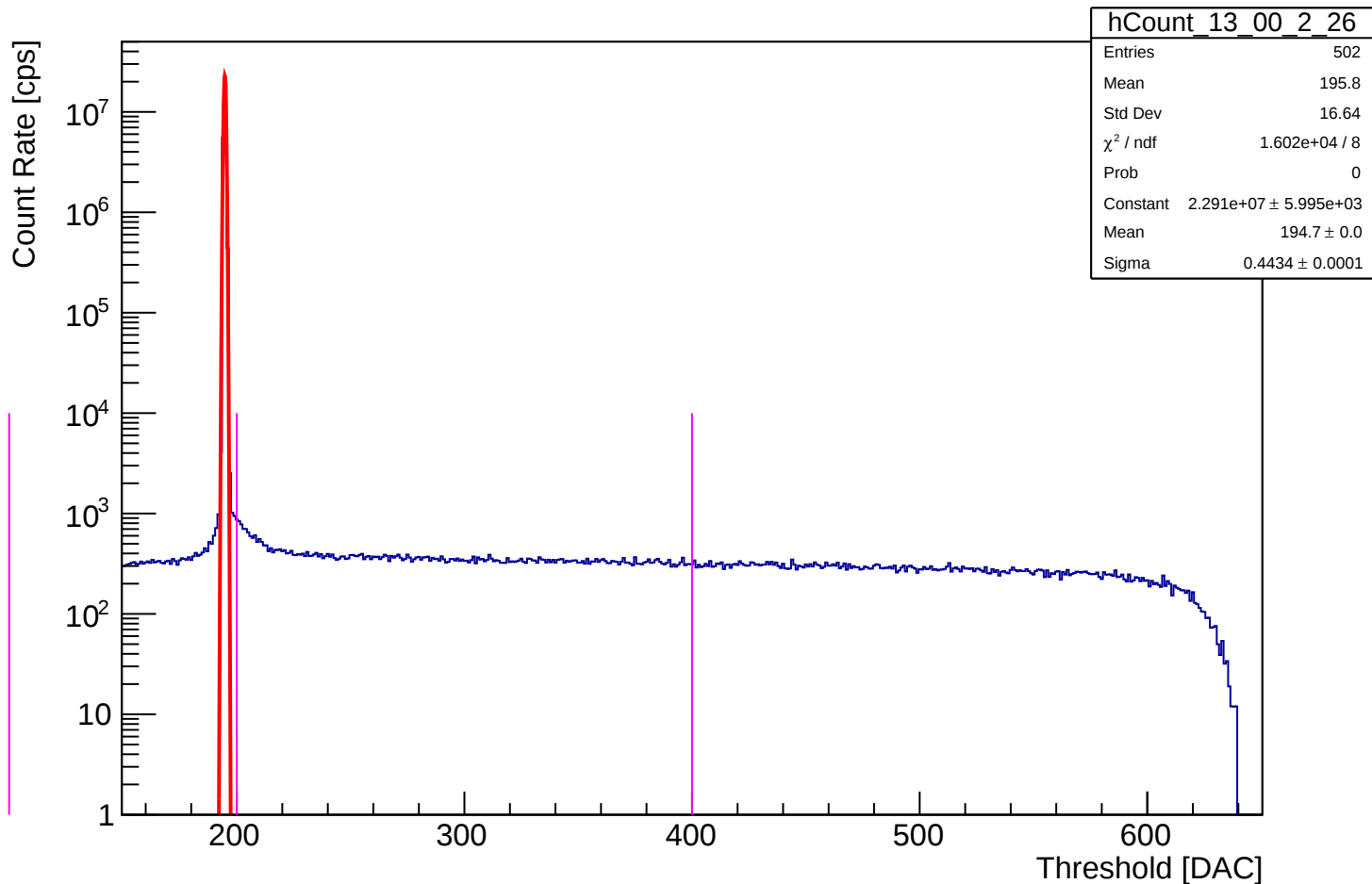
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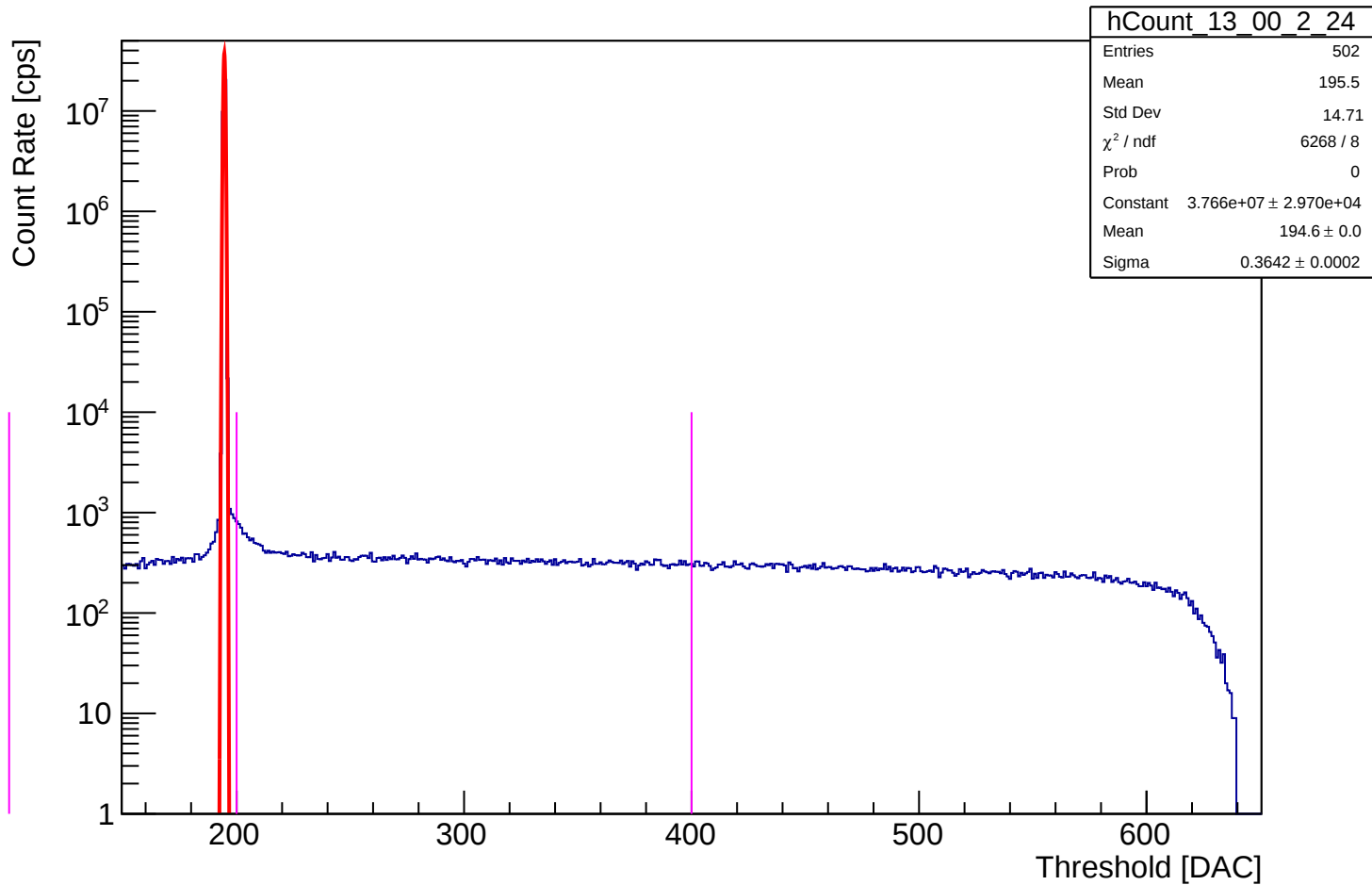
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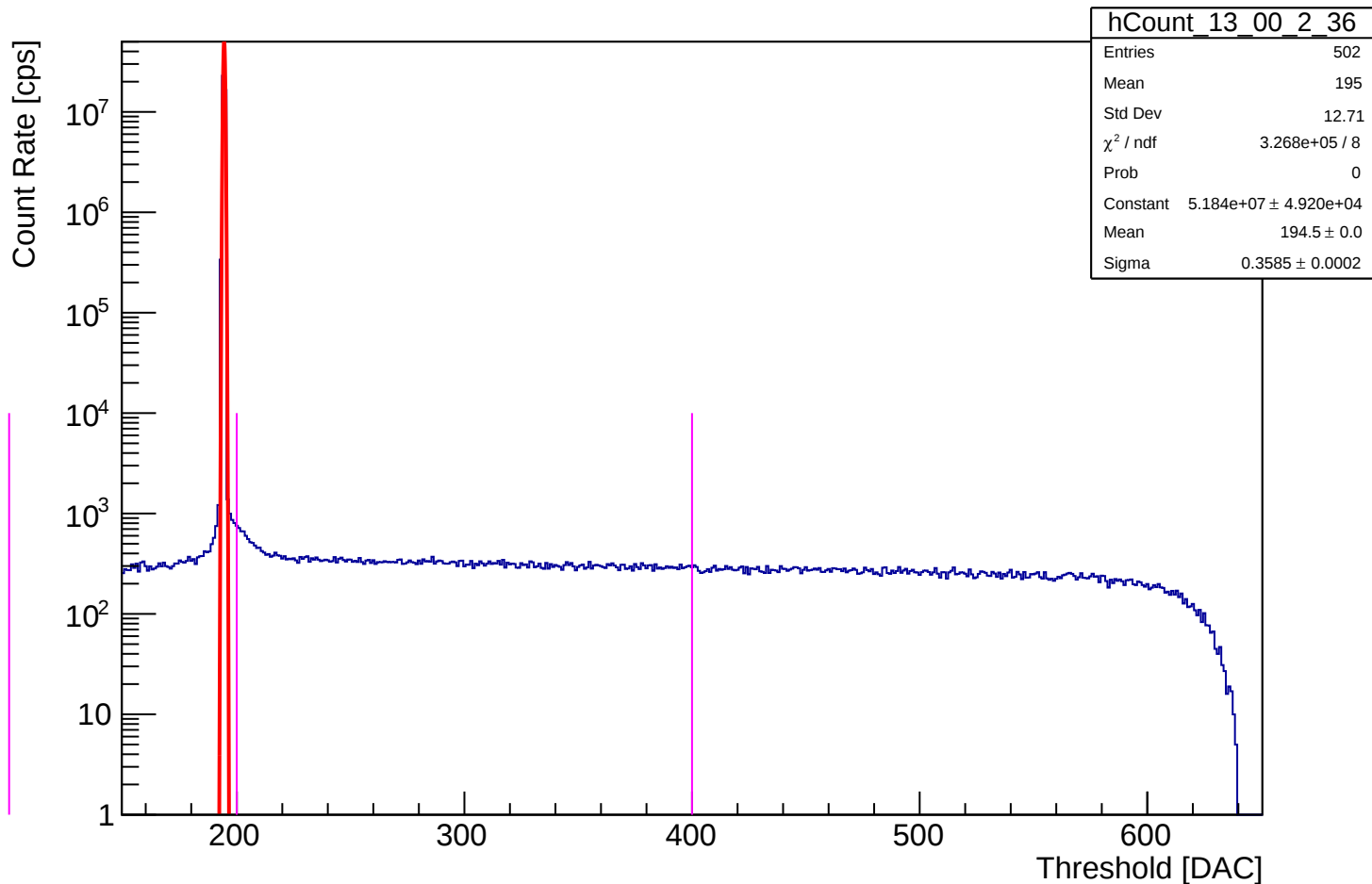
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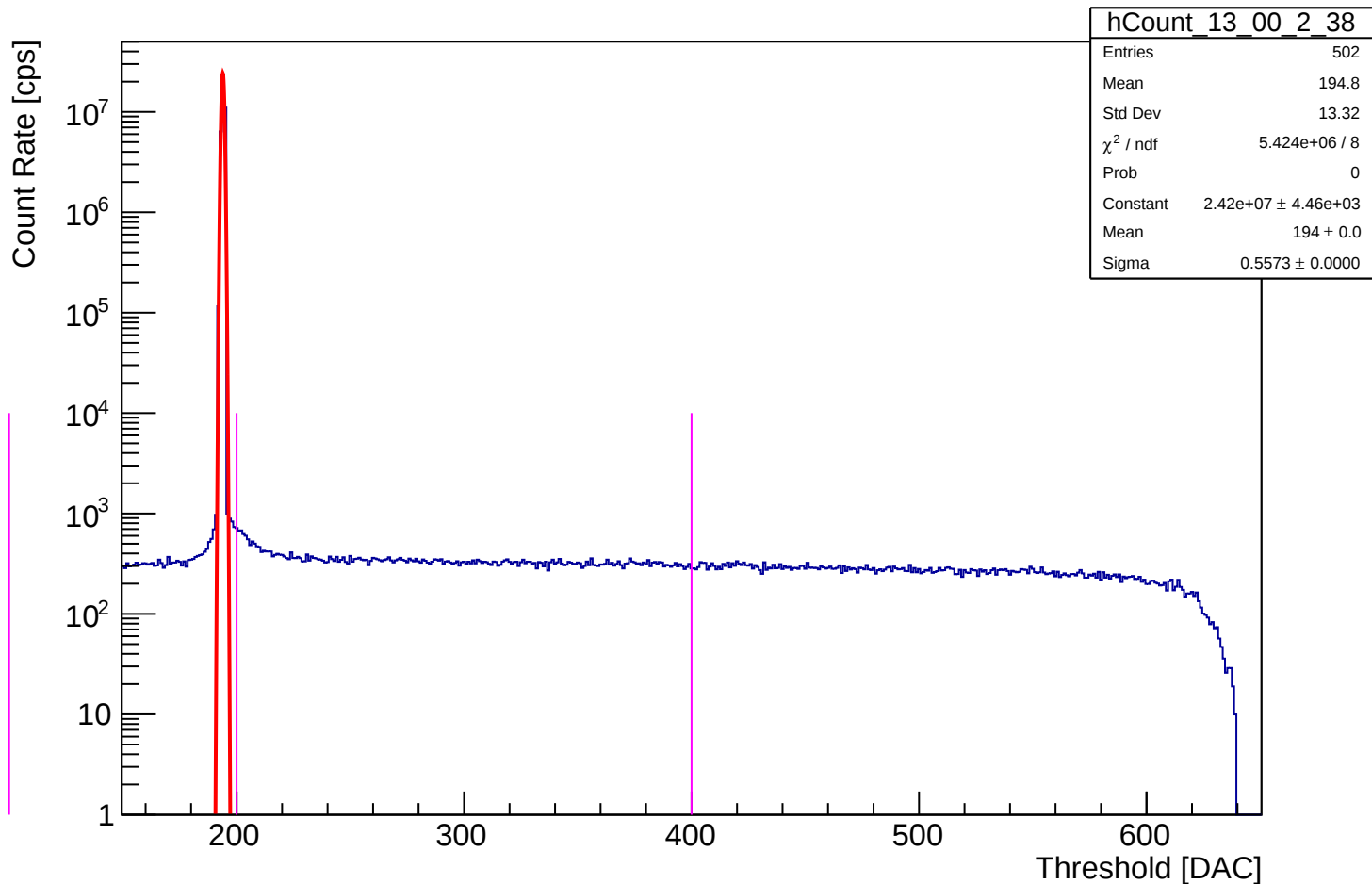
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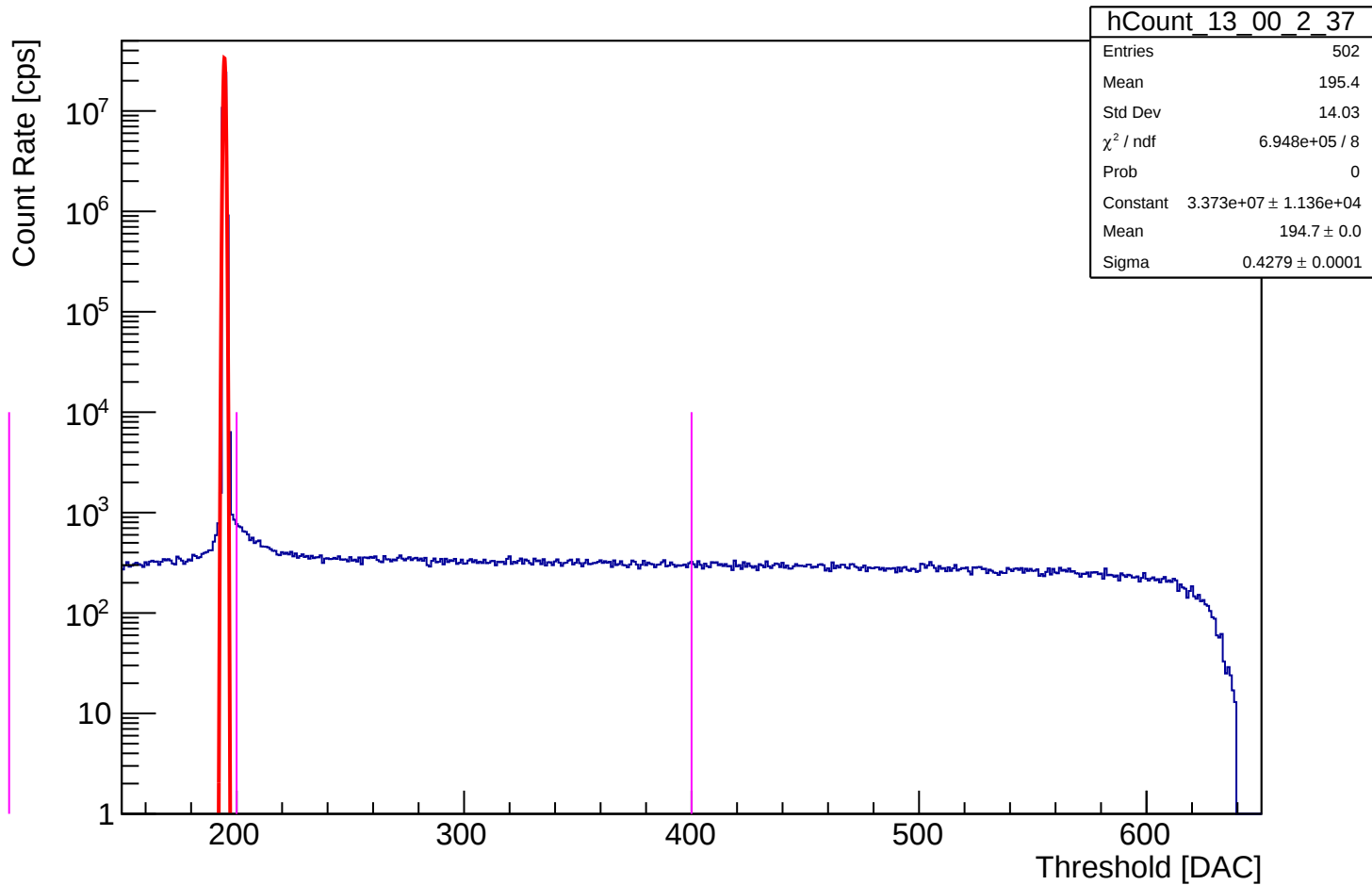
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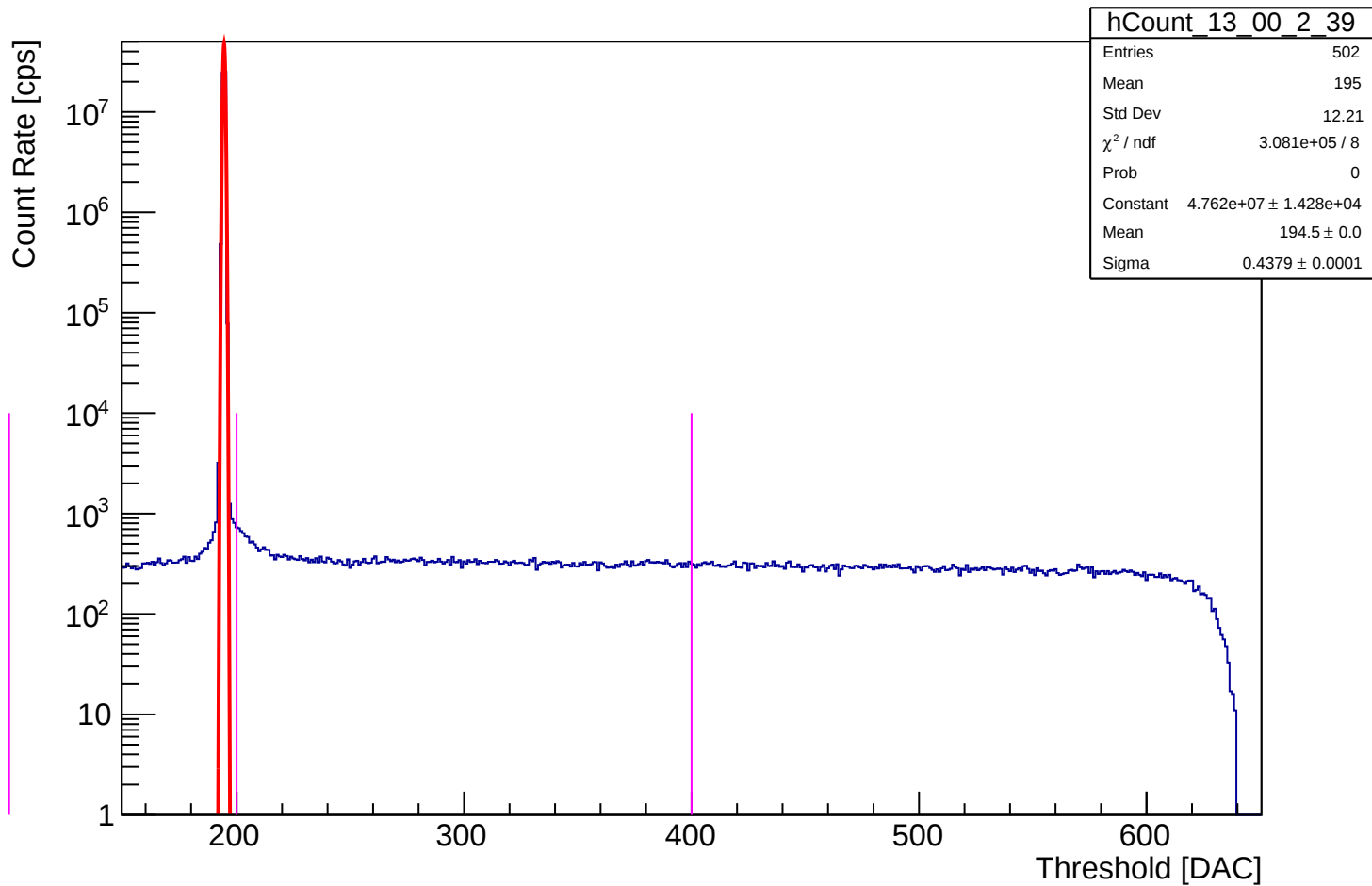
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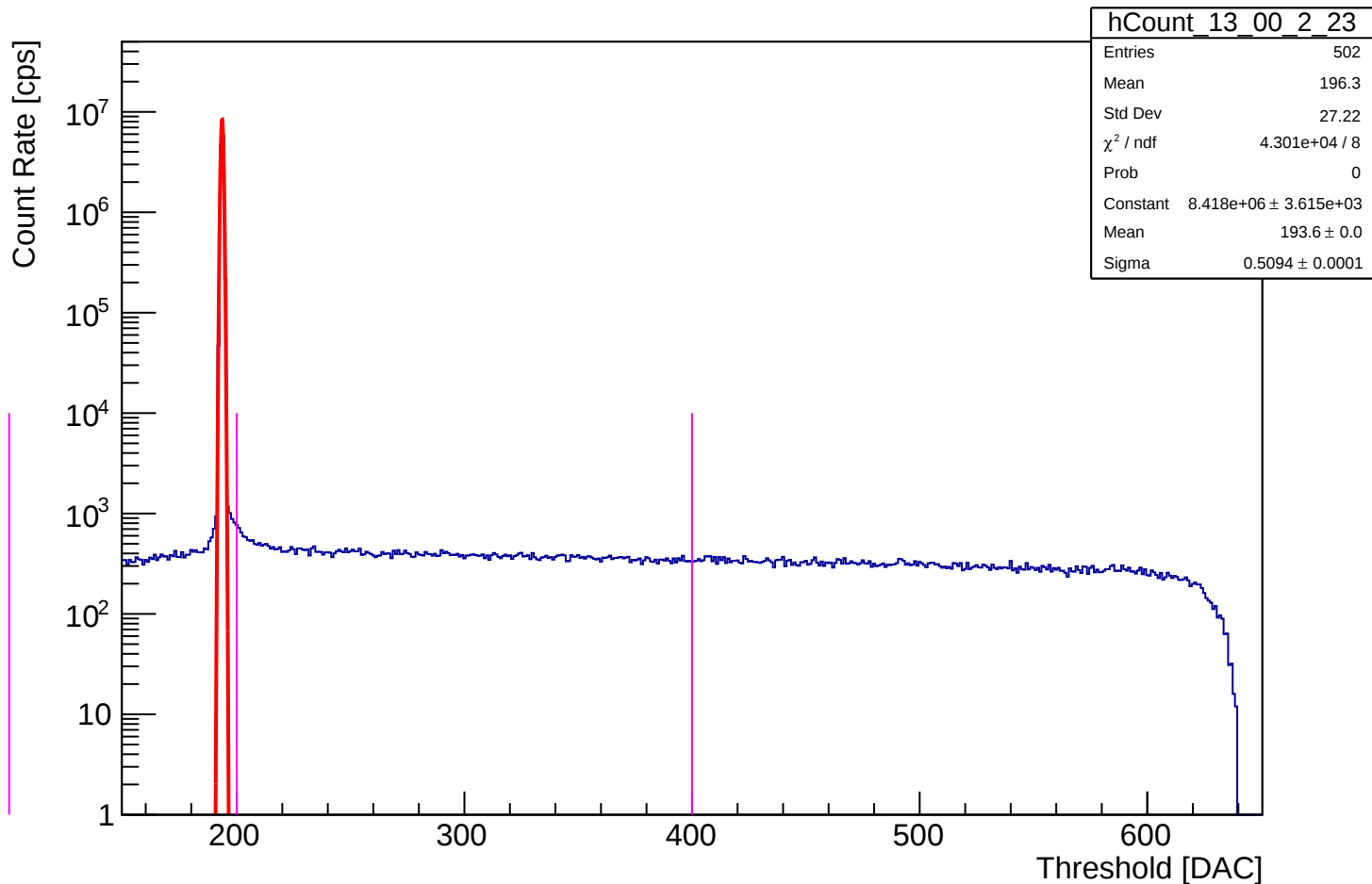
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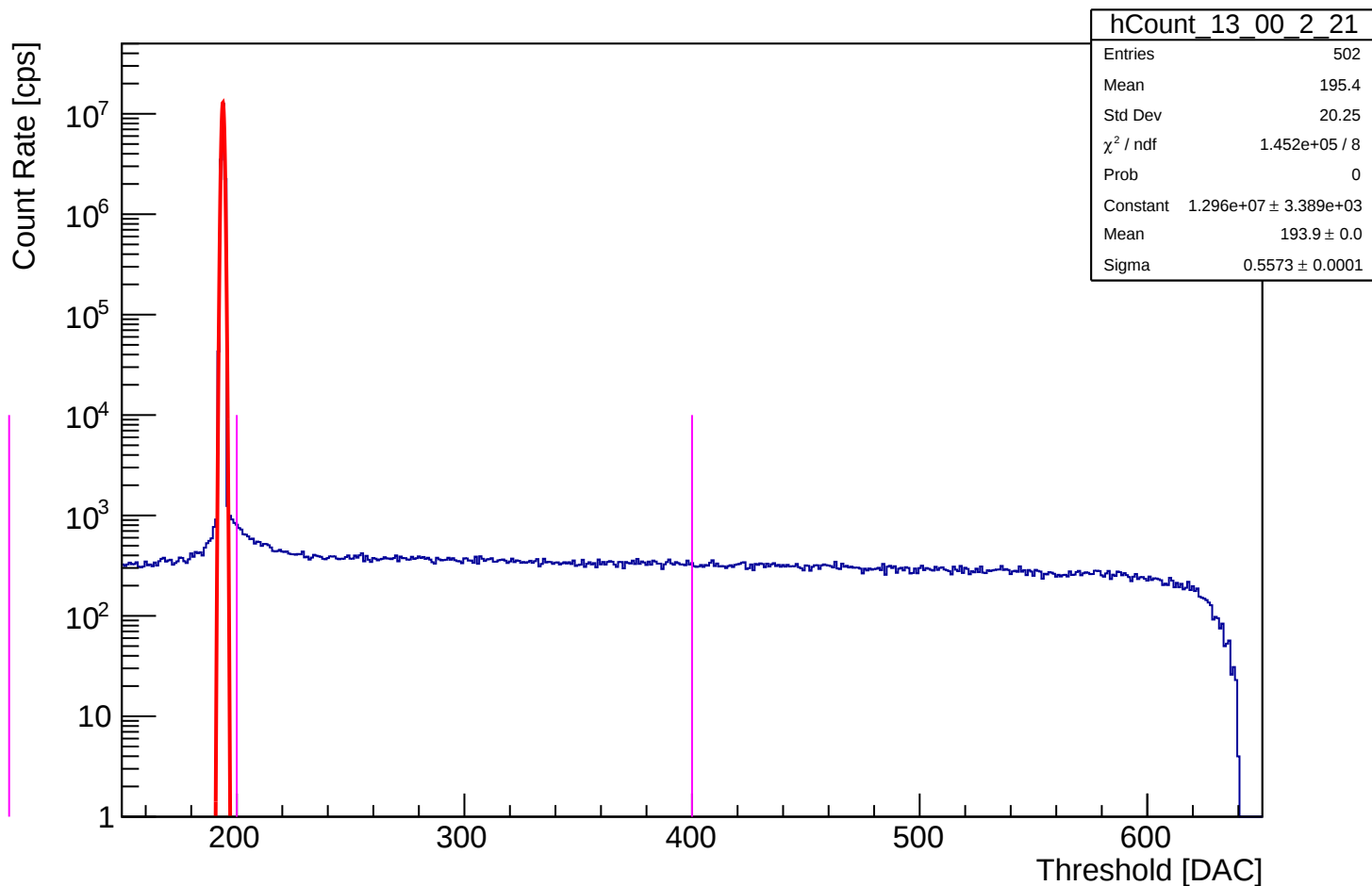
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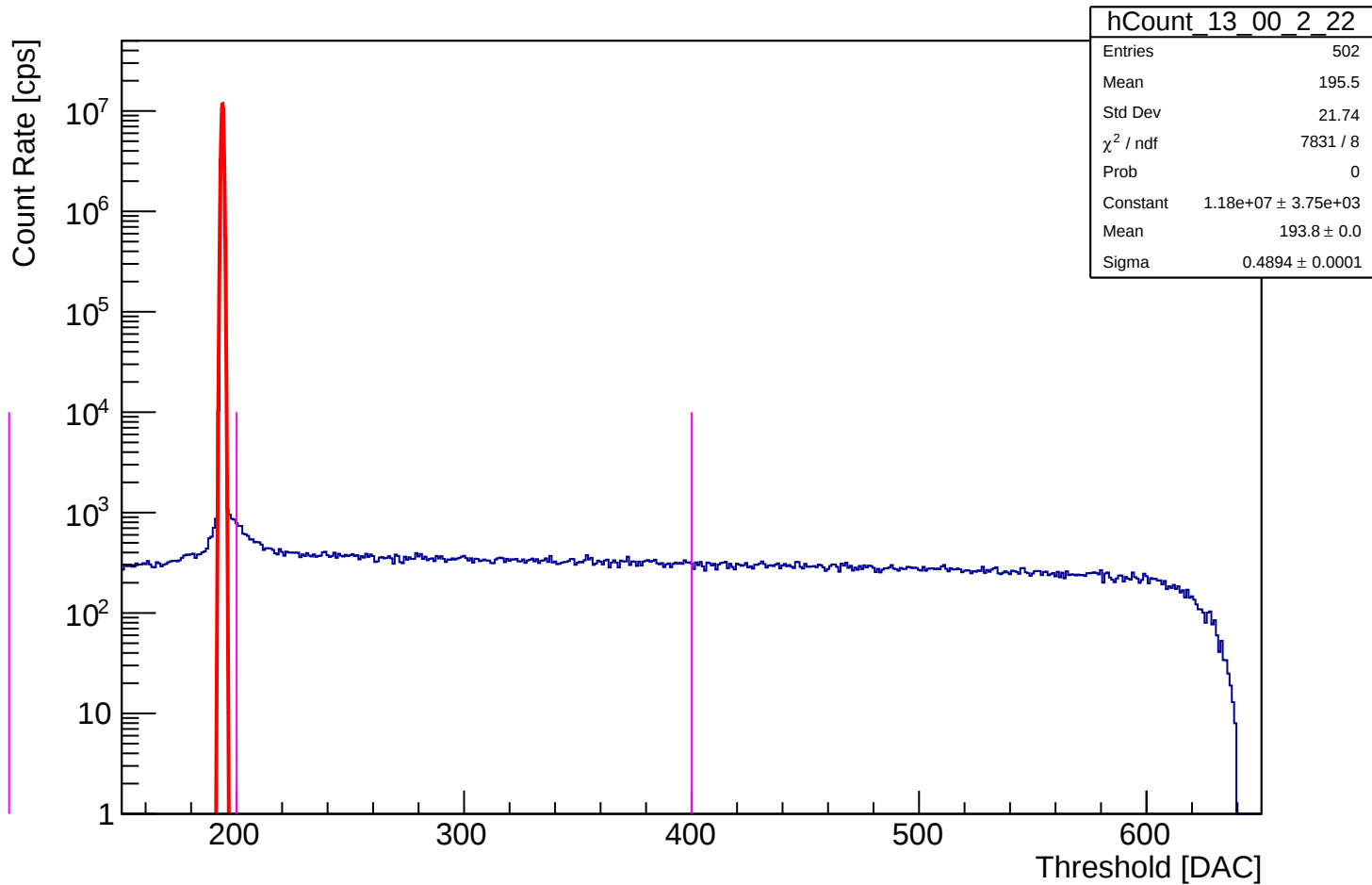
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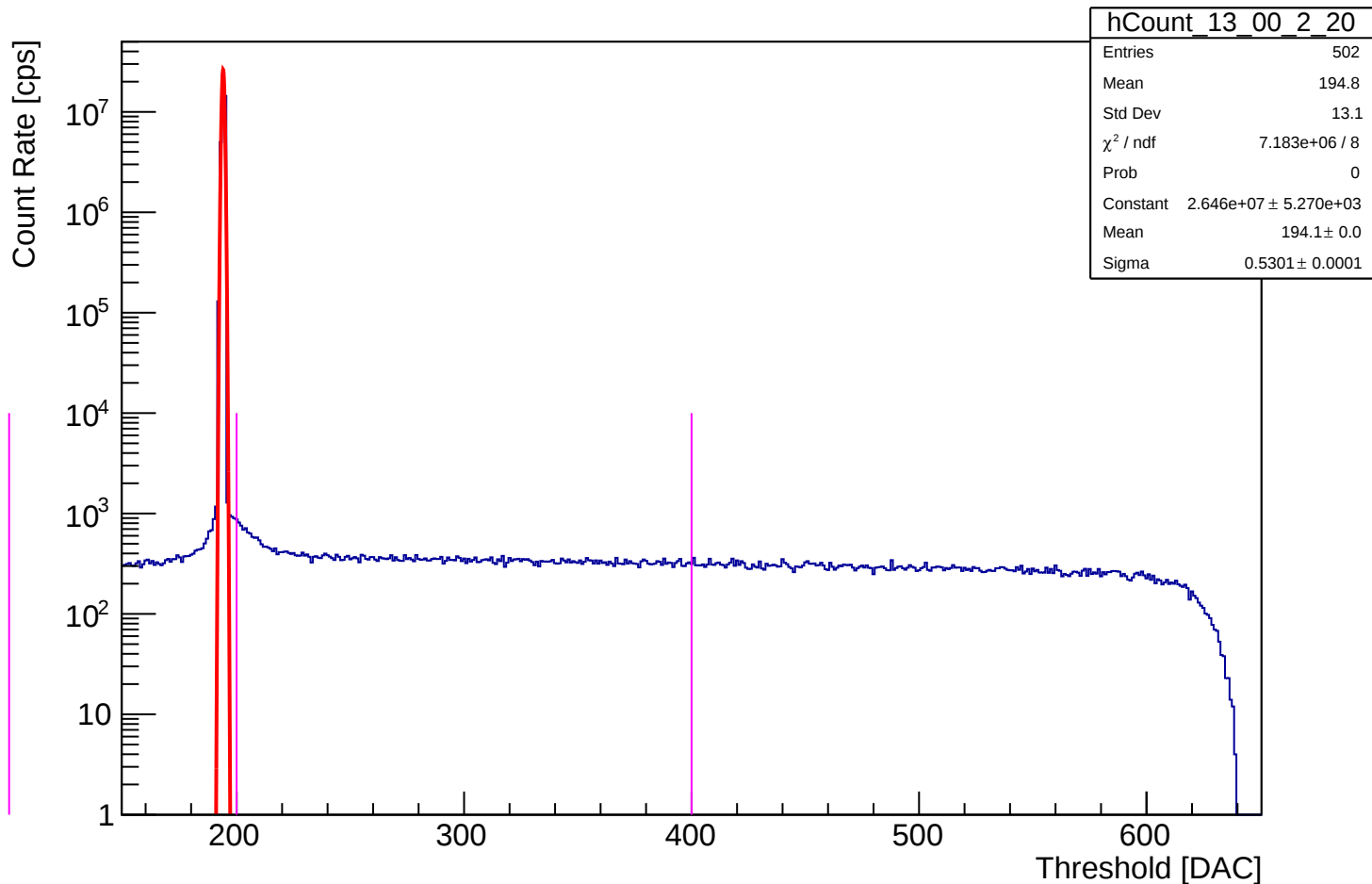
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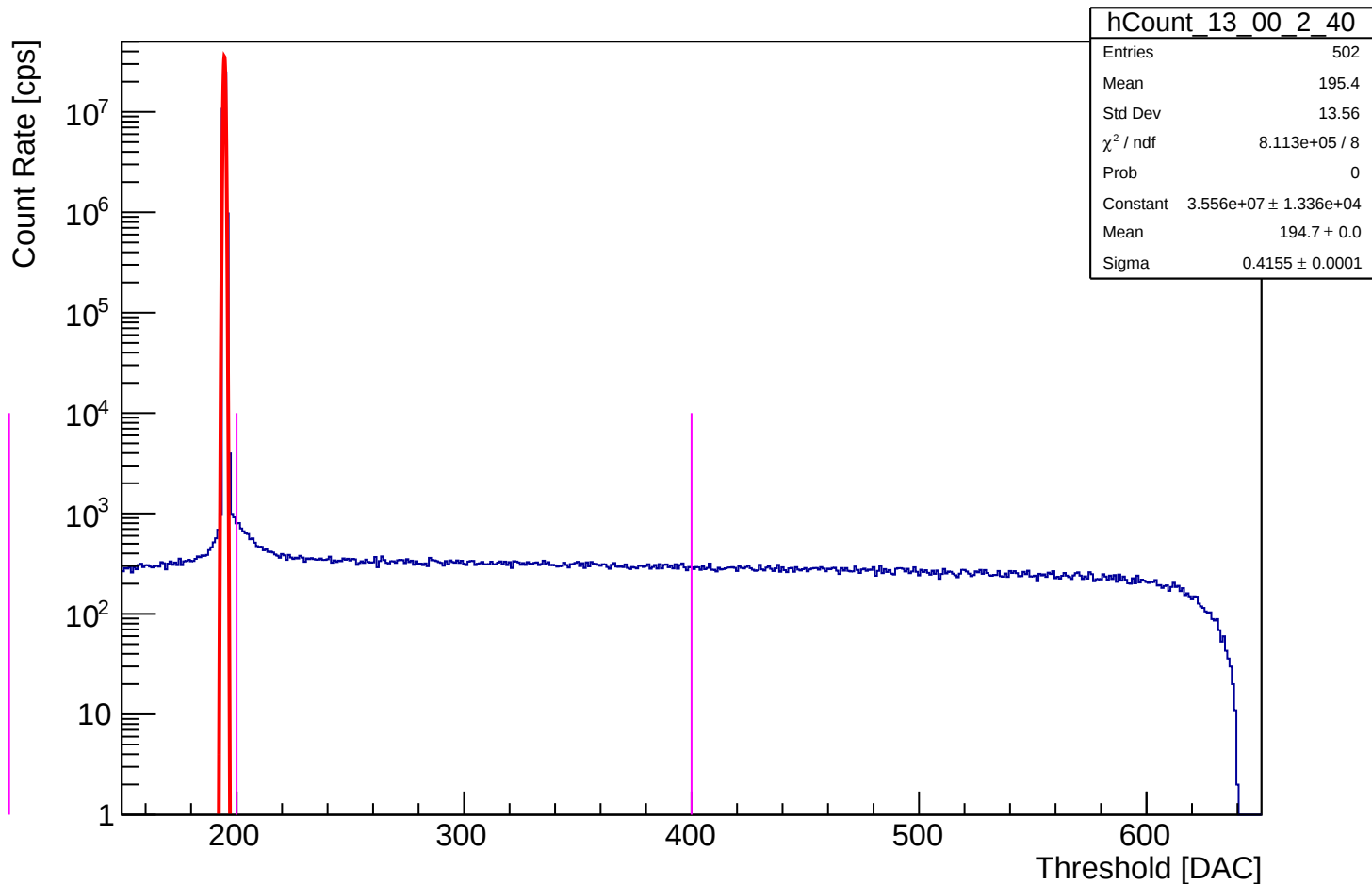
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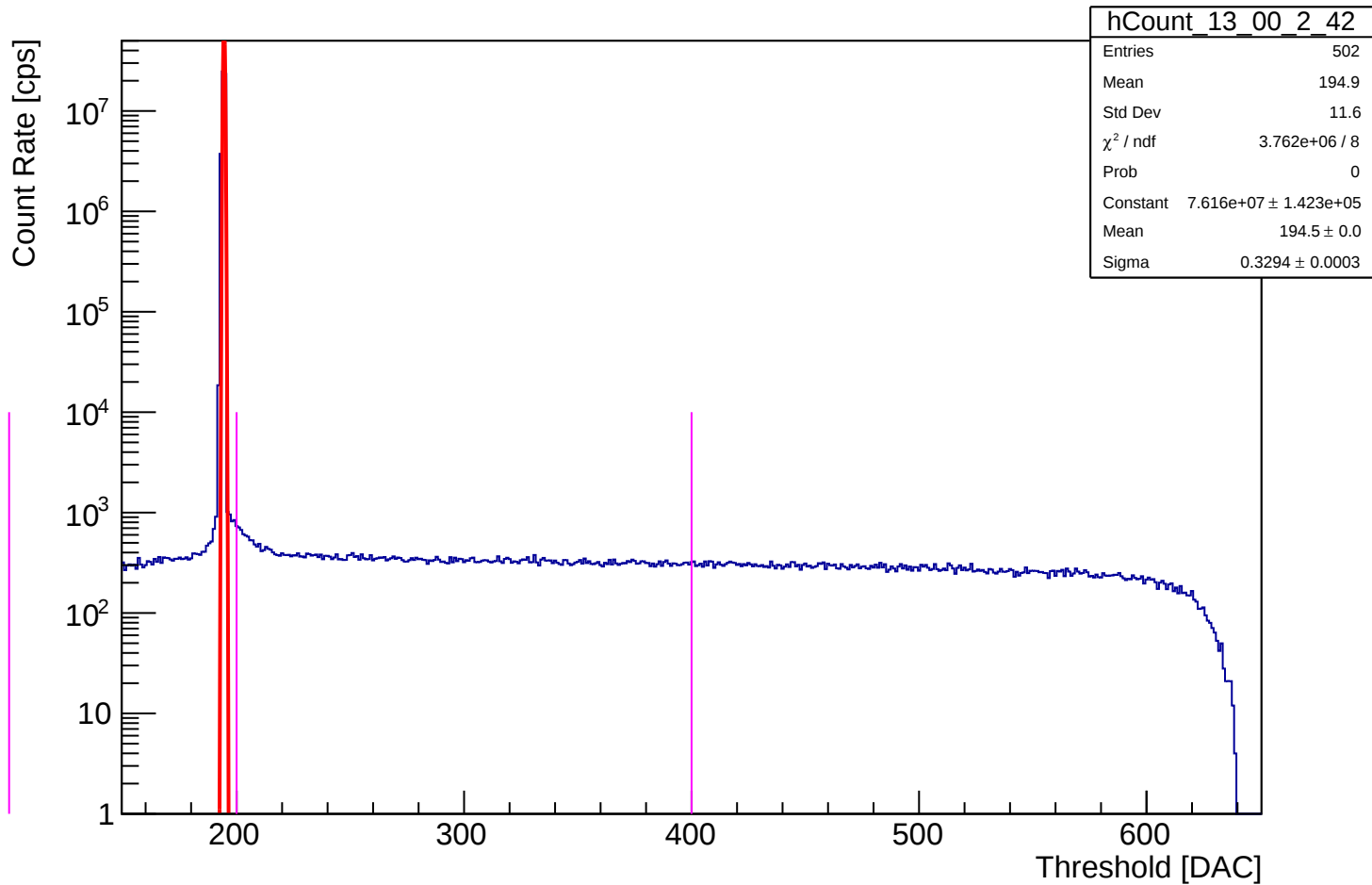
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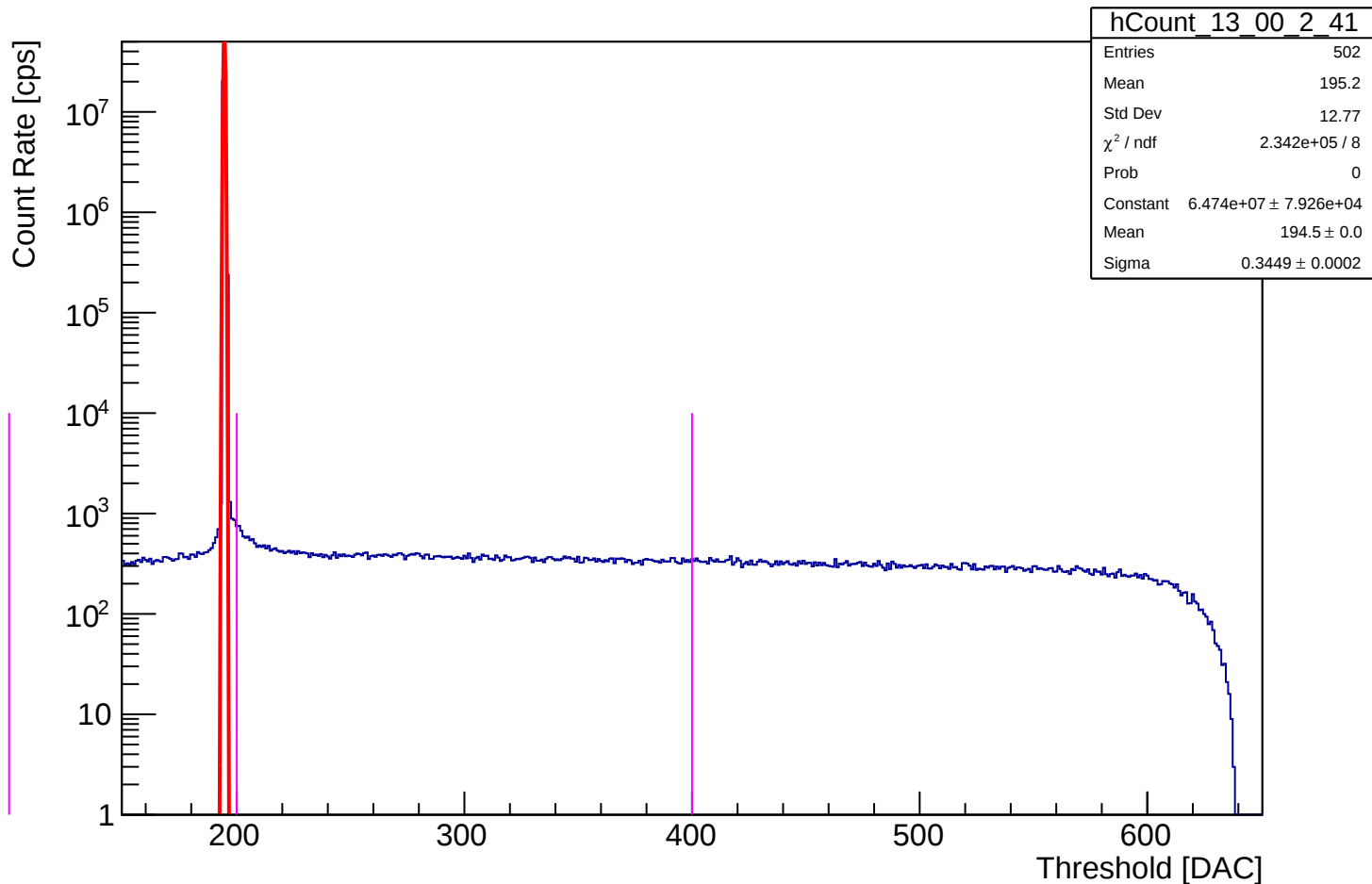
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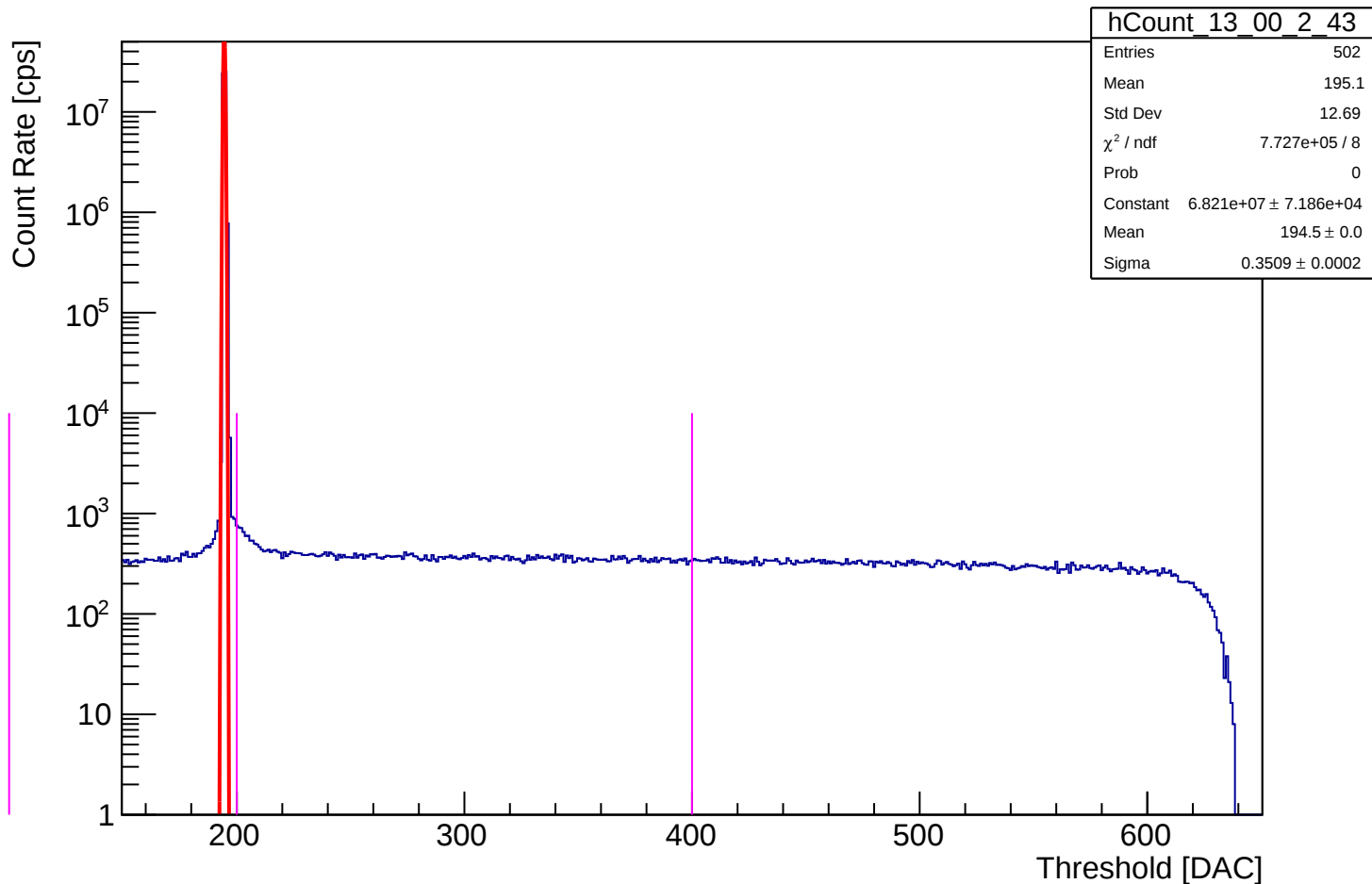
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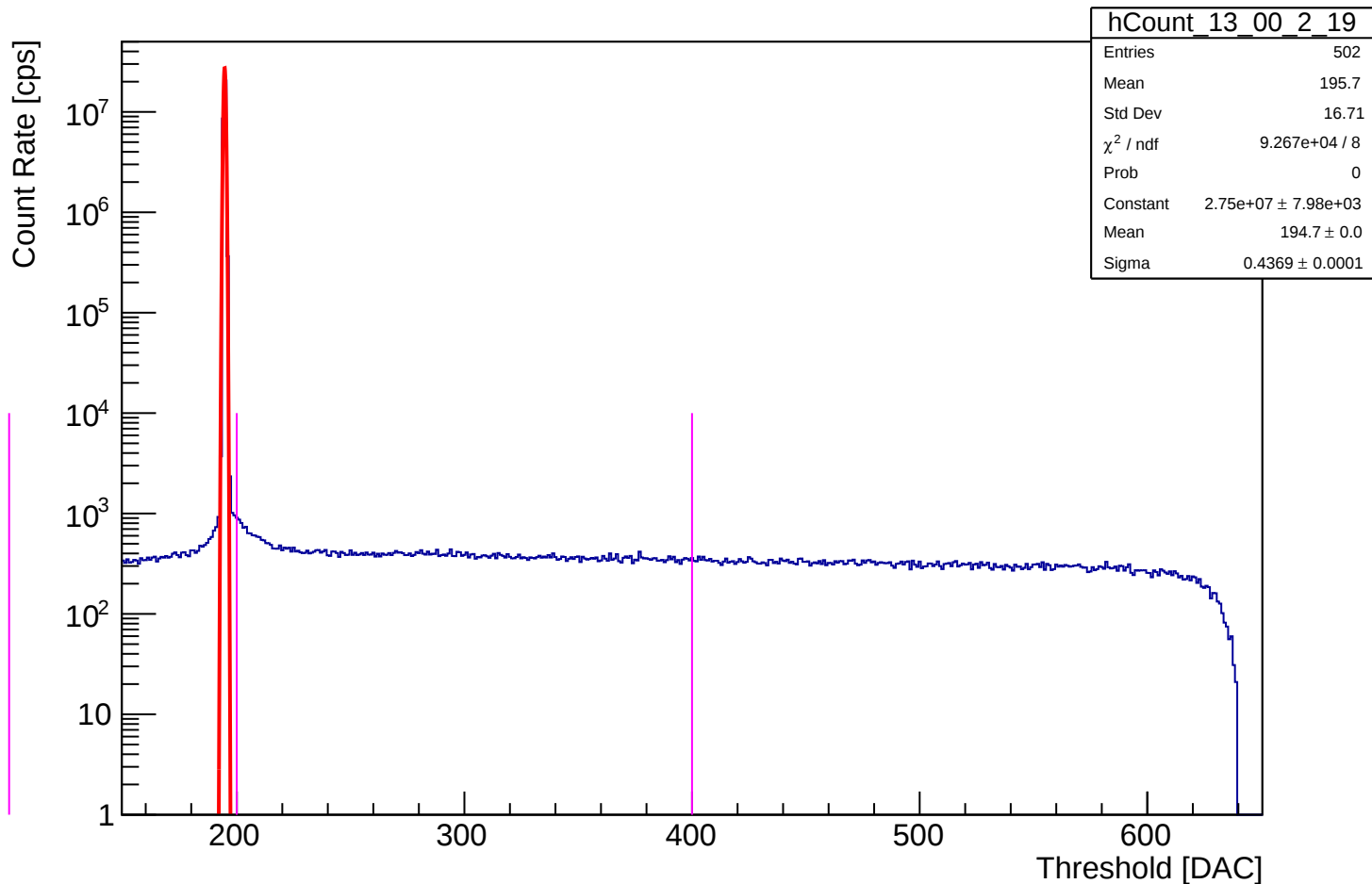
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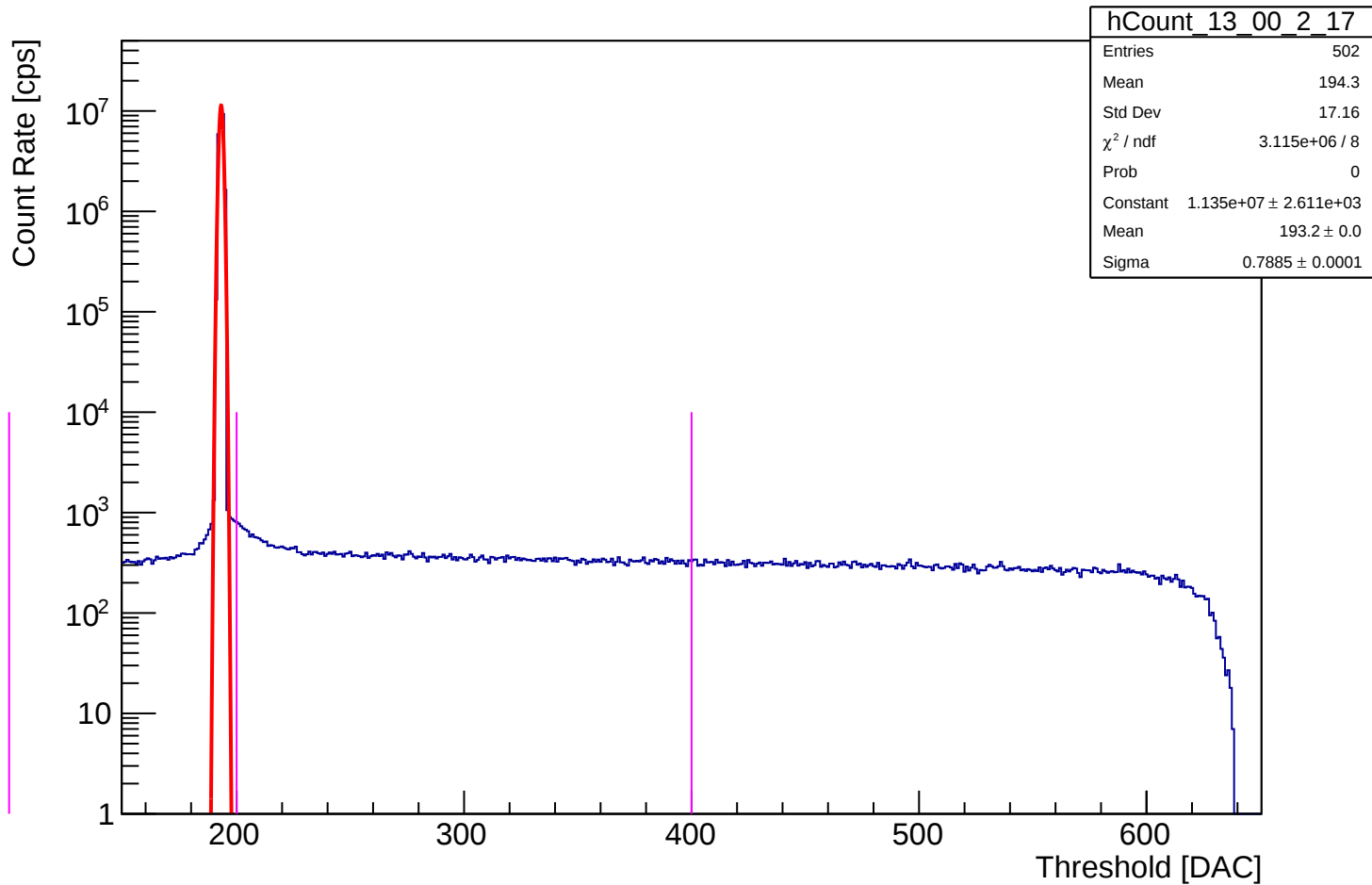
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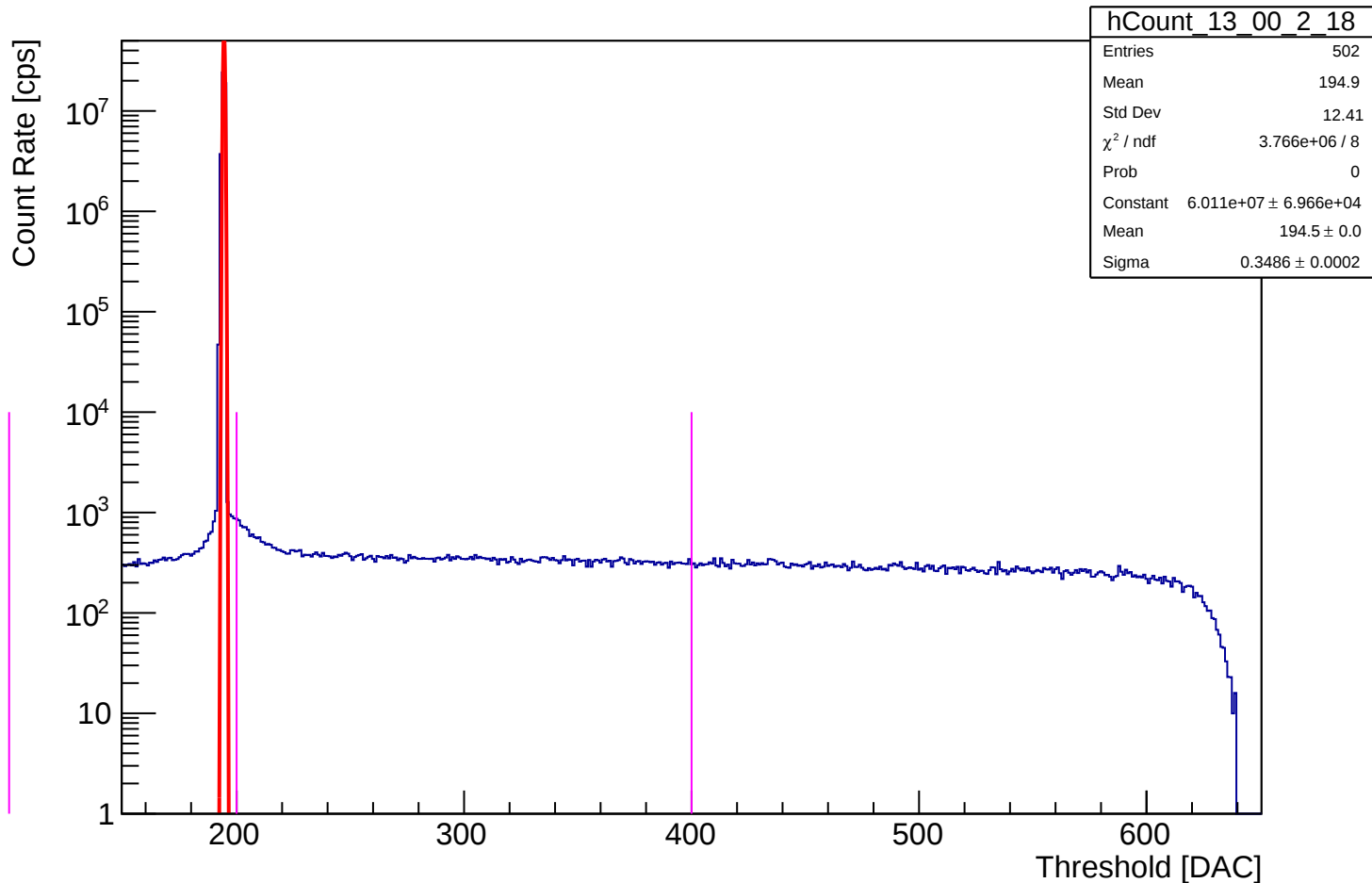
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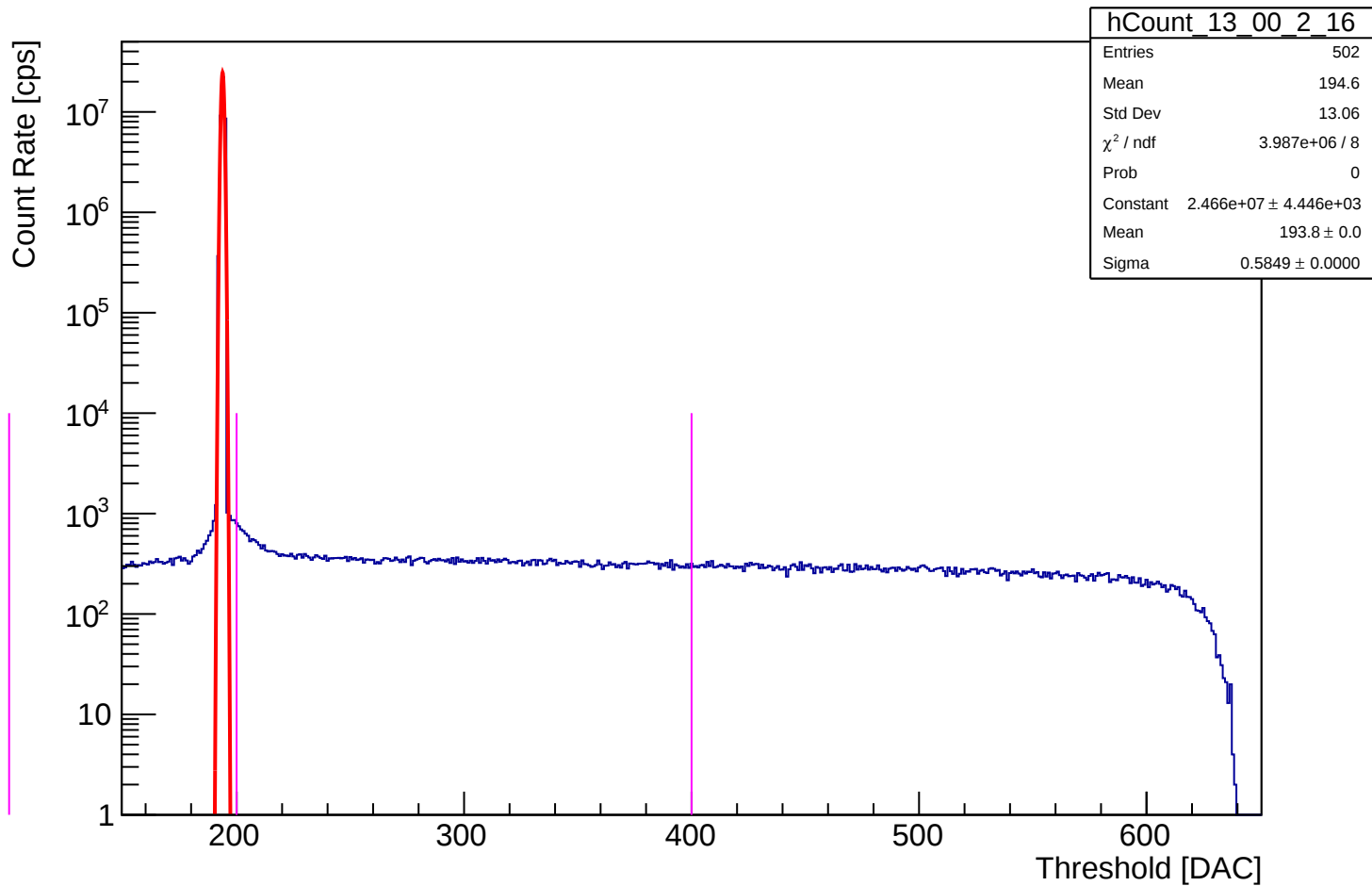
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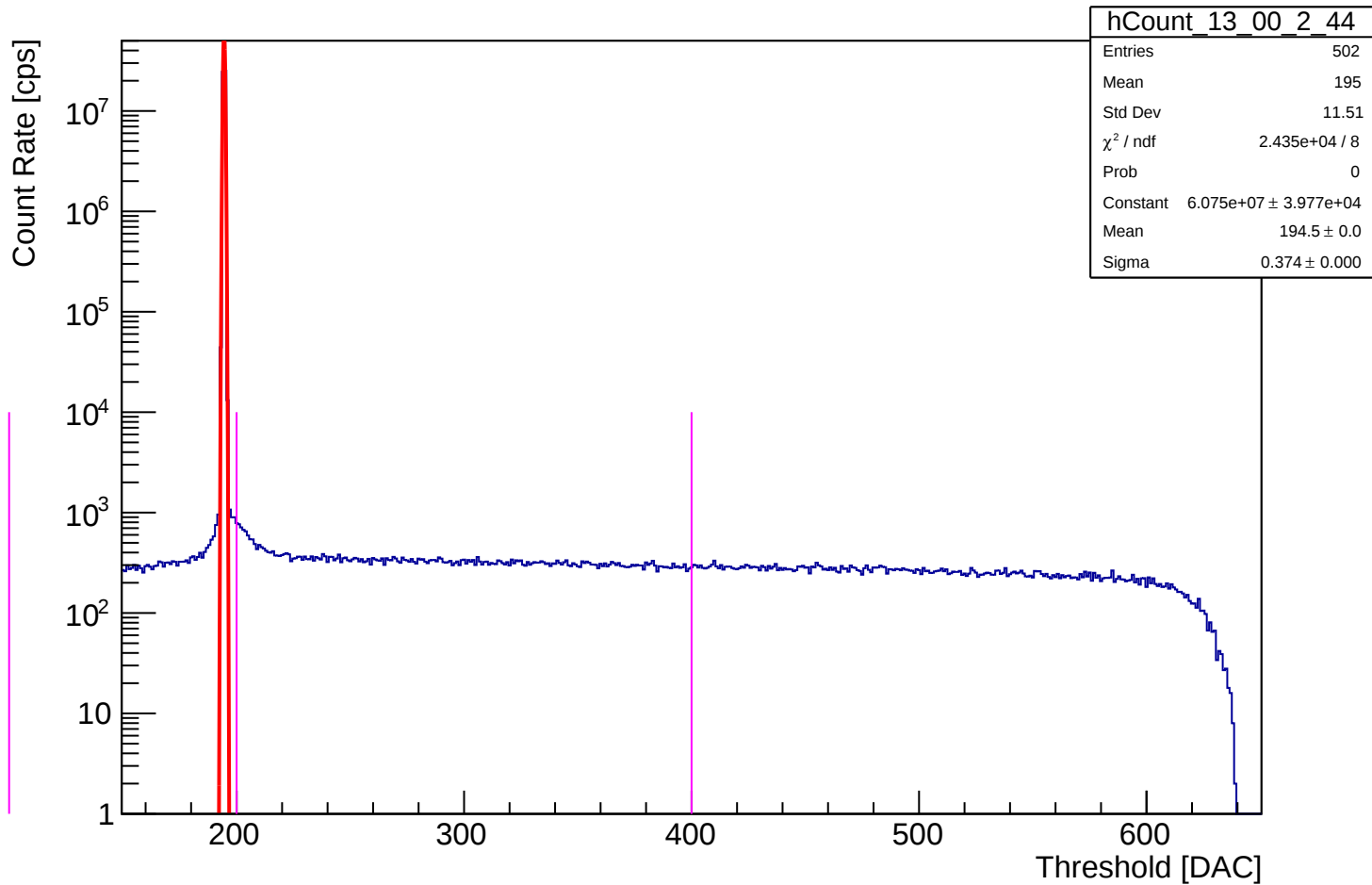
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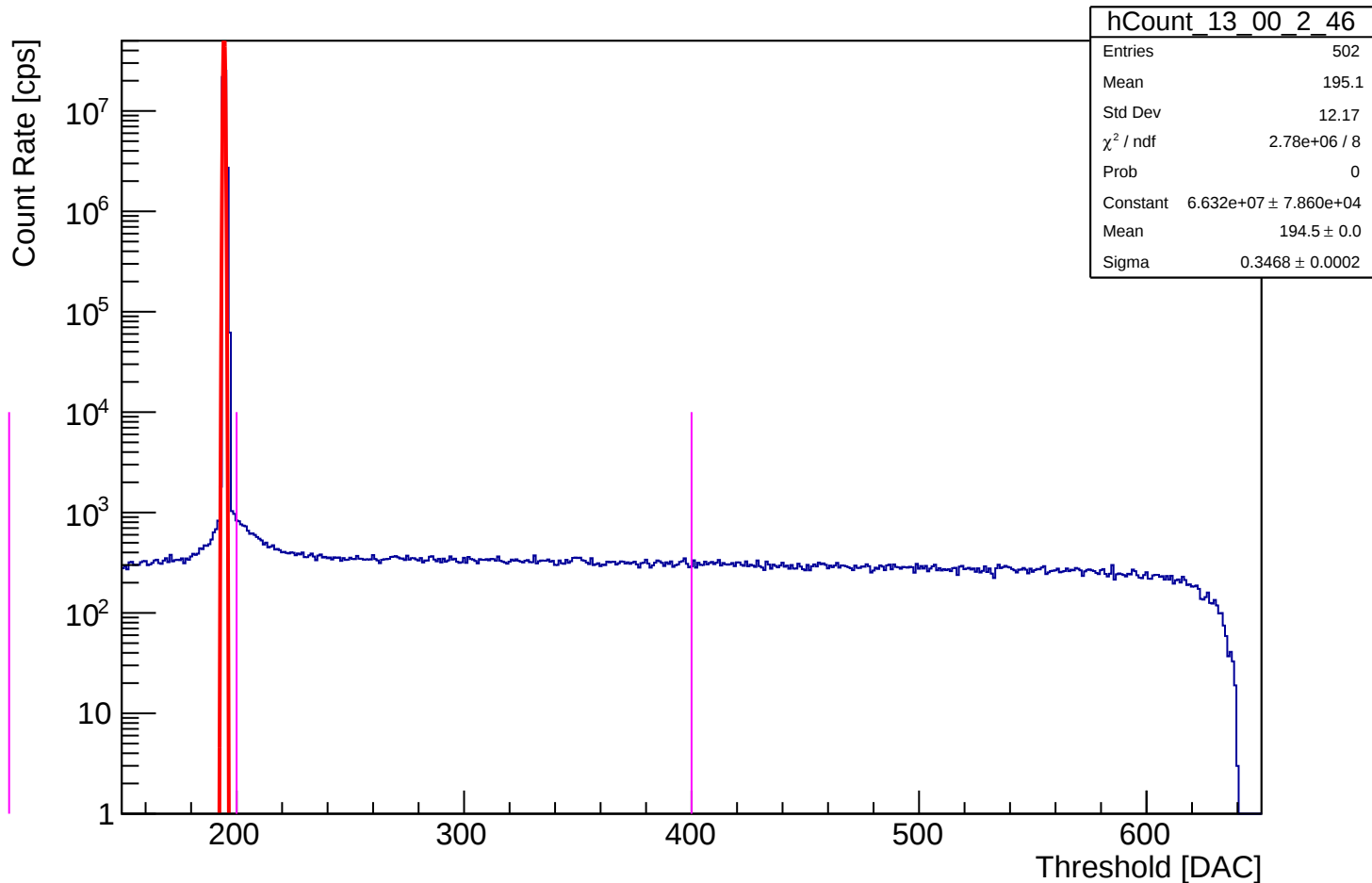
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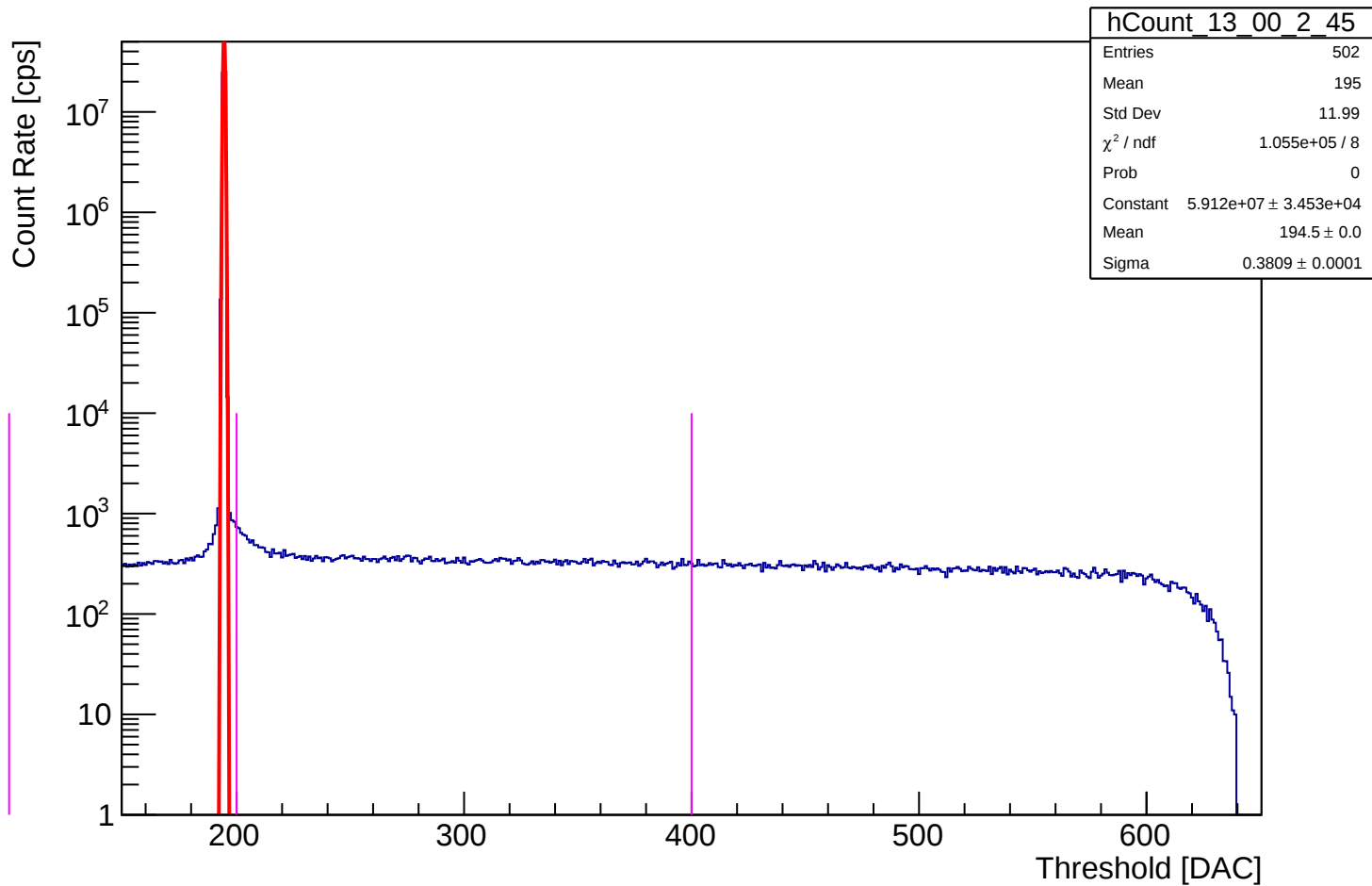
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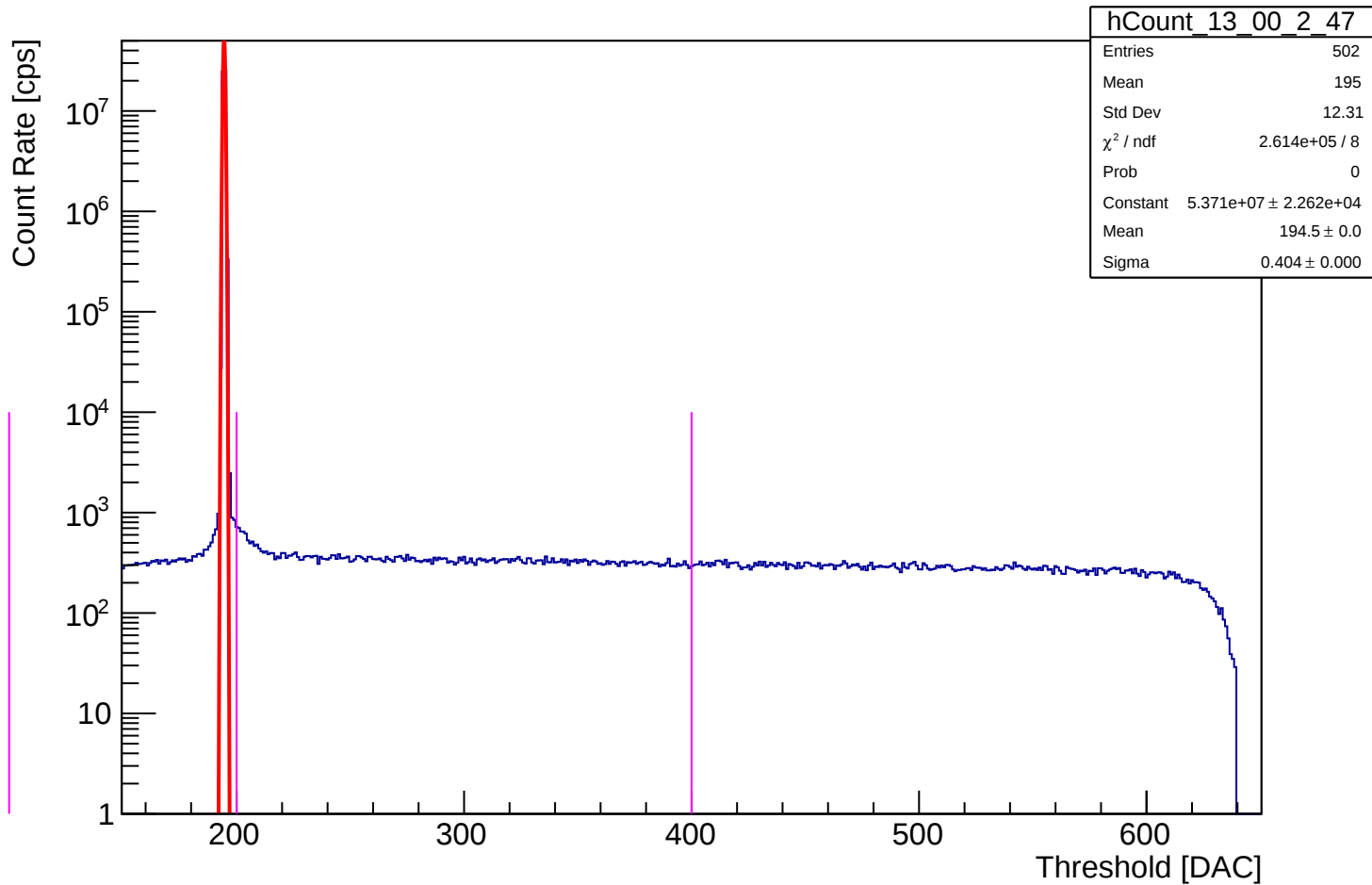
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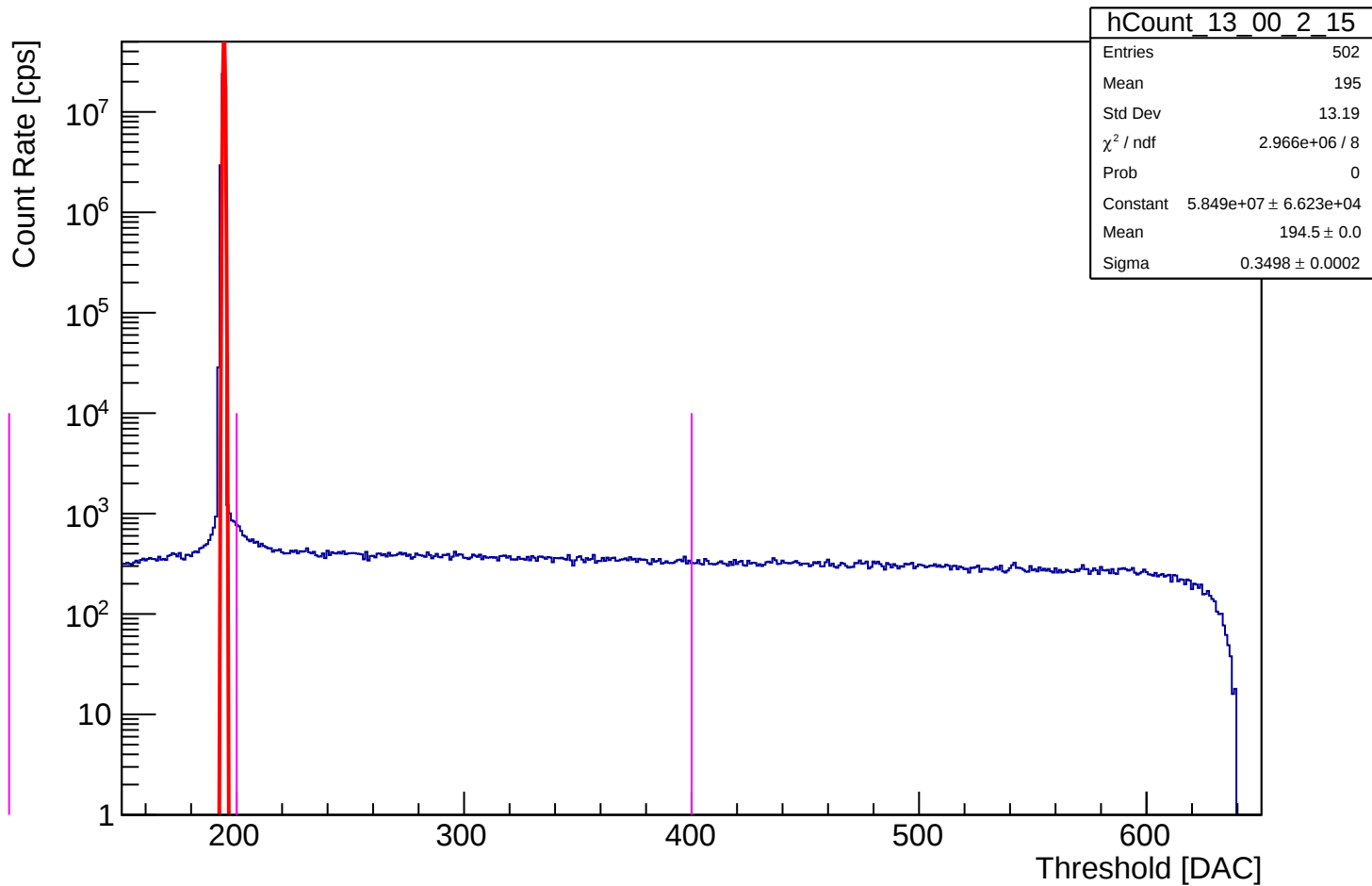
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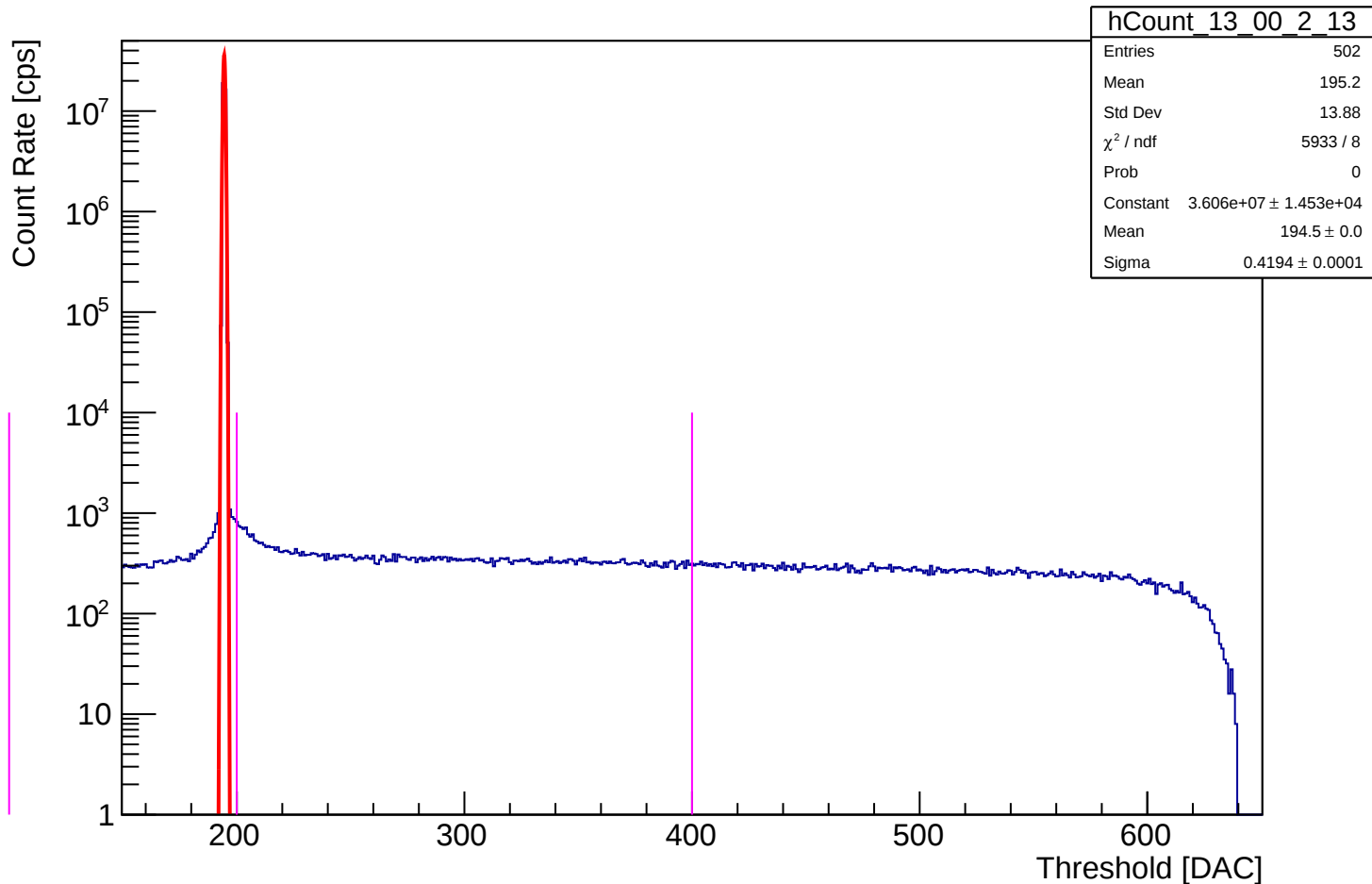
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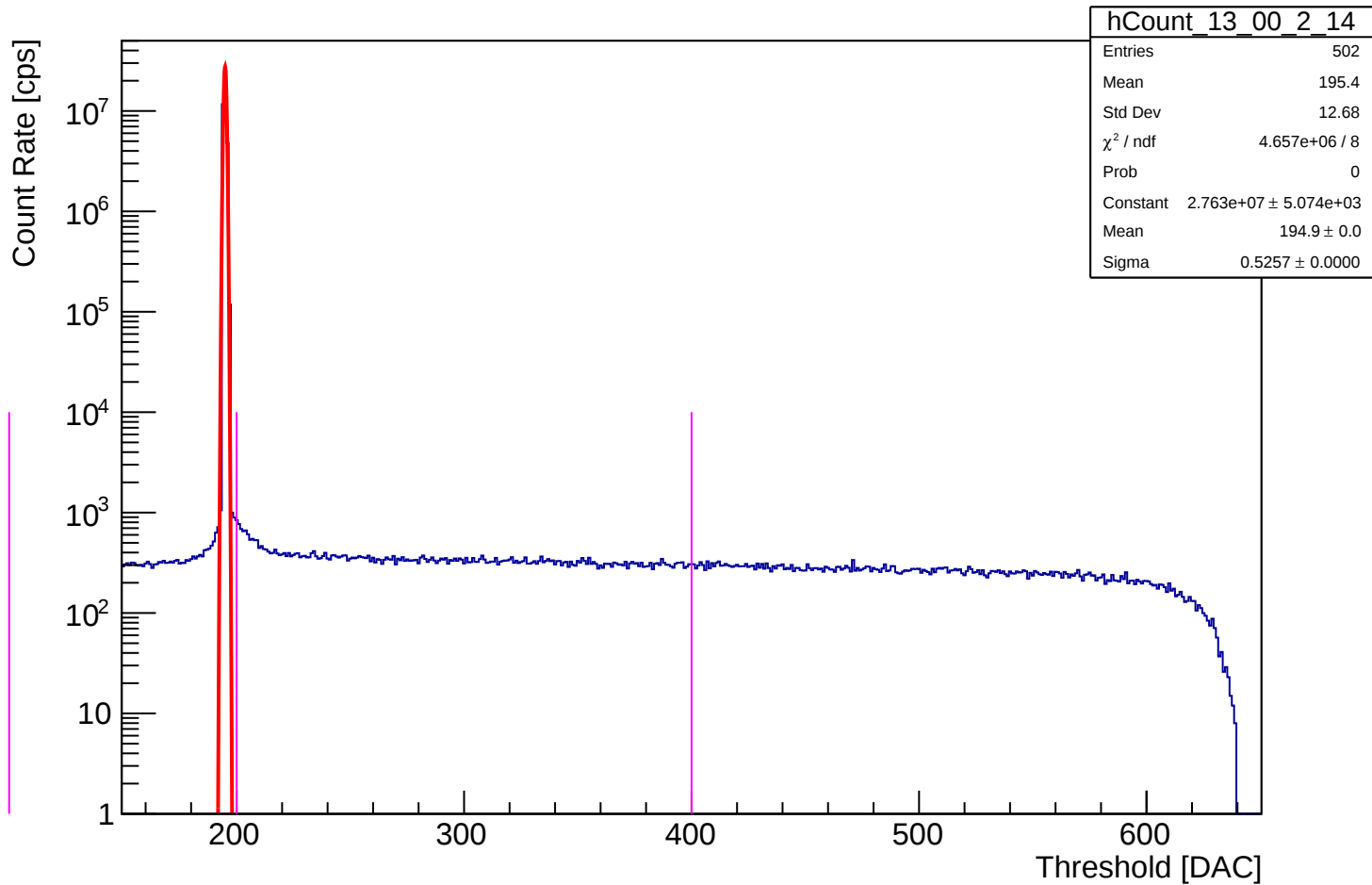
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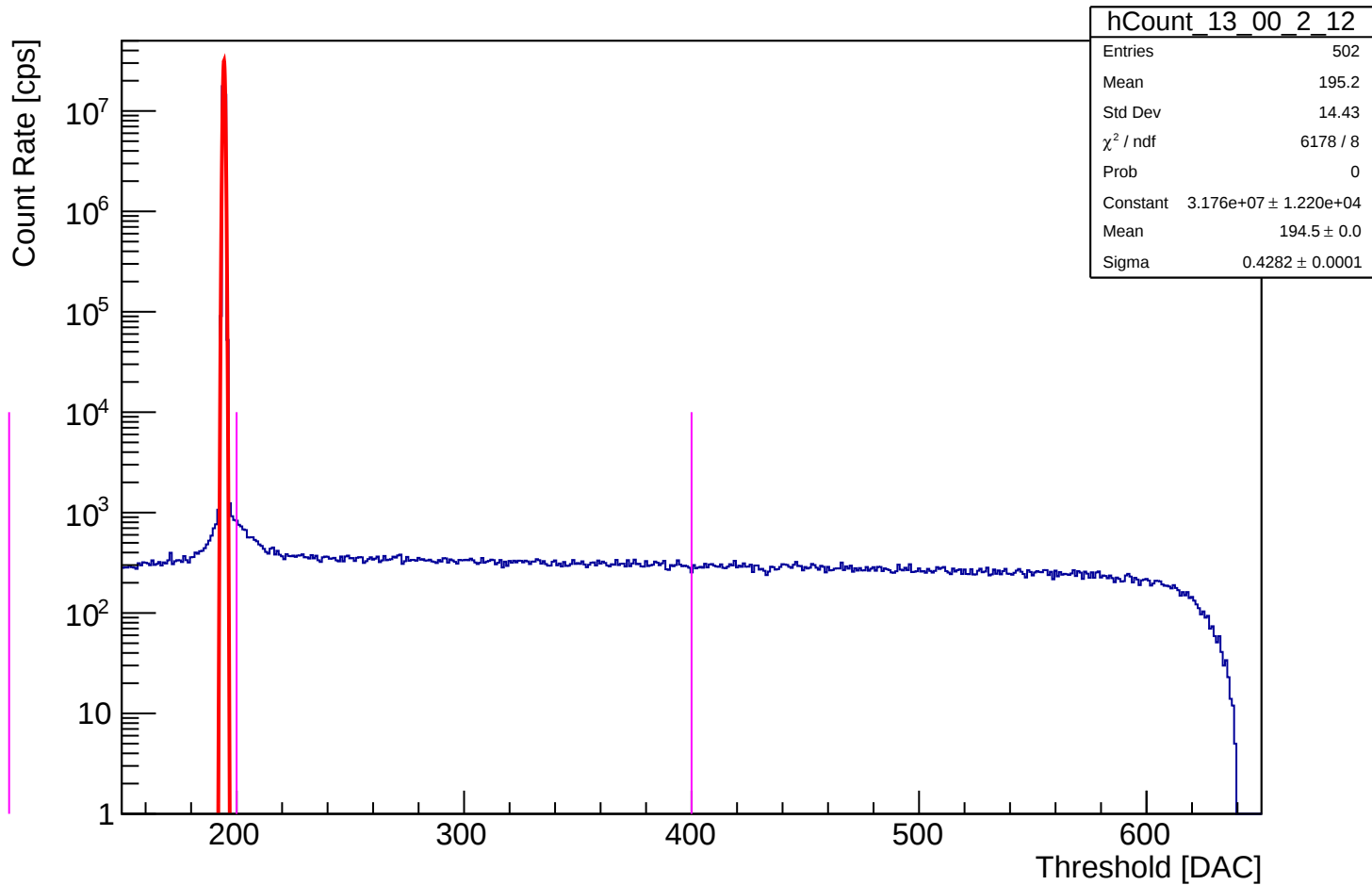
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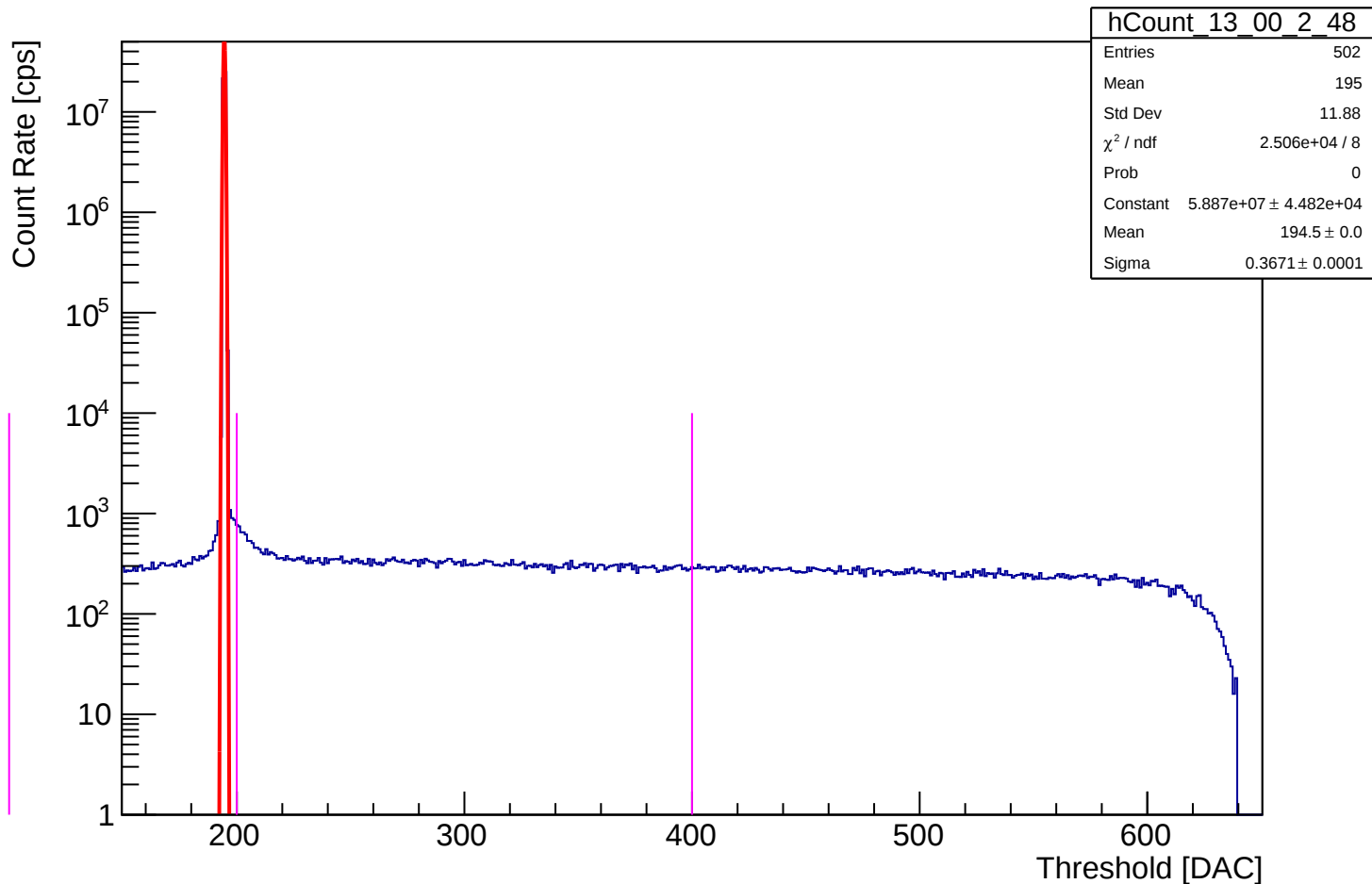
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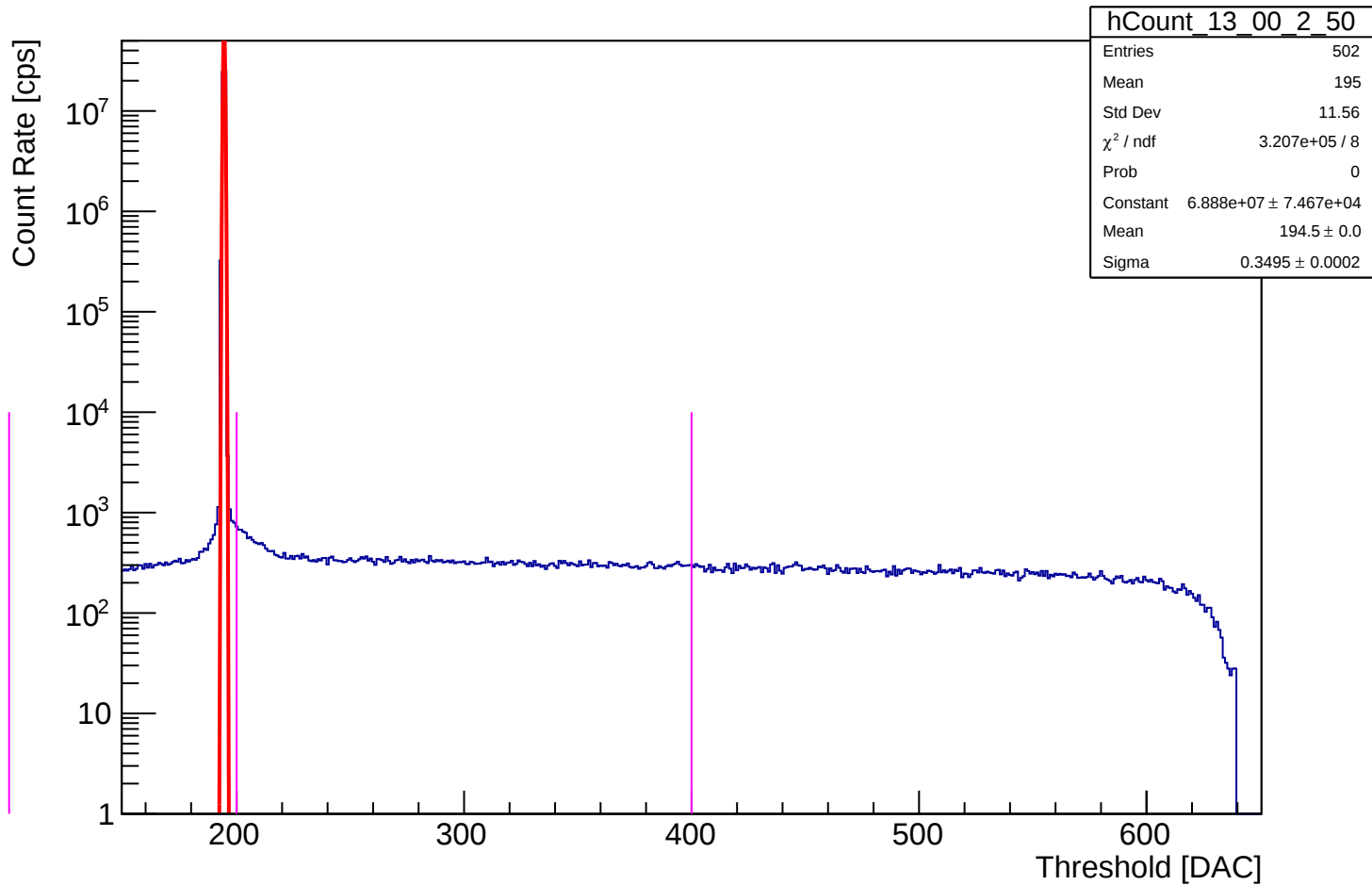
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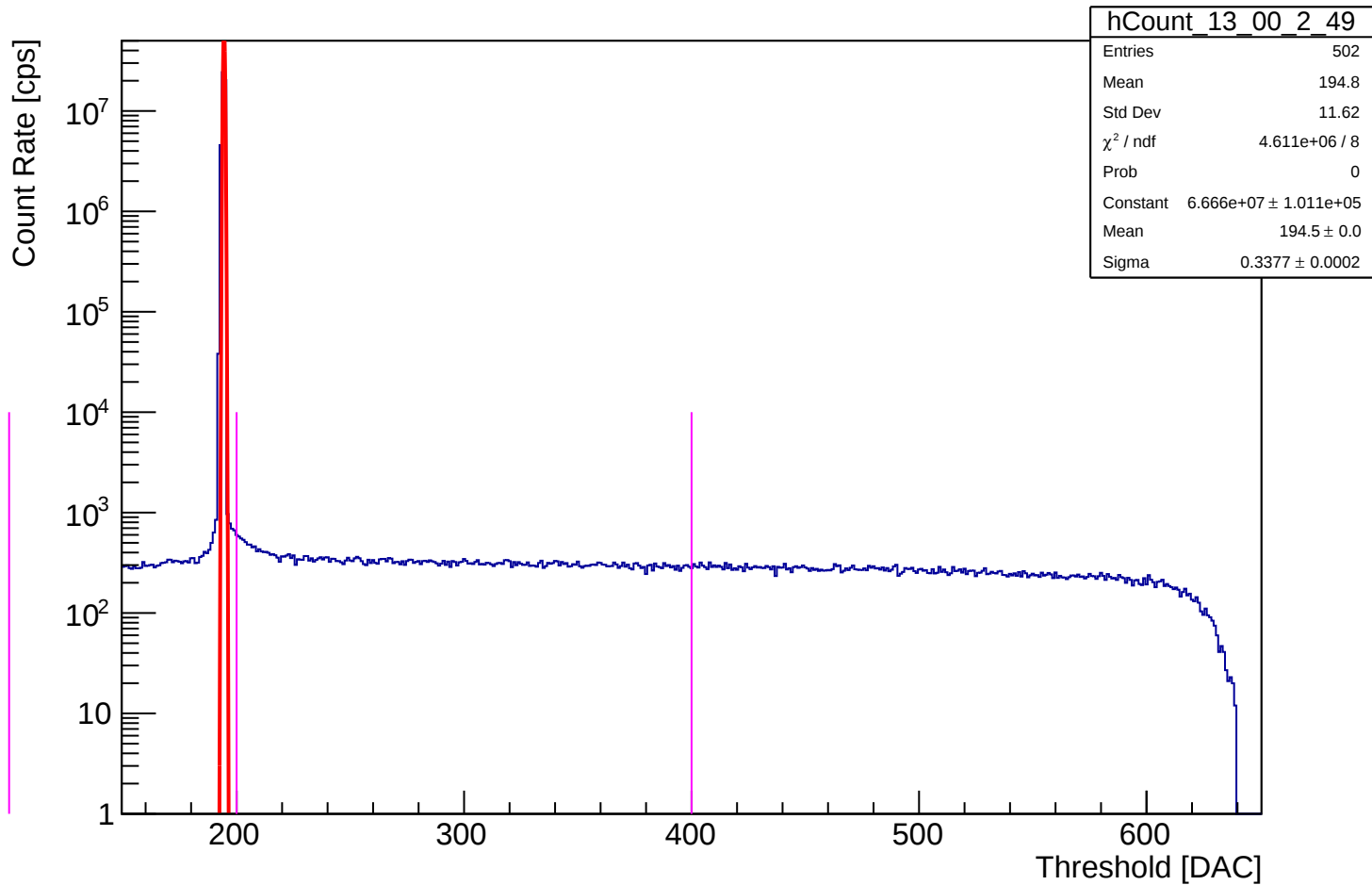
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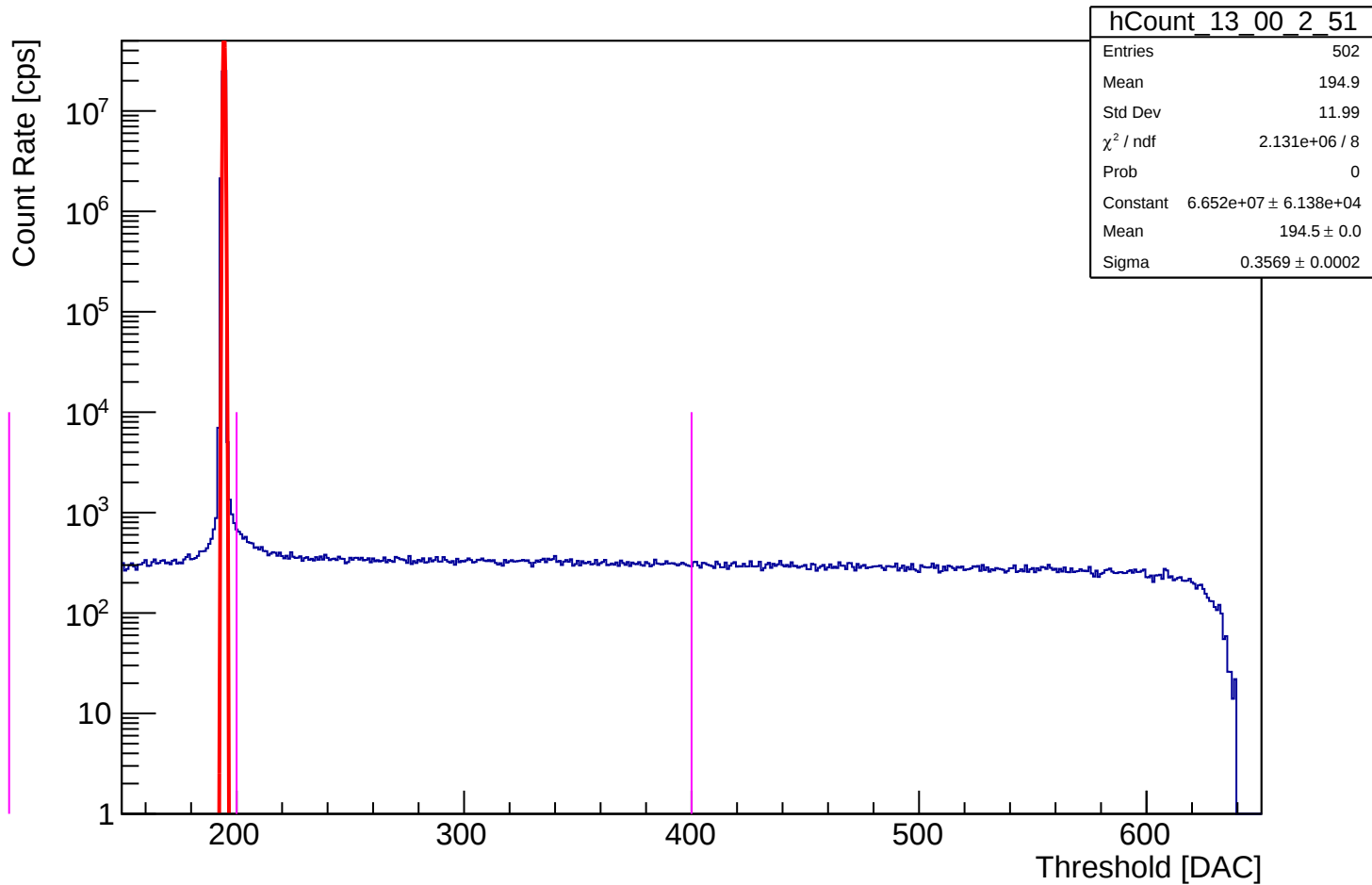
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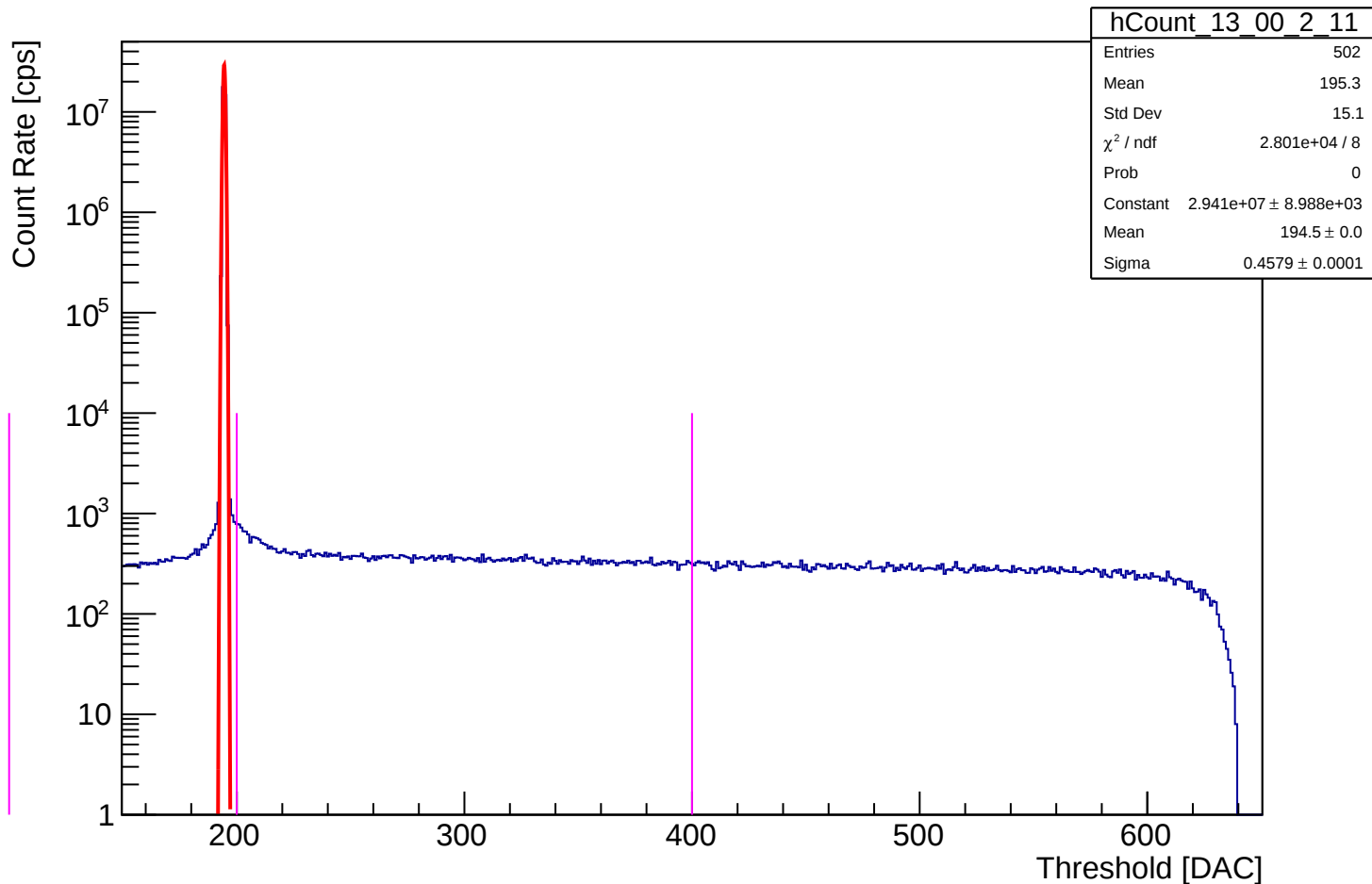
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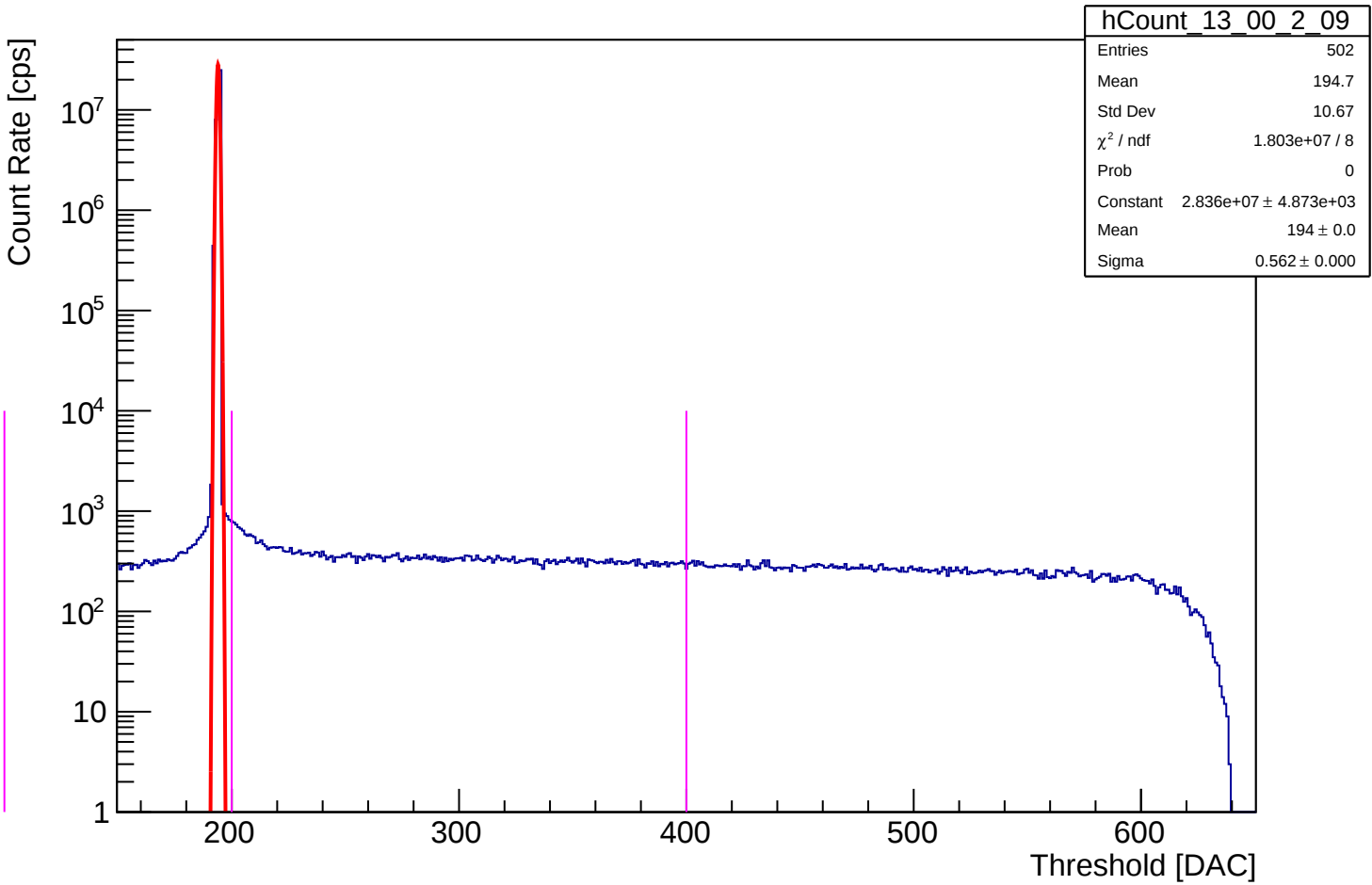


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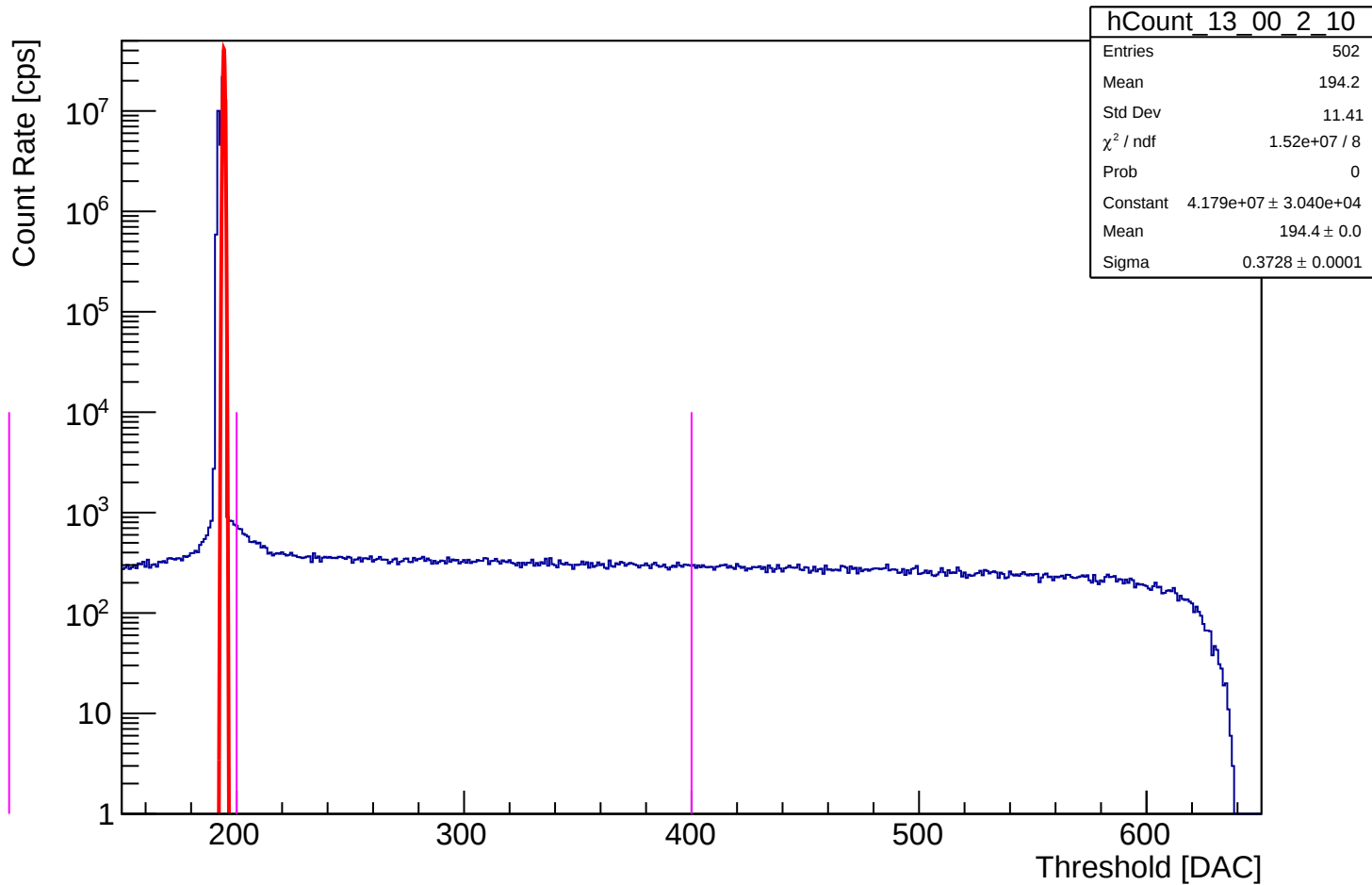


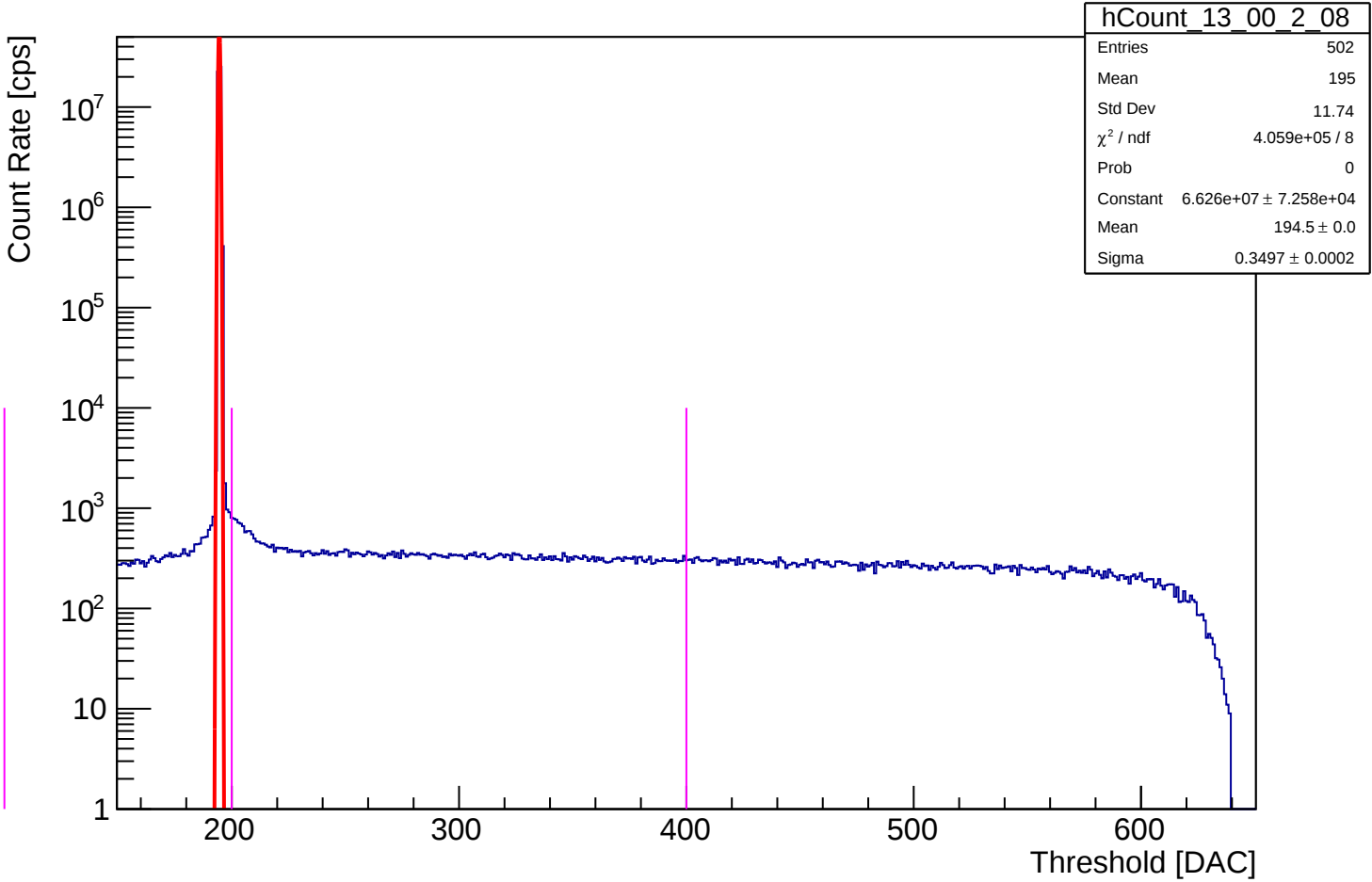
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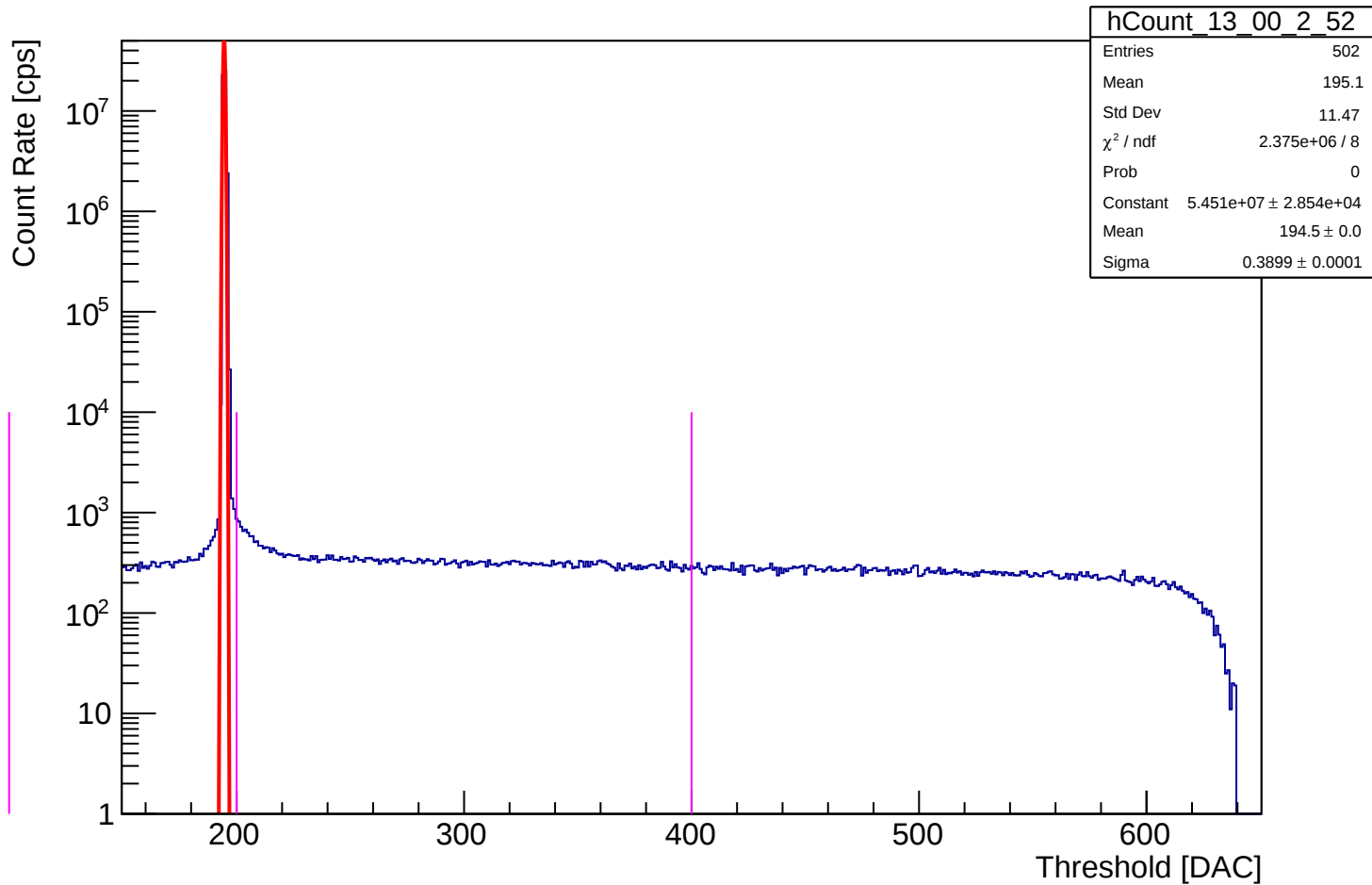


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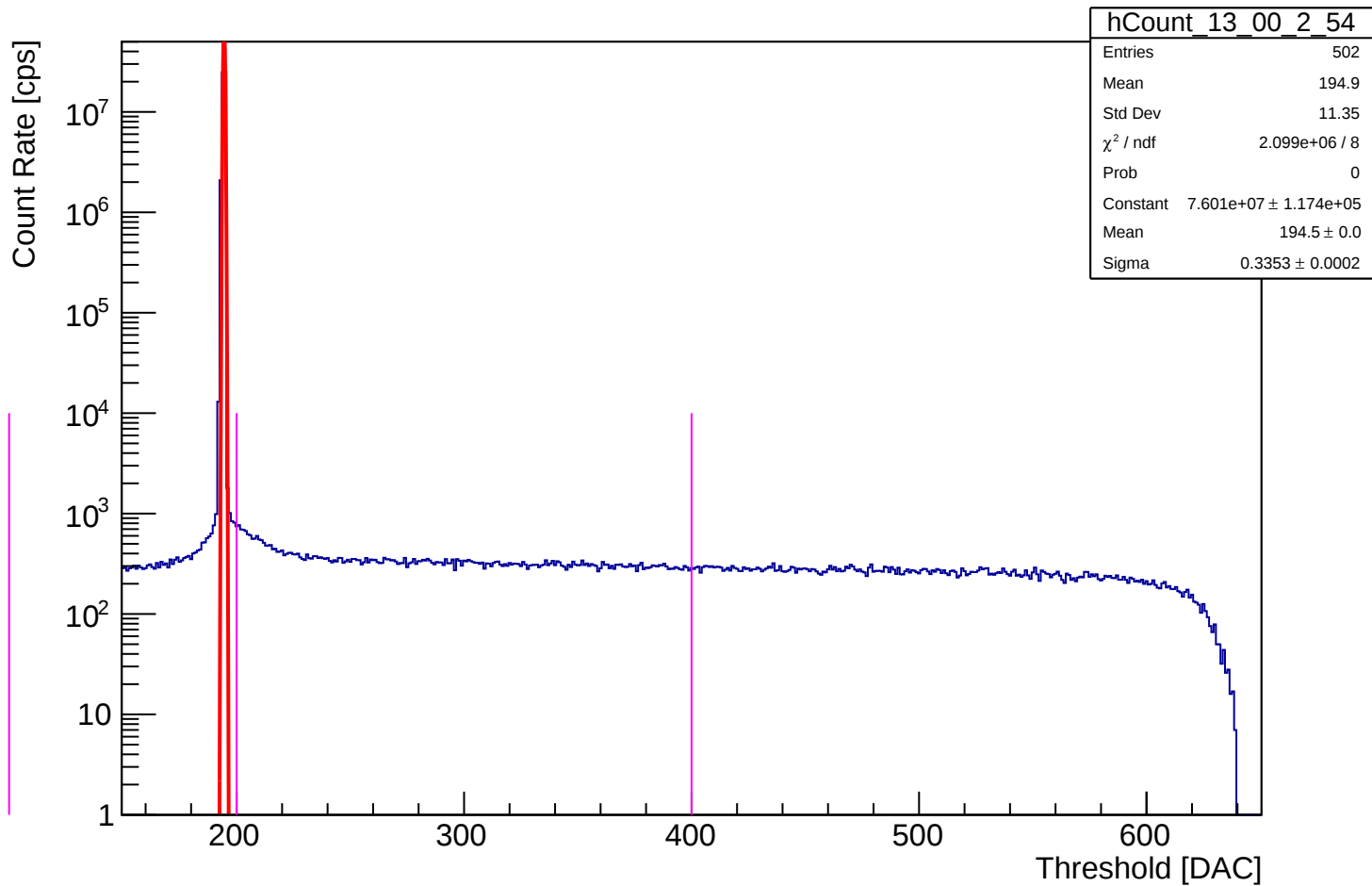




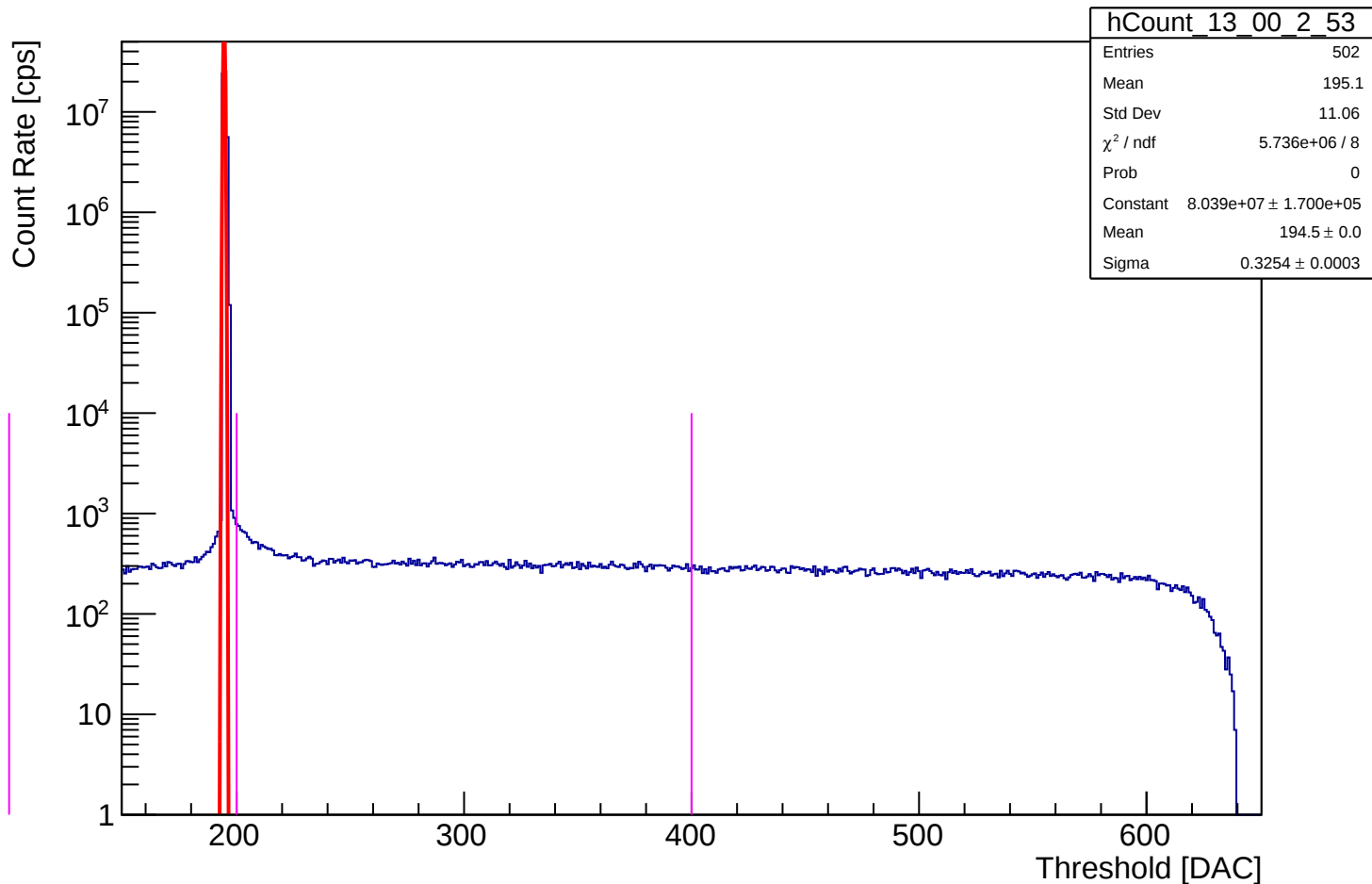
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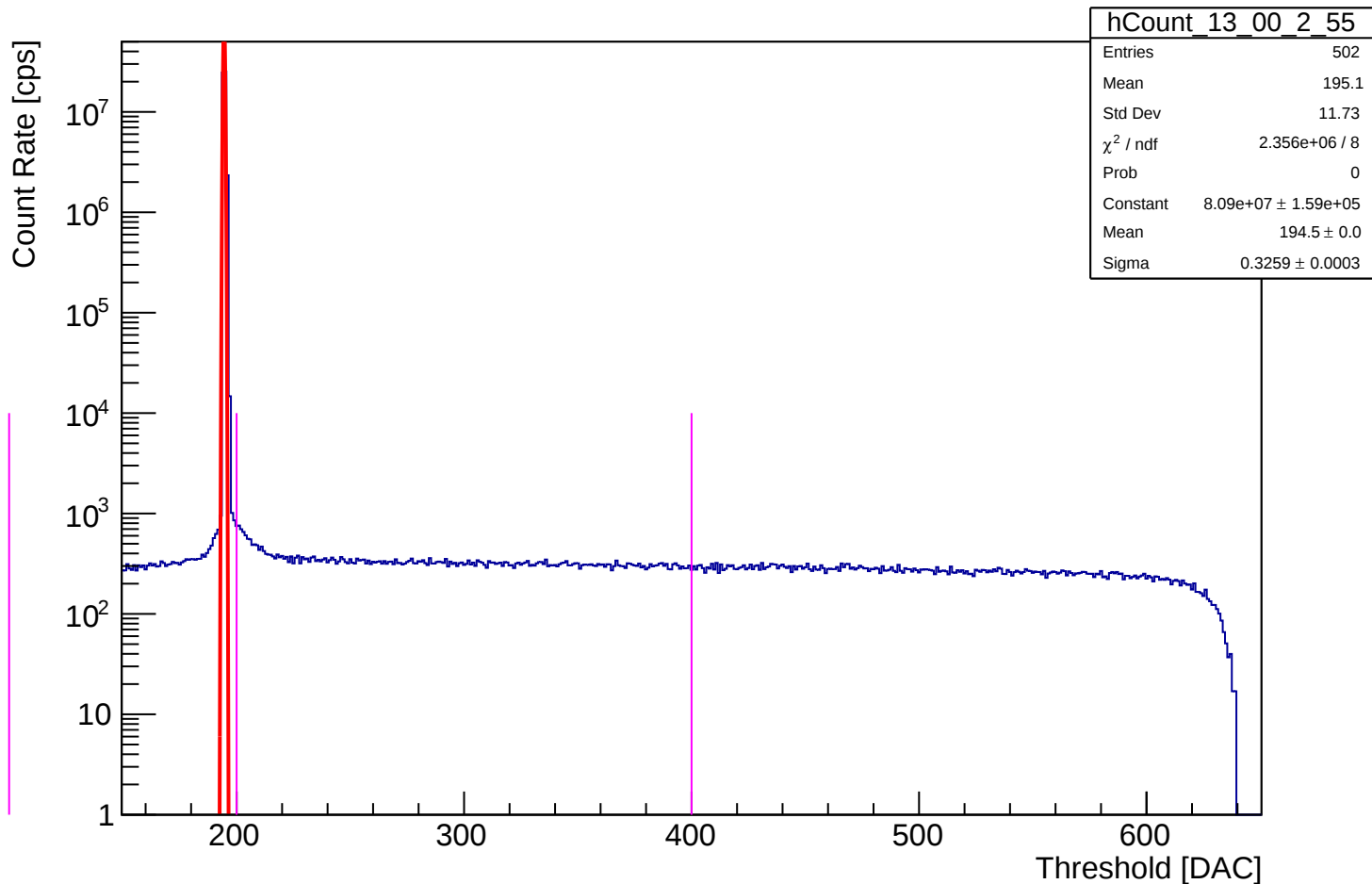
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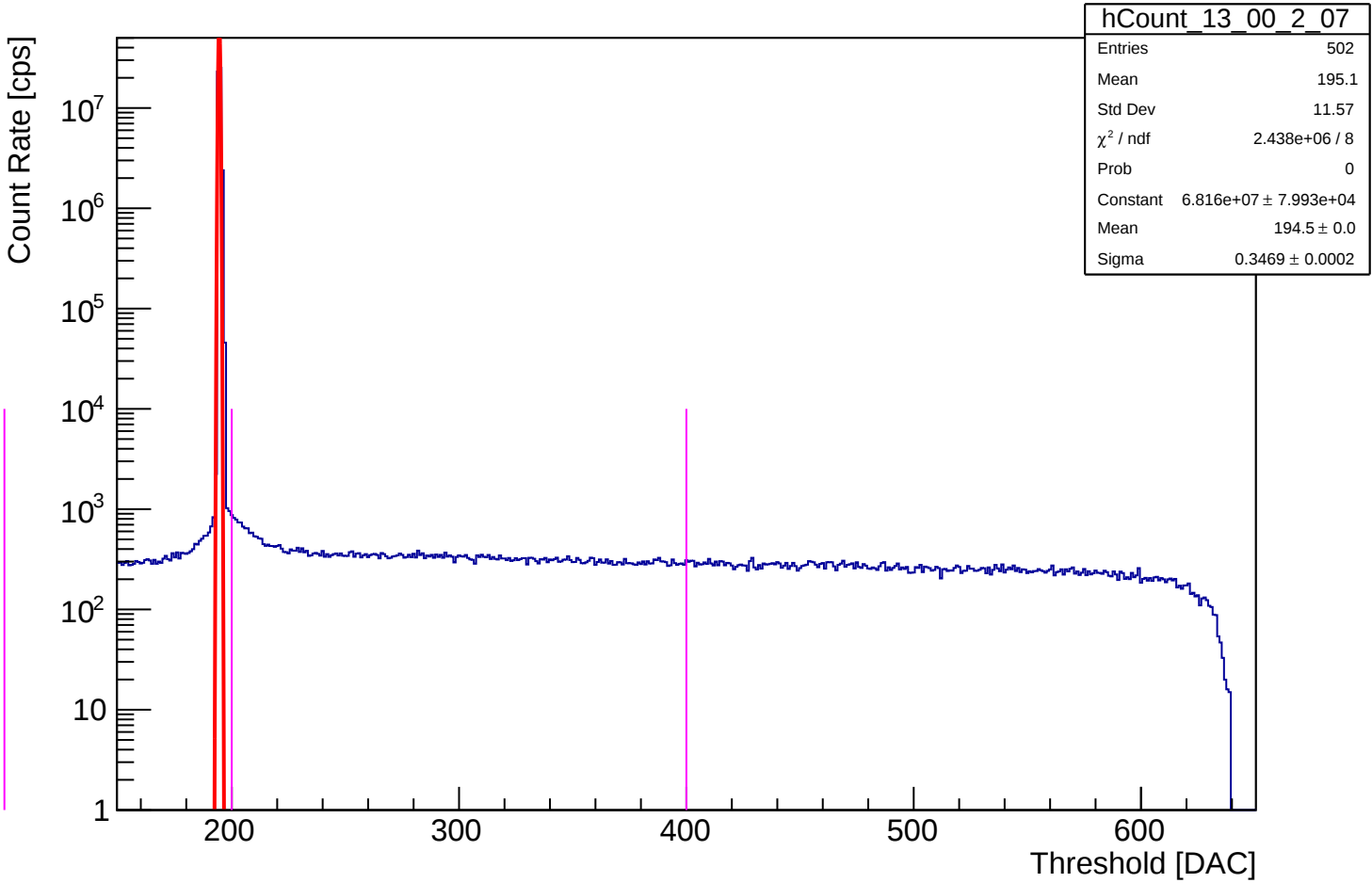


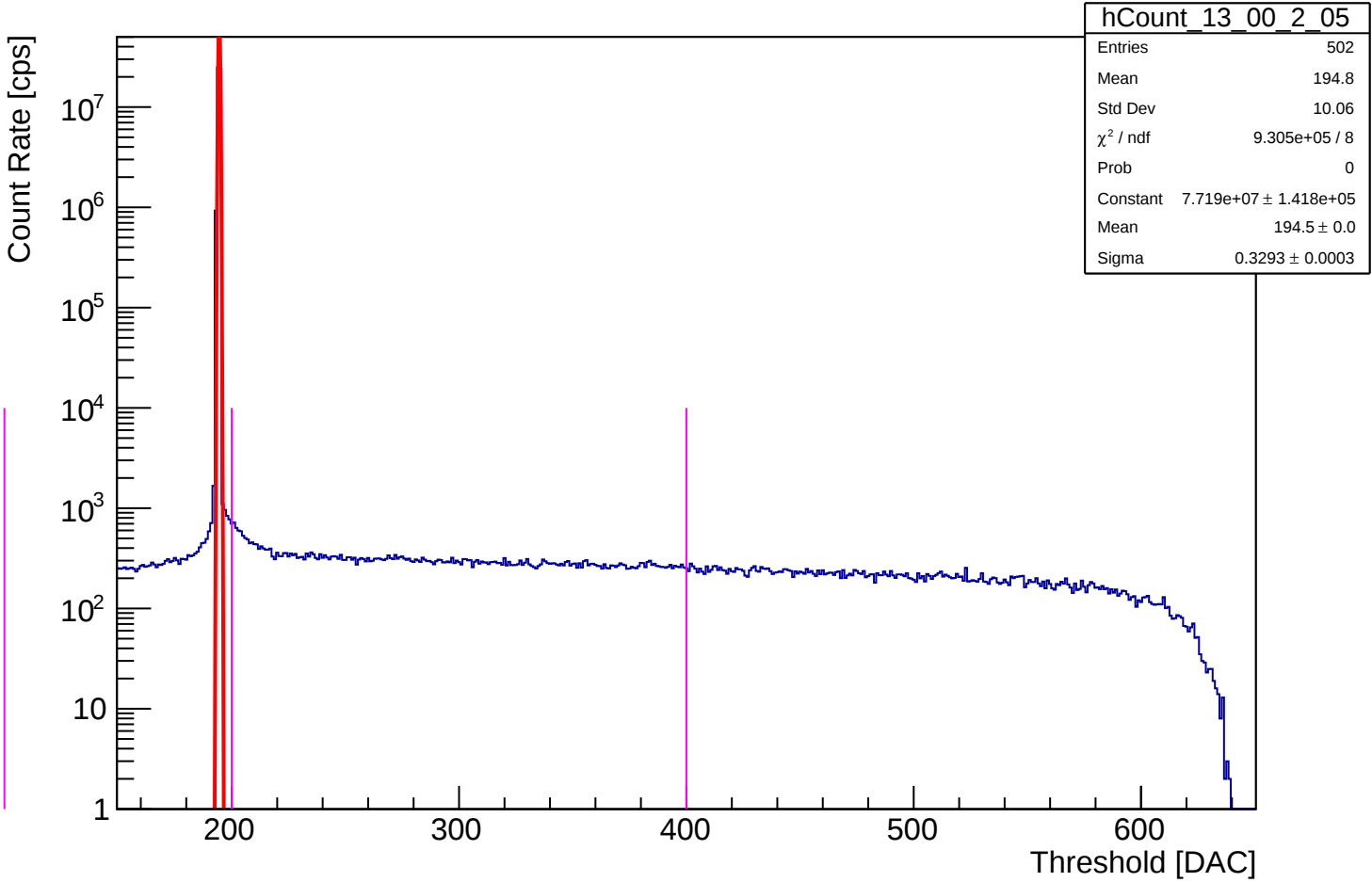
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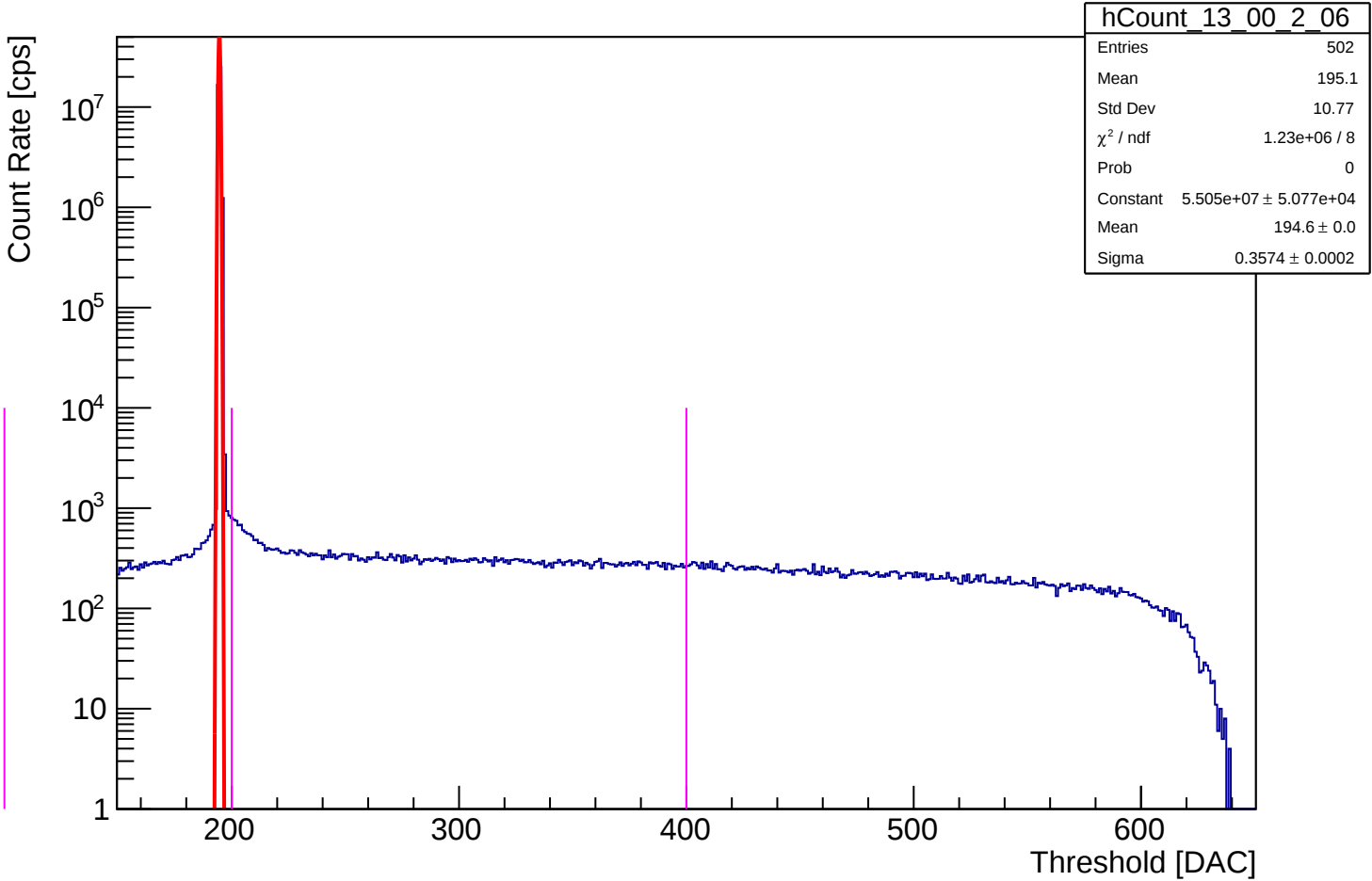


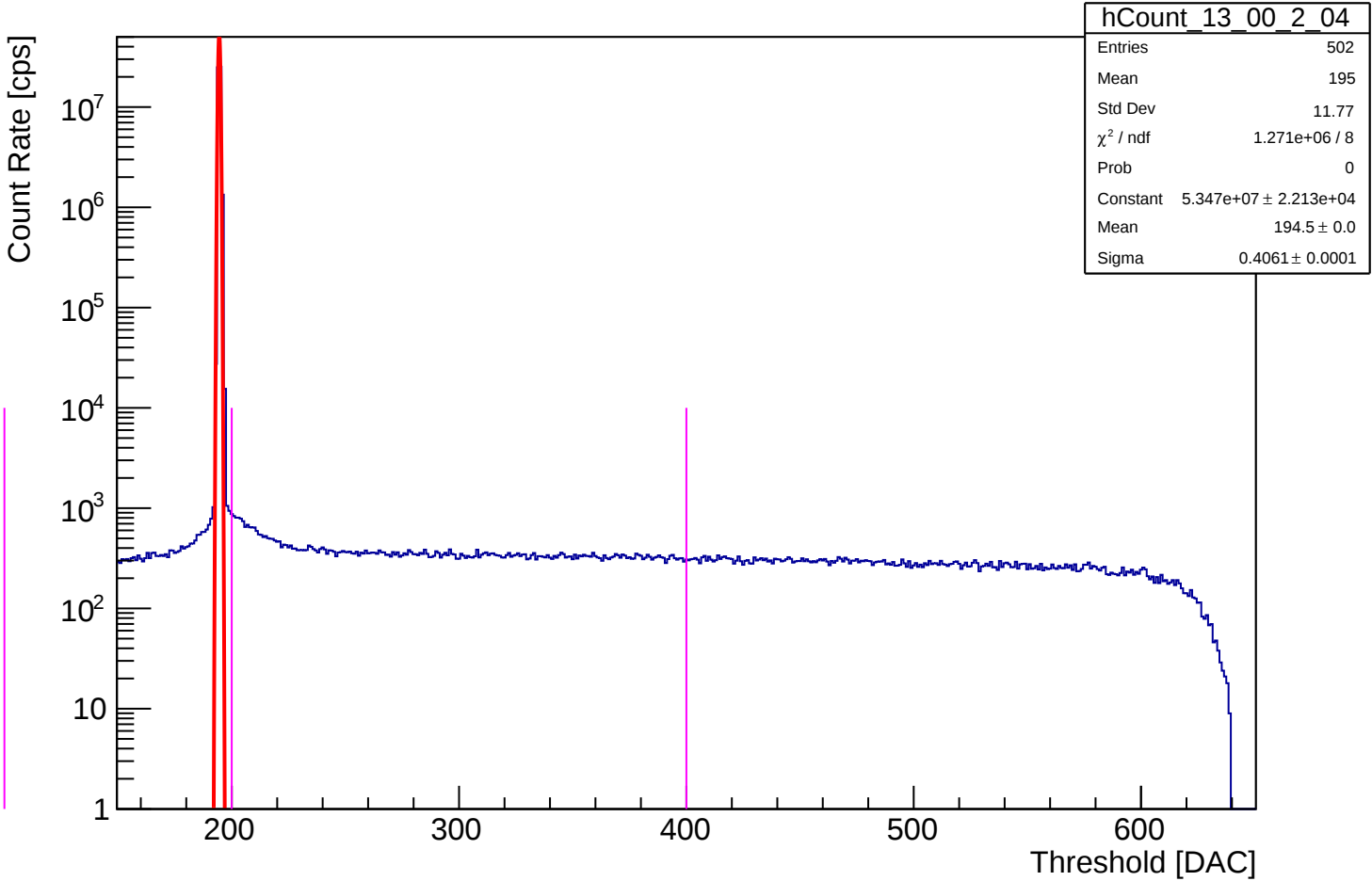
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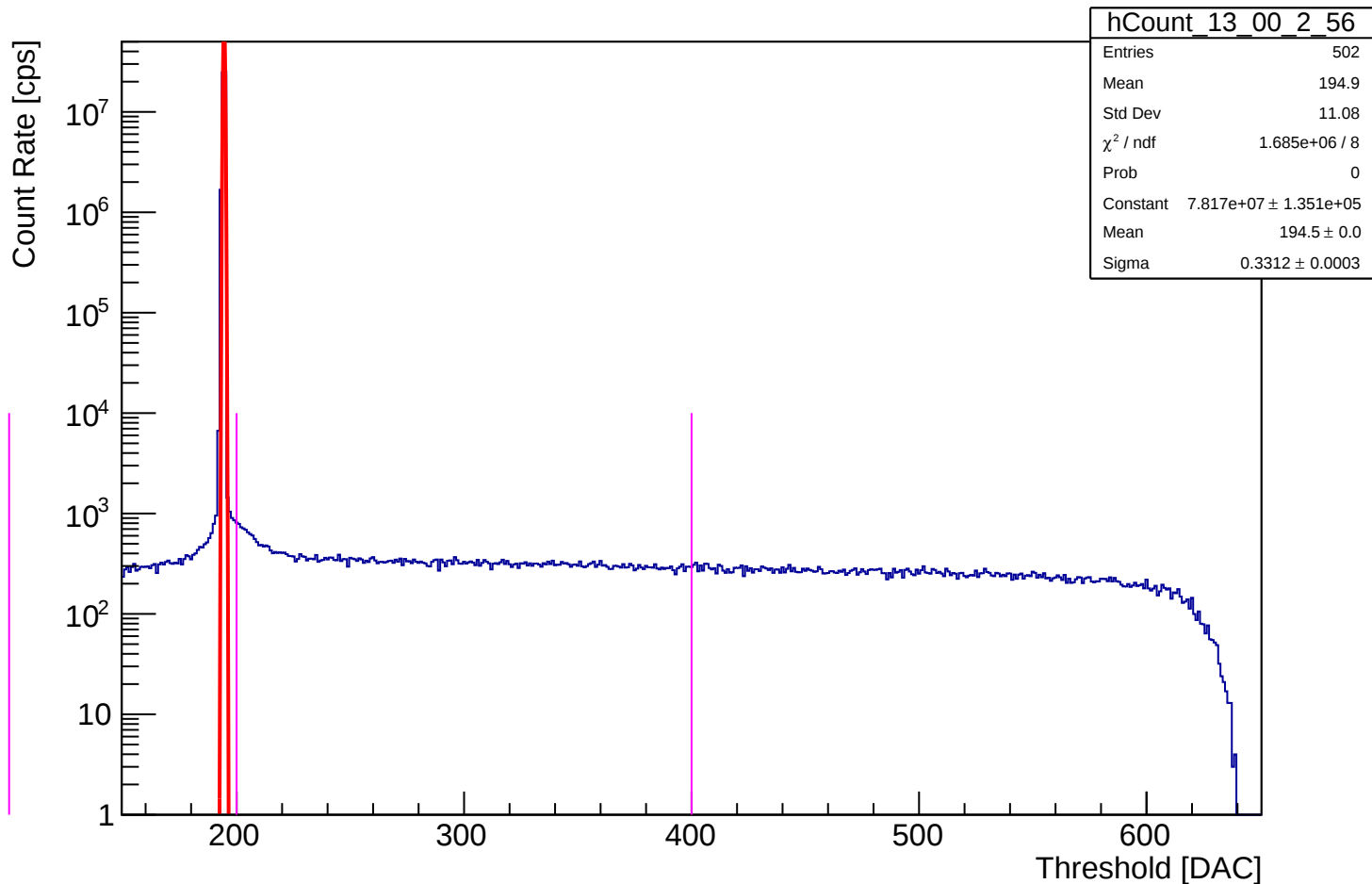




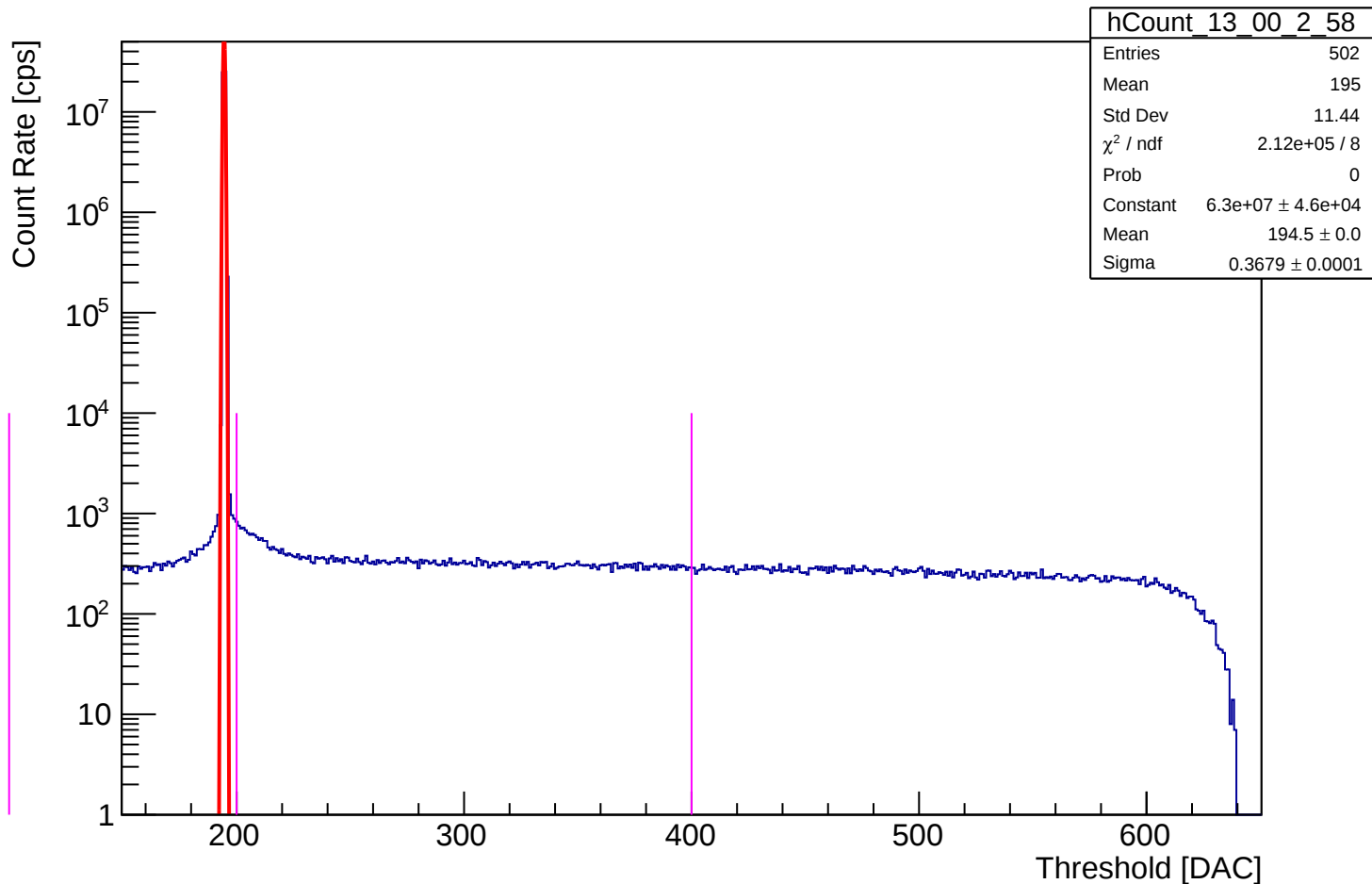




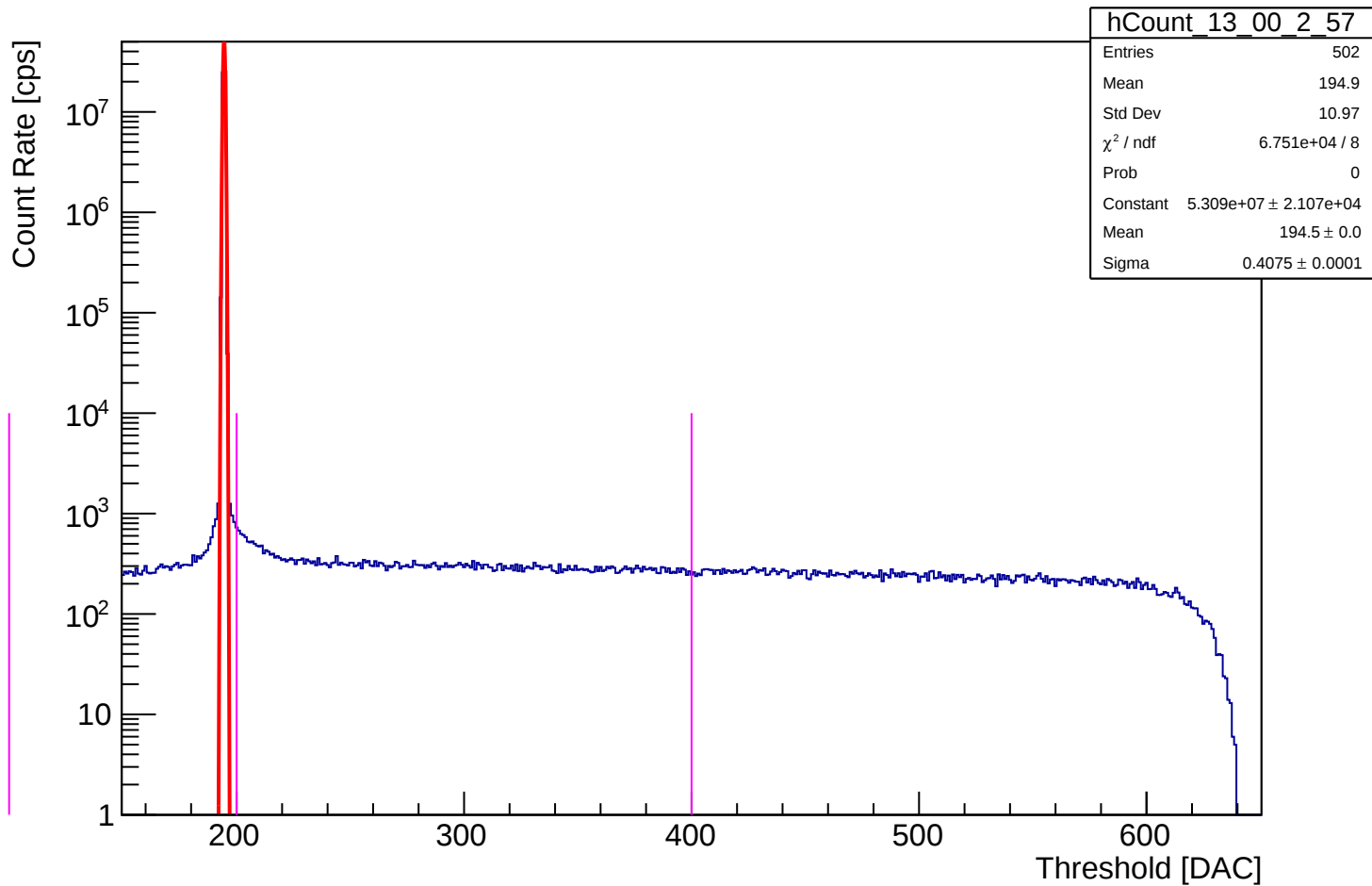
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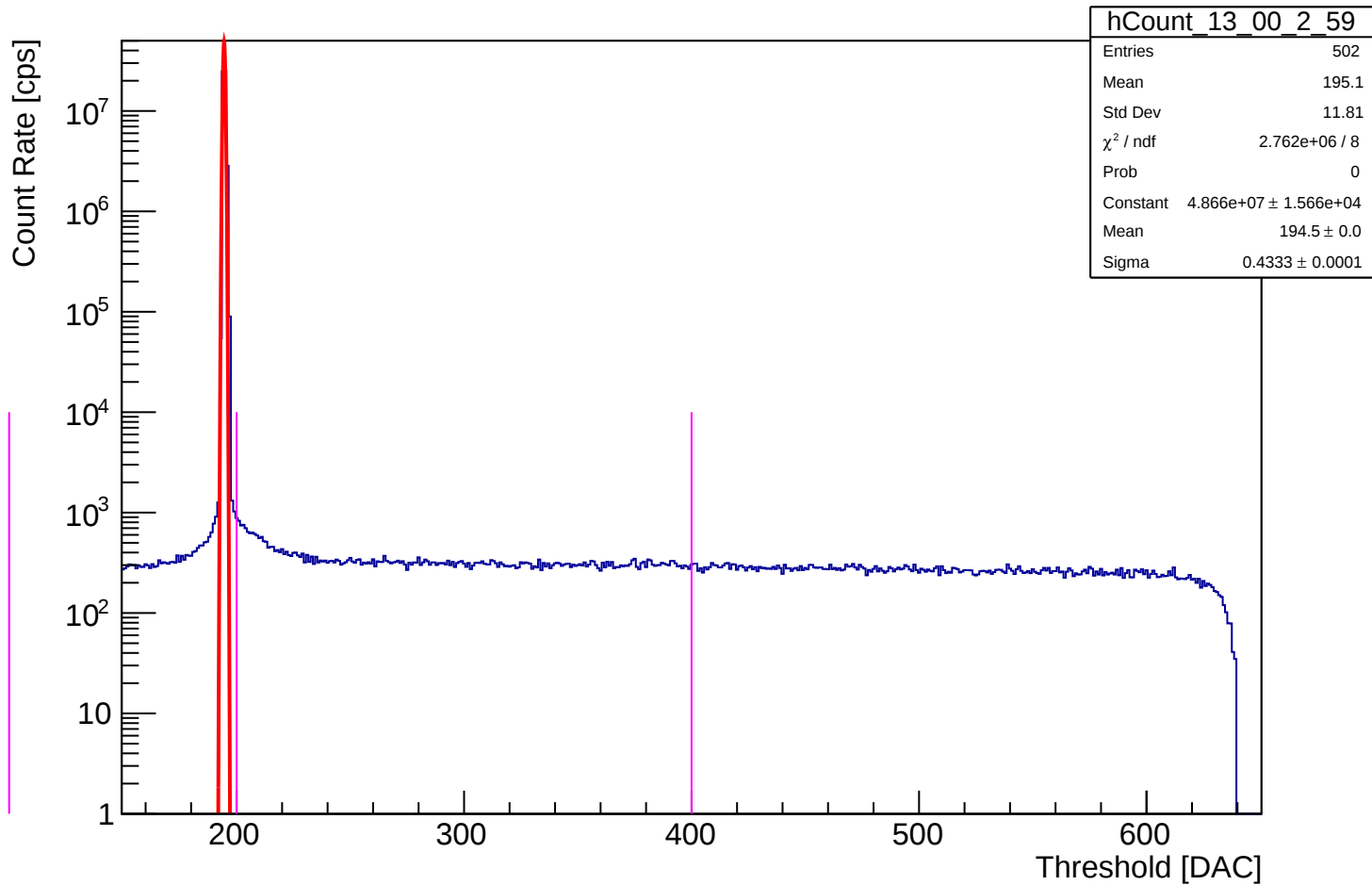
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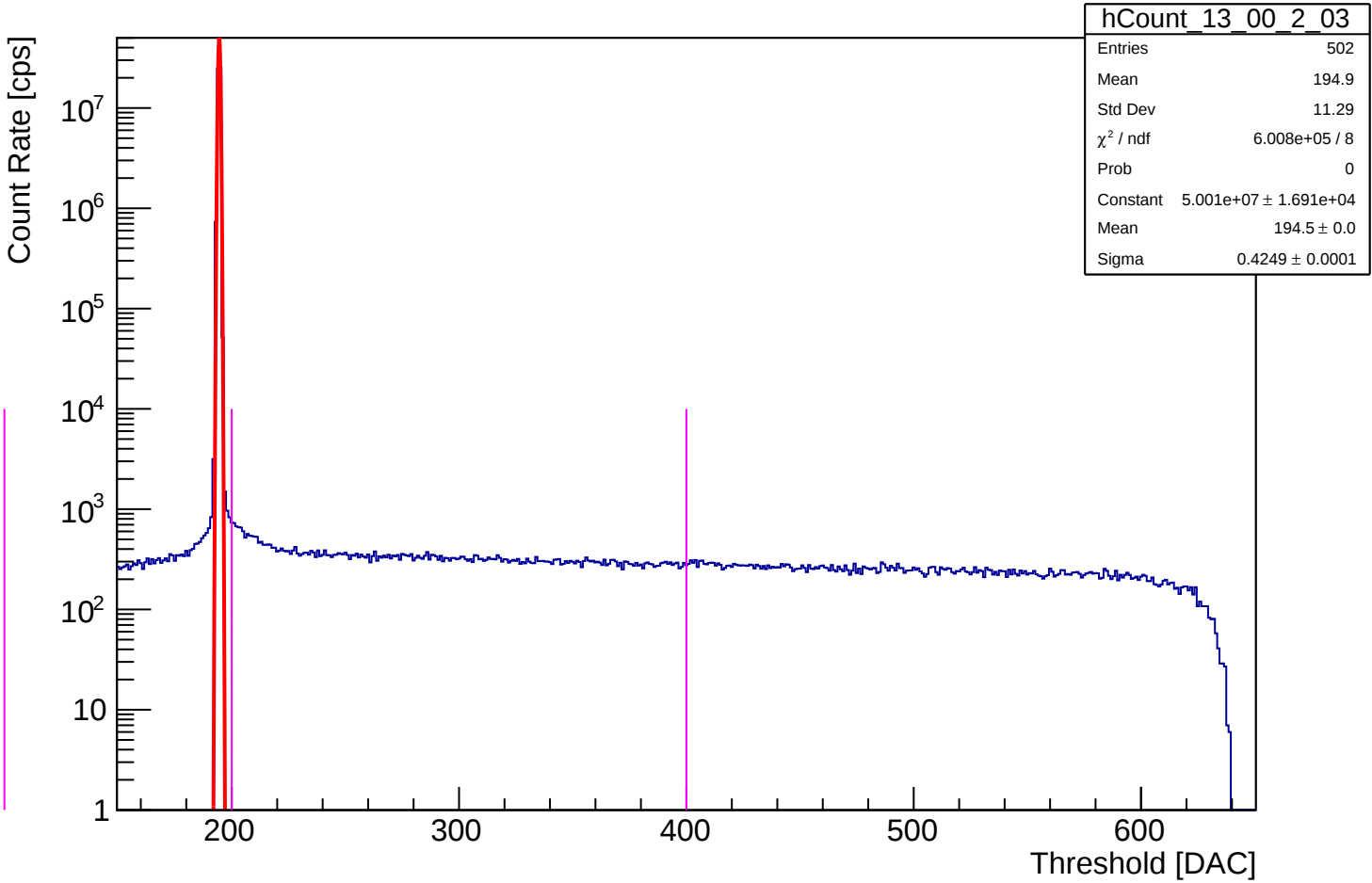


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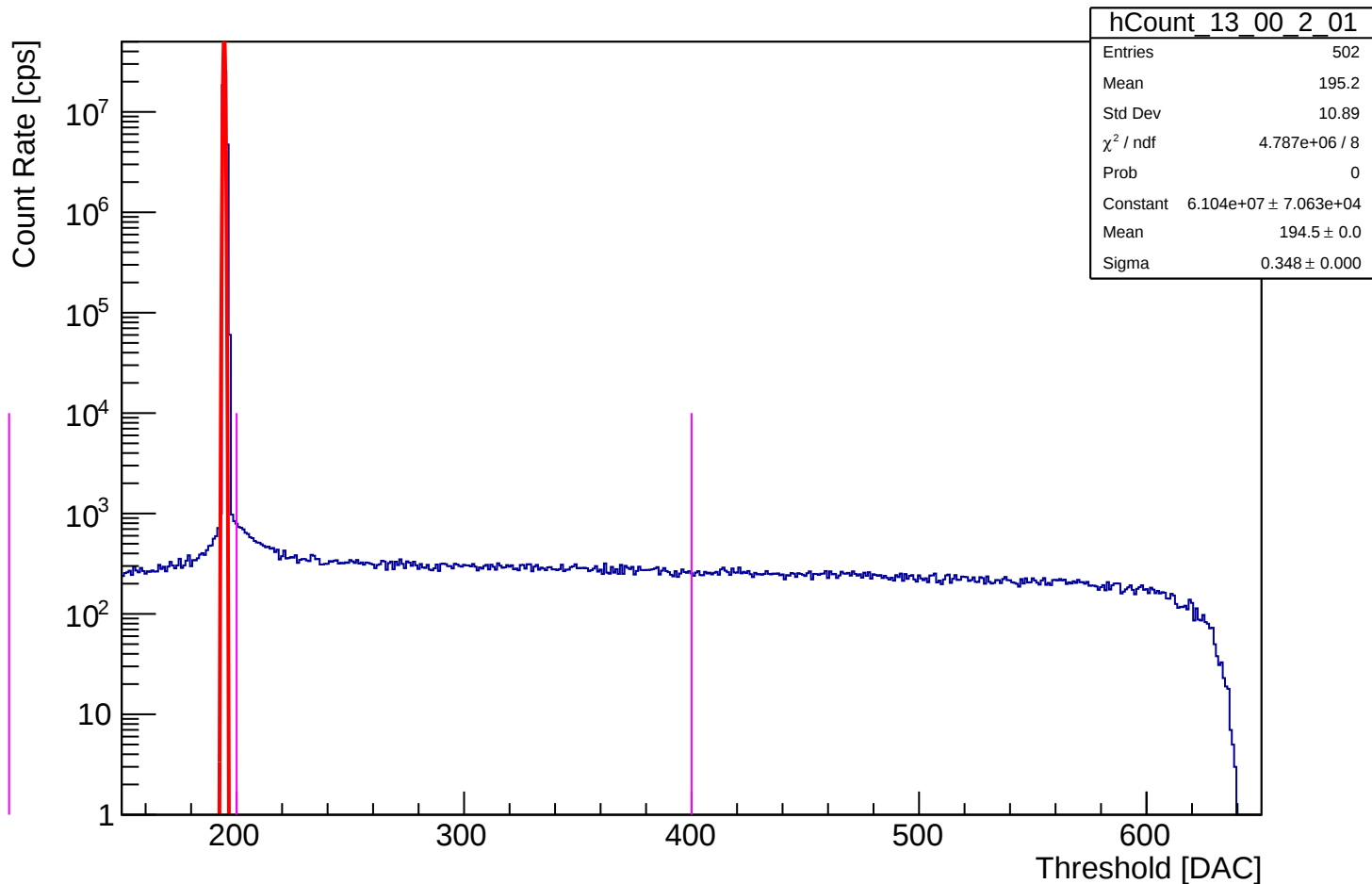


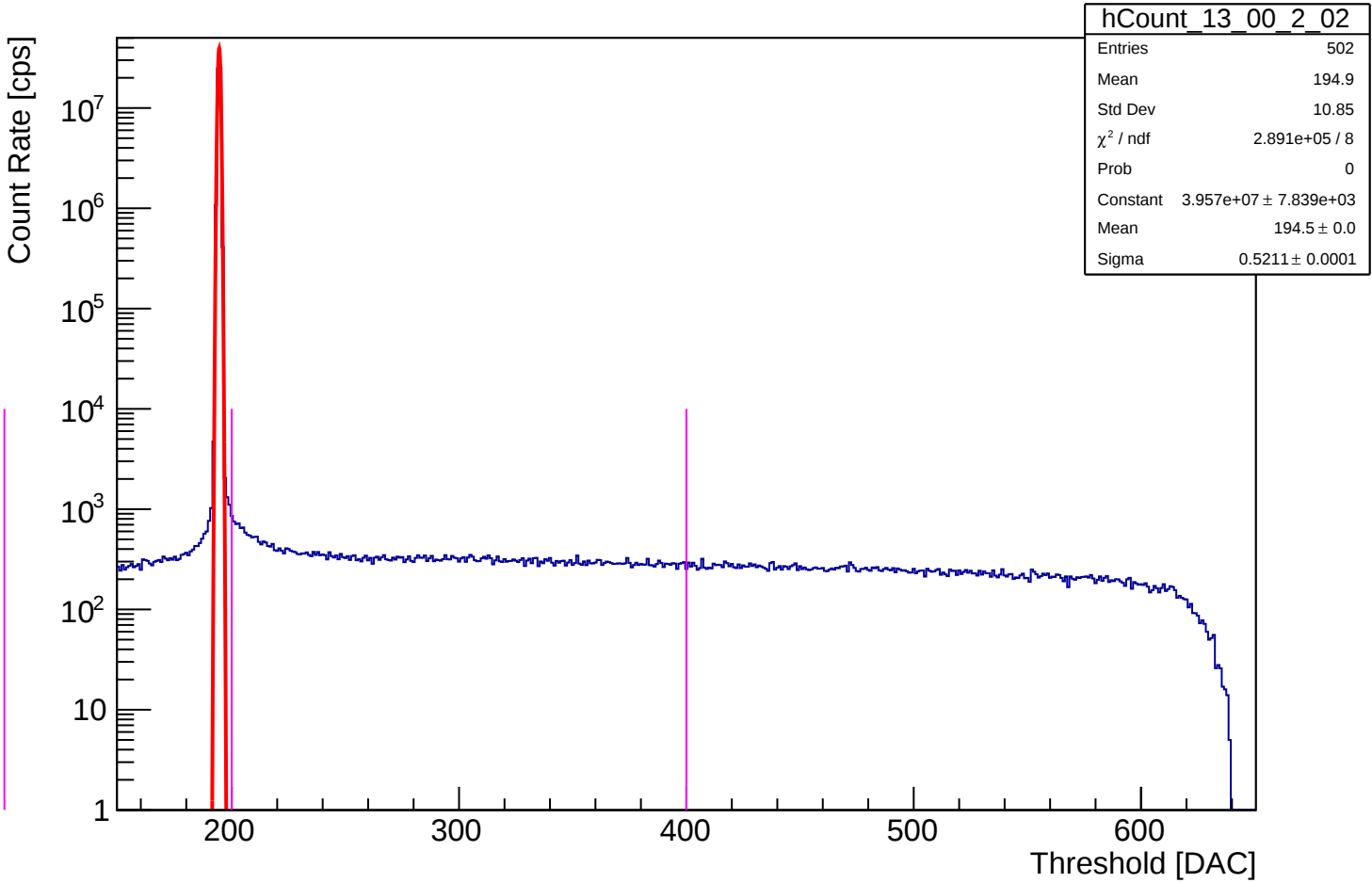
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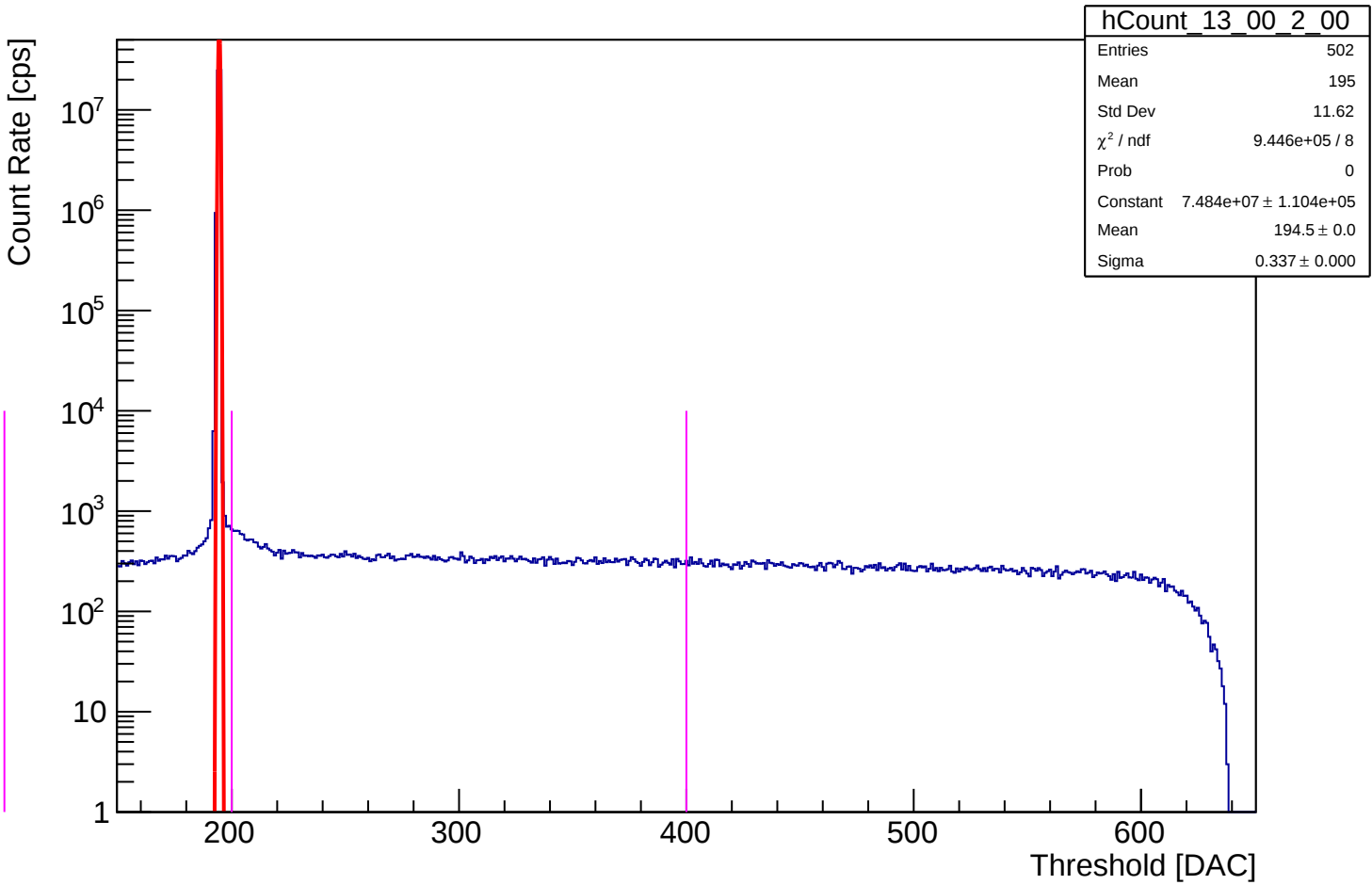




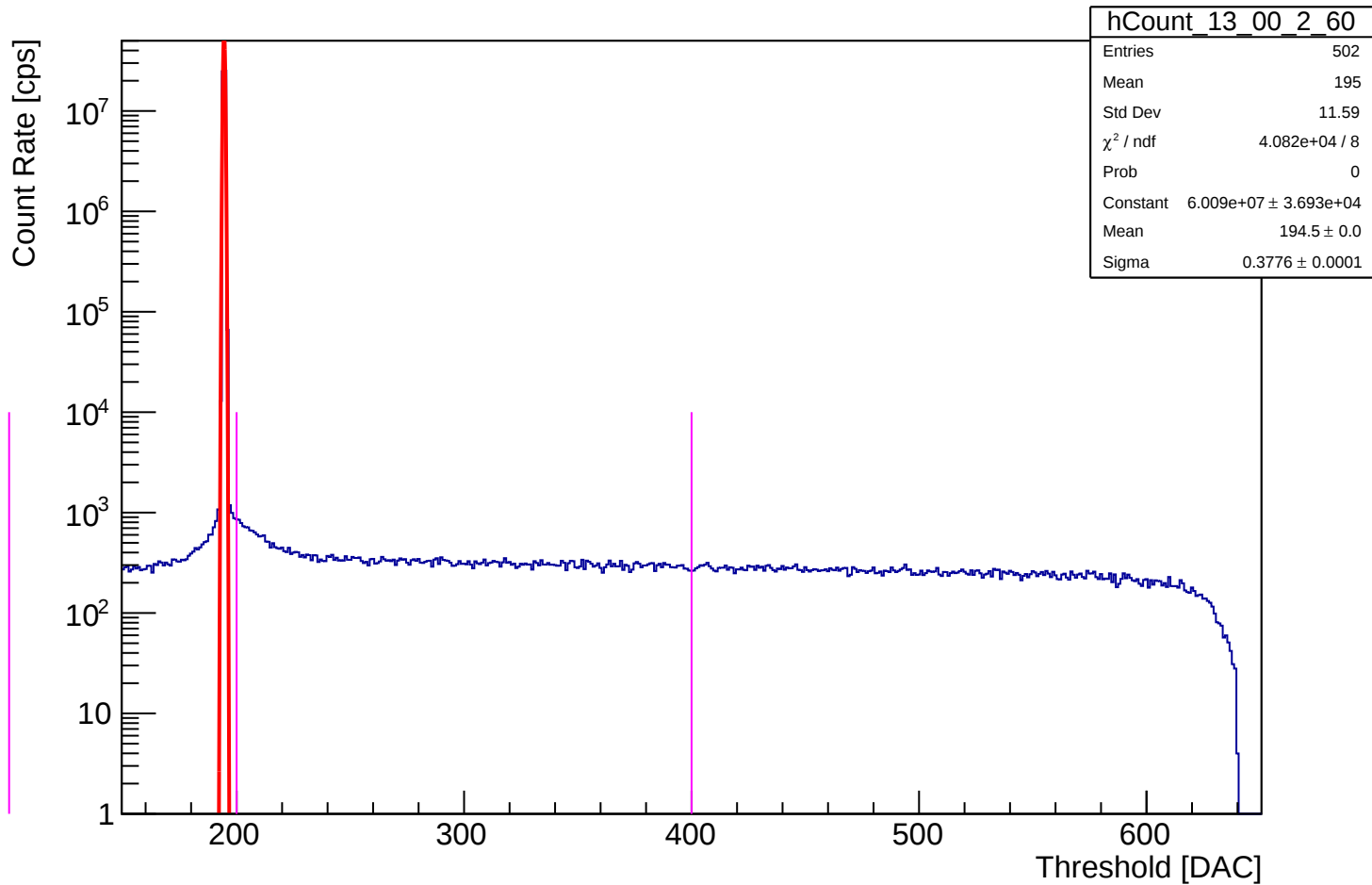
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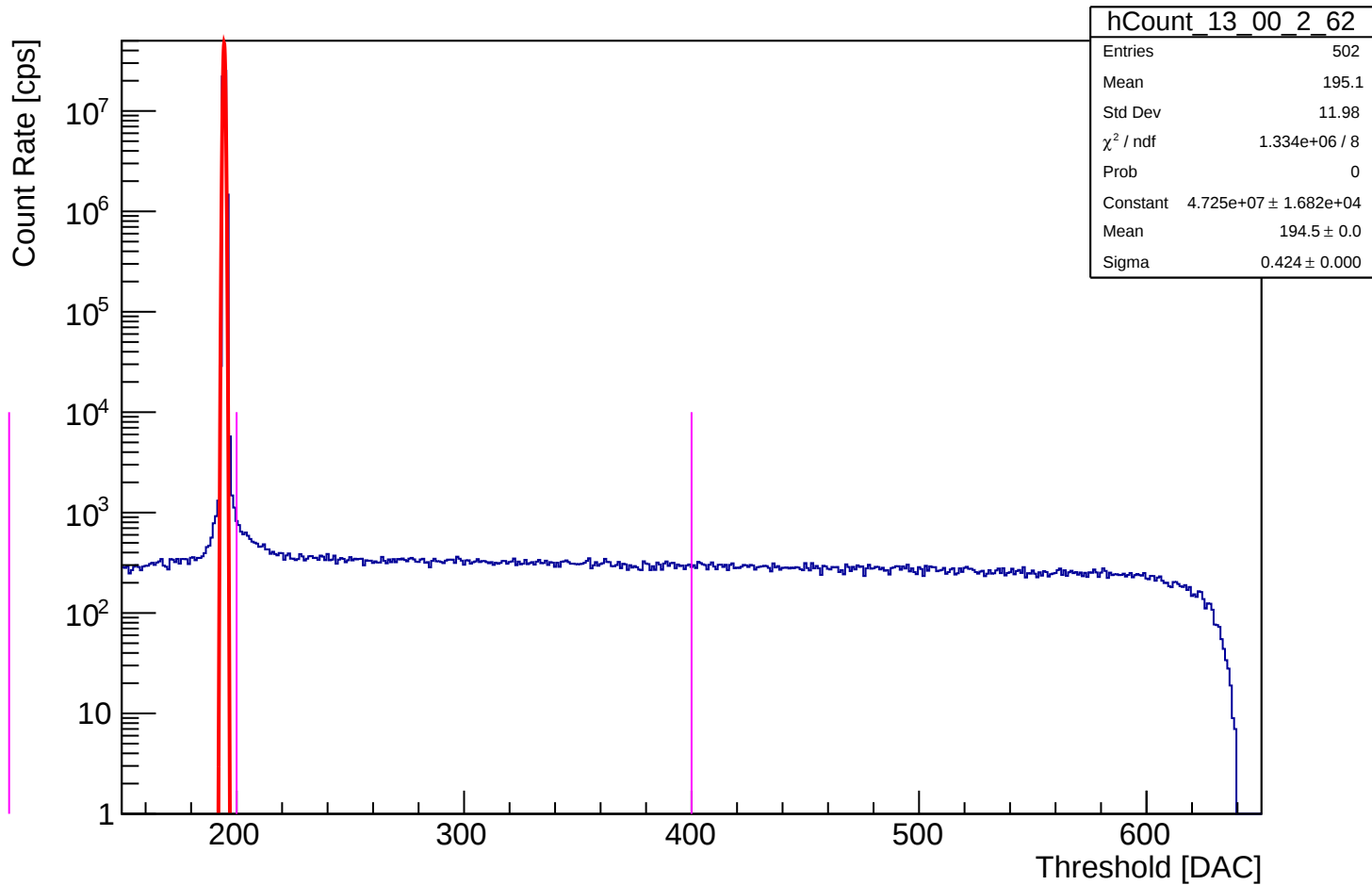




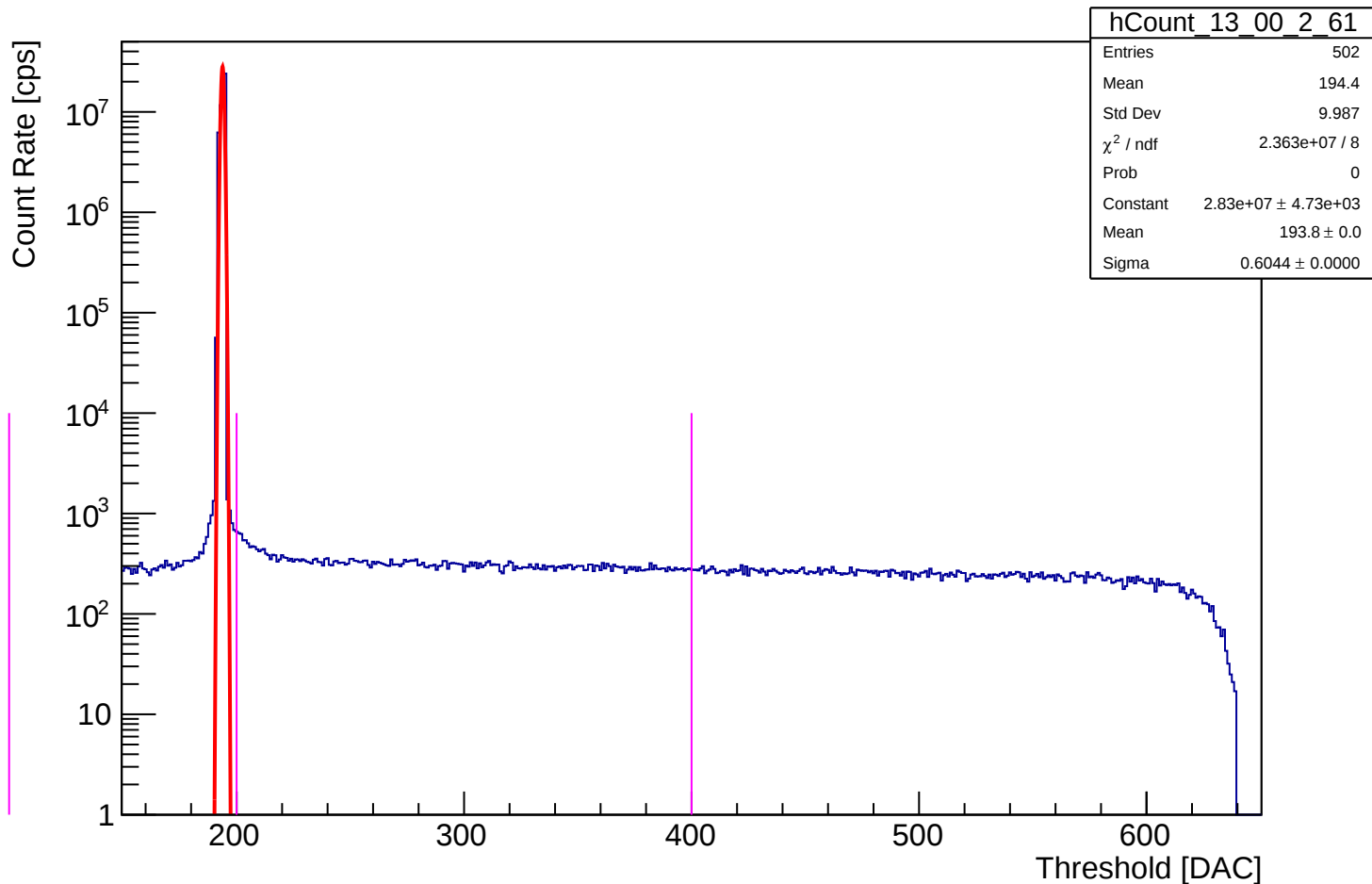
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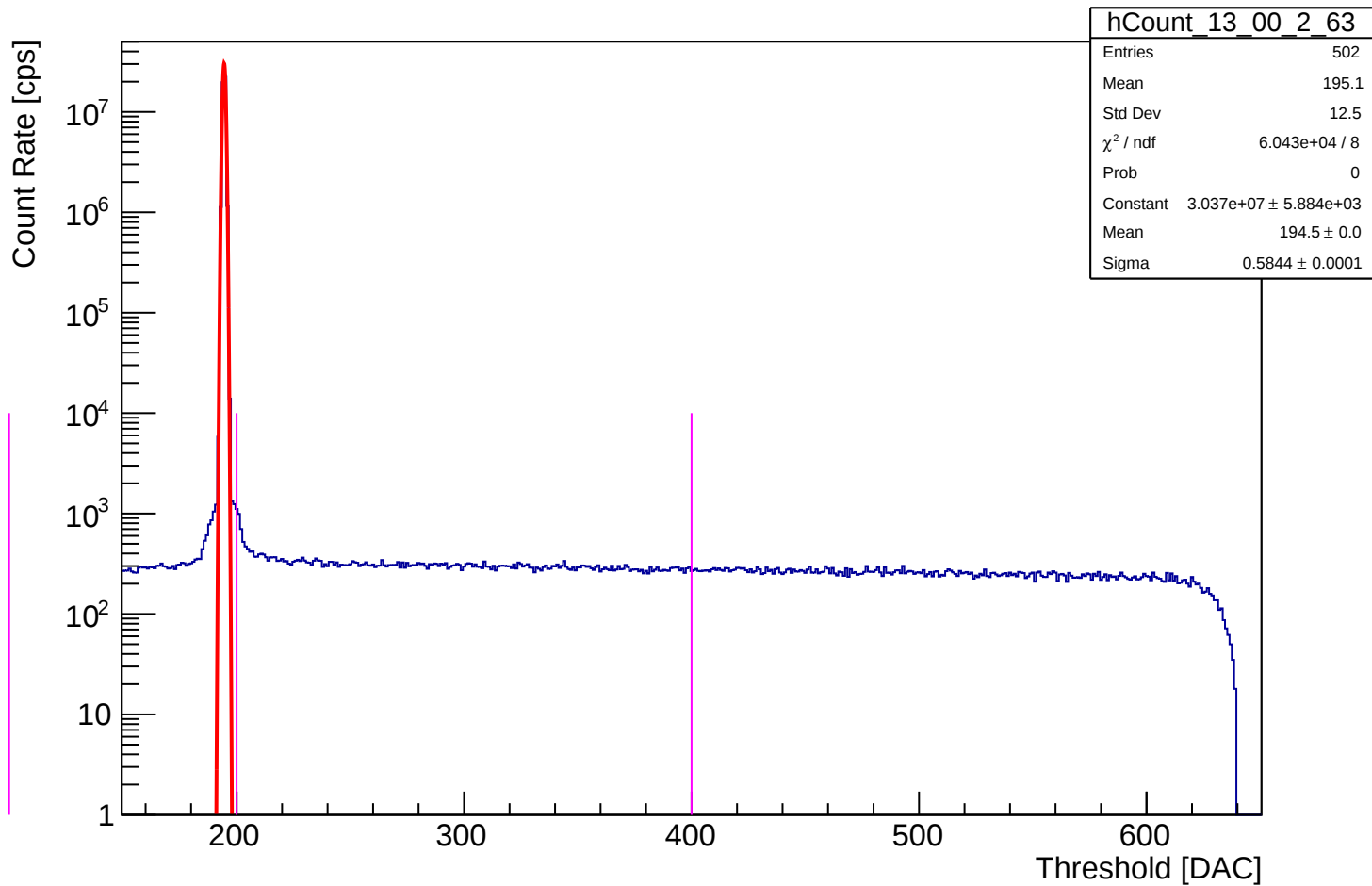
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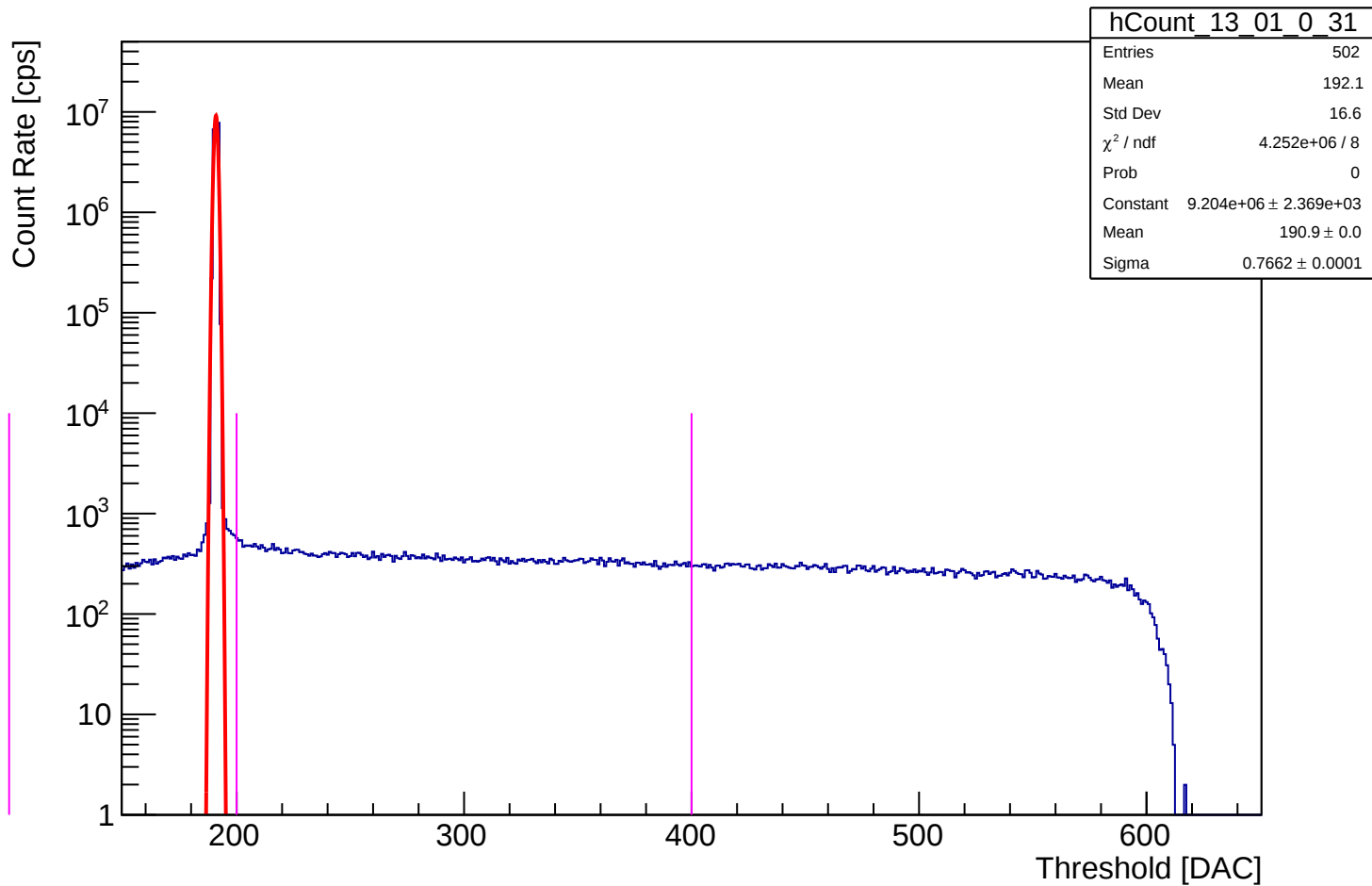
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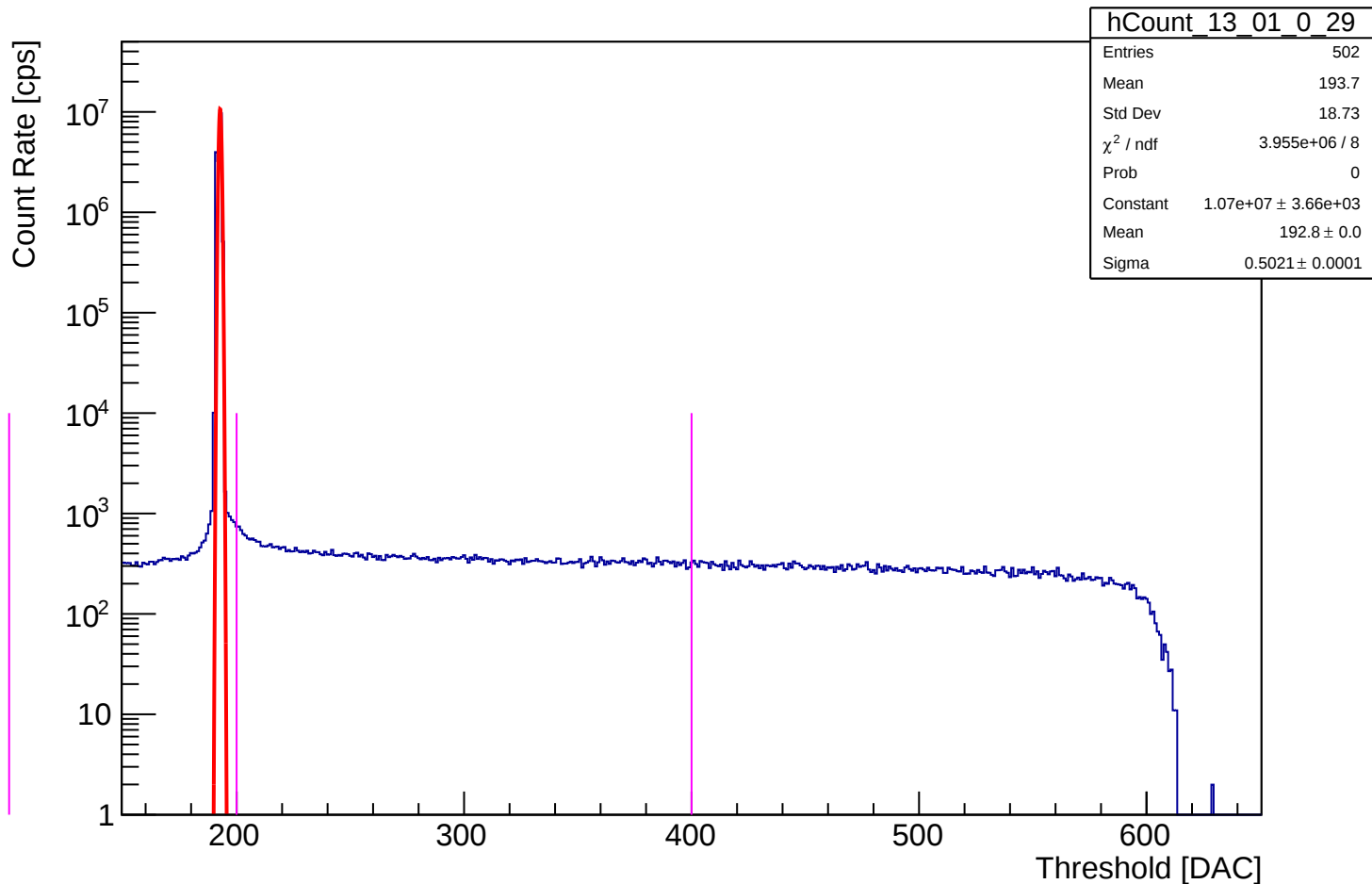
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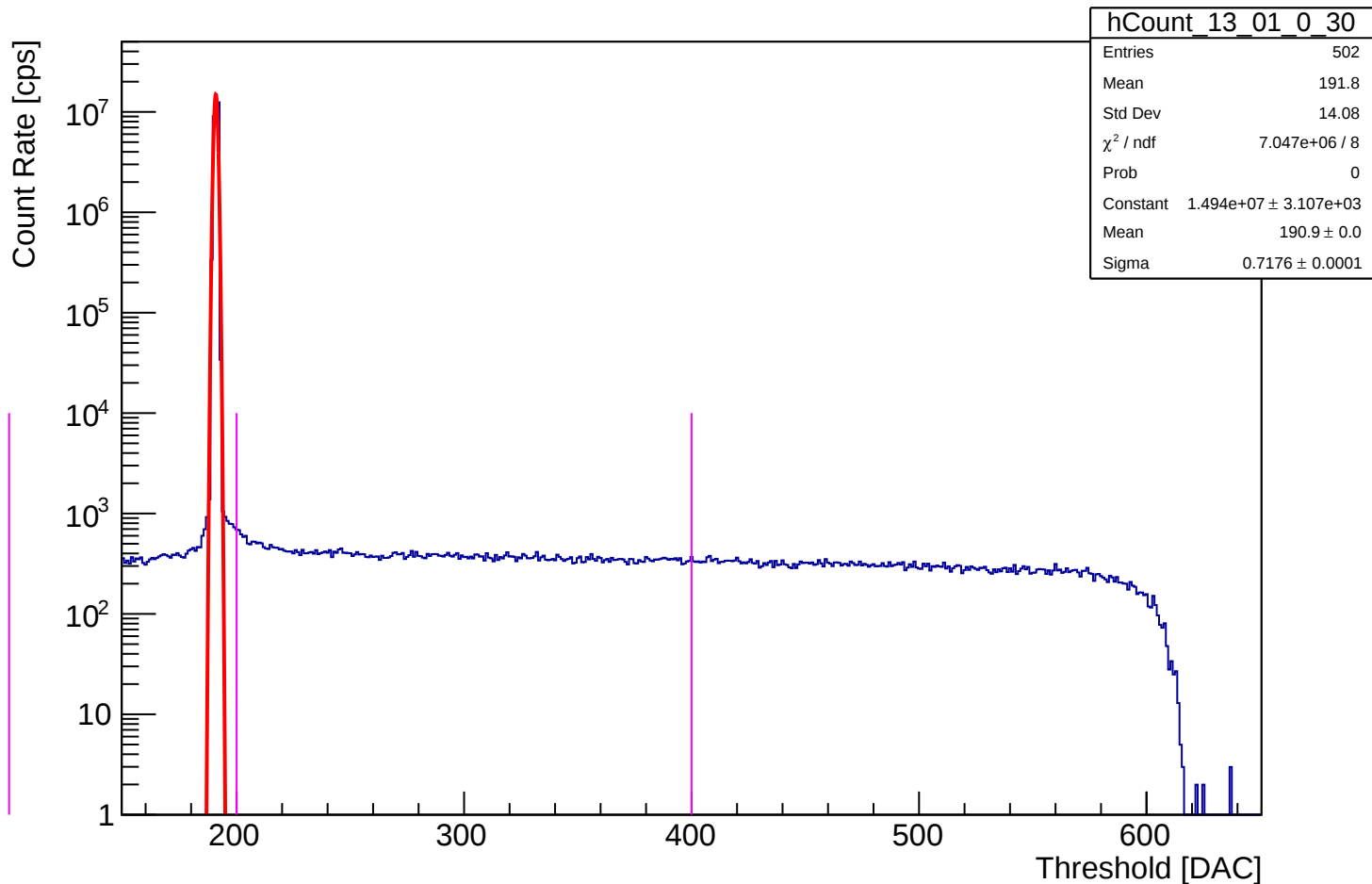
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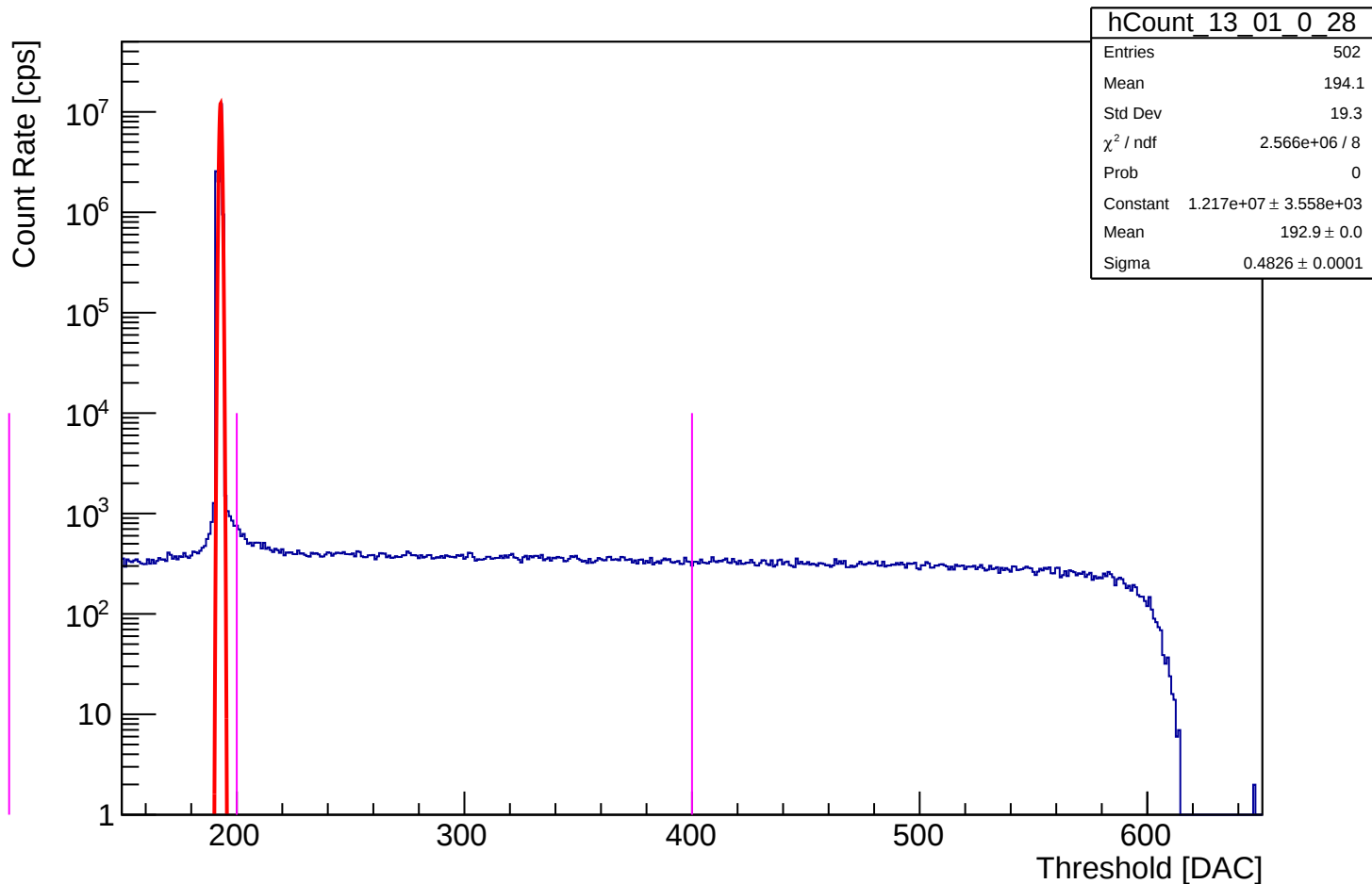
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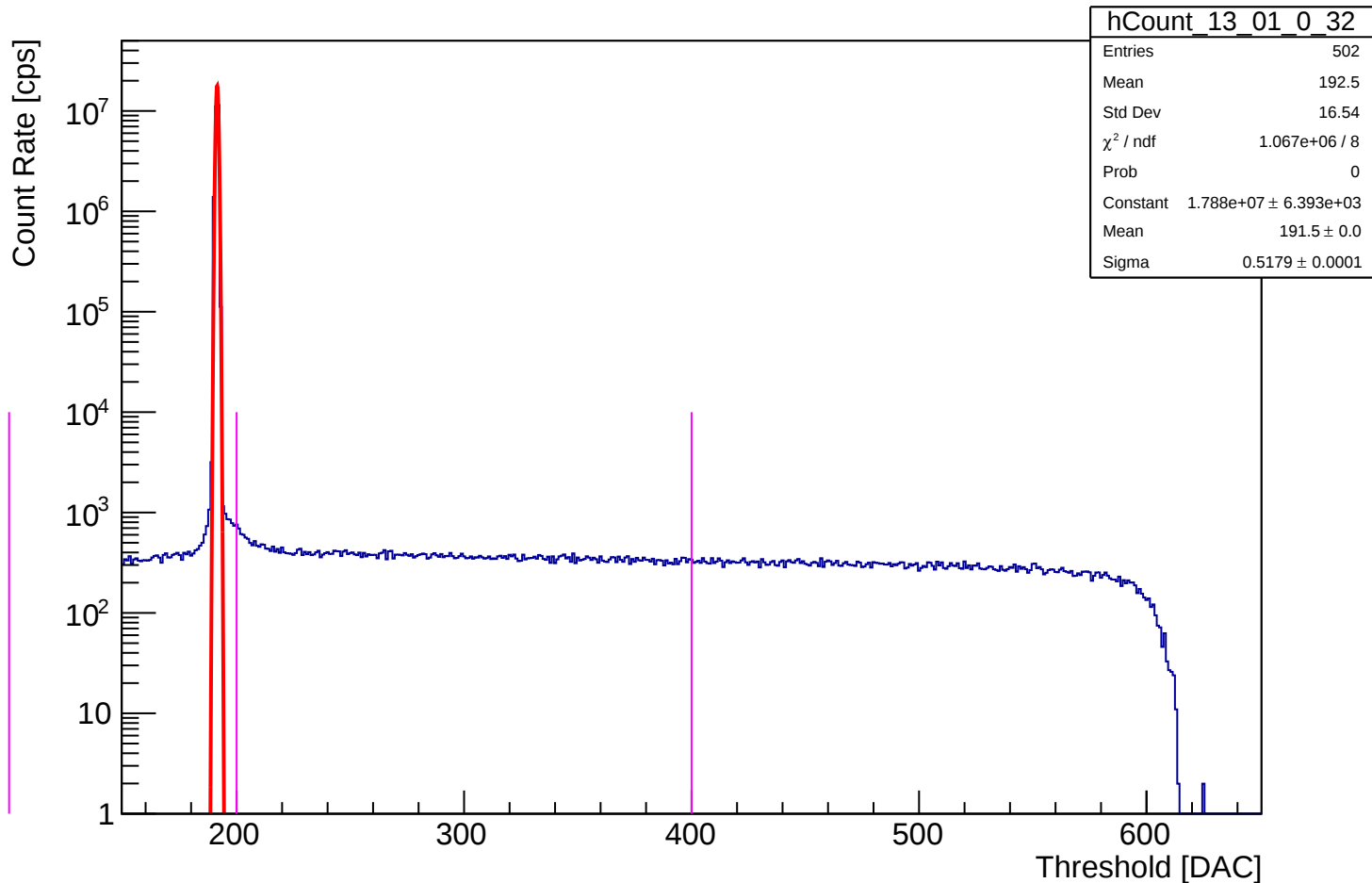
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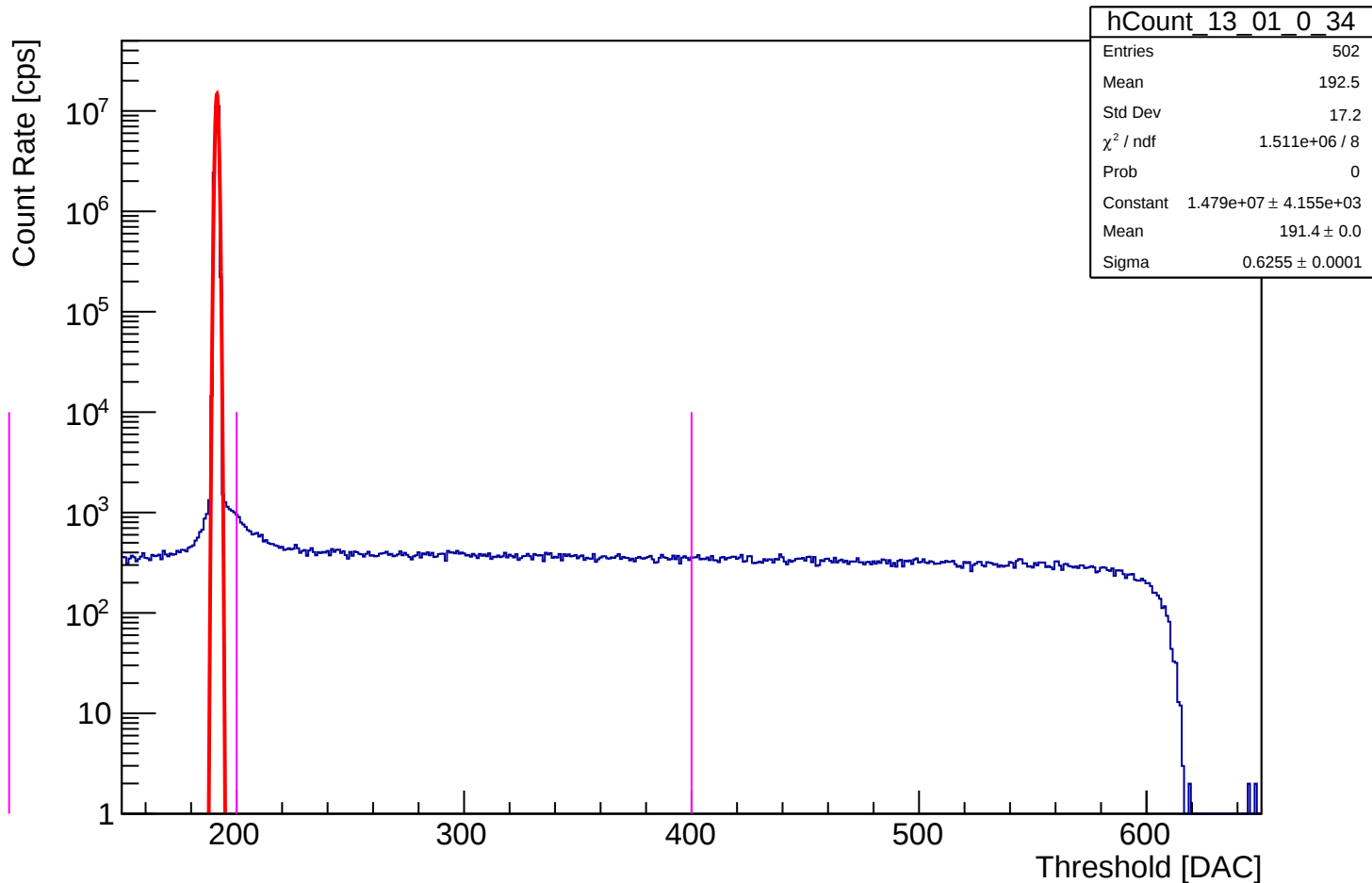
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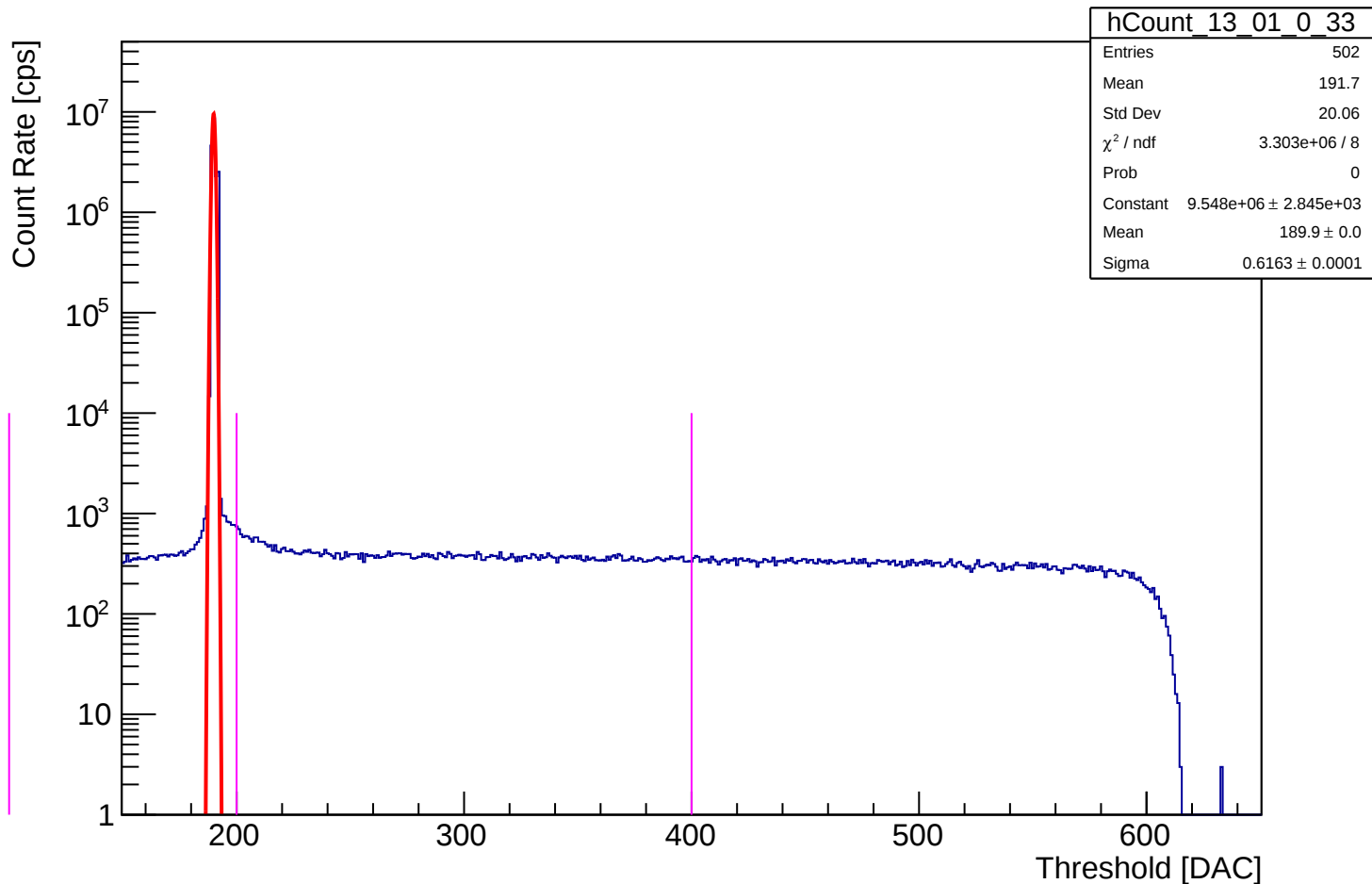
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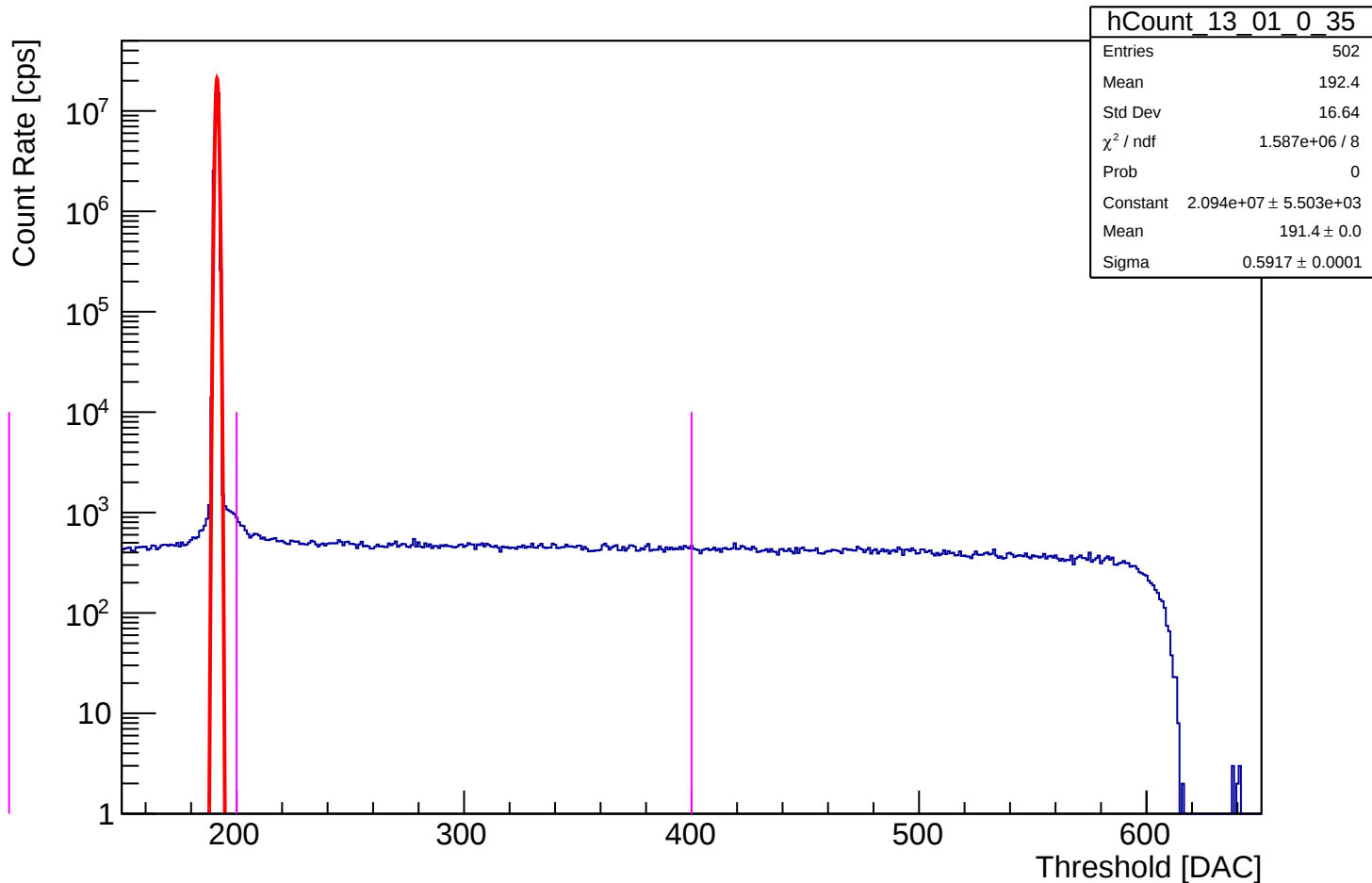
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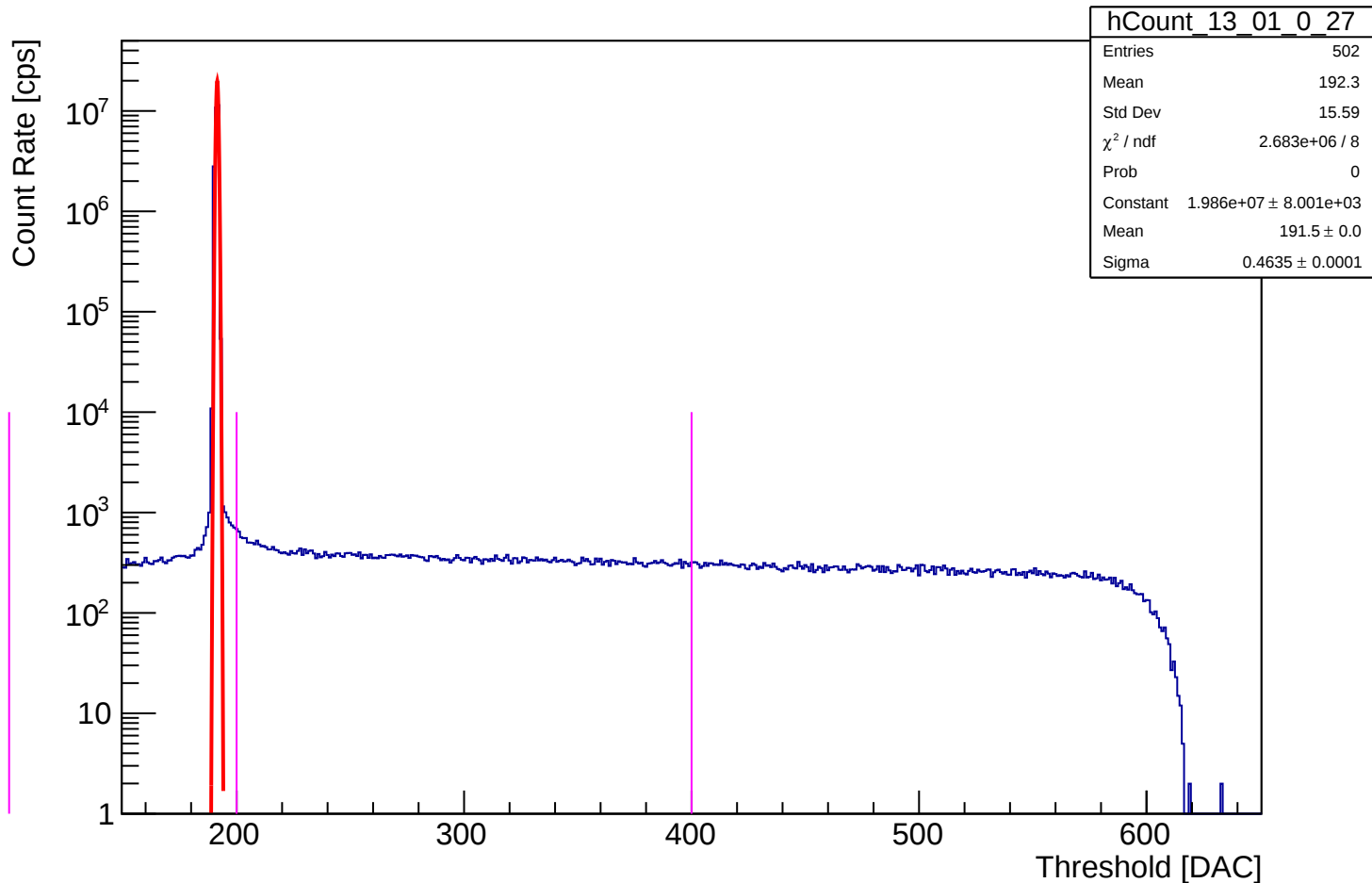
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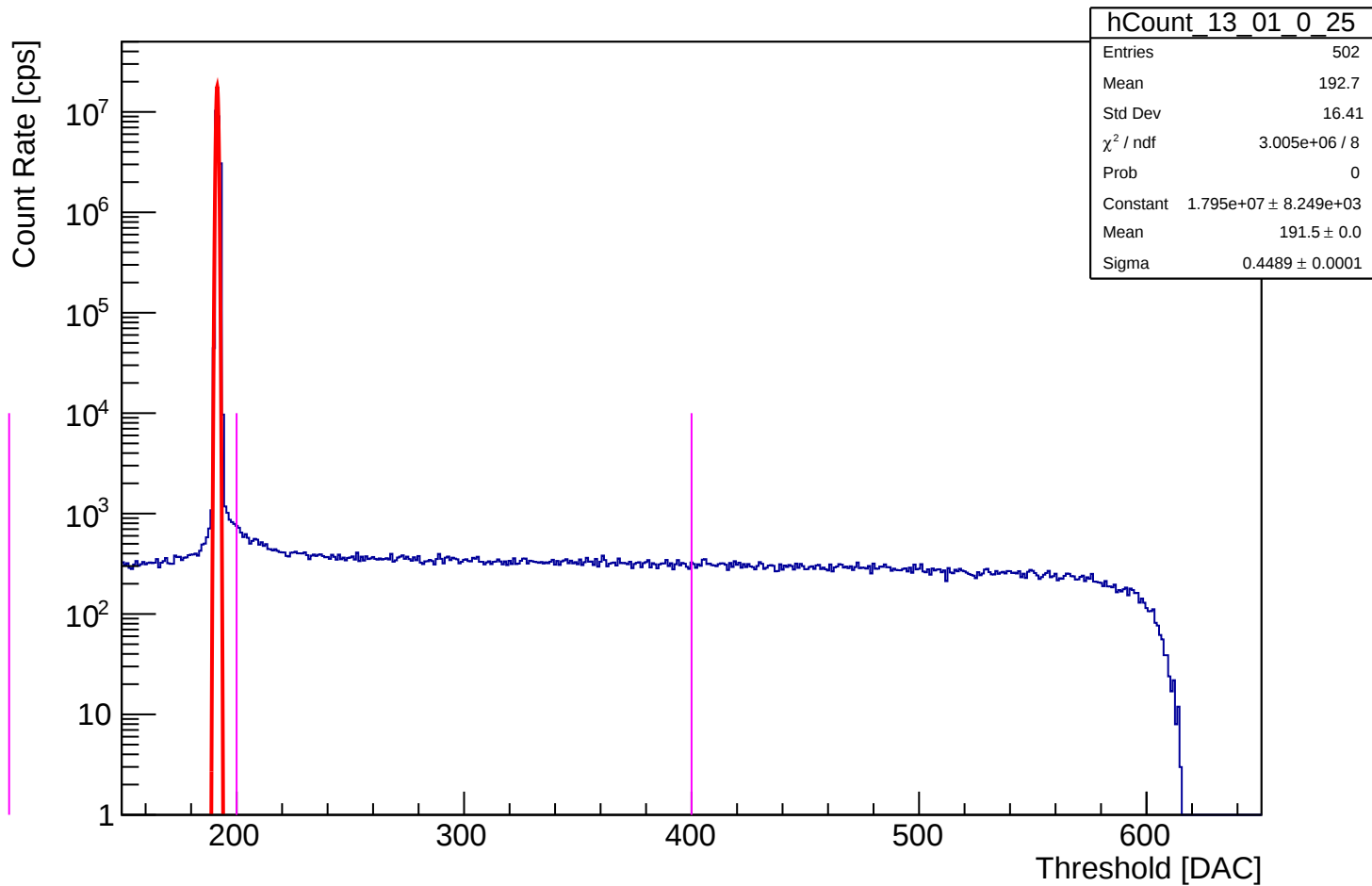
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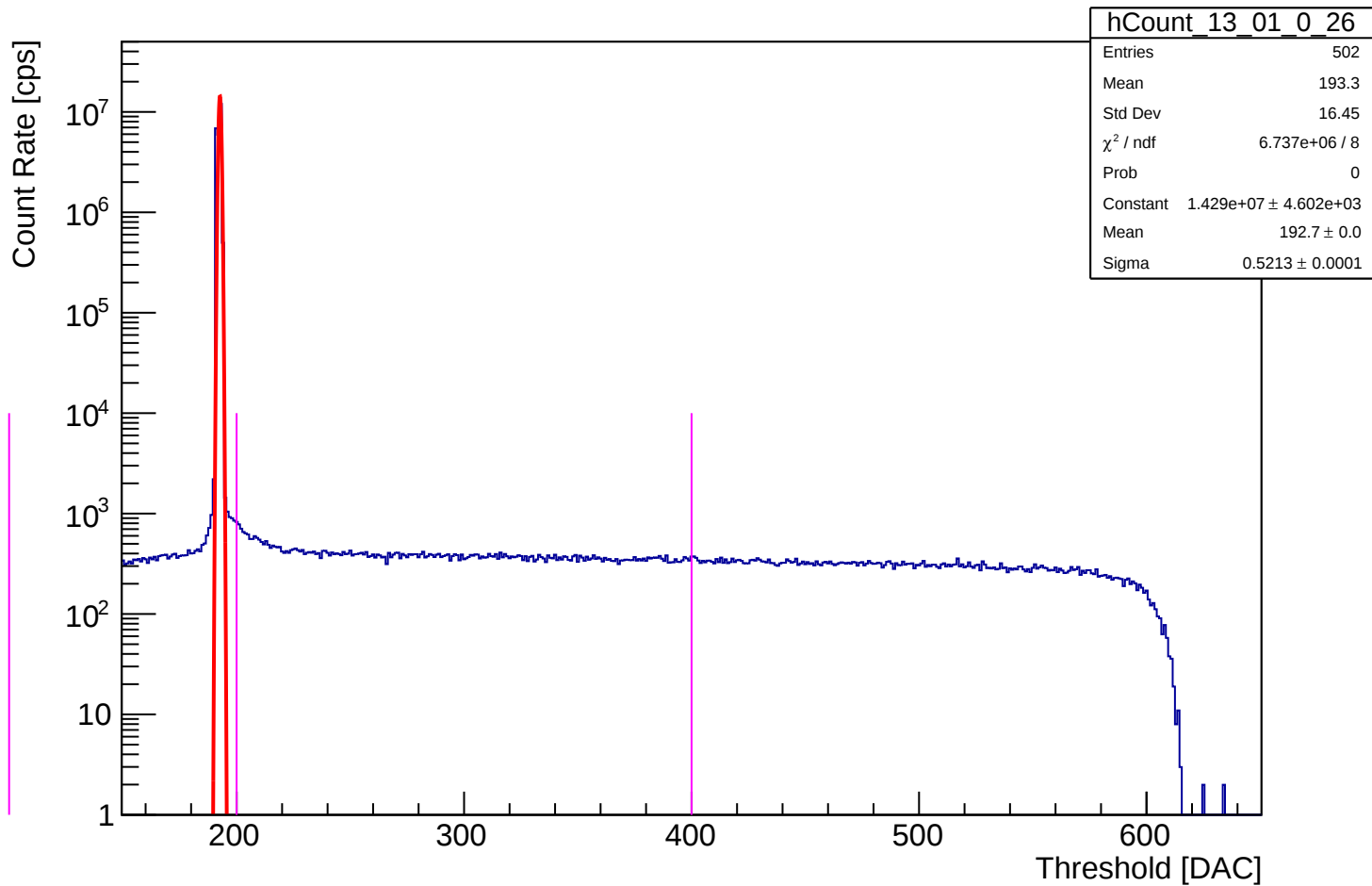
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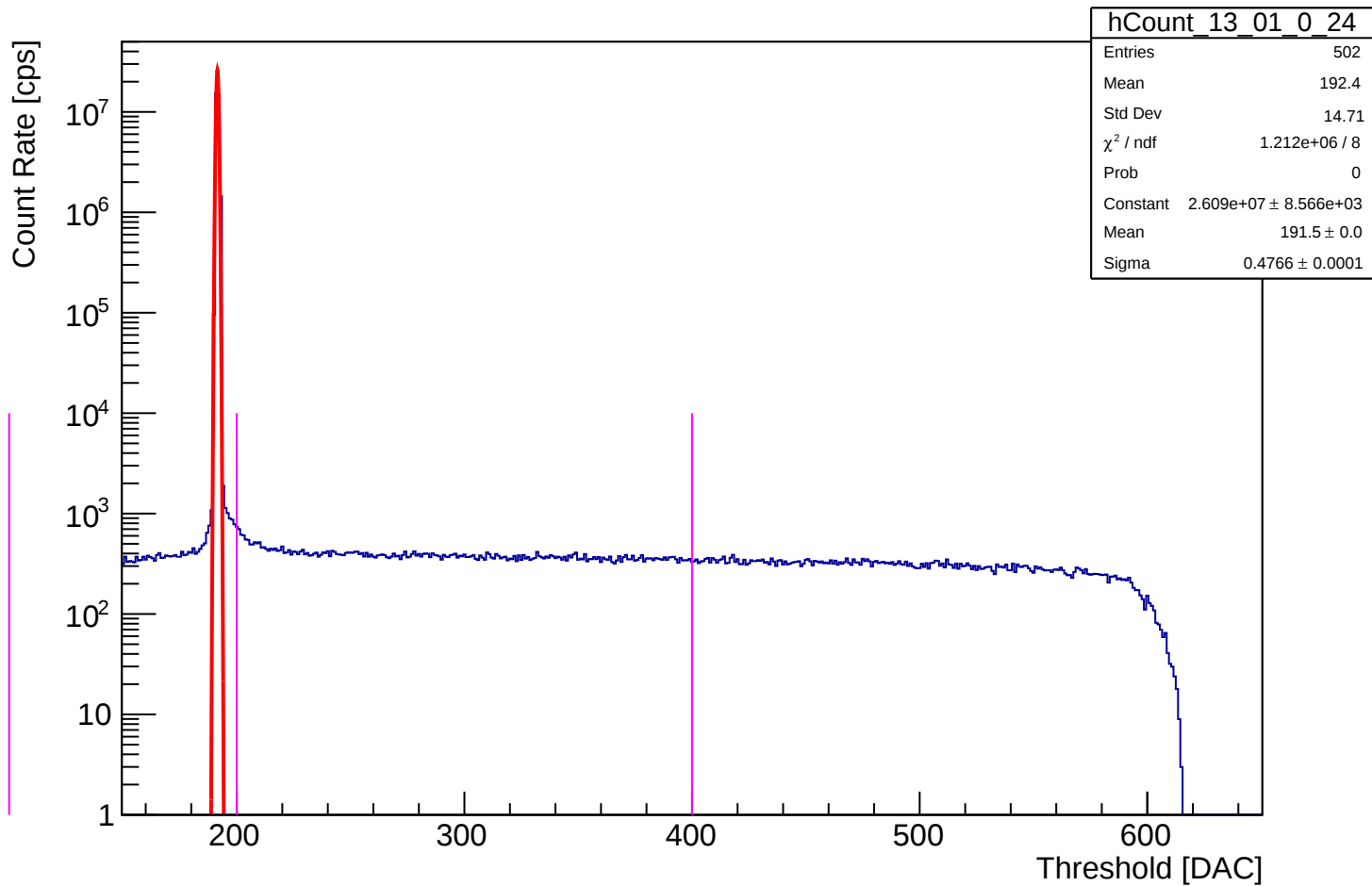
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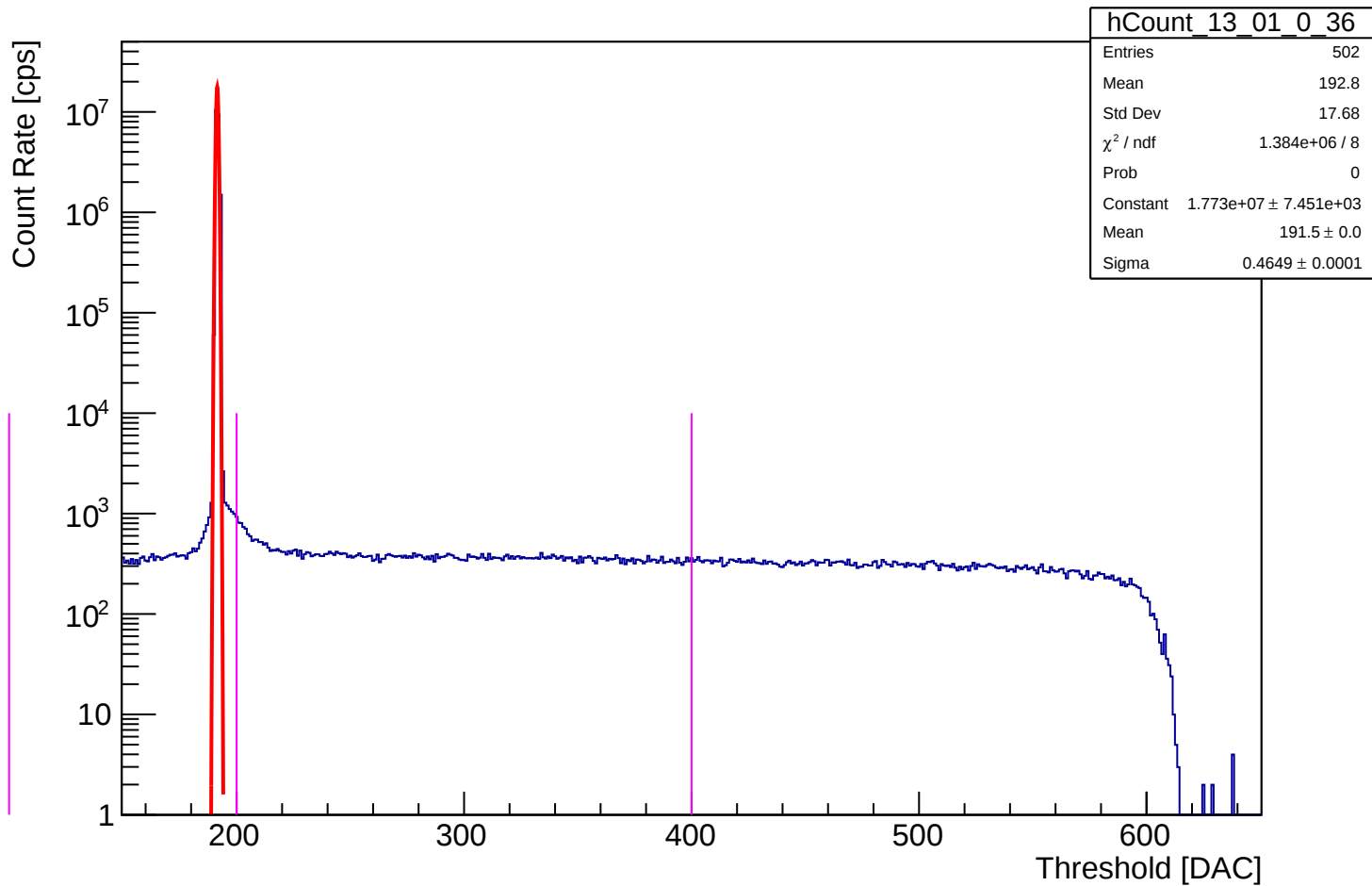
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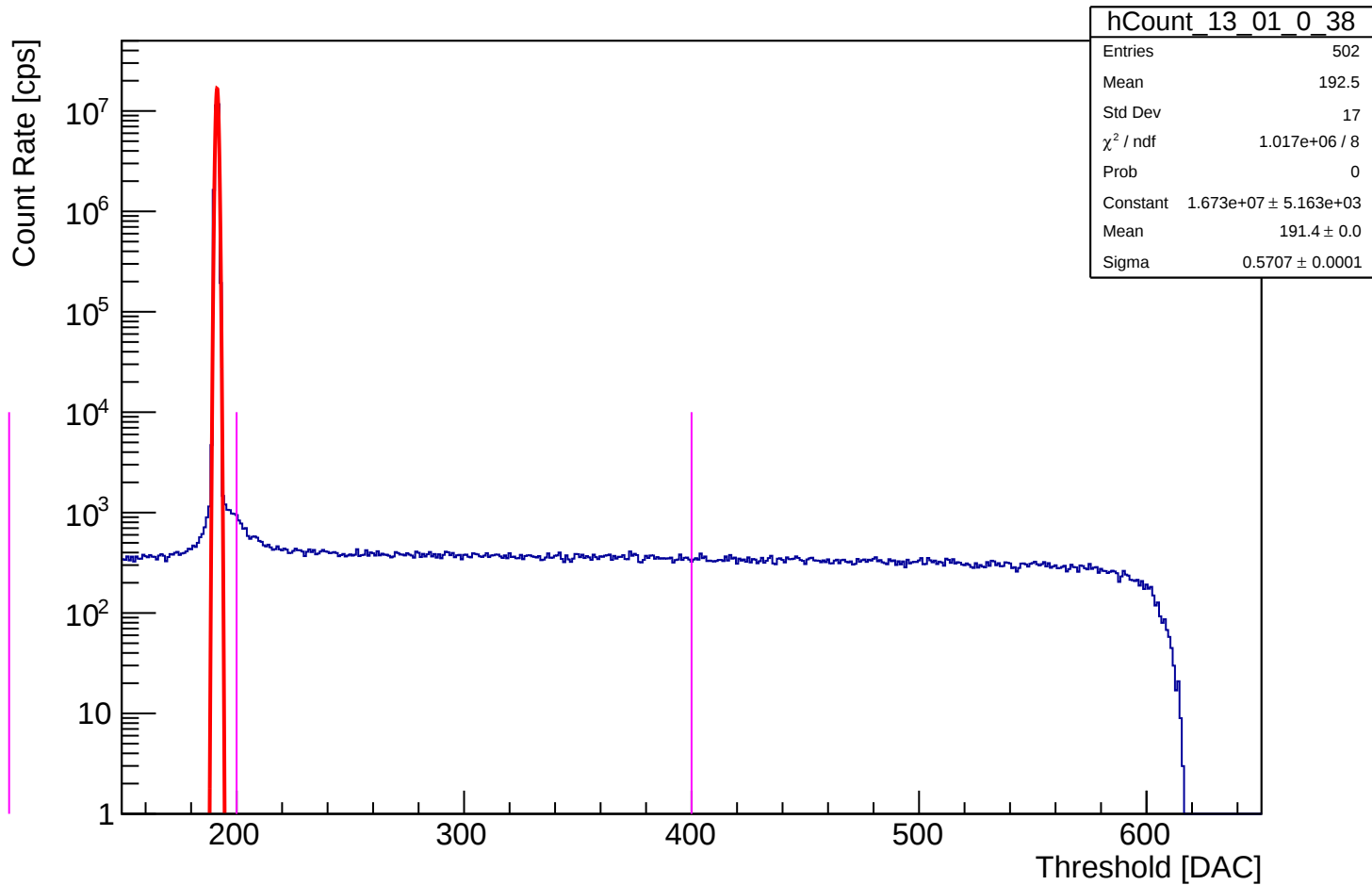
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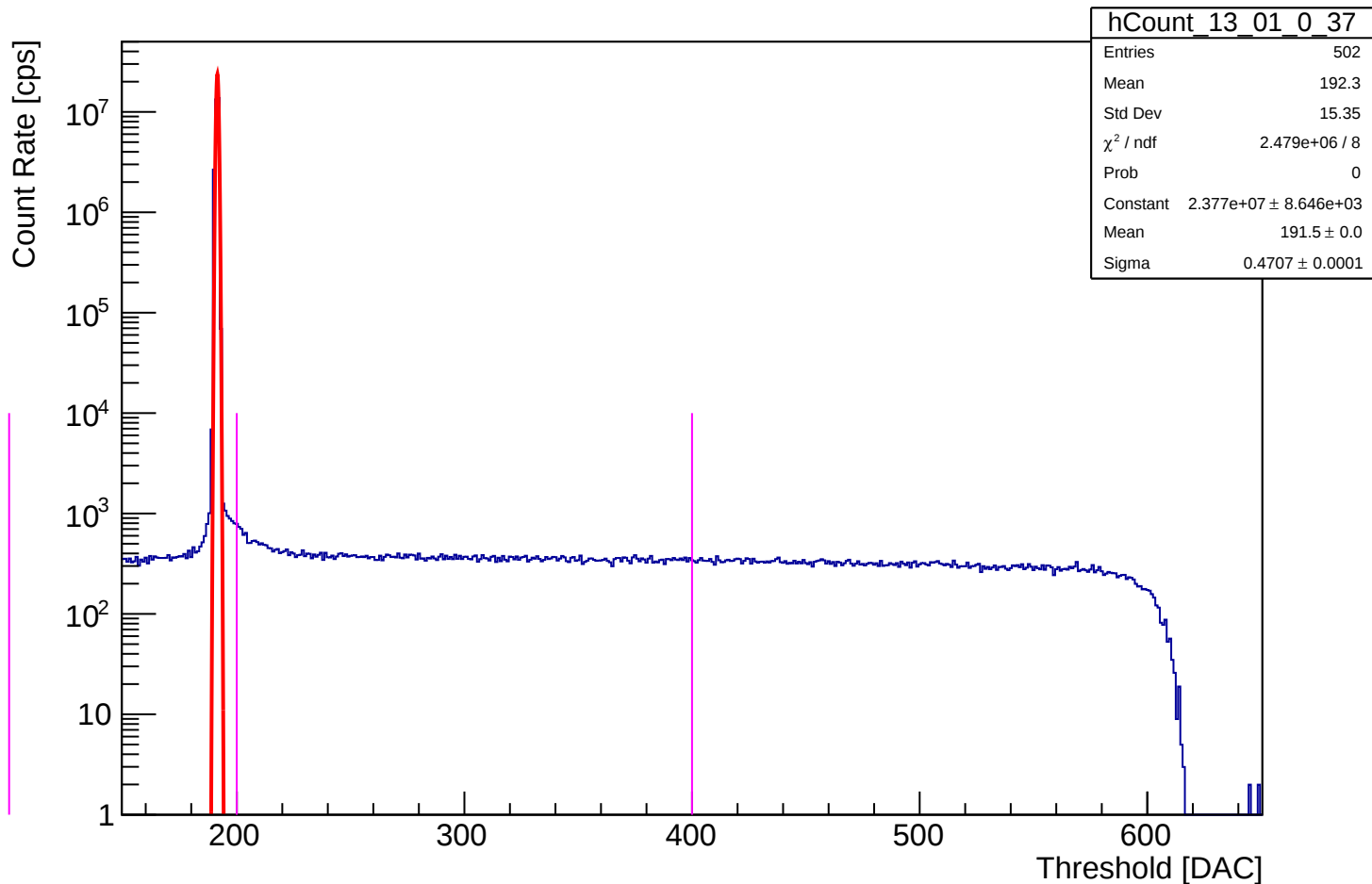
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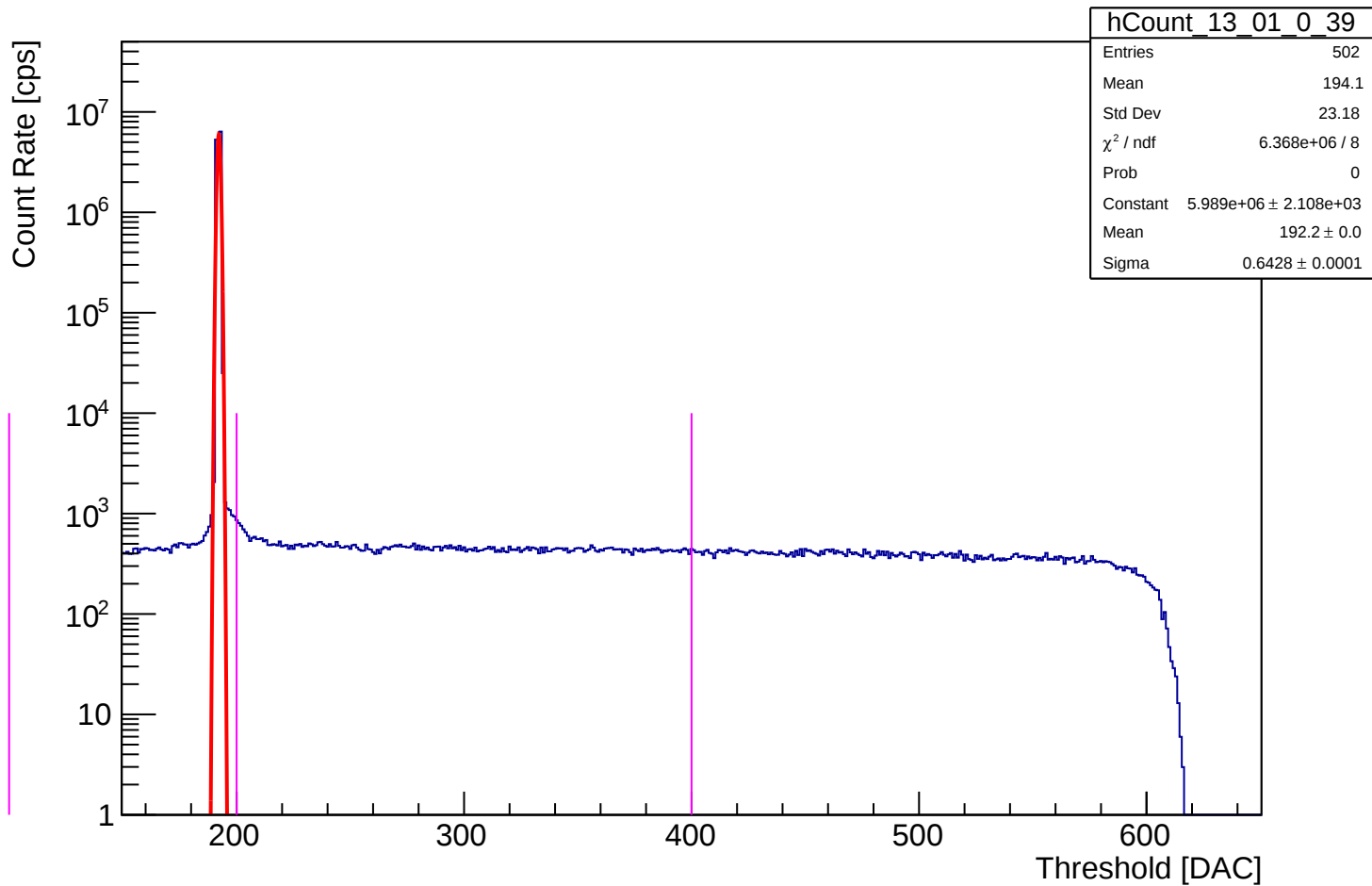
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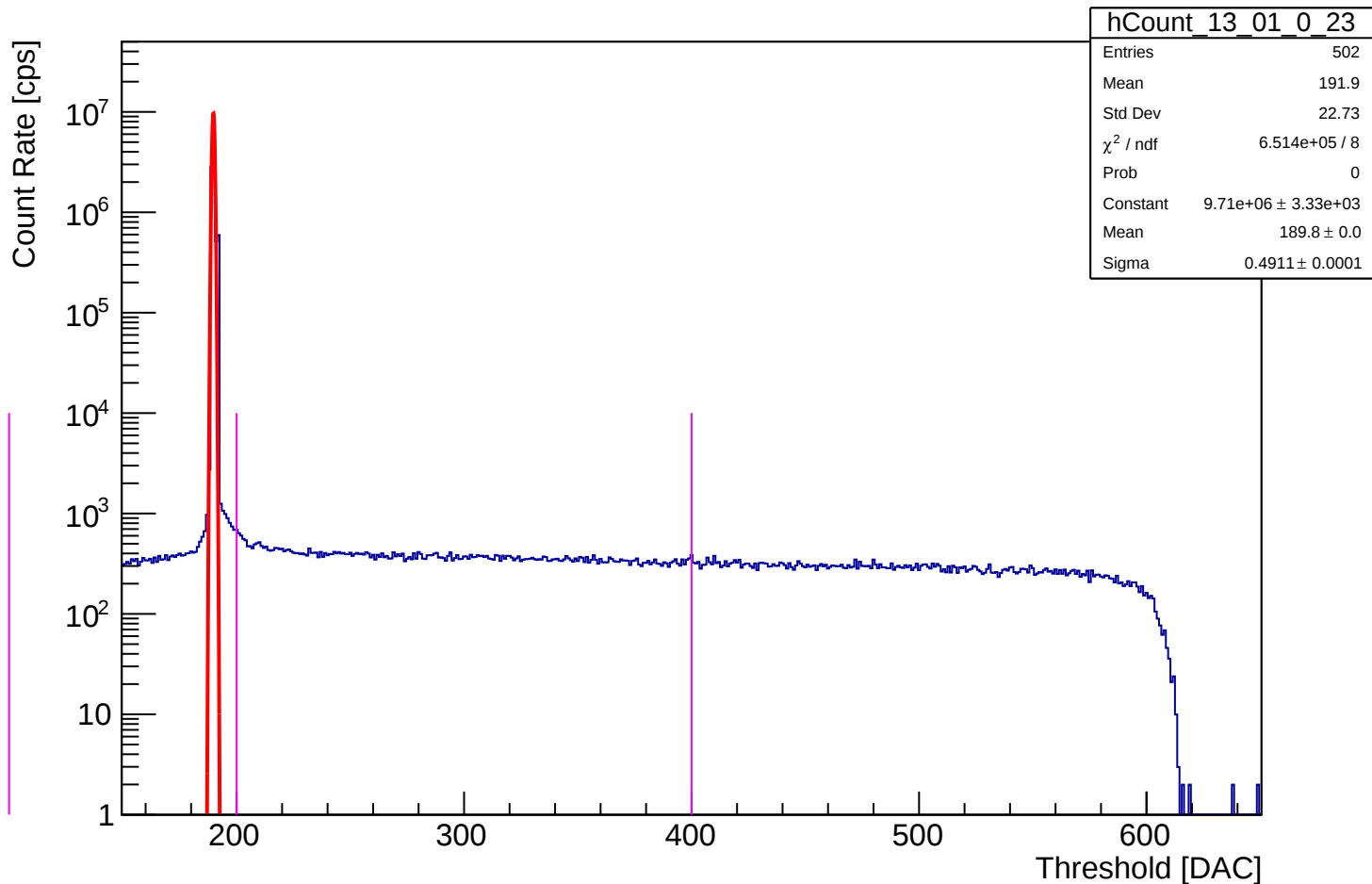
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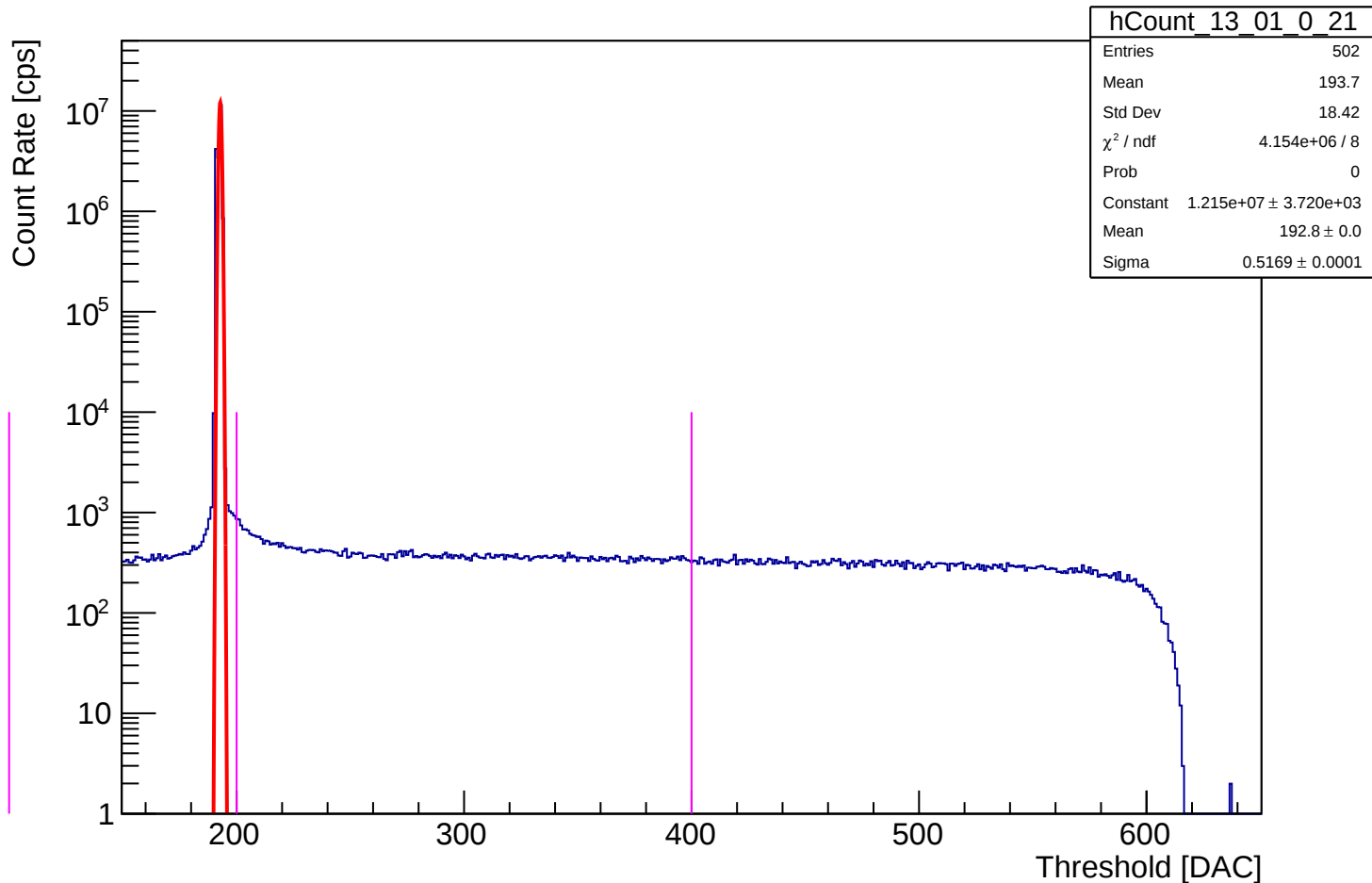
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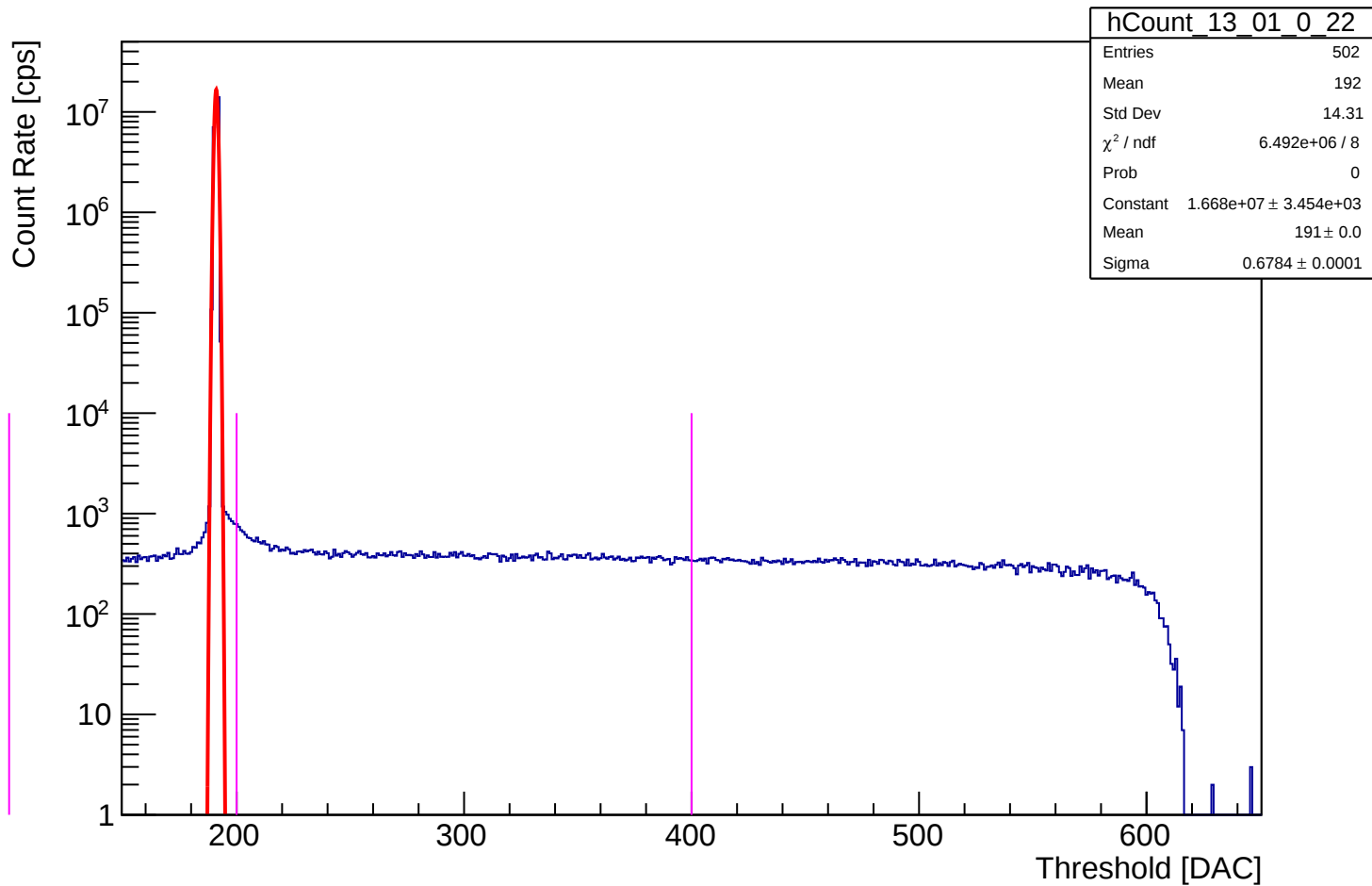
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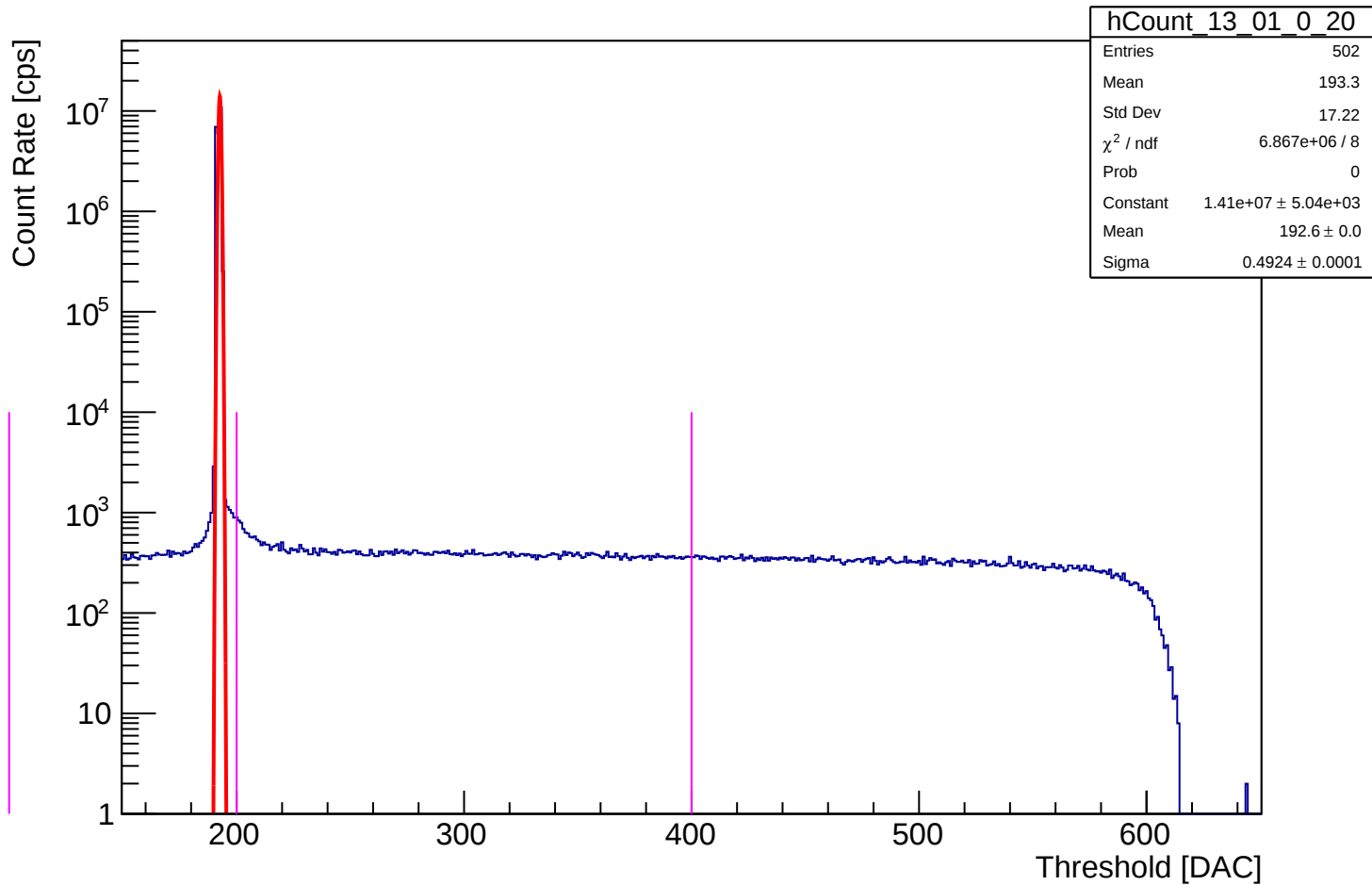
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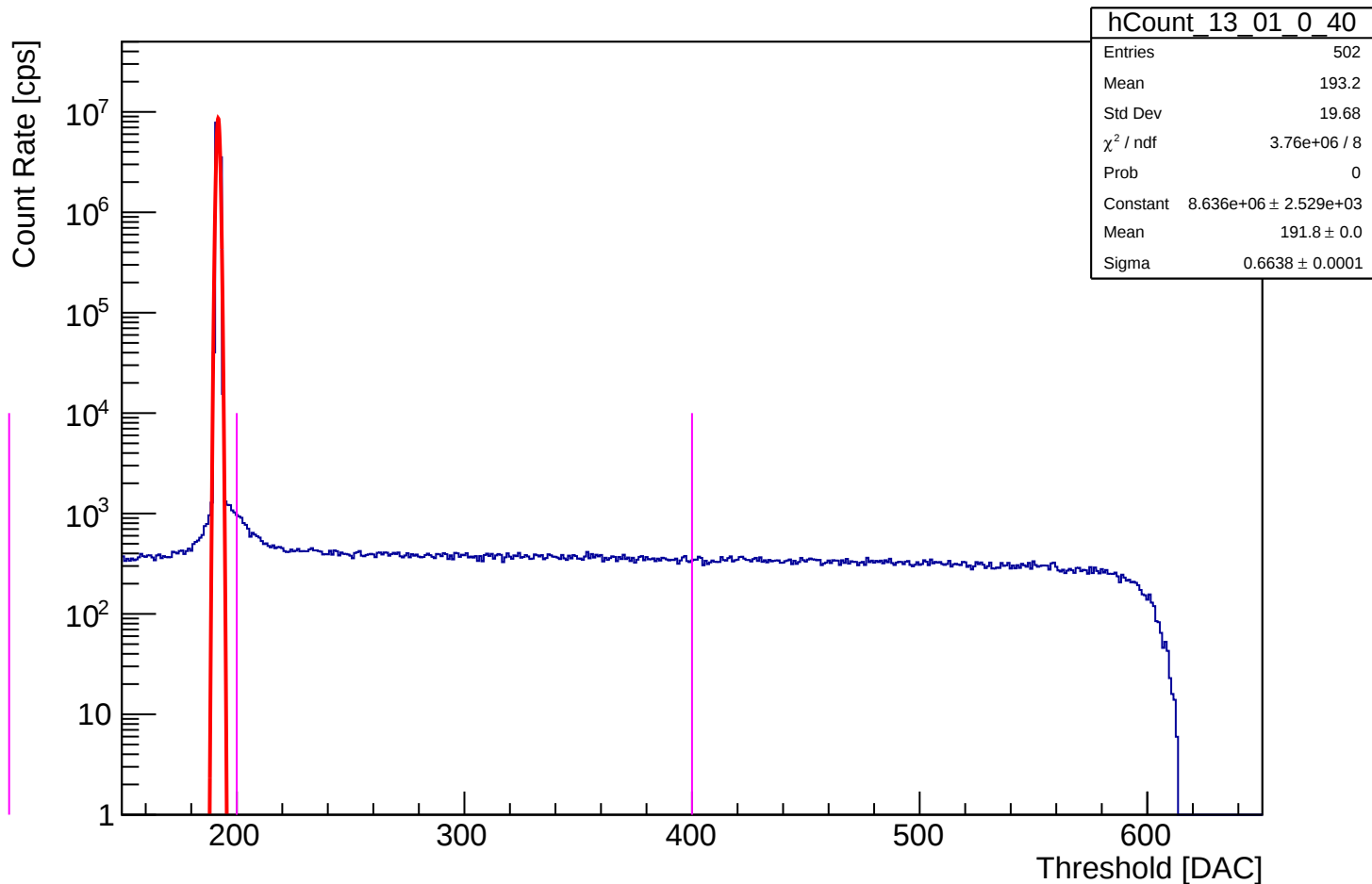
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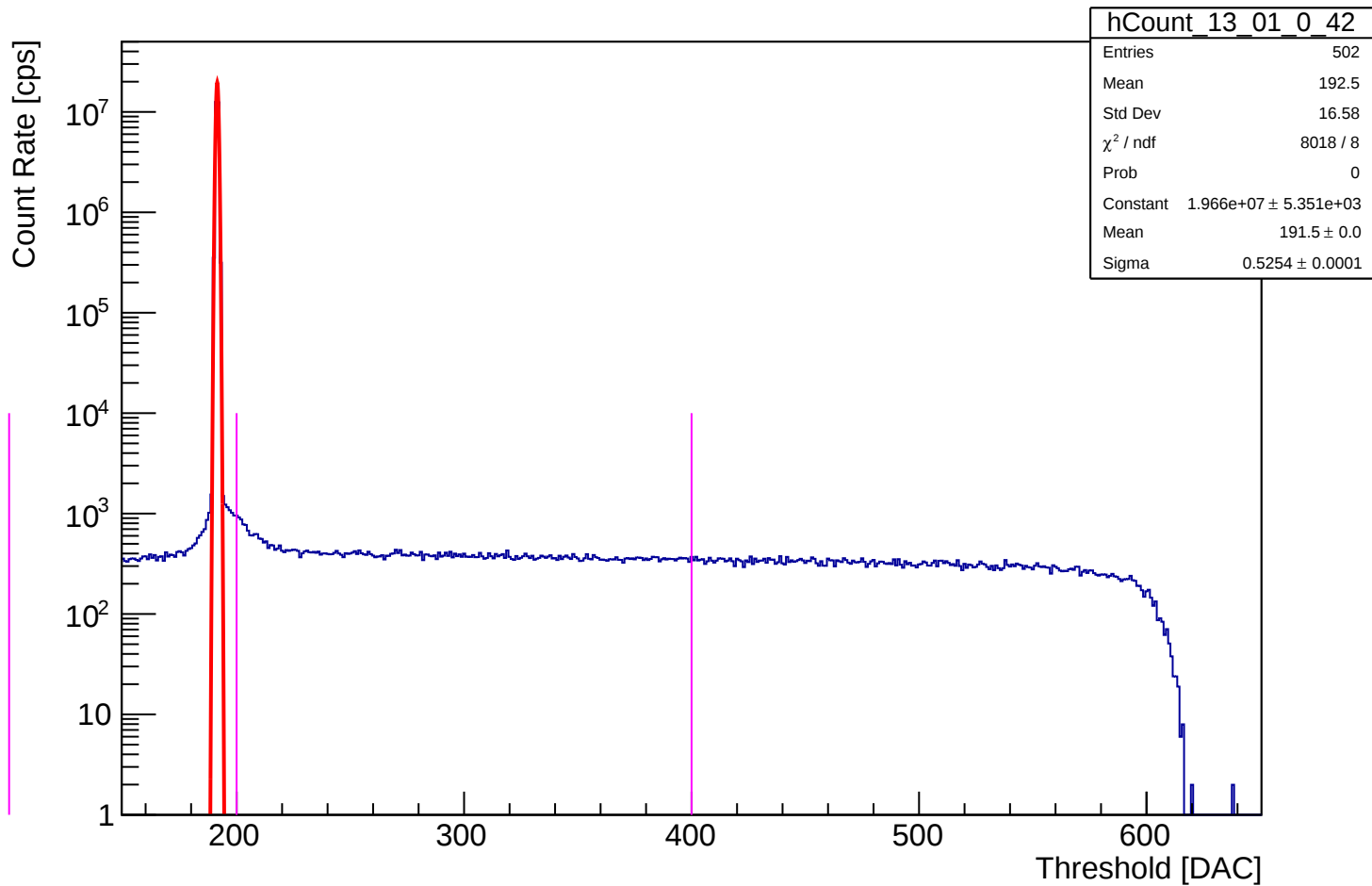
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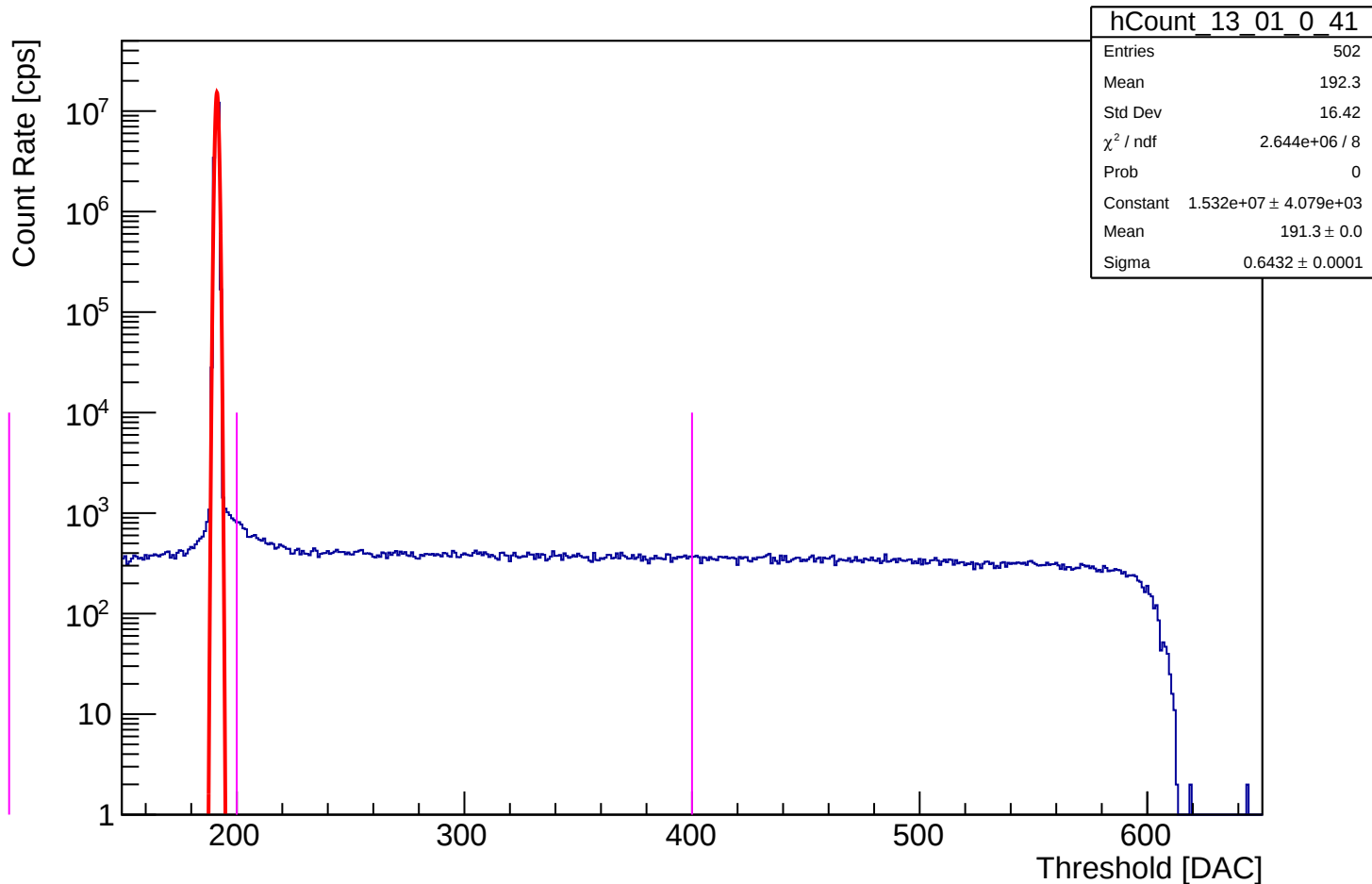
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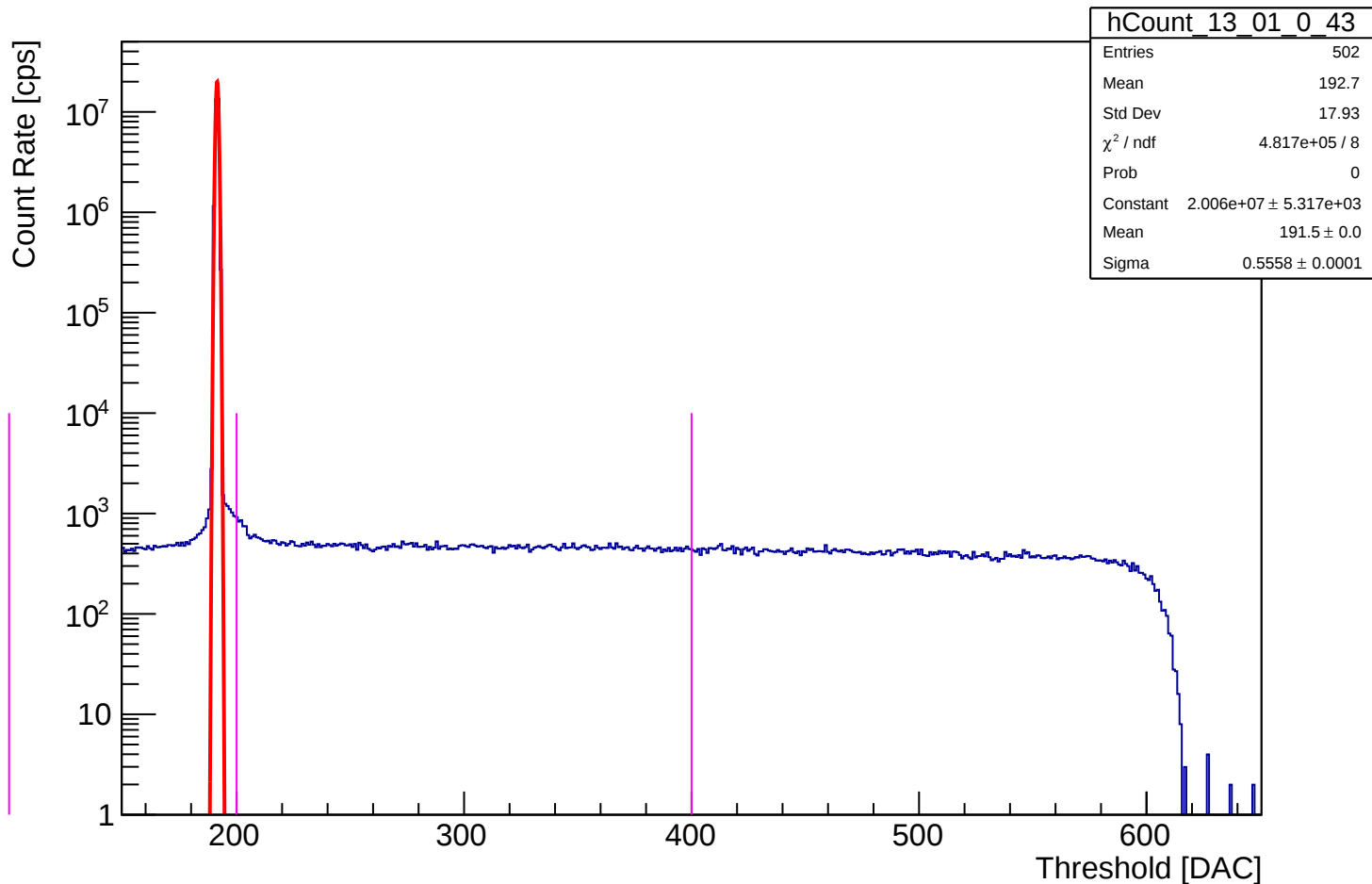
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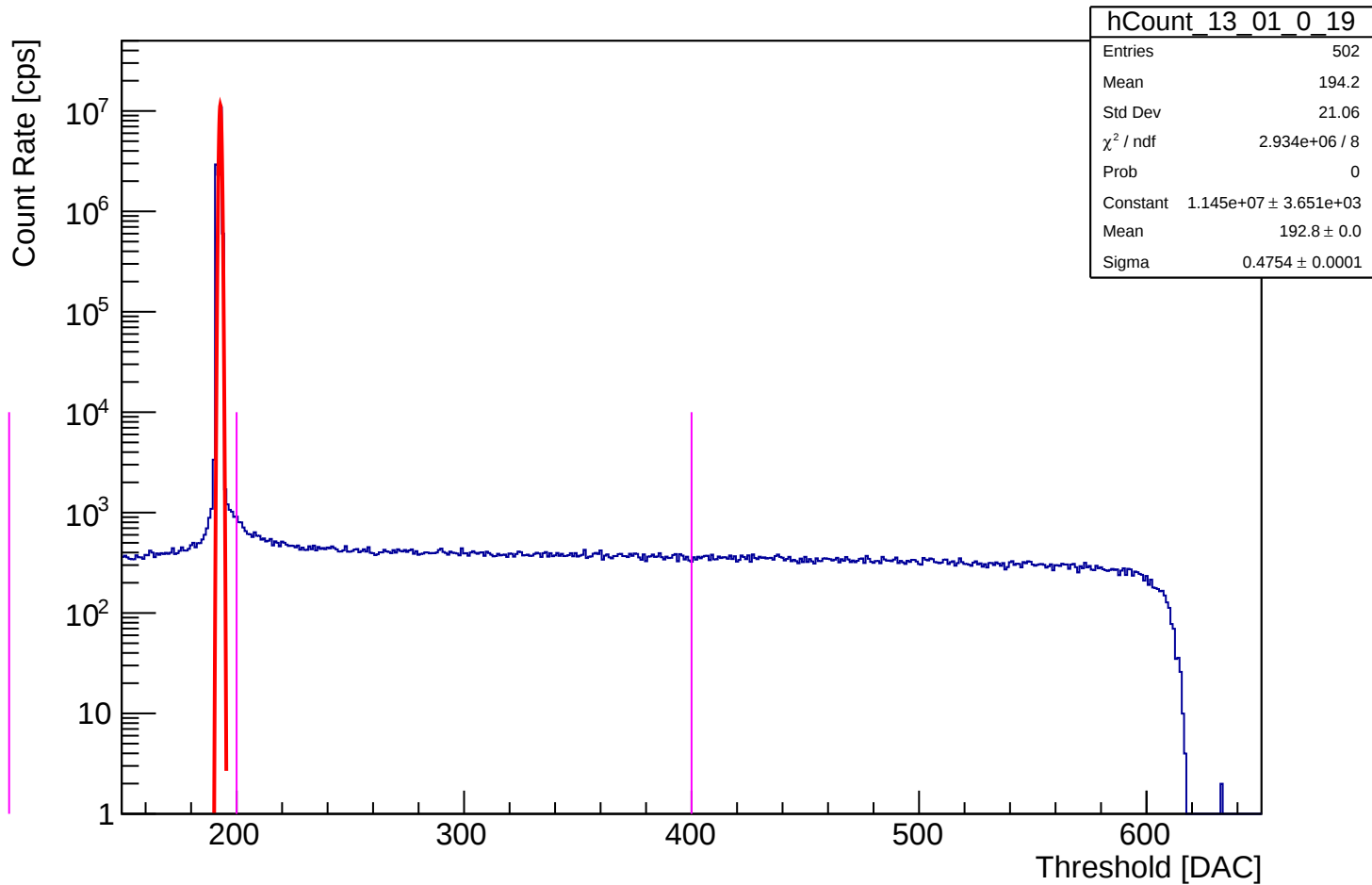
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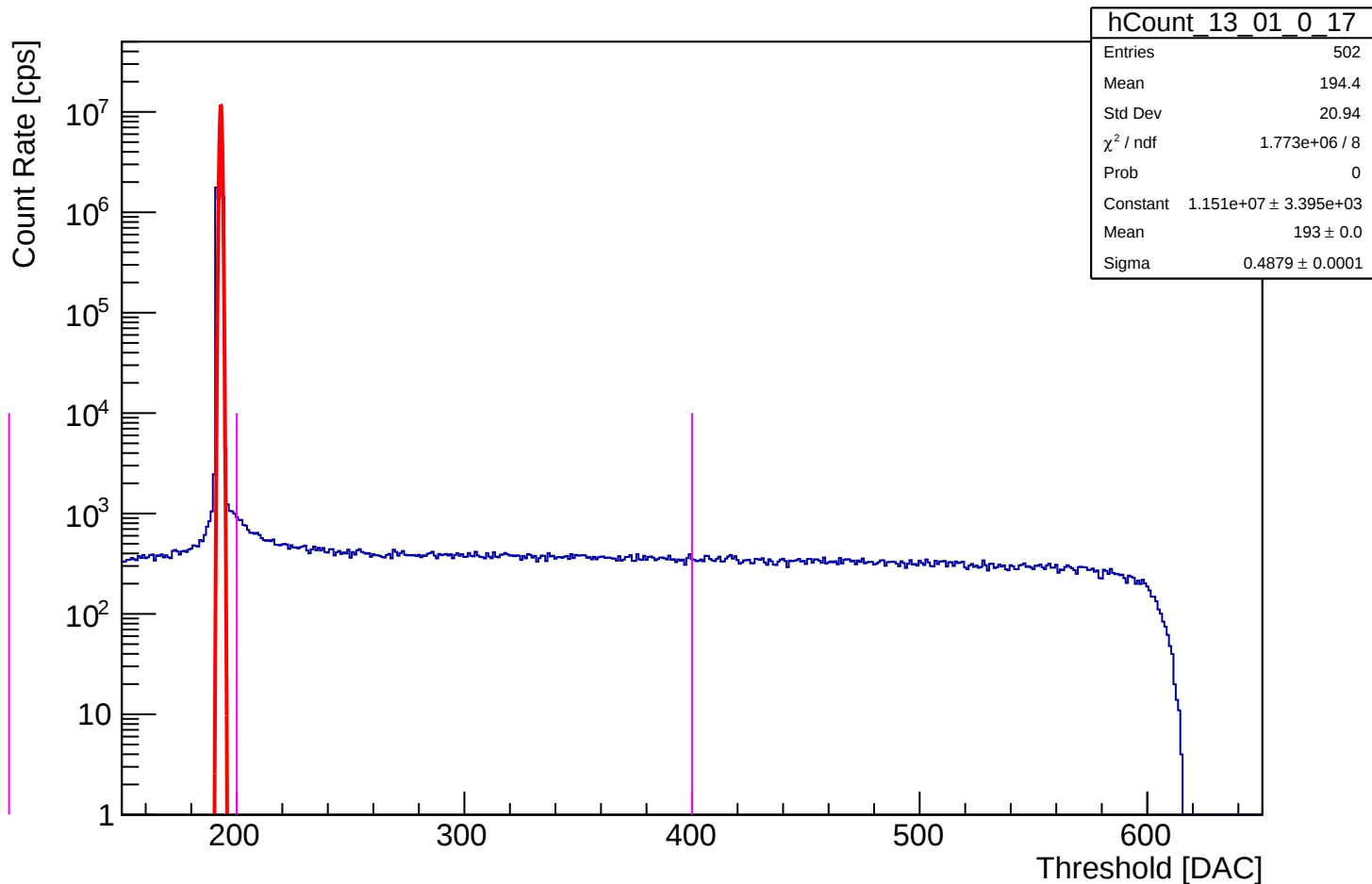
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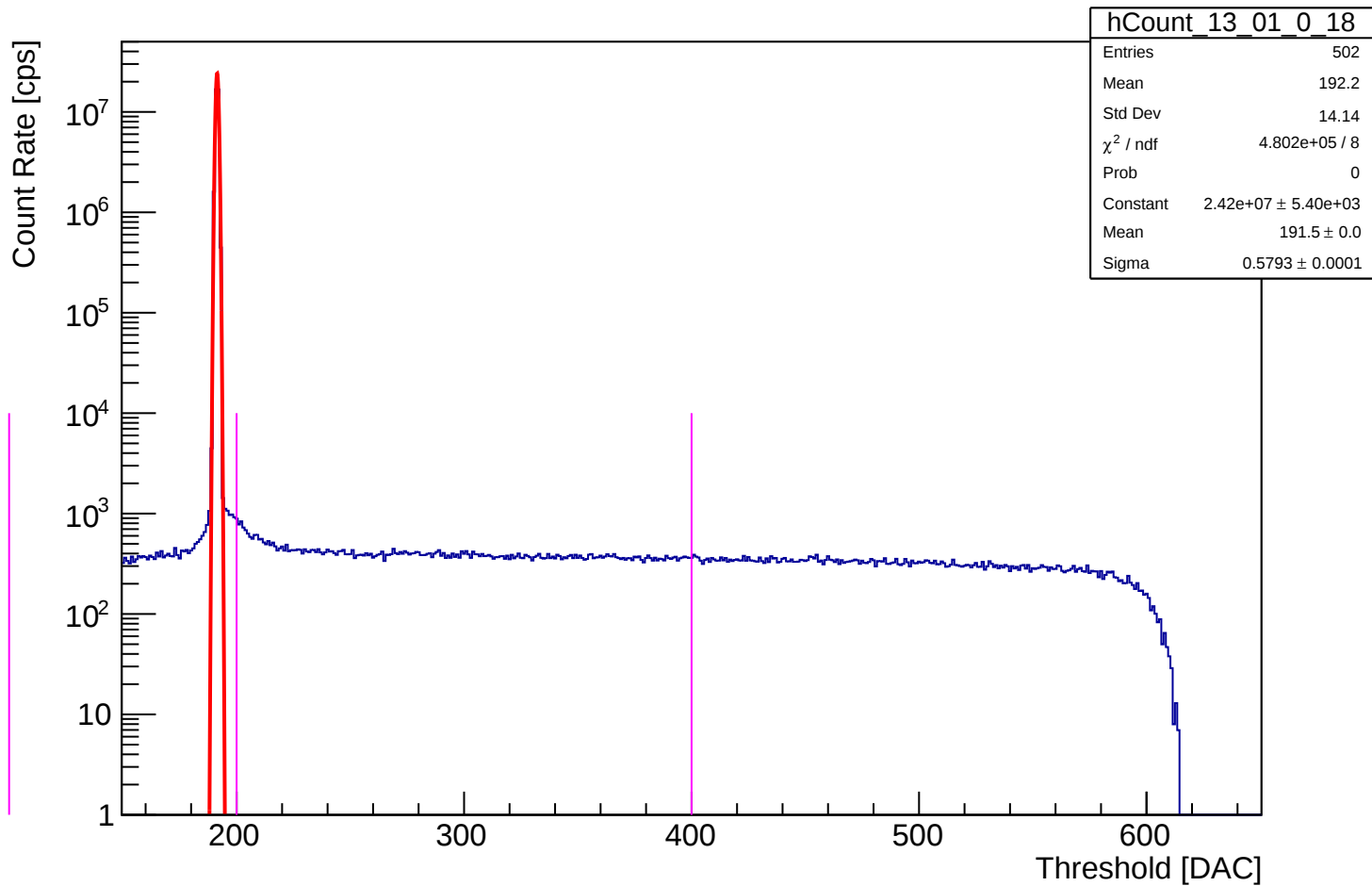
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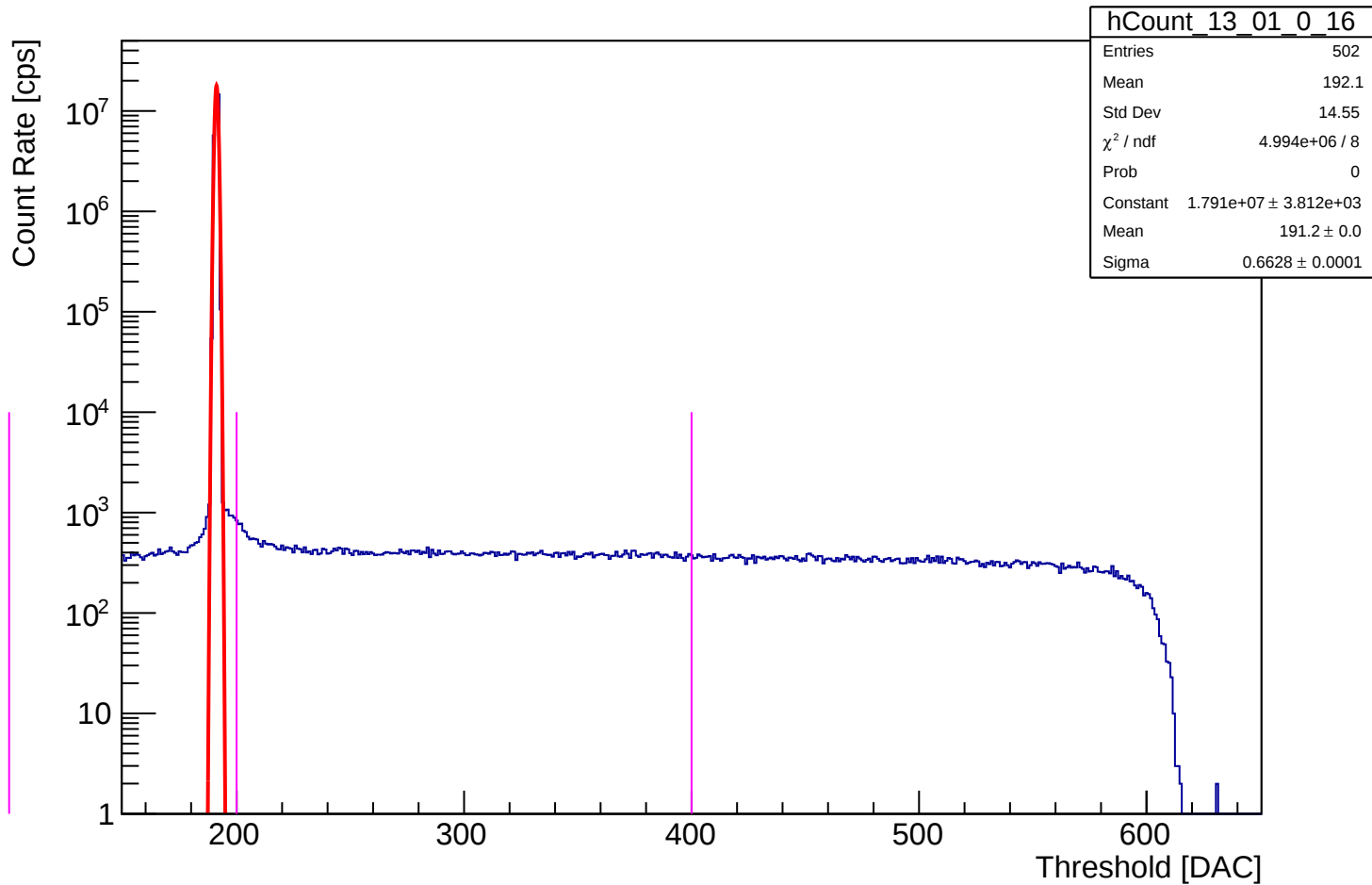
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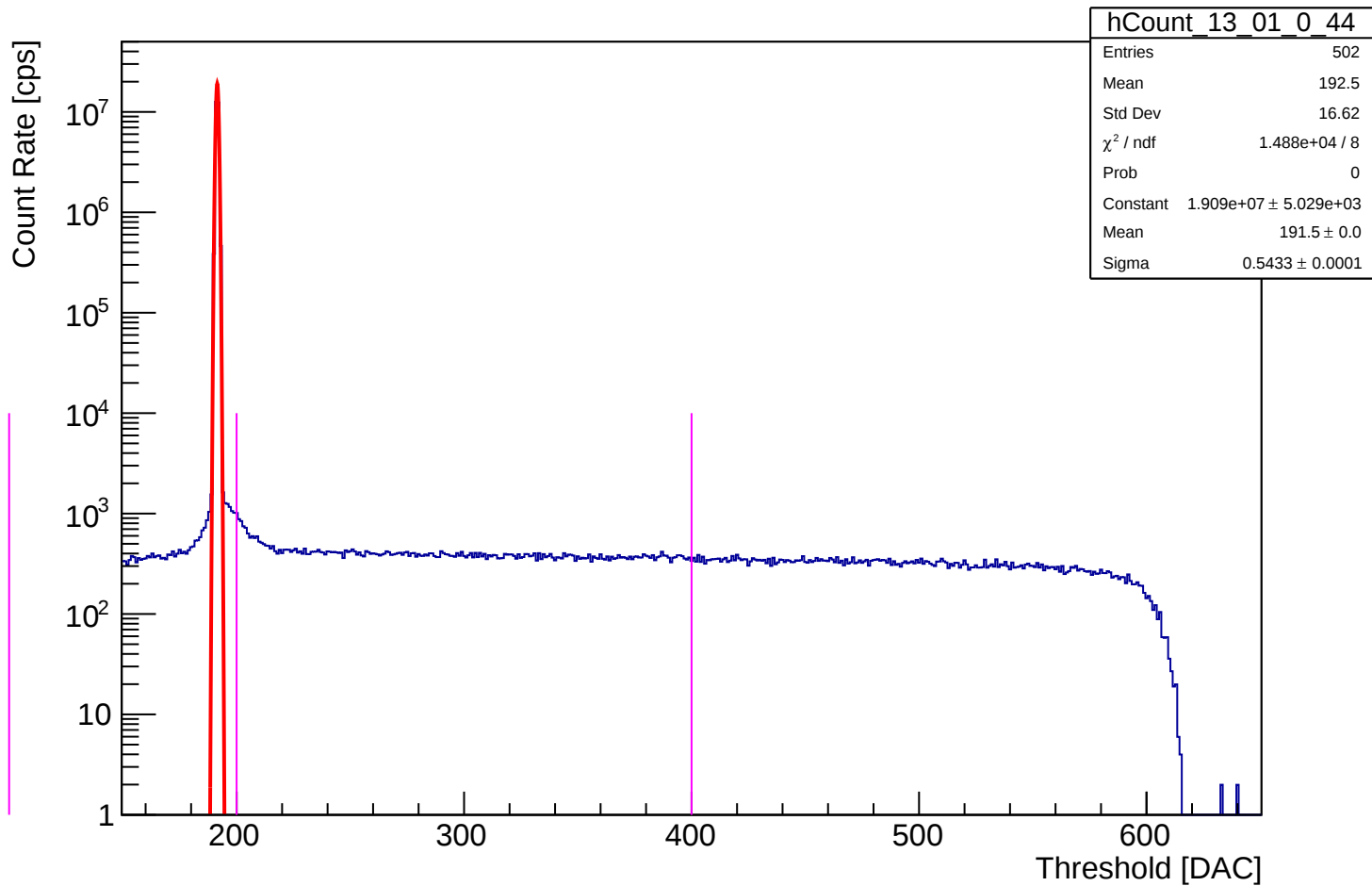
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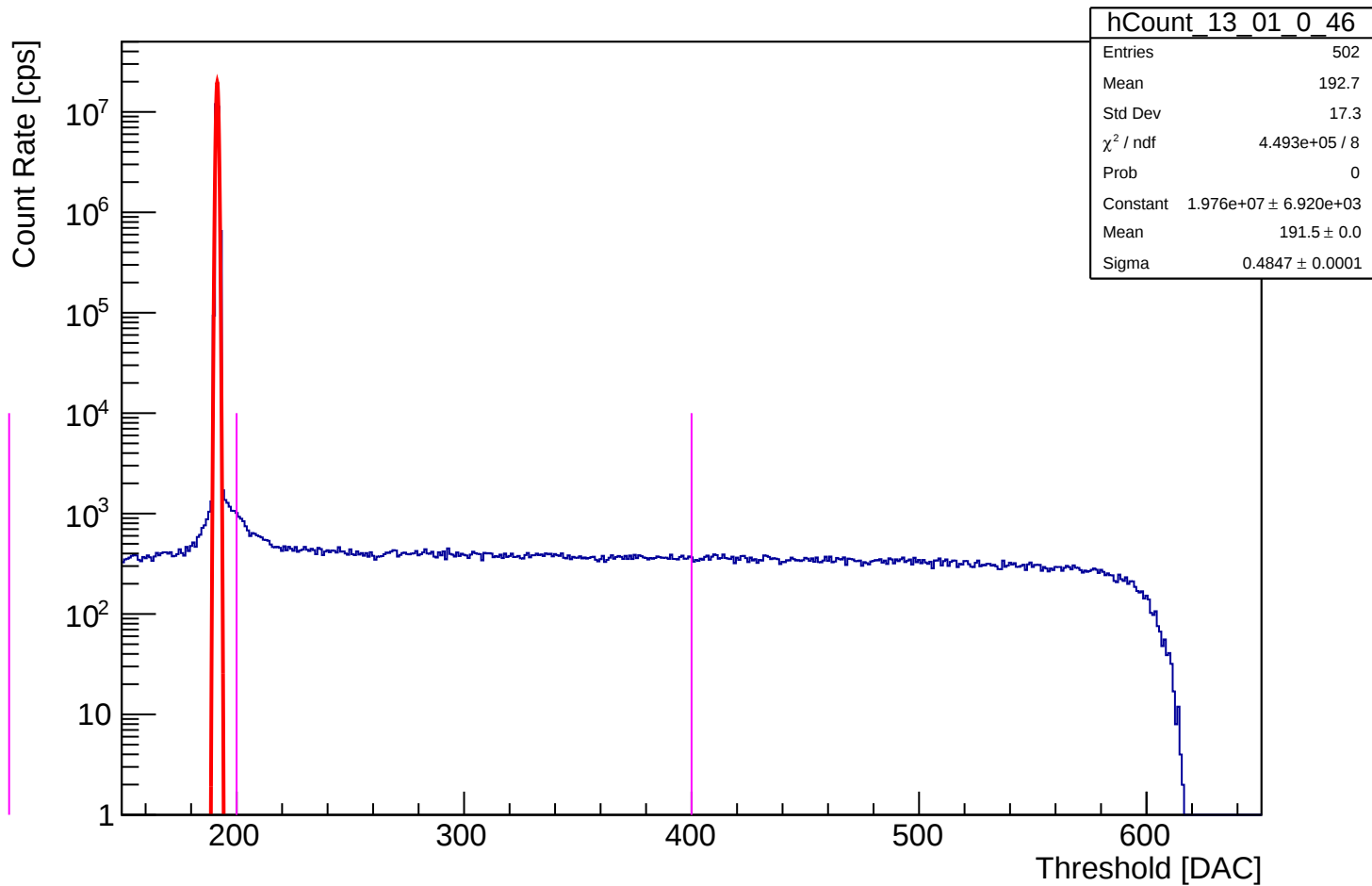
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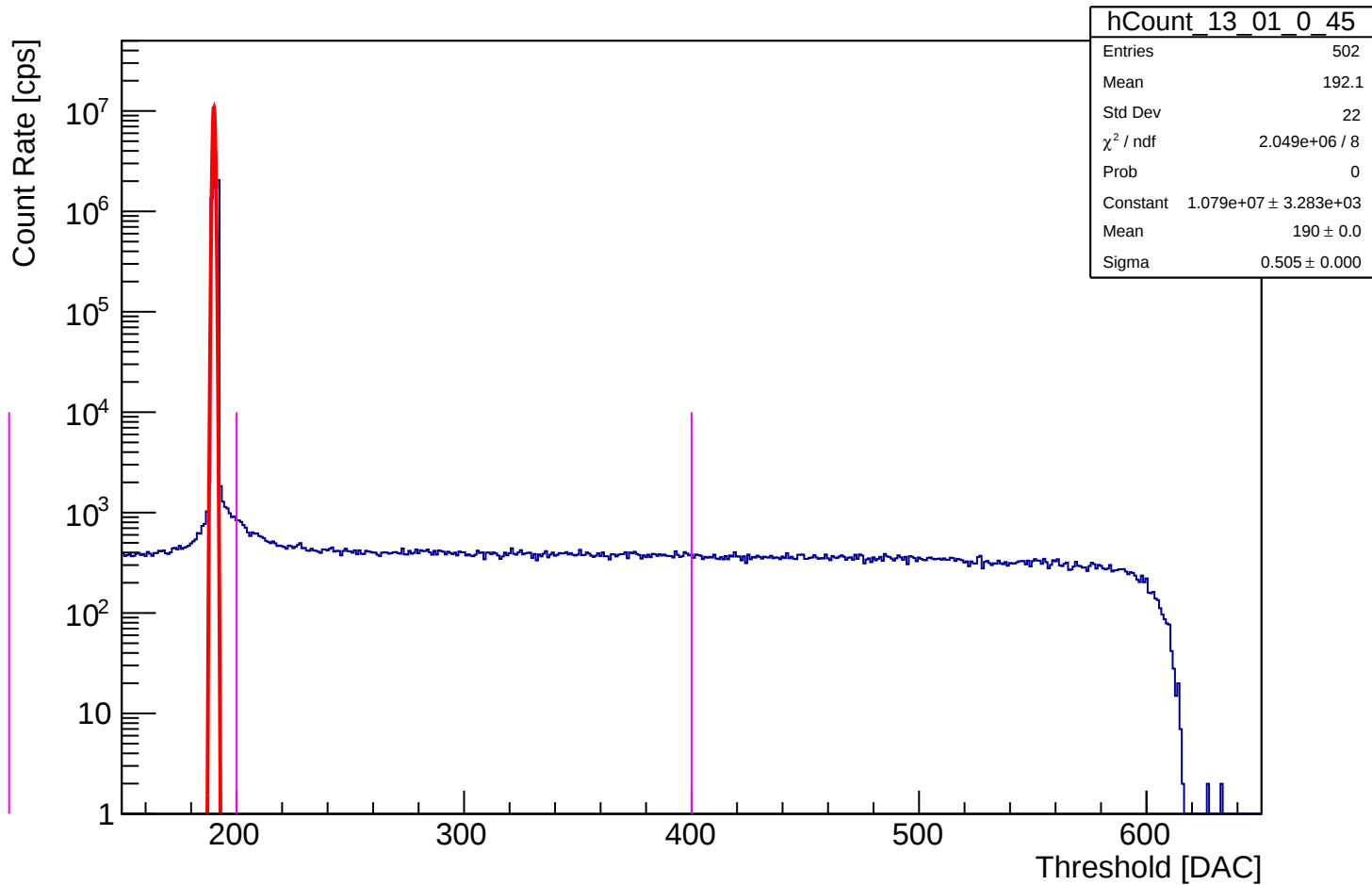
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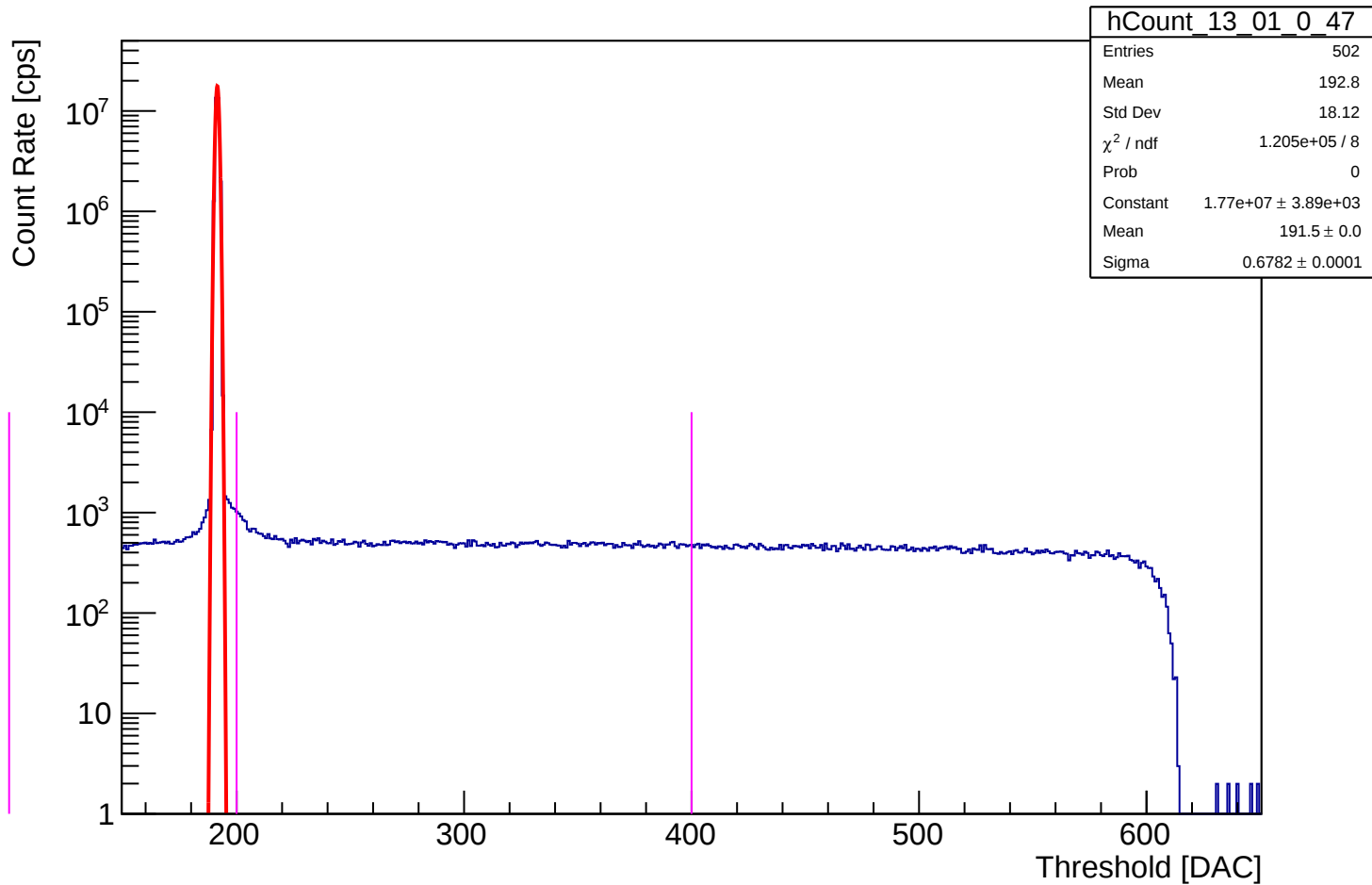
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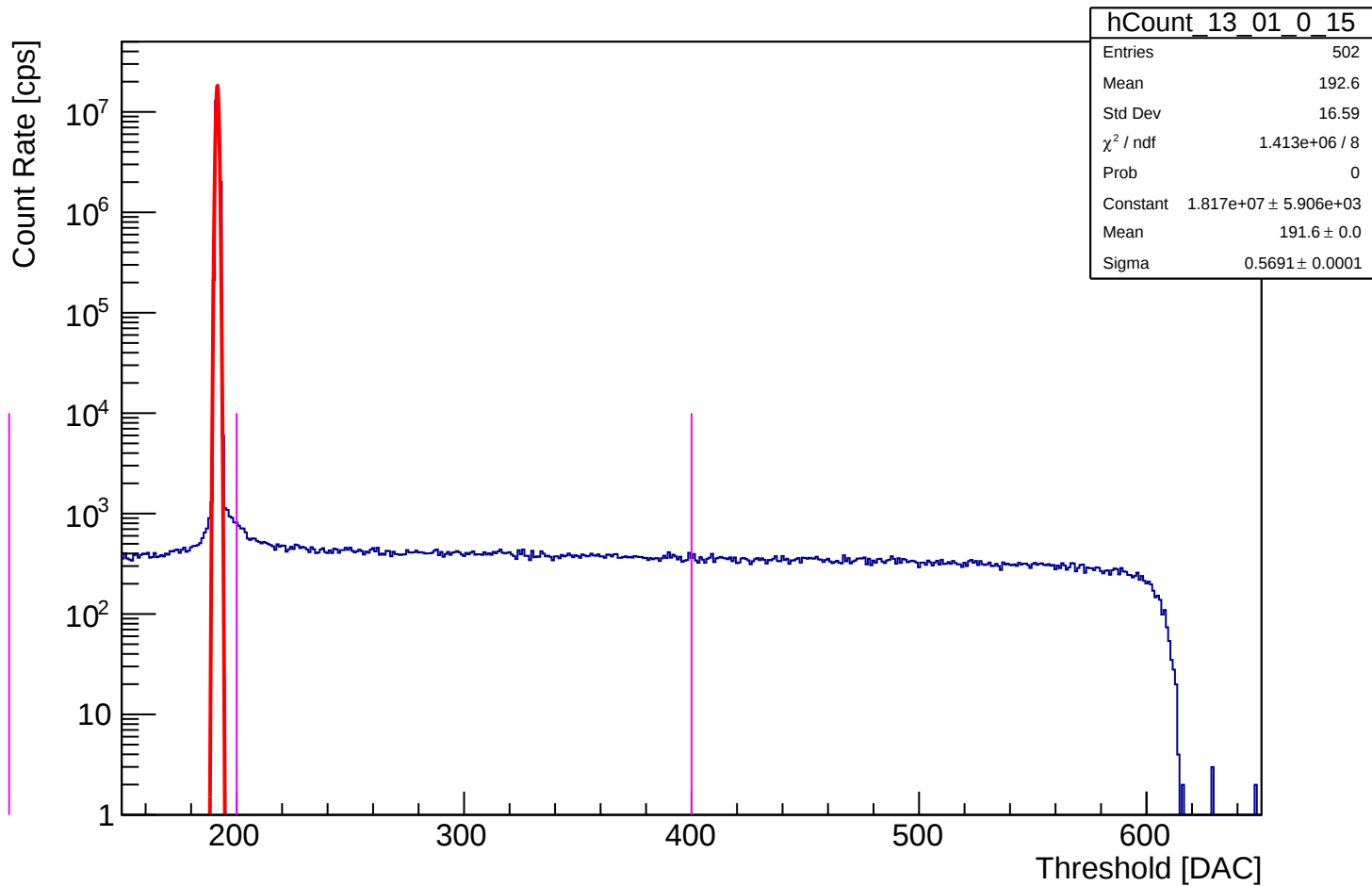
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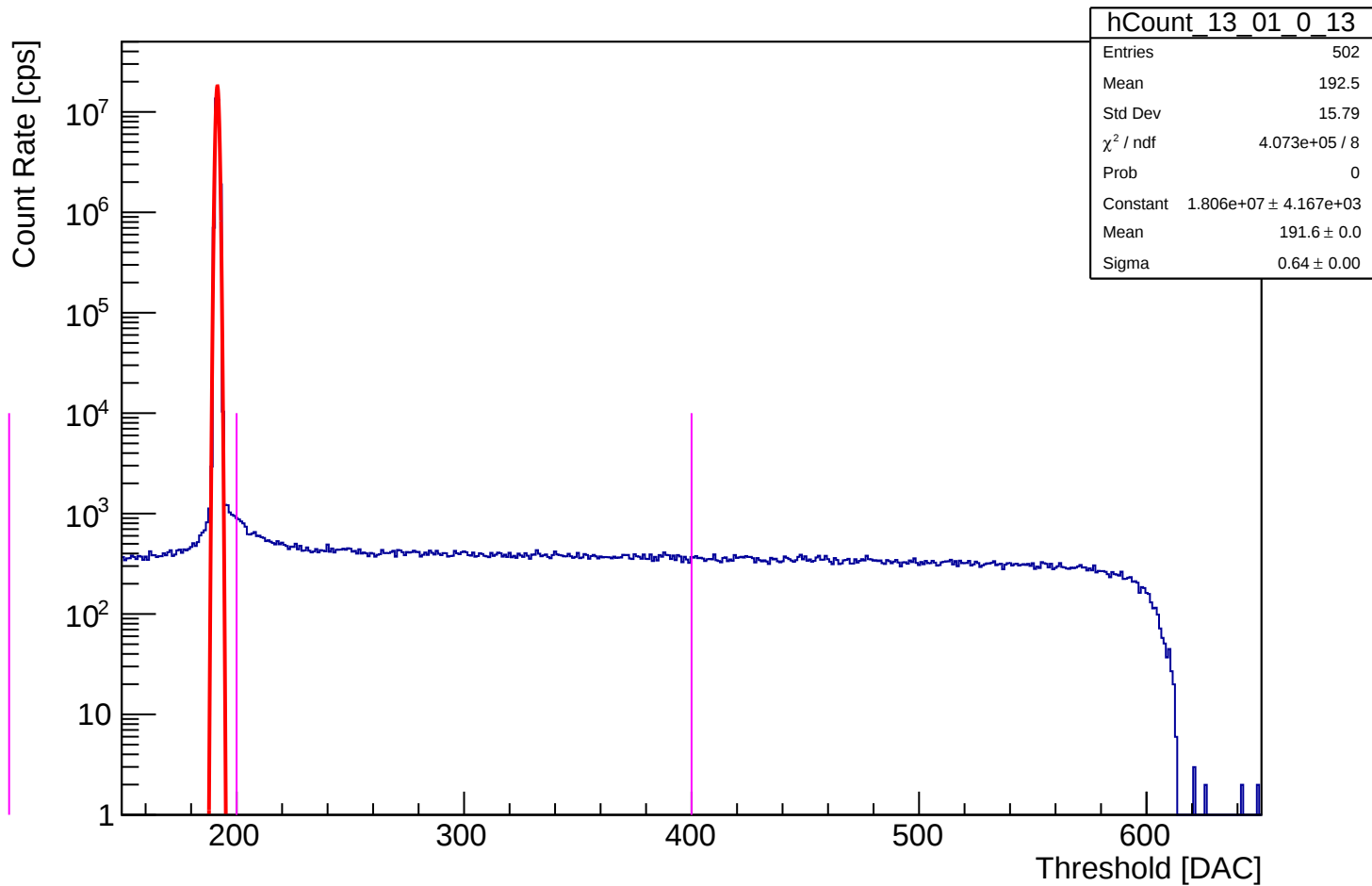
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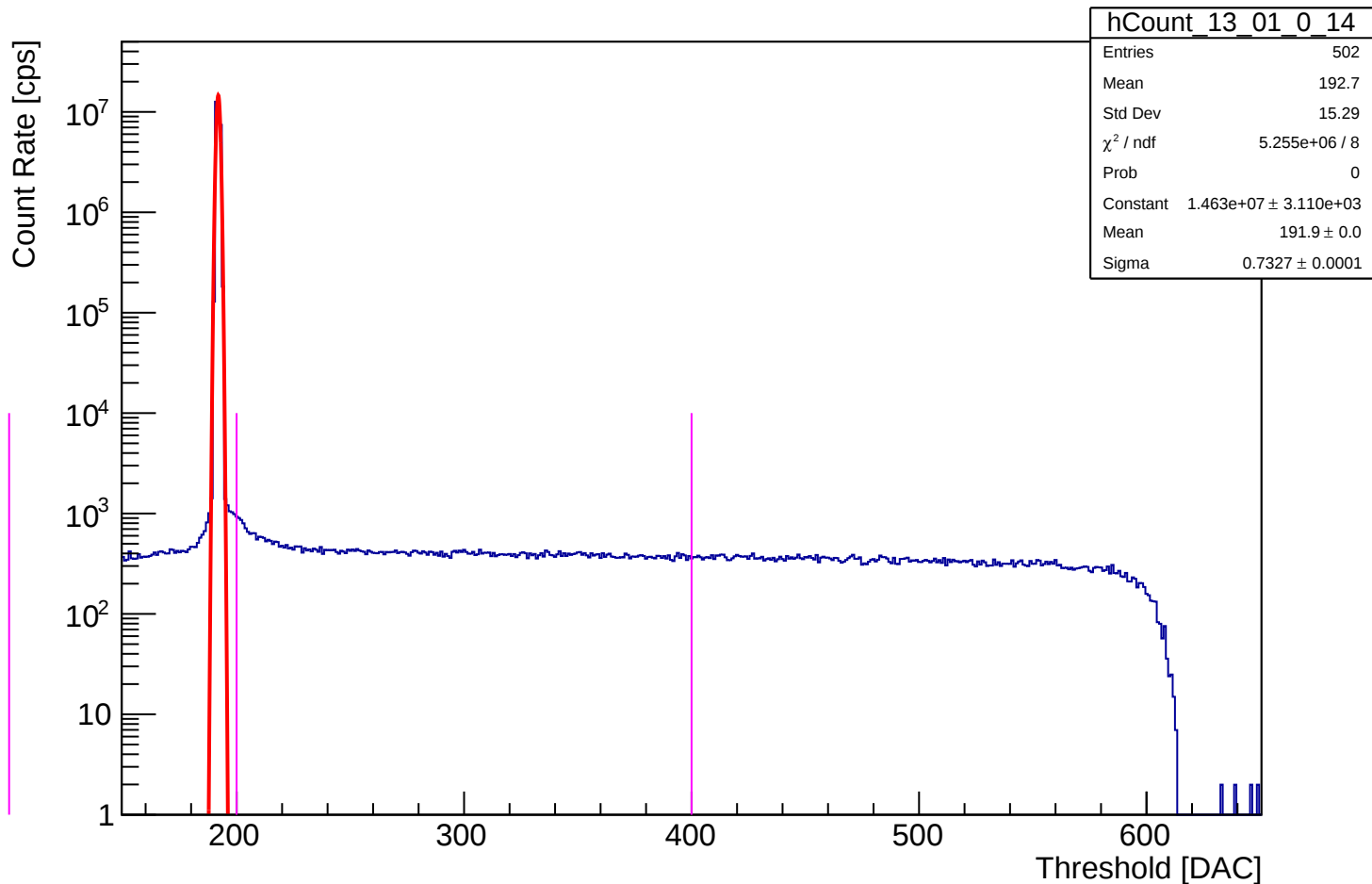
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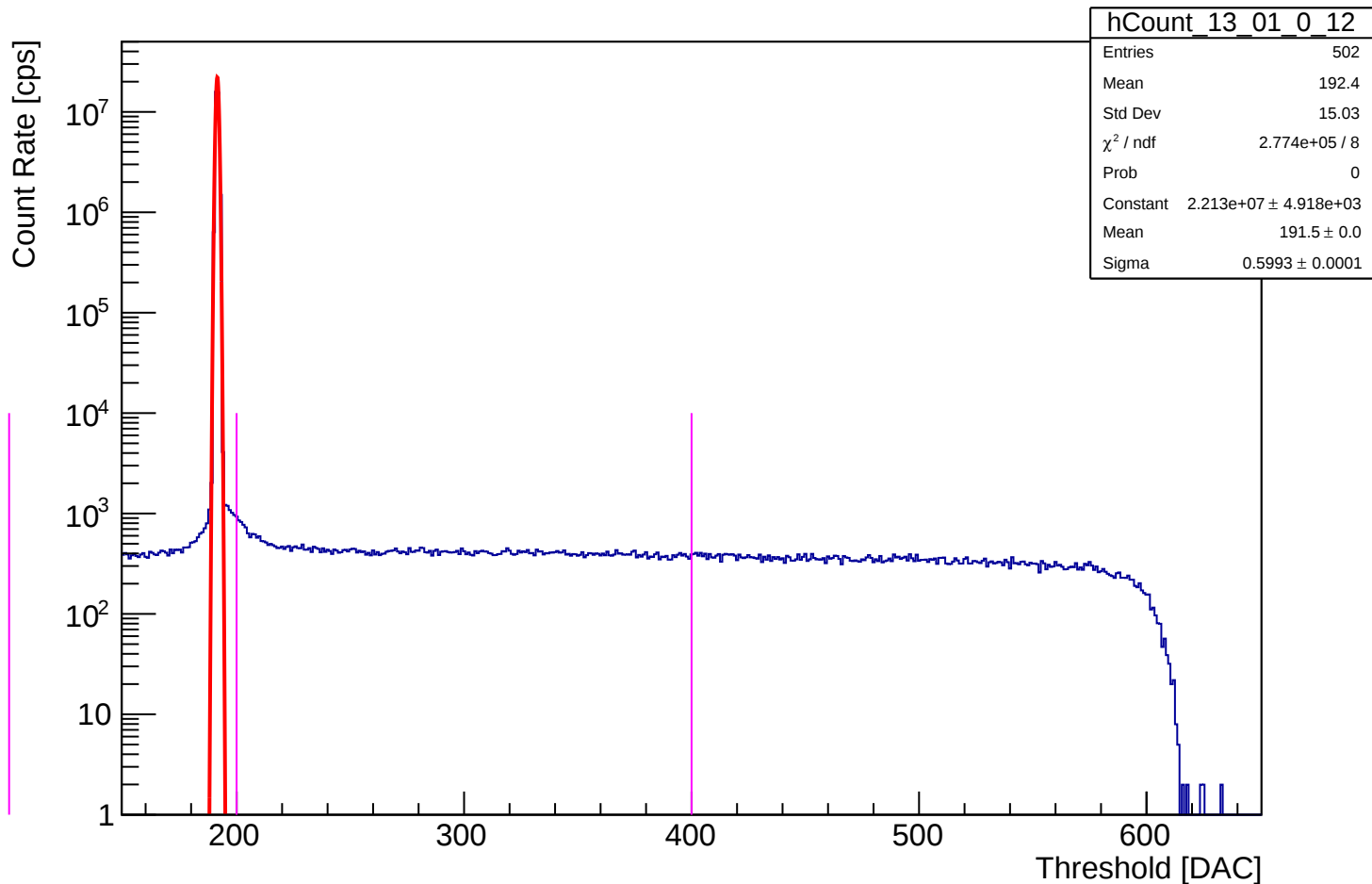
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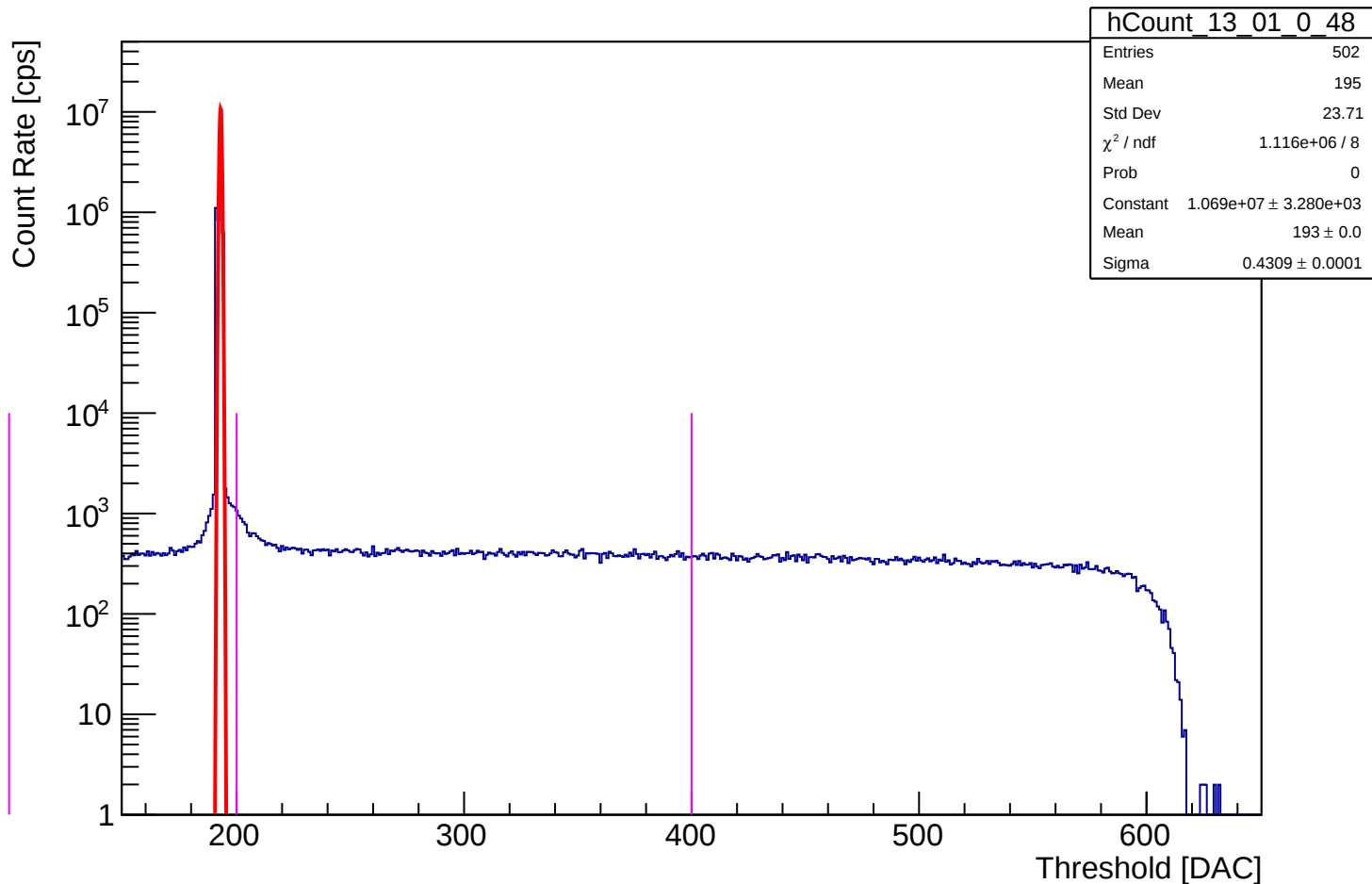
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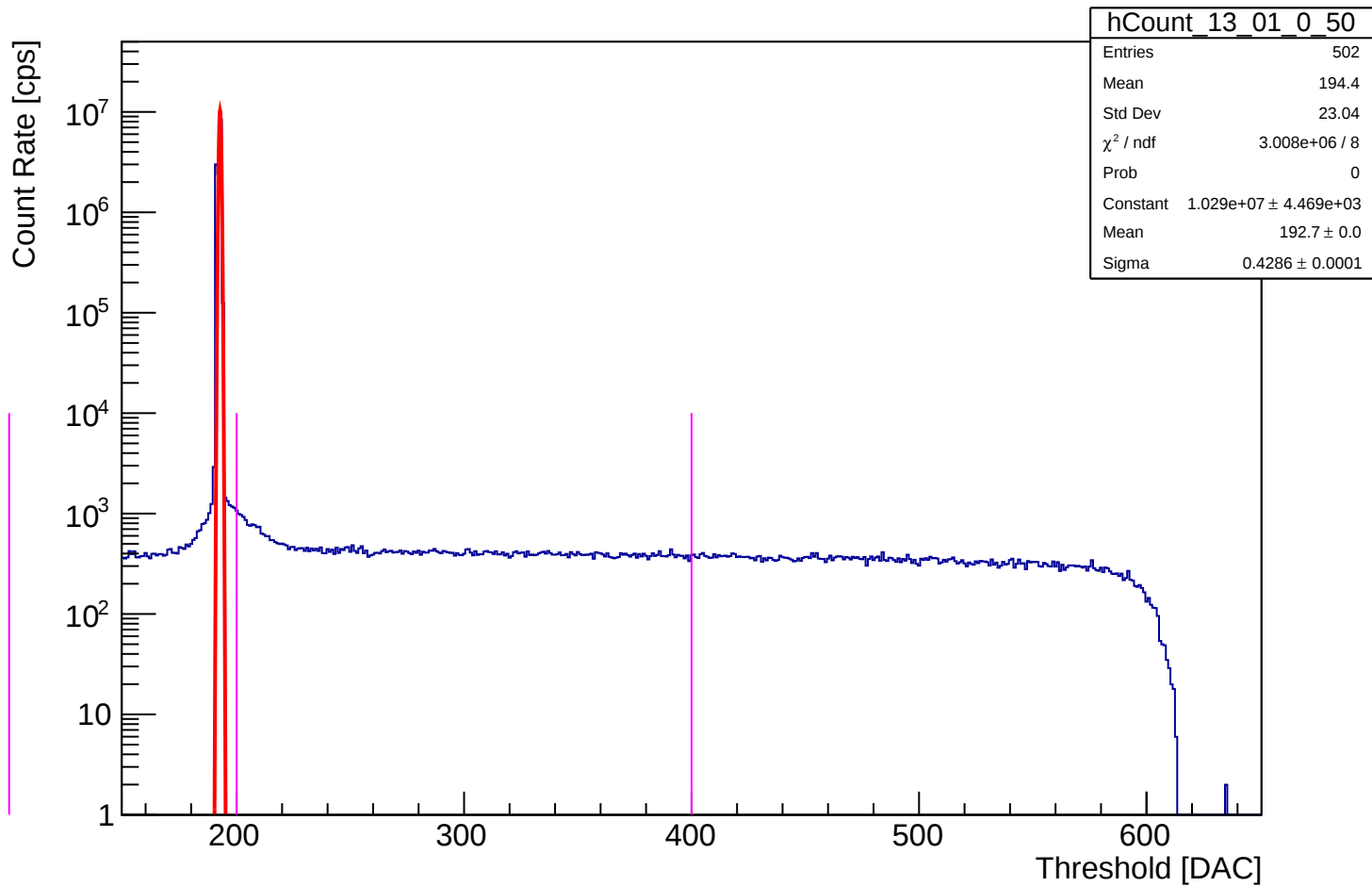
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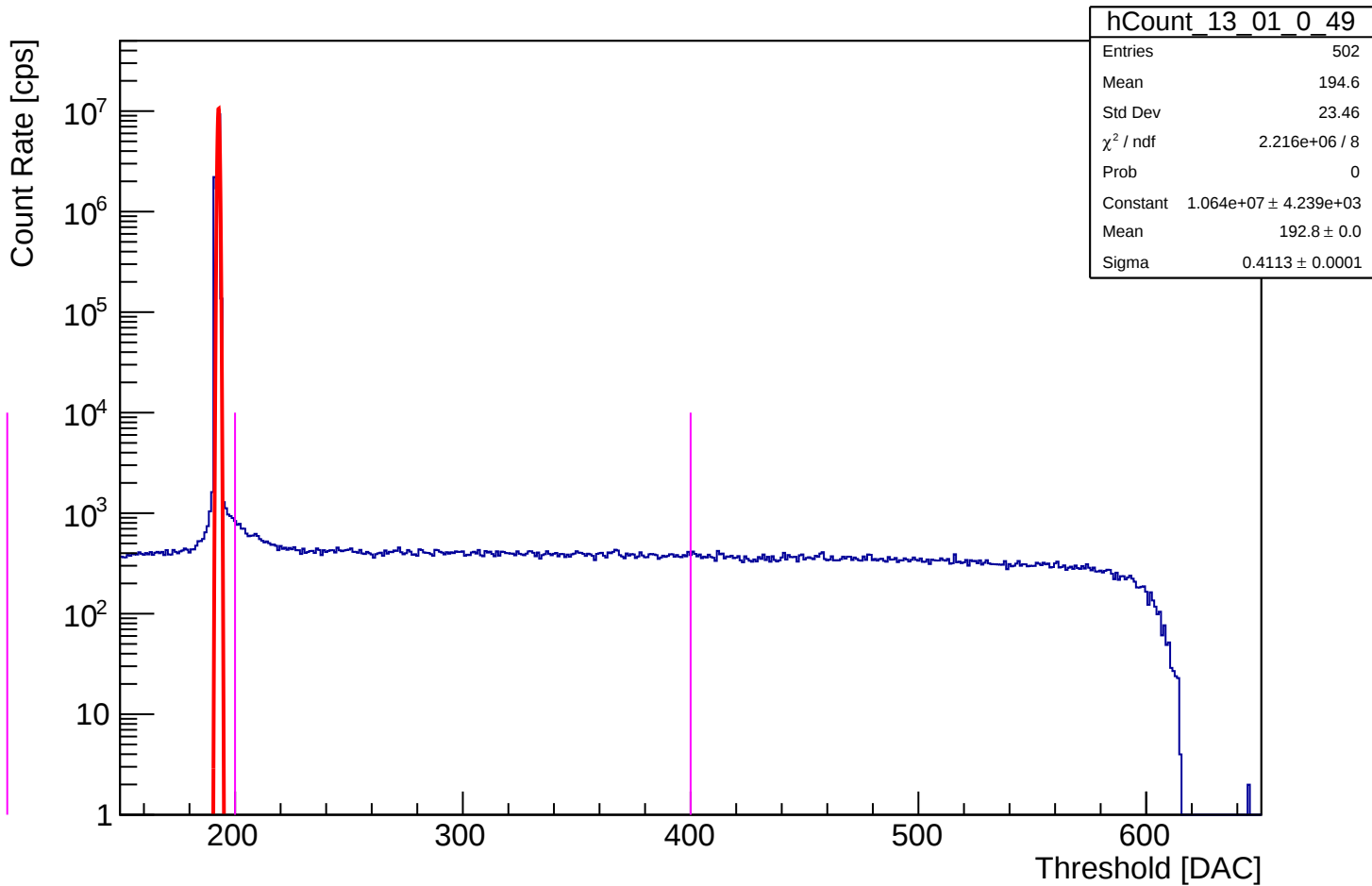
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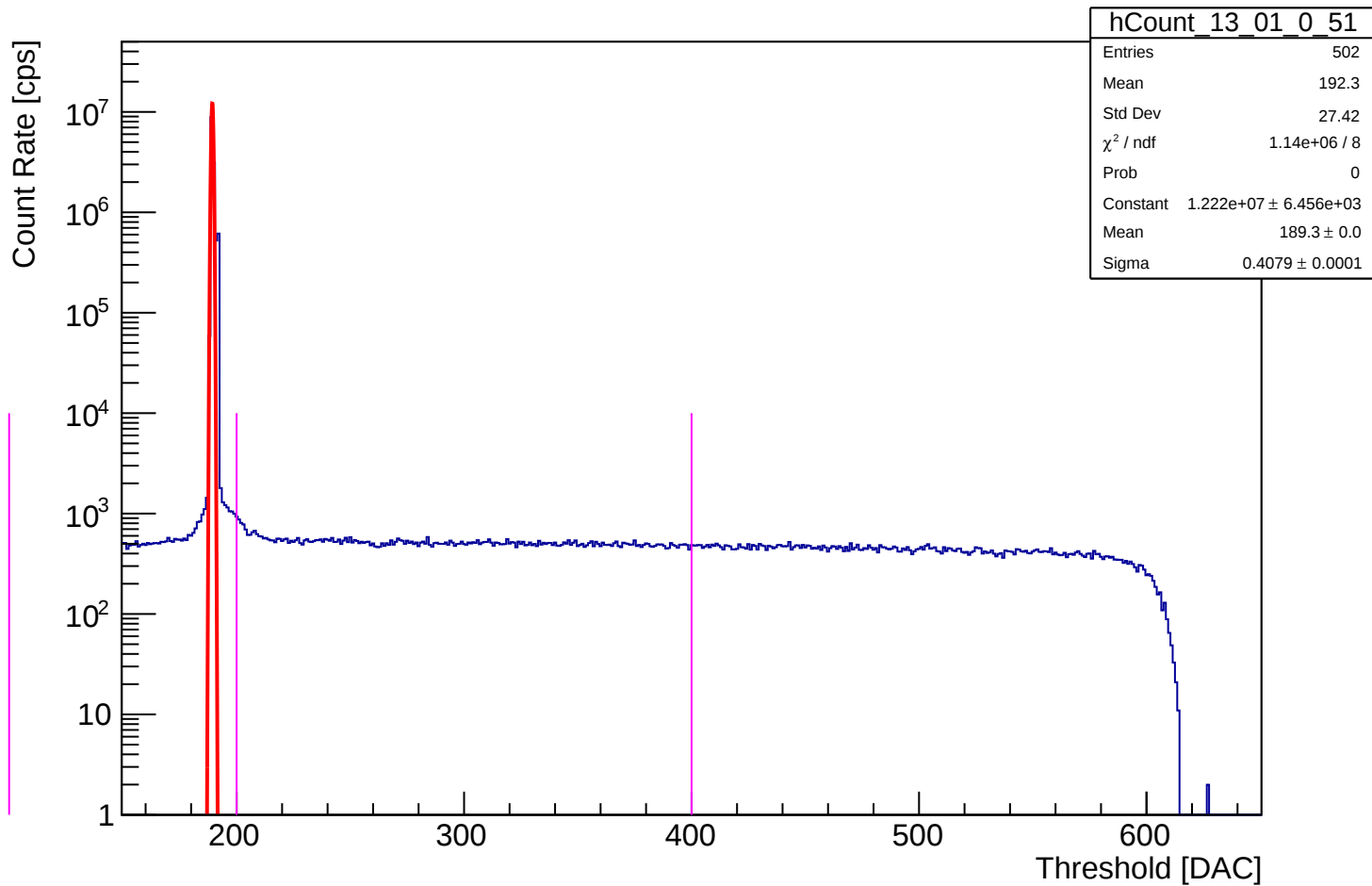
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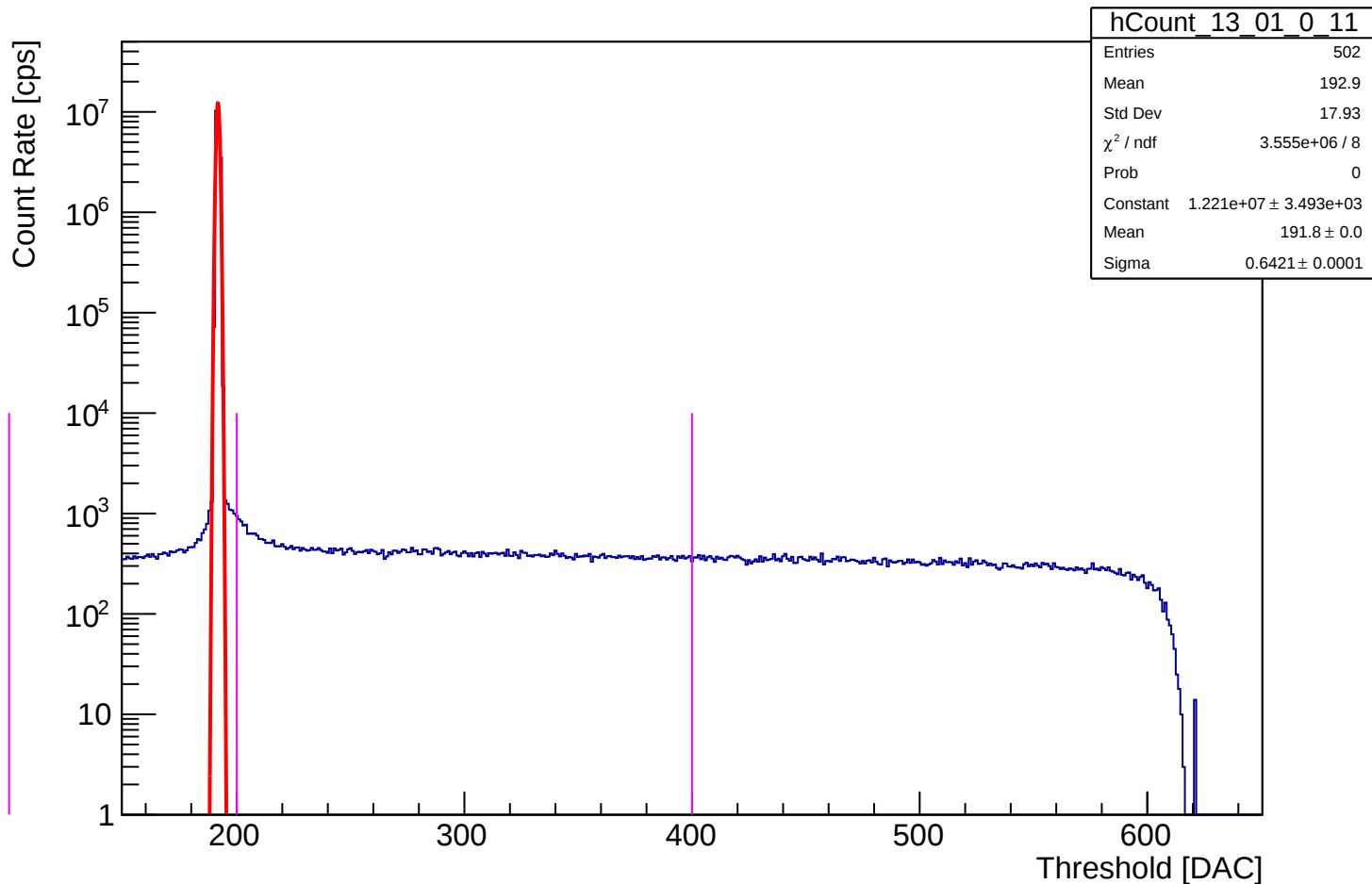
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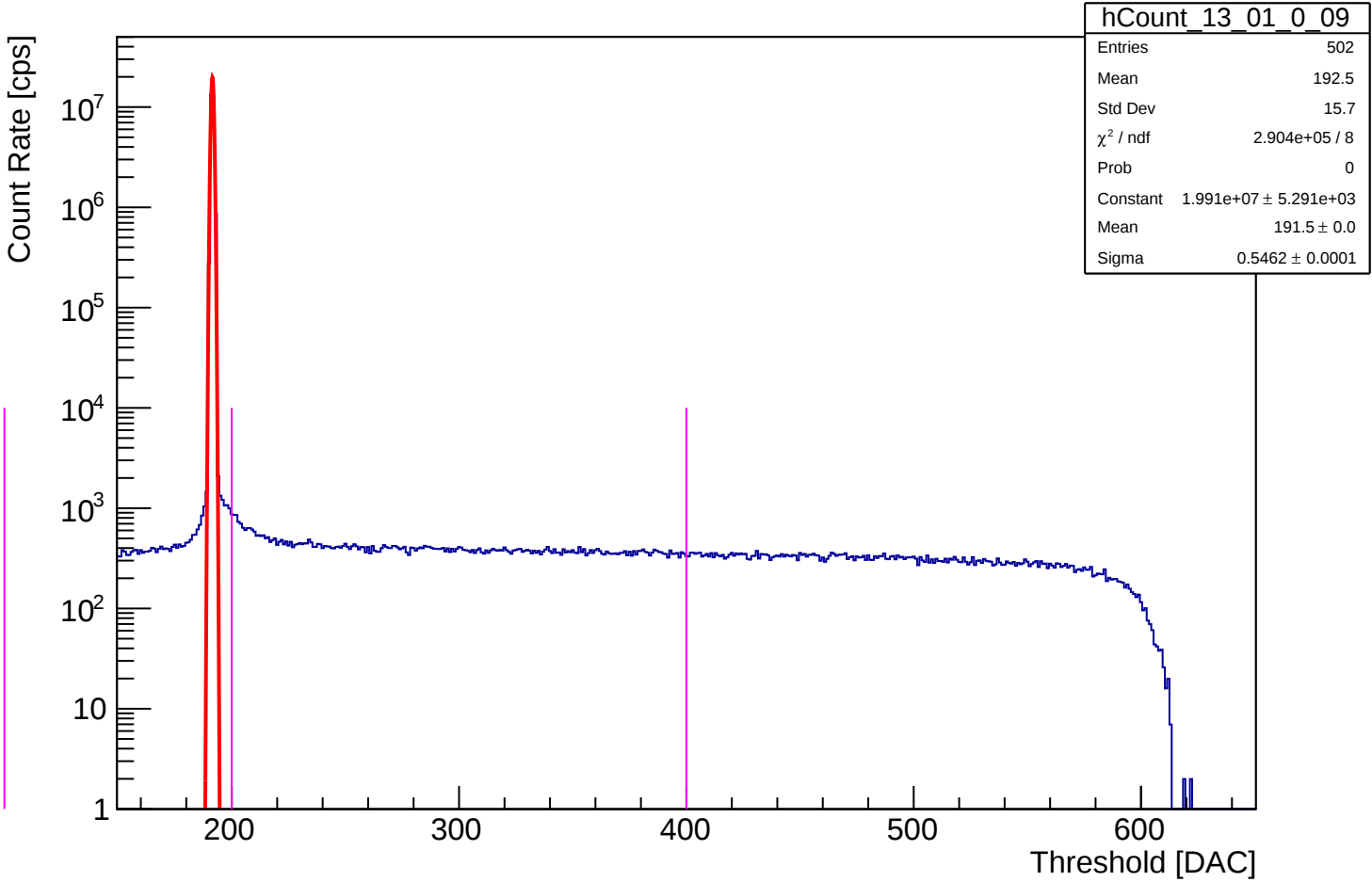


Row 1 Tile 1 Pmt 4 Pixel 40 Slot 13 Fiber 1 Asic 0 Channel 51

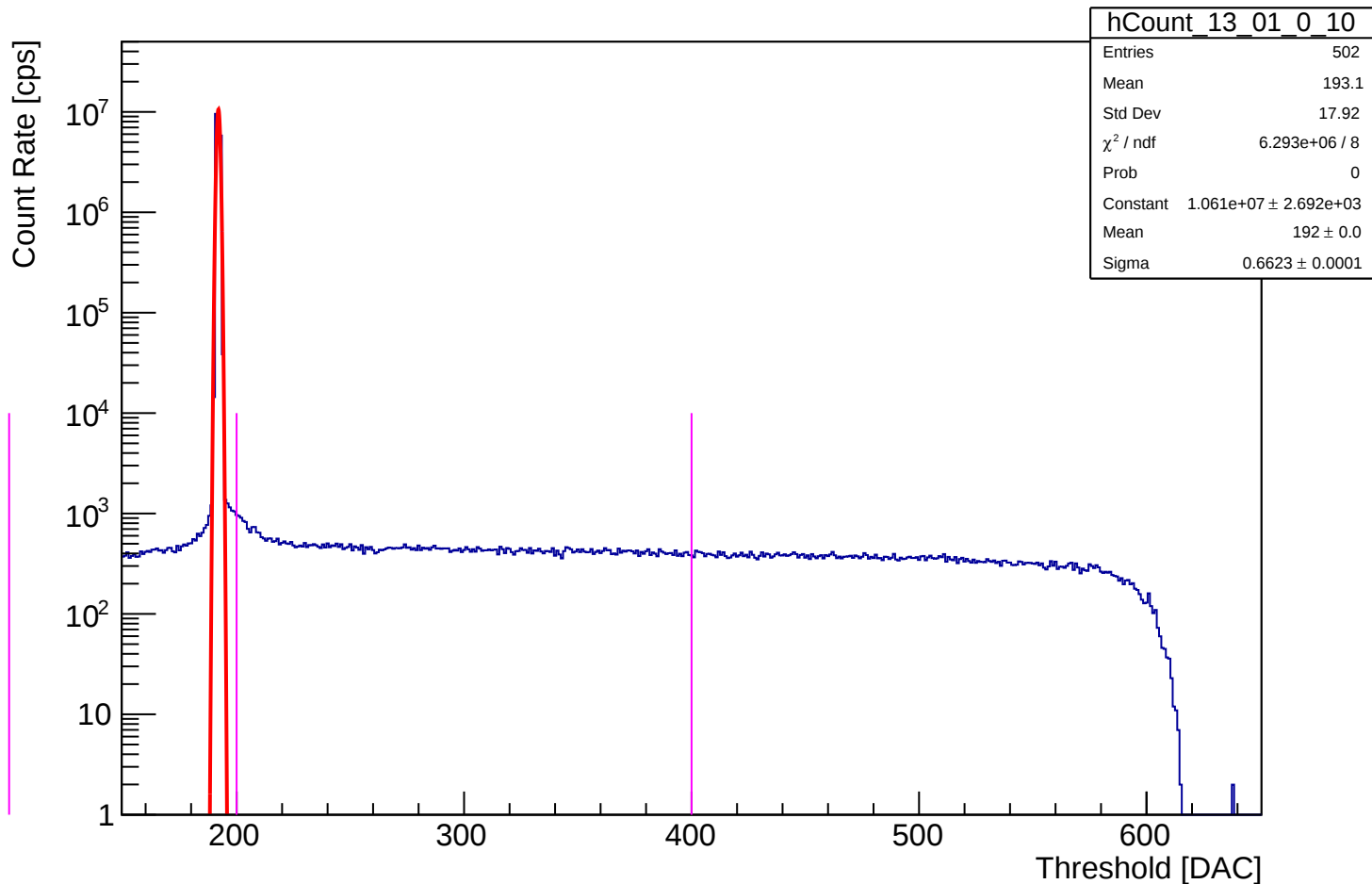


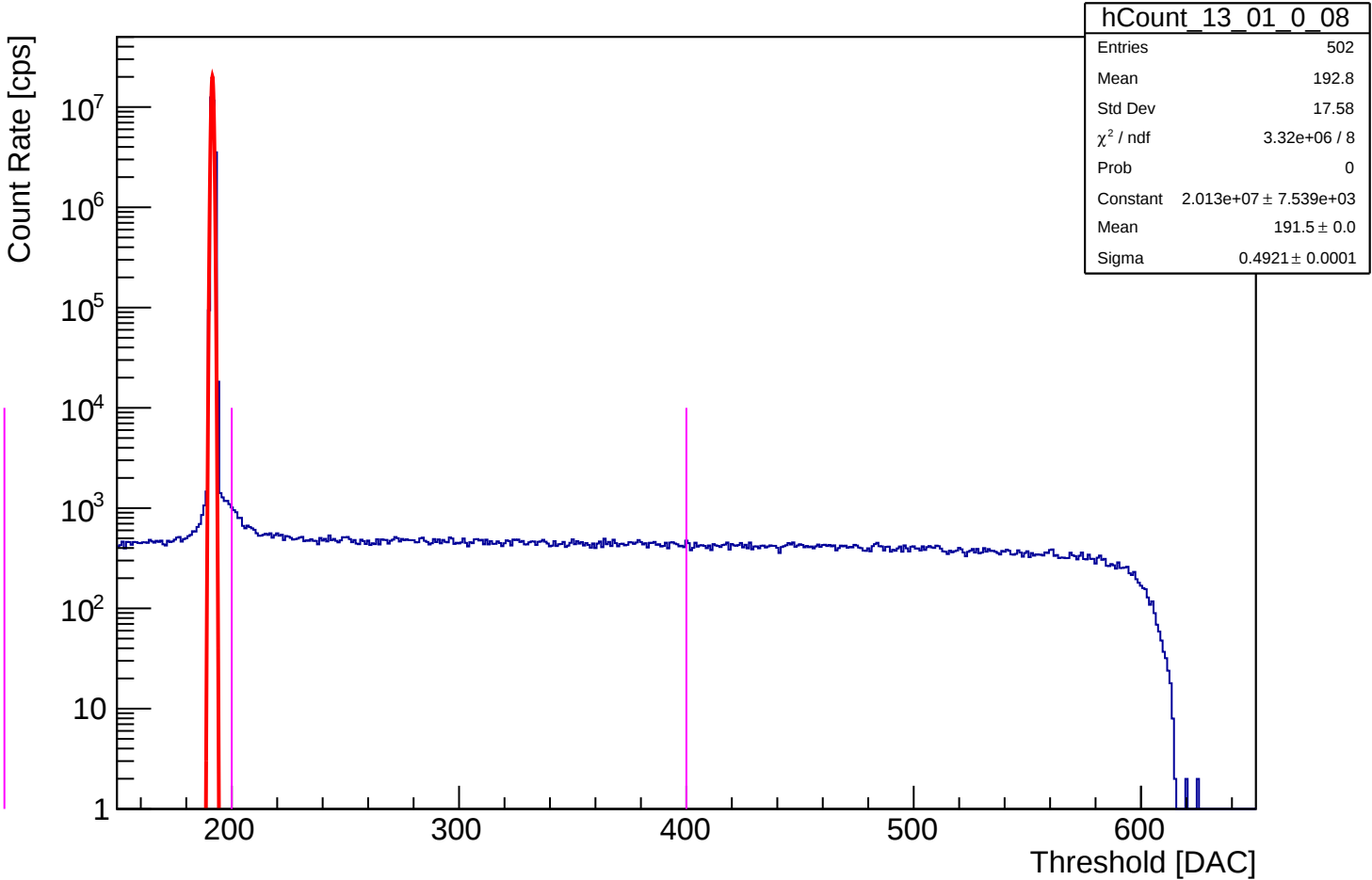
Row 1 Tile 1 Pmt 4 Pixel 41 Slot 13 Fiber 1 Asic 0 Channel 11



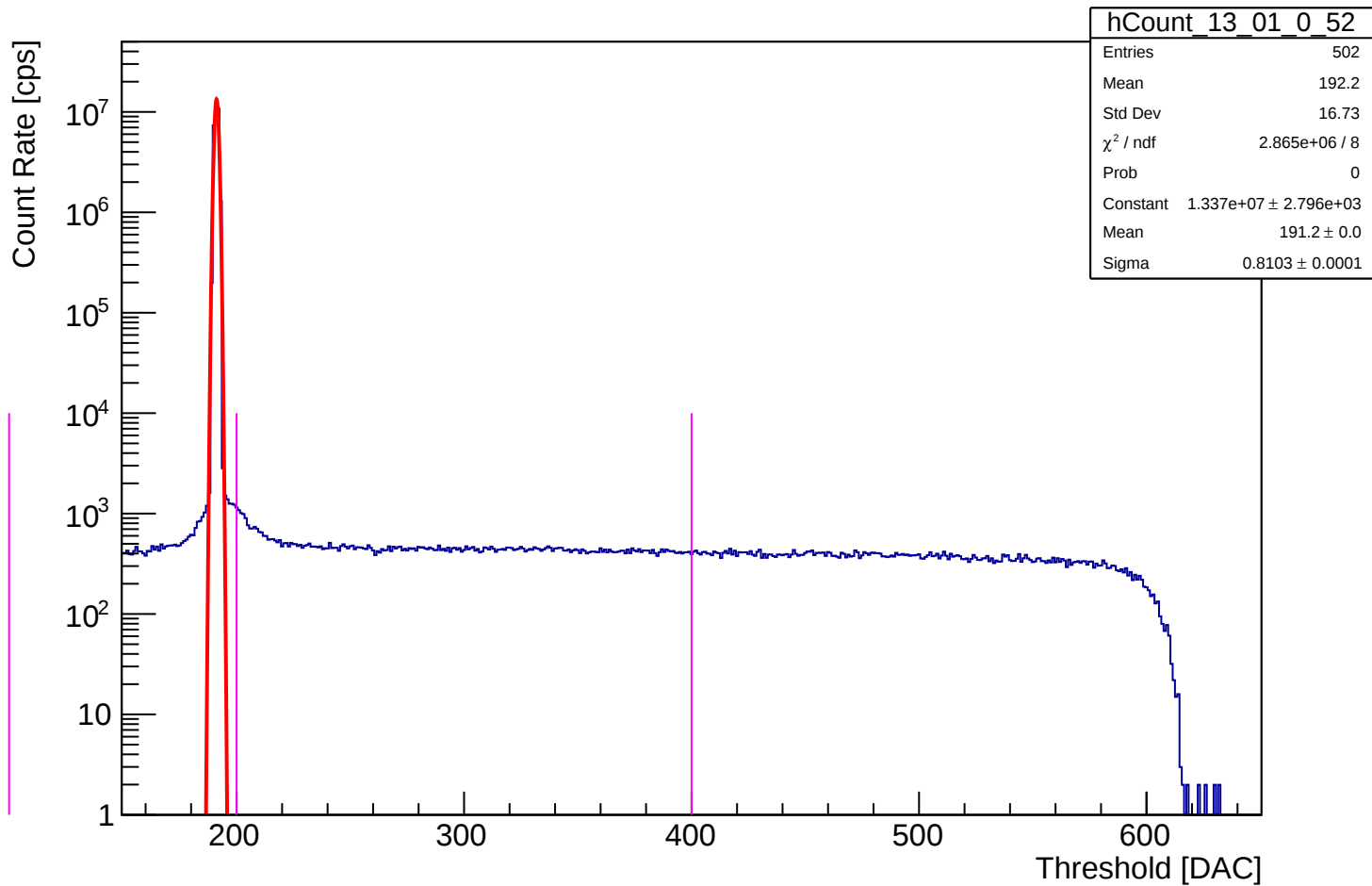


Row 1 Tile 1 Pmt 4 Pixel 43 Slot 13 Fiber 1 Asic 0 Channel 10

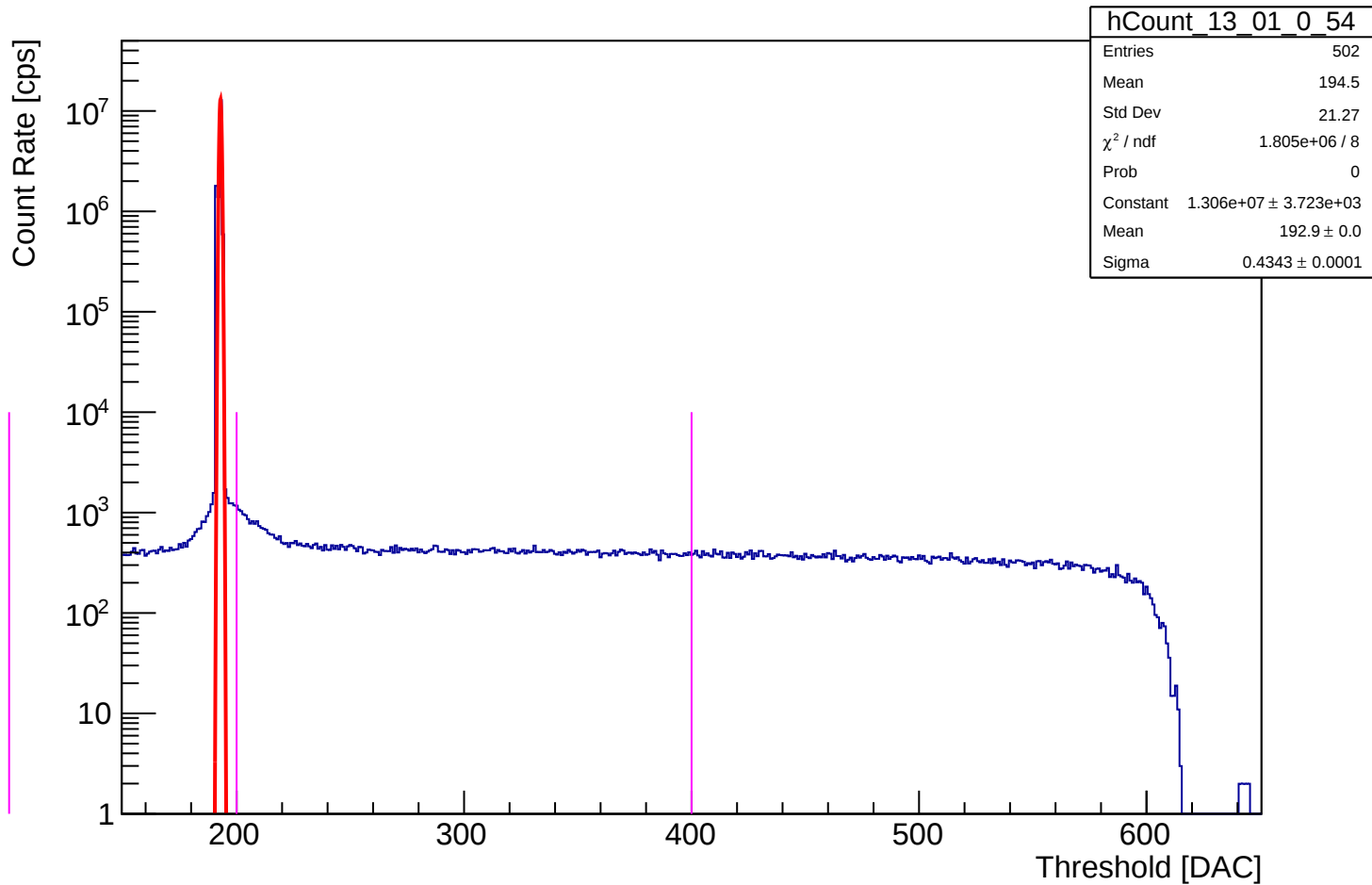




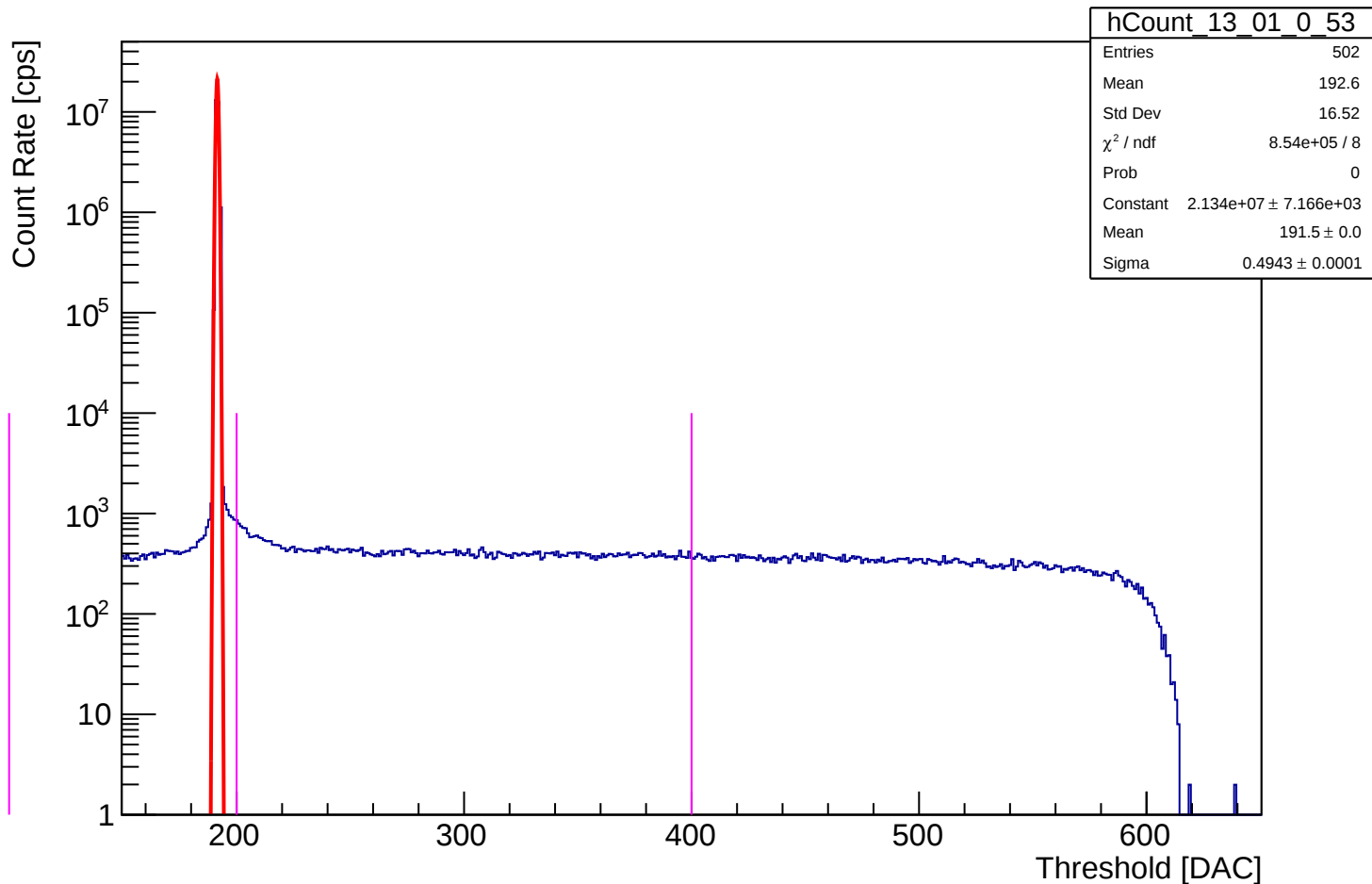
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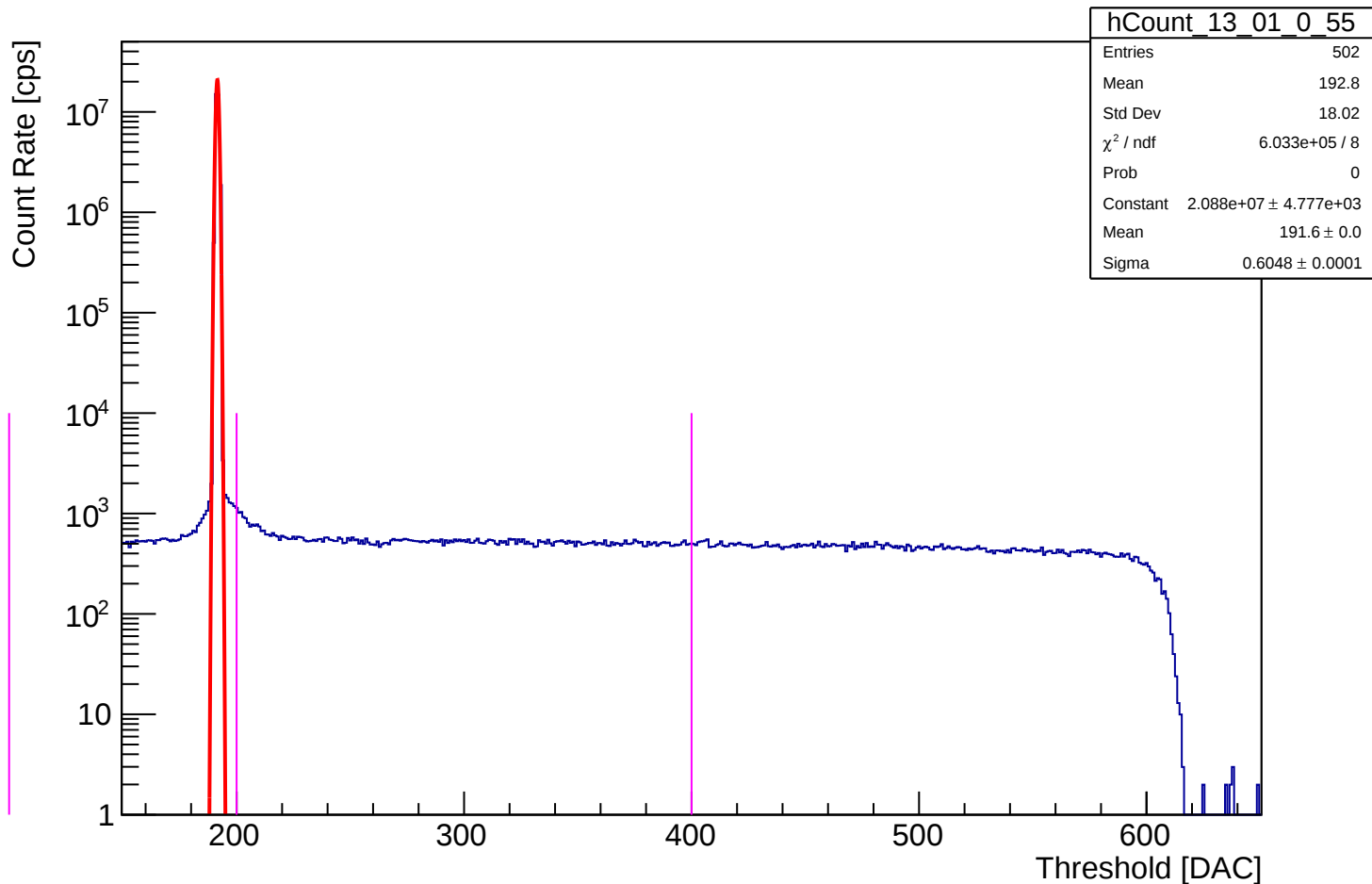
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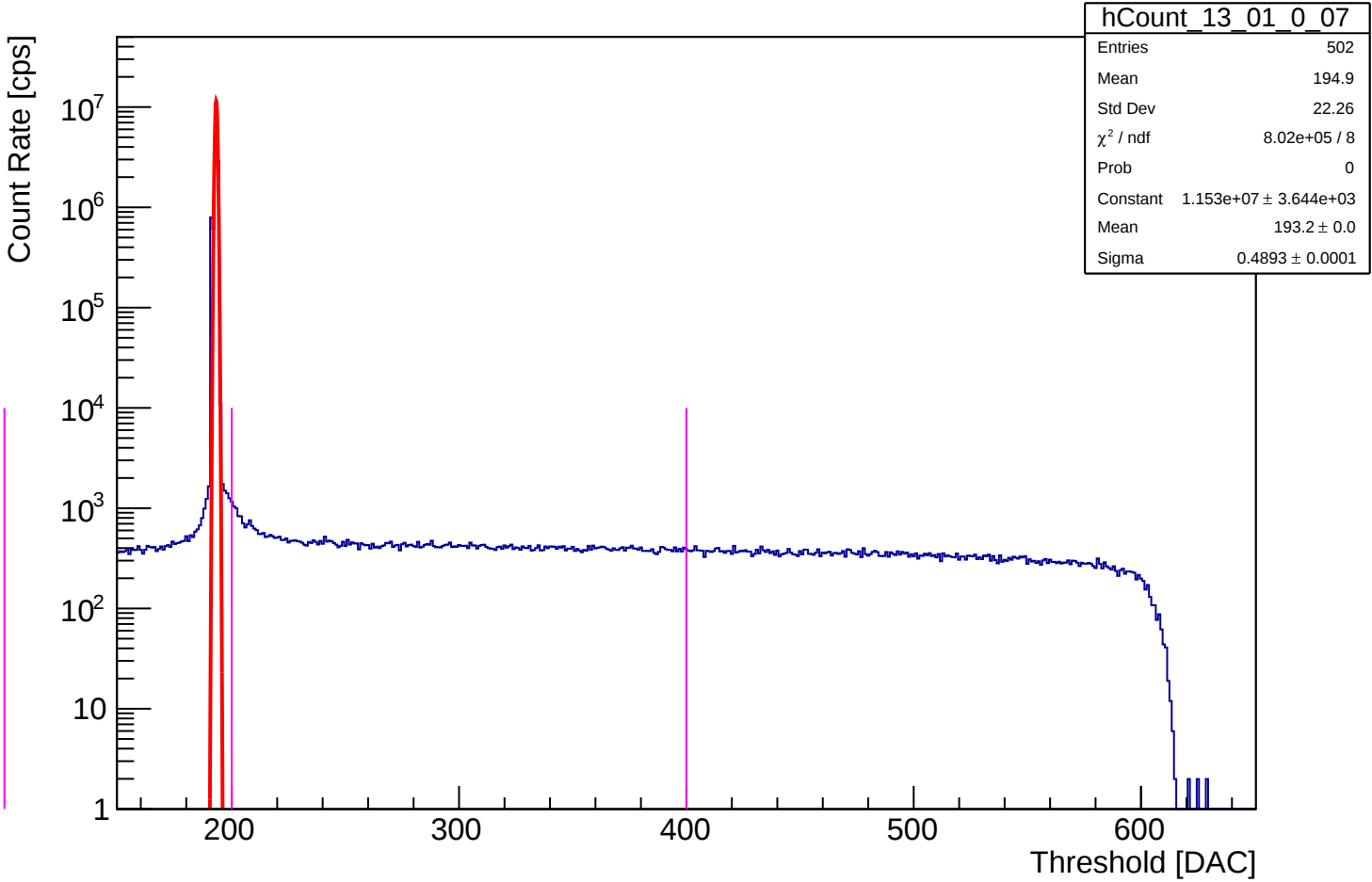


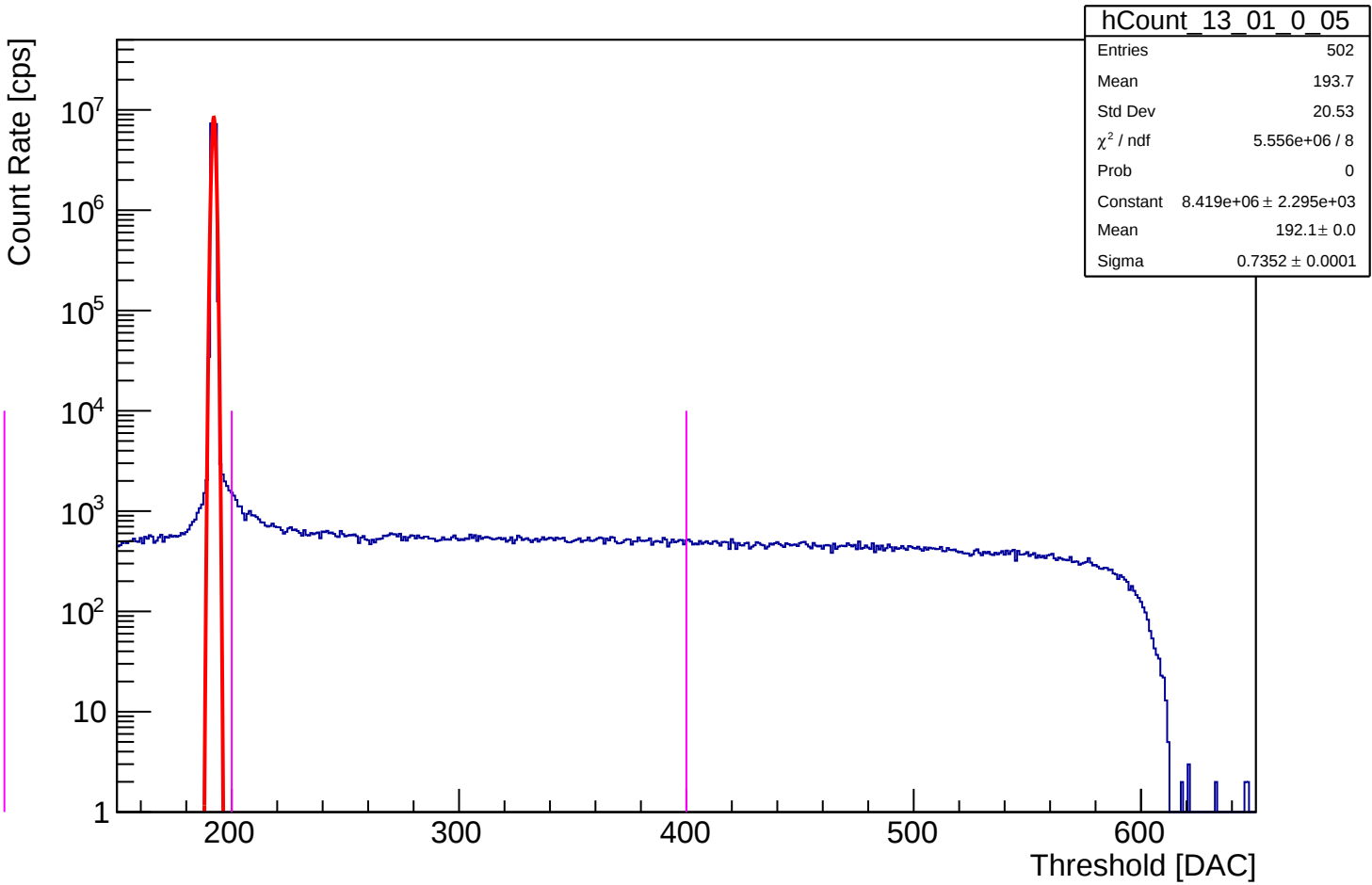
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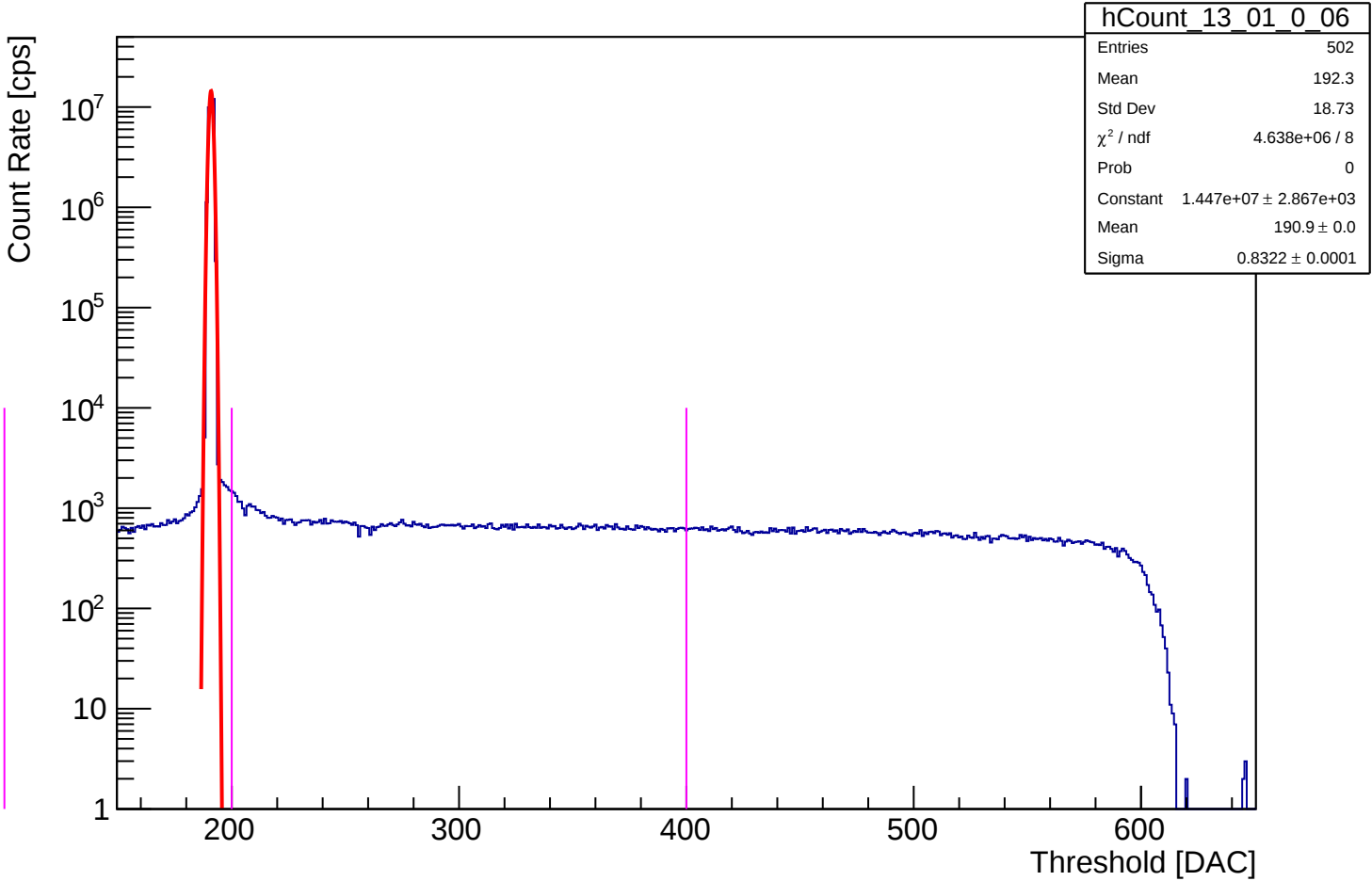


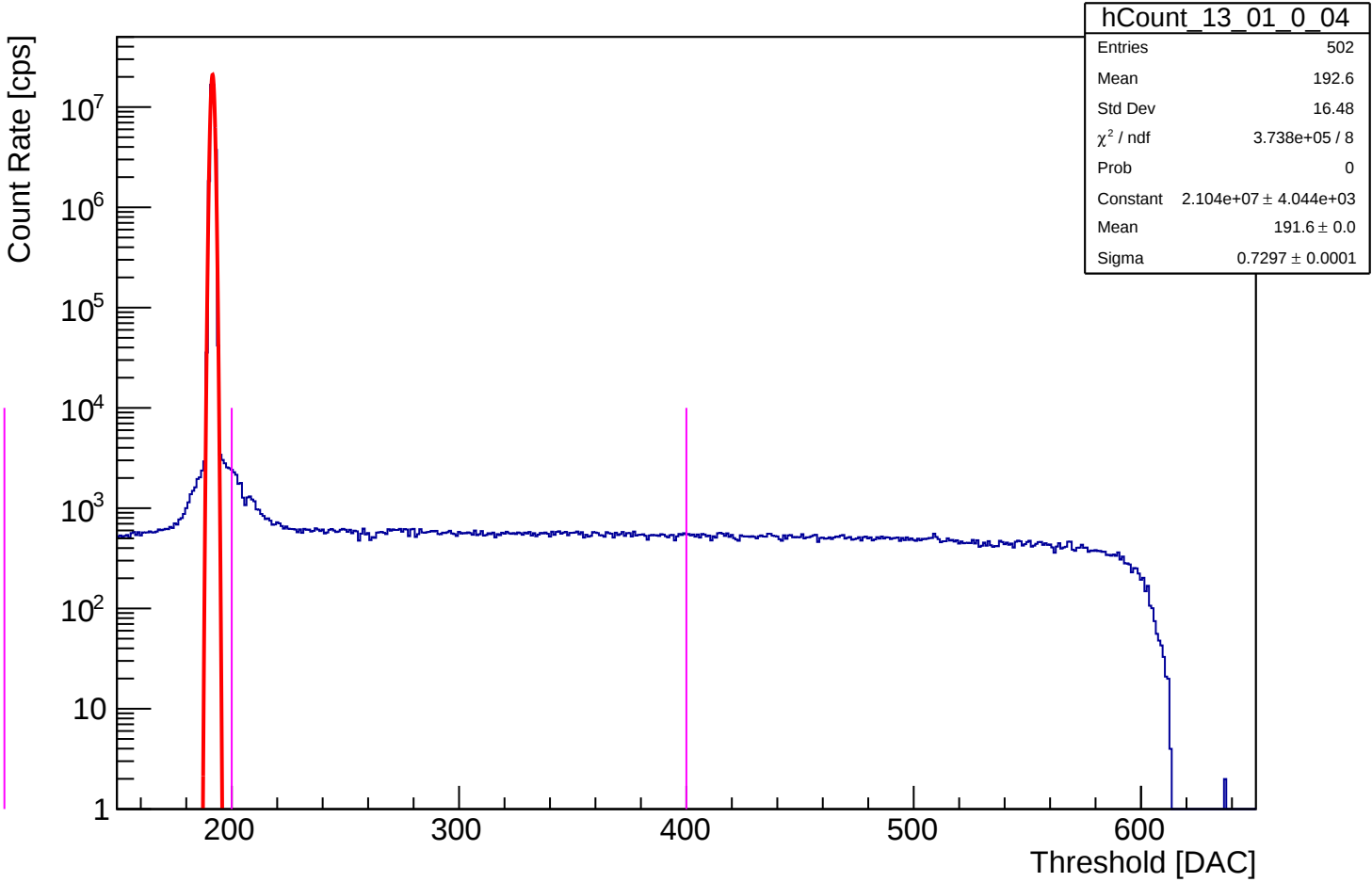
Row 1 Tile 1 Pmt 4 Pixel 48 Slot 13 Fiber 1 Asic 0 Channel 55



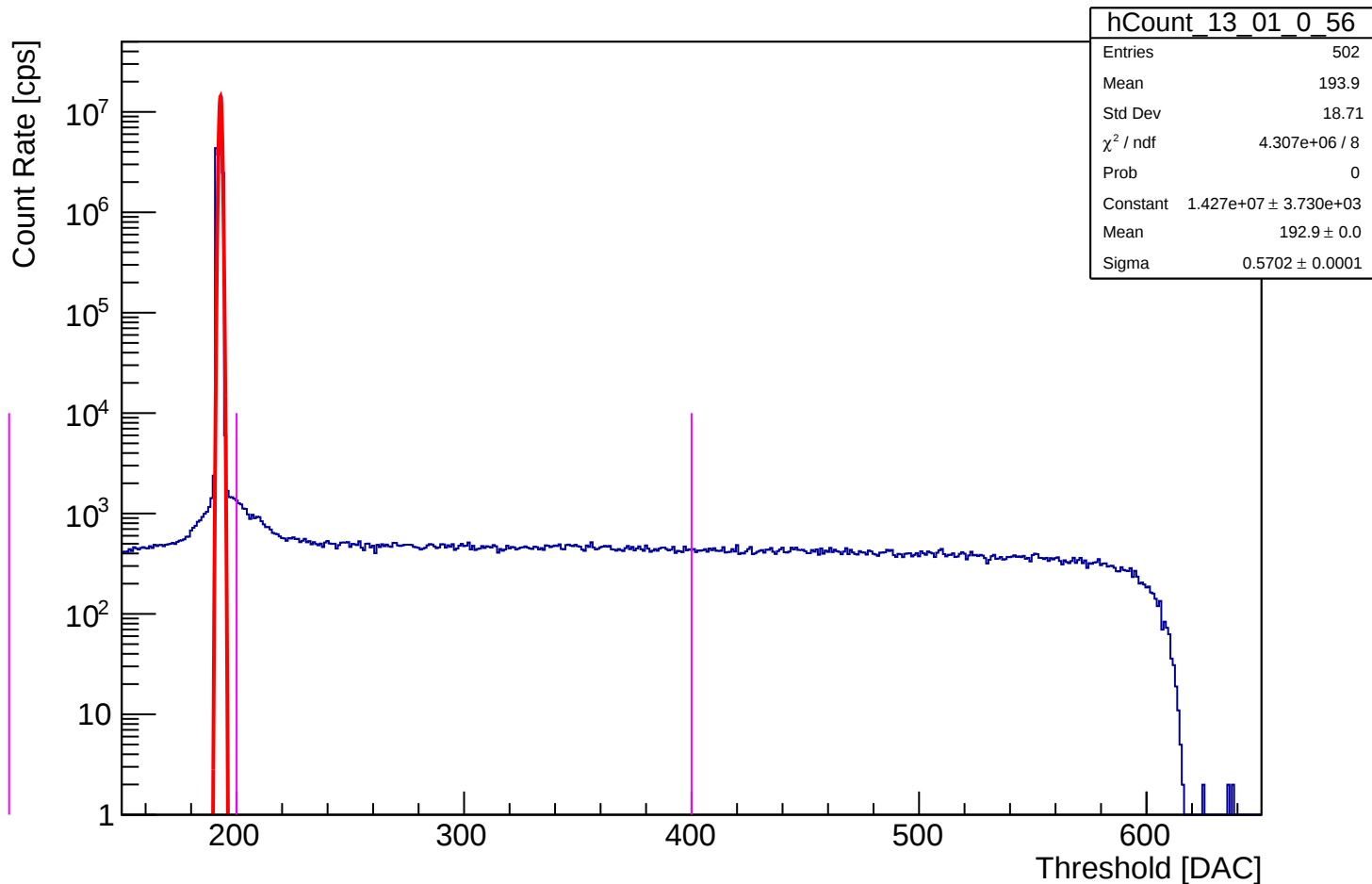




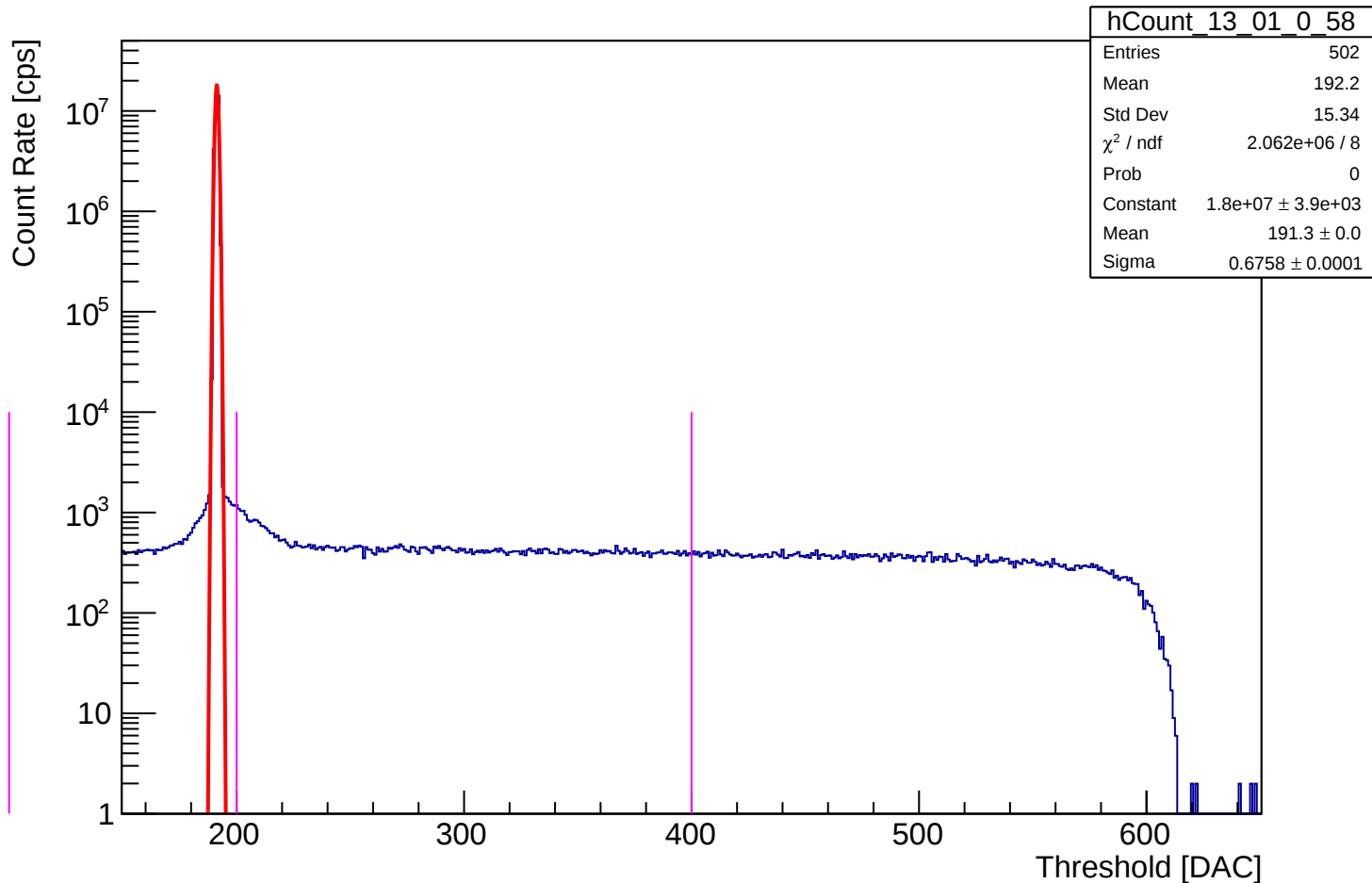




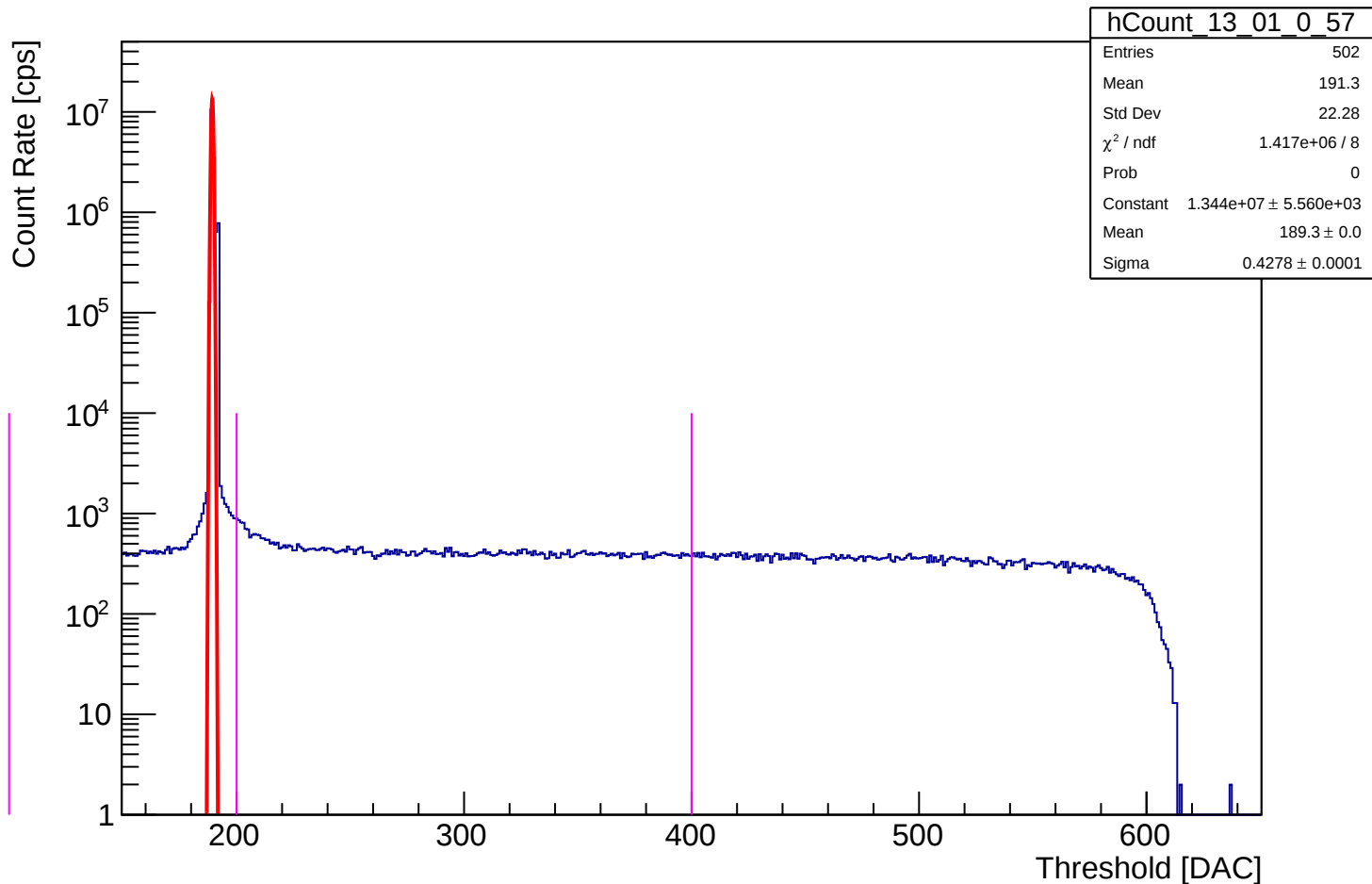
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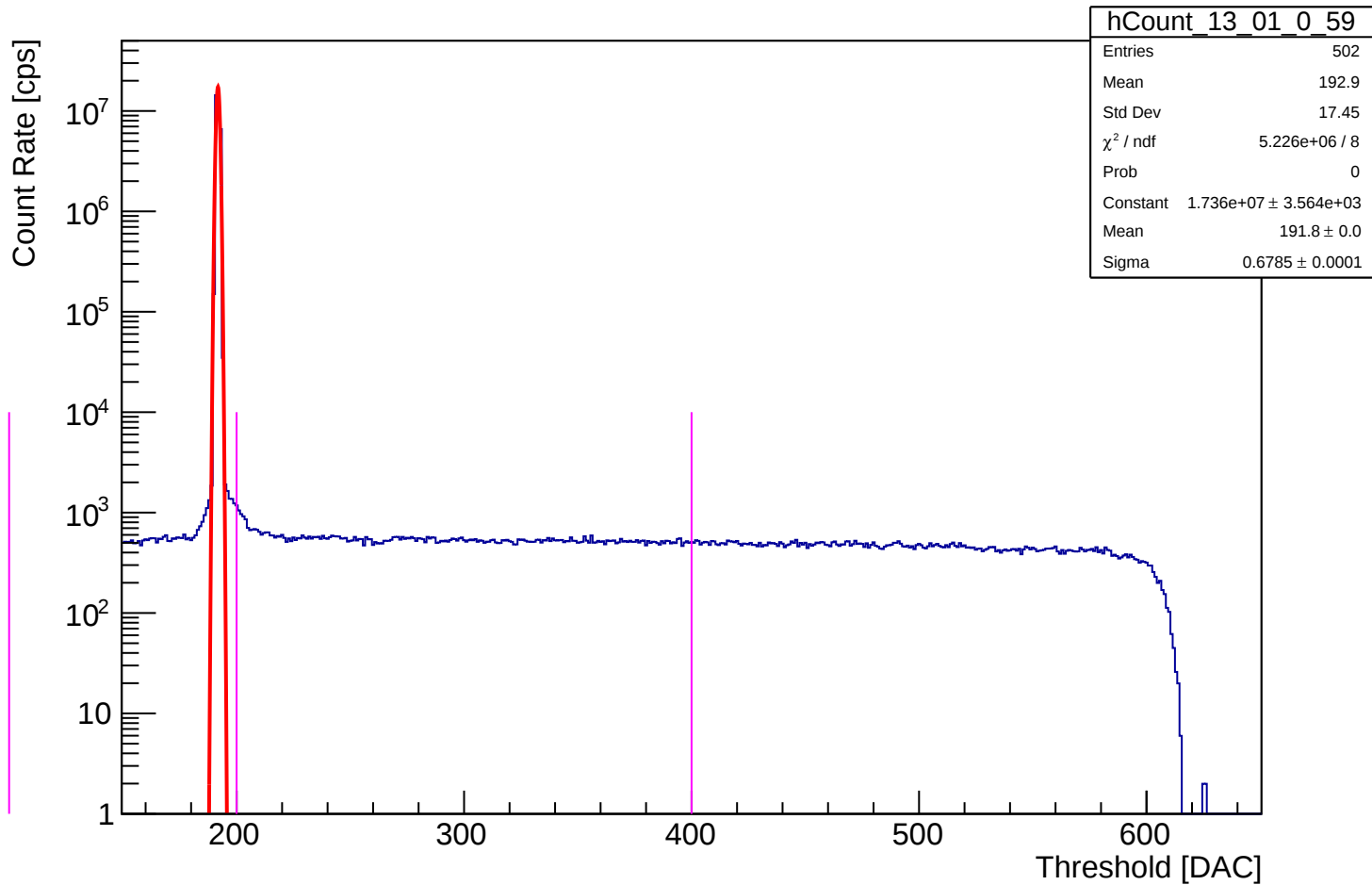
Row 1 Tile 1 Pmt 4 Pixel 54 Slot 13 Fiber 1 Asic 0 Channel 58



Row 1 Tile 1 Pmt 4 Pixel 55 Slot 13 Fiber 1 Asic 0 Channel 57



Row 1 Tile 1 Pmt 4 Pixel 56 Slot 13 Fiber 1 Asic 0 Channel 59



Count Rate [cps]

10^7
 10^6
 10^5
 10^4
 10^3
 10^2
 10
 1

200

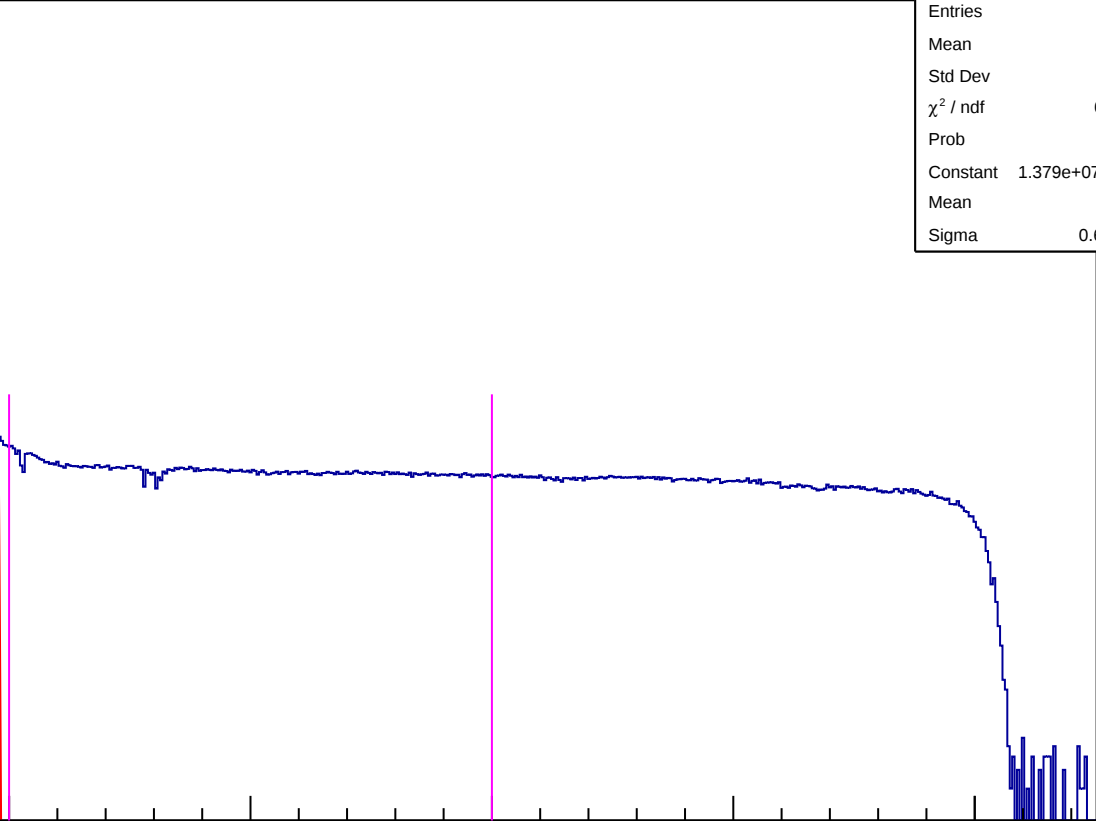
300

400

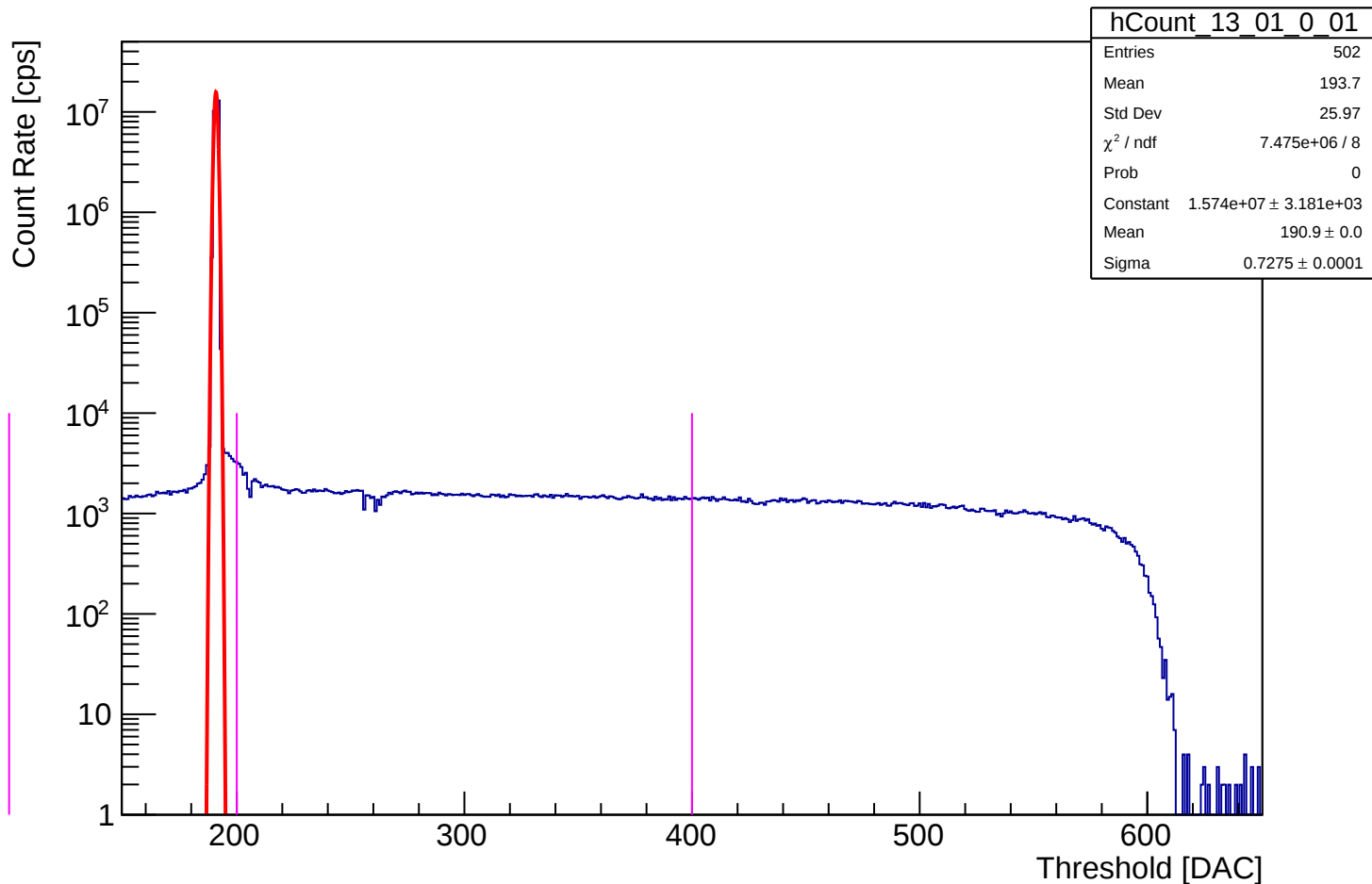
500

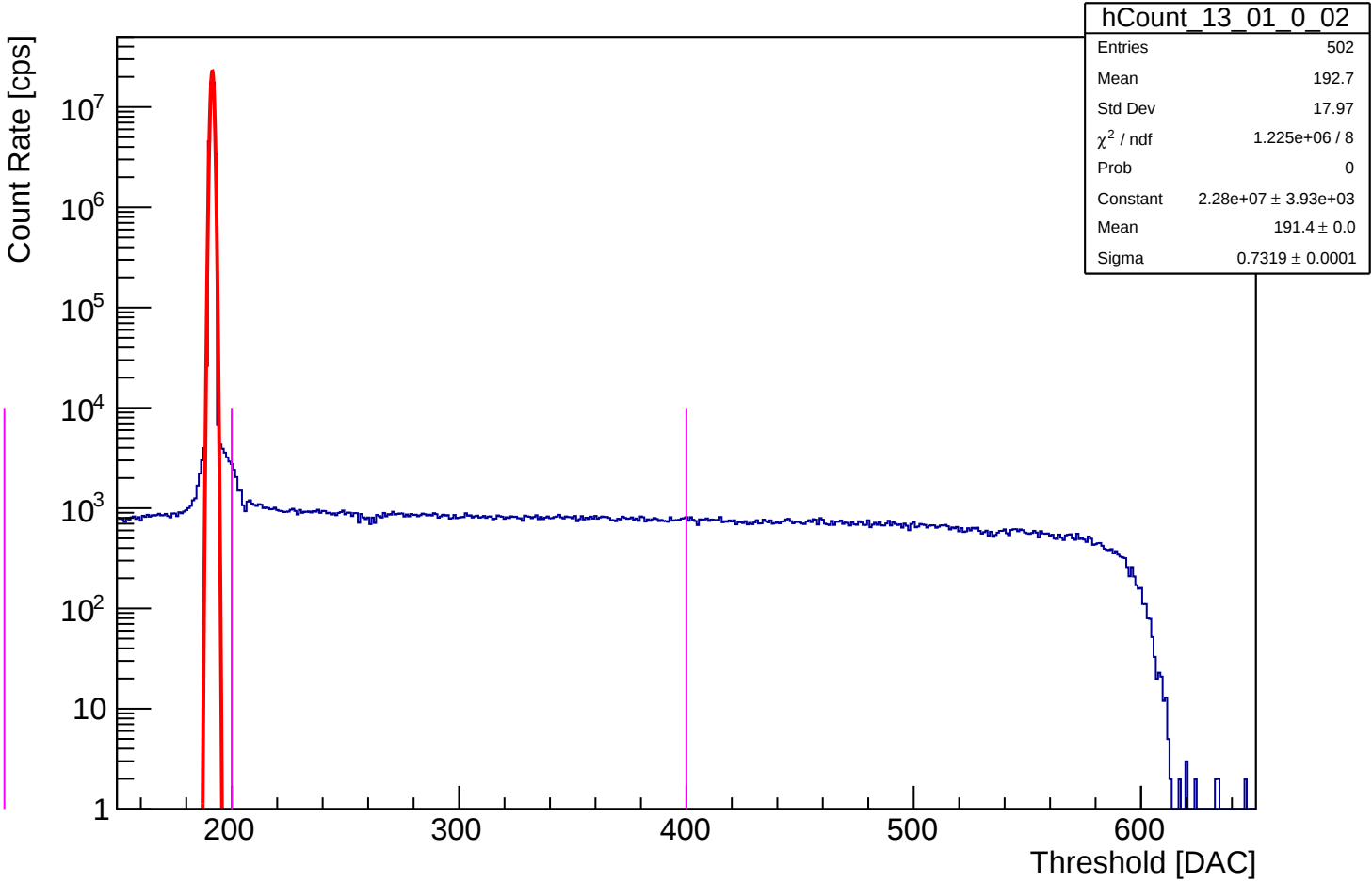
Threshold [DAC]

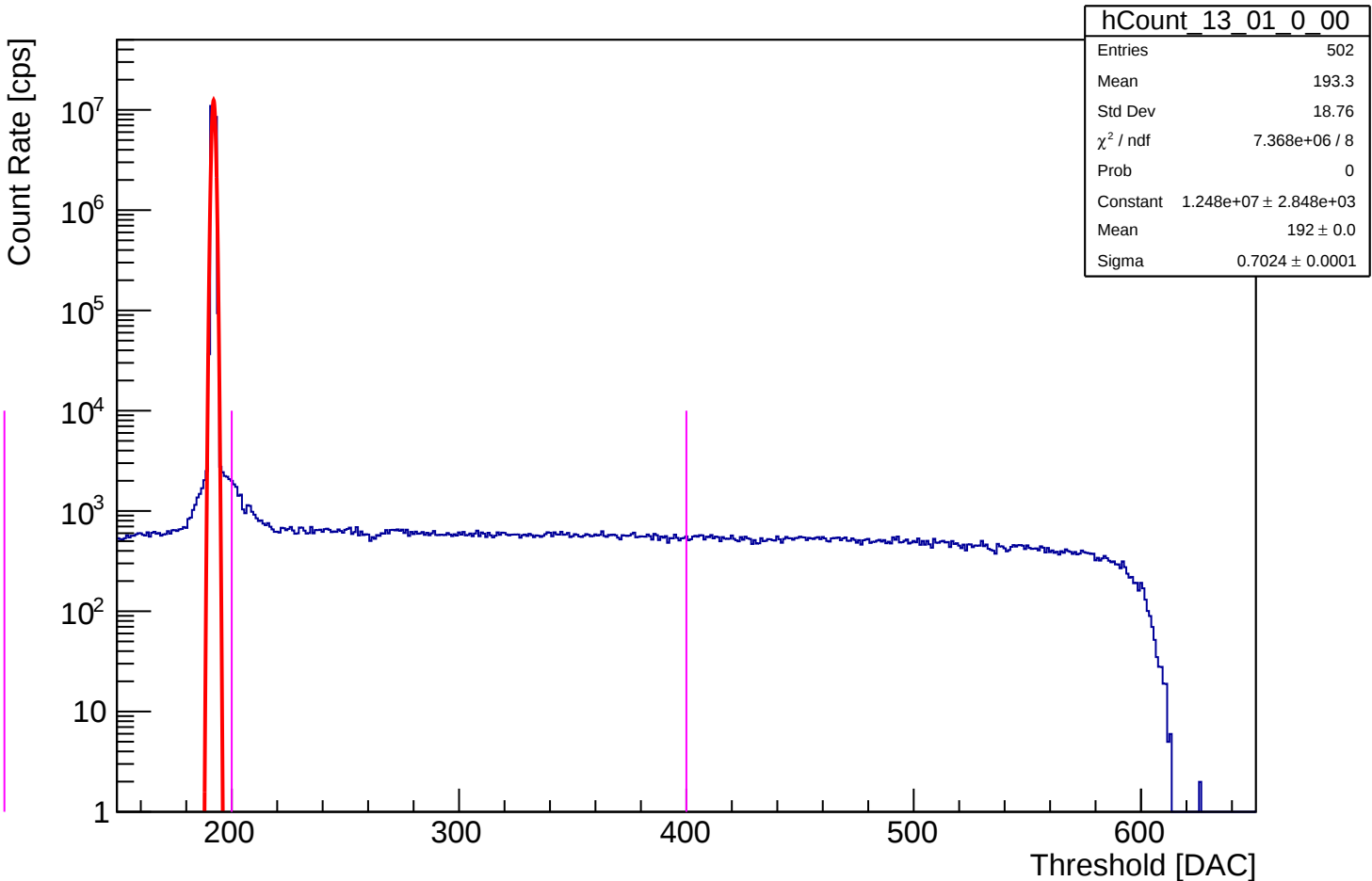
hCount_13_01_0_03	
Entries	502
Mean	196.8
Std Dev	33.83
χ^2 / ndf	6.404e+06 / 8
Prob	0
Constant	1.379e+07 $\pm$ 4.170e+03
Mean	192.7 $\pm$ 0.0
Sigma	0.6141 $\pm$ 0.0001



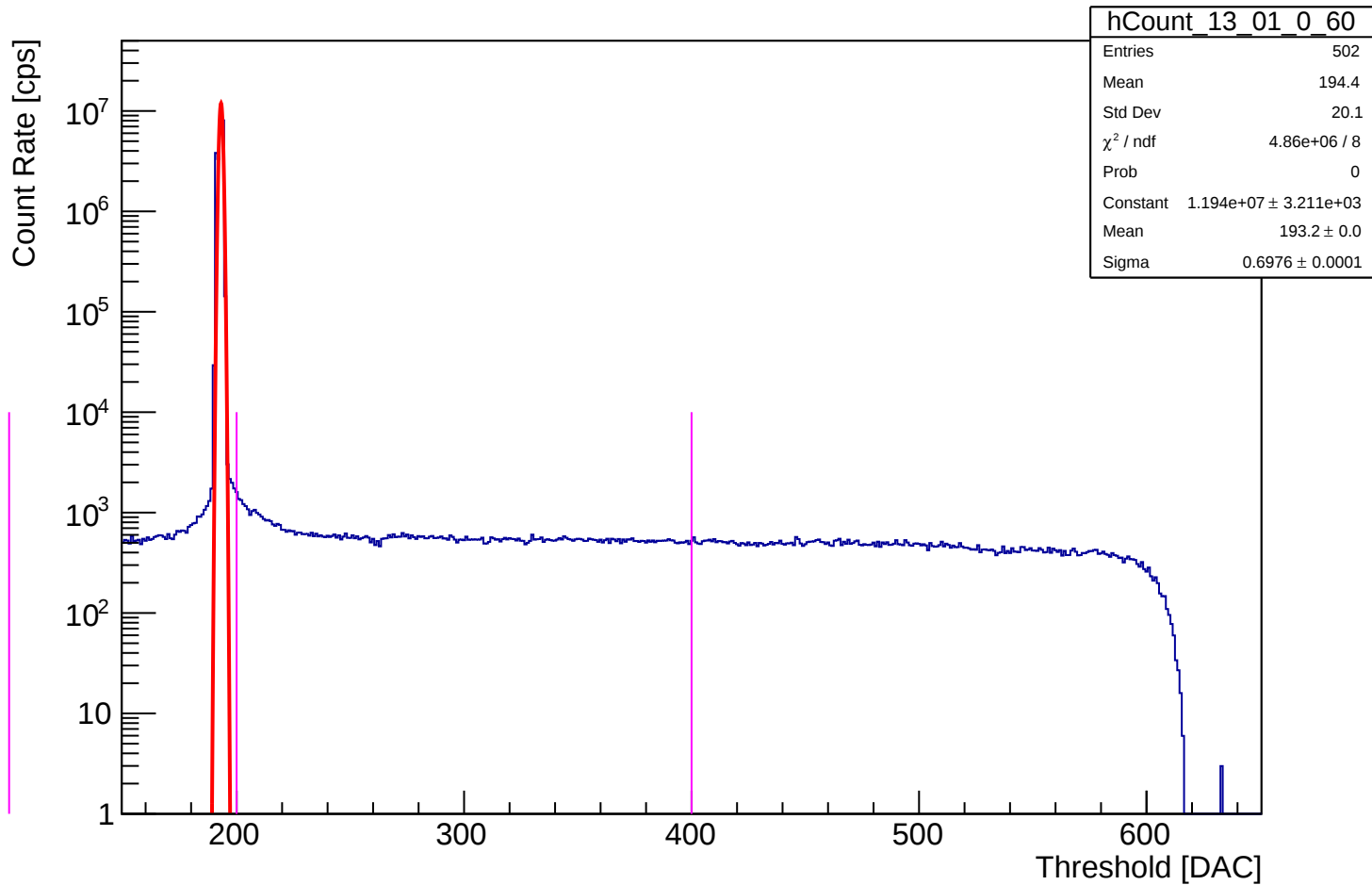
Row 1 Tile 1 Pmt 4 Pixel 58 Slot 13 Fiber 1 Asic 0 Channel 1



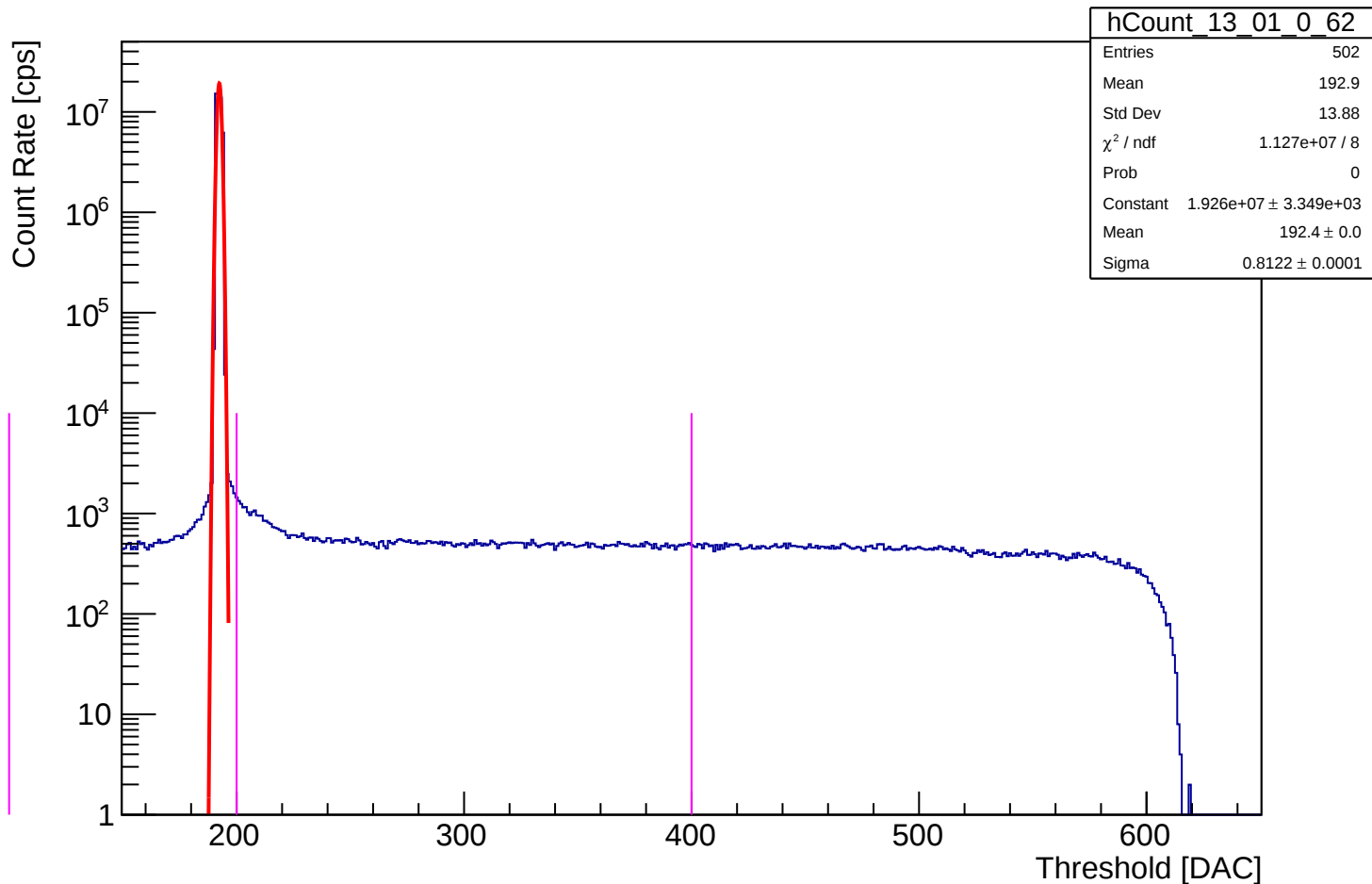


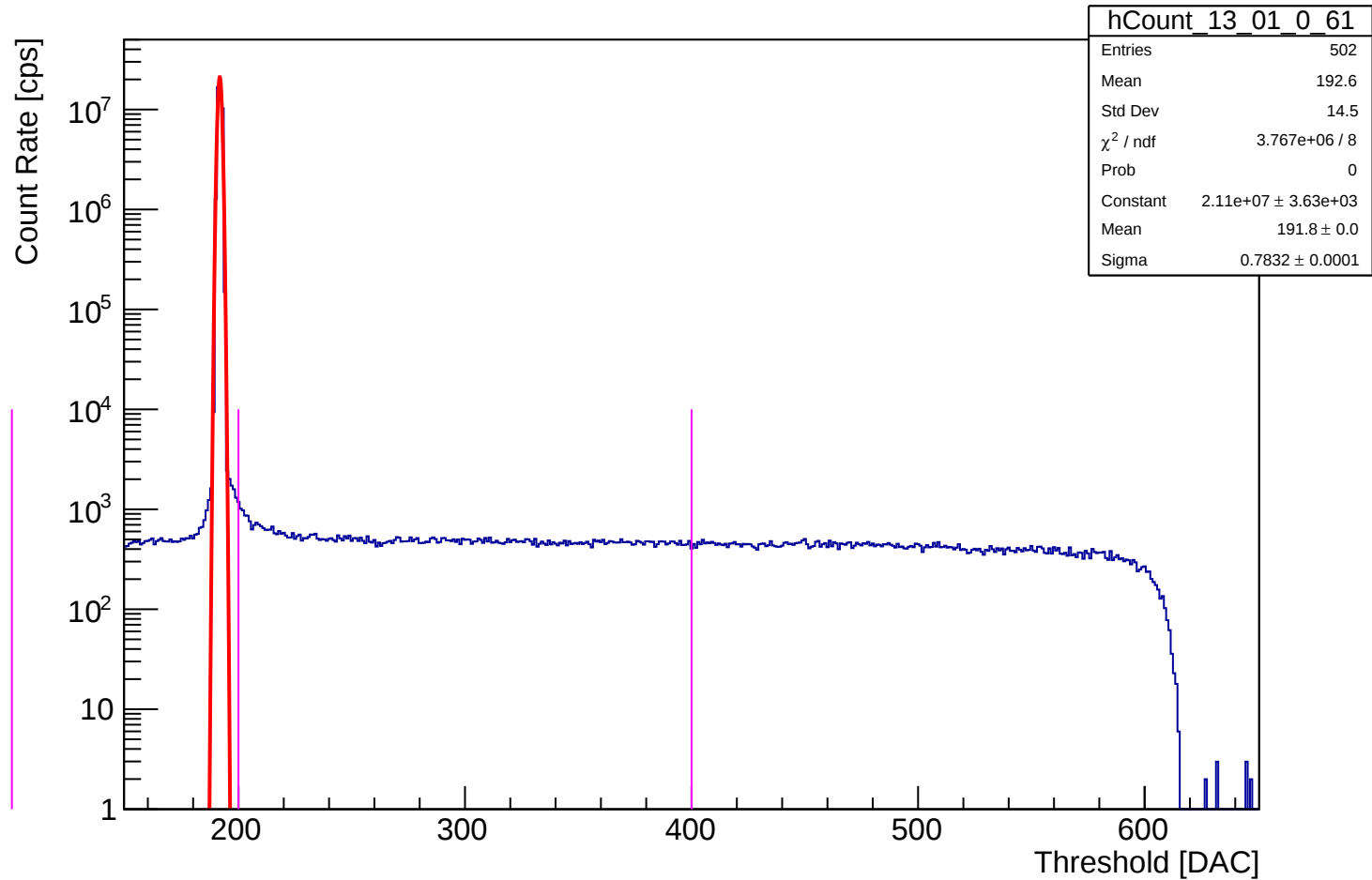


Row 1 Tile 1 Pmt 4 Pixel 61 Slot 13 Fiber 1 Asic 0 Channel 60

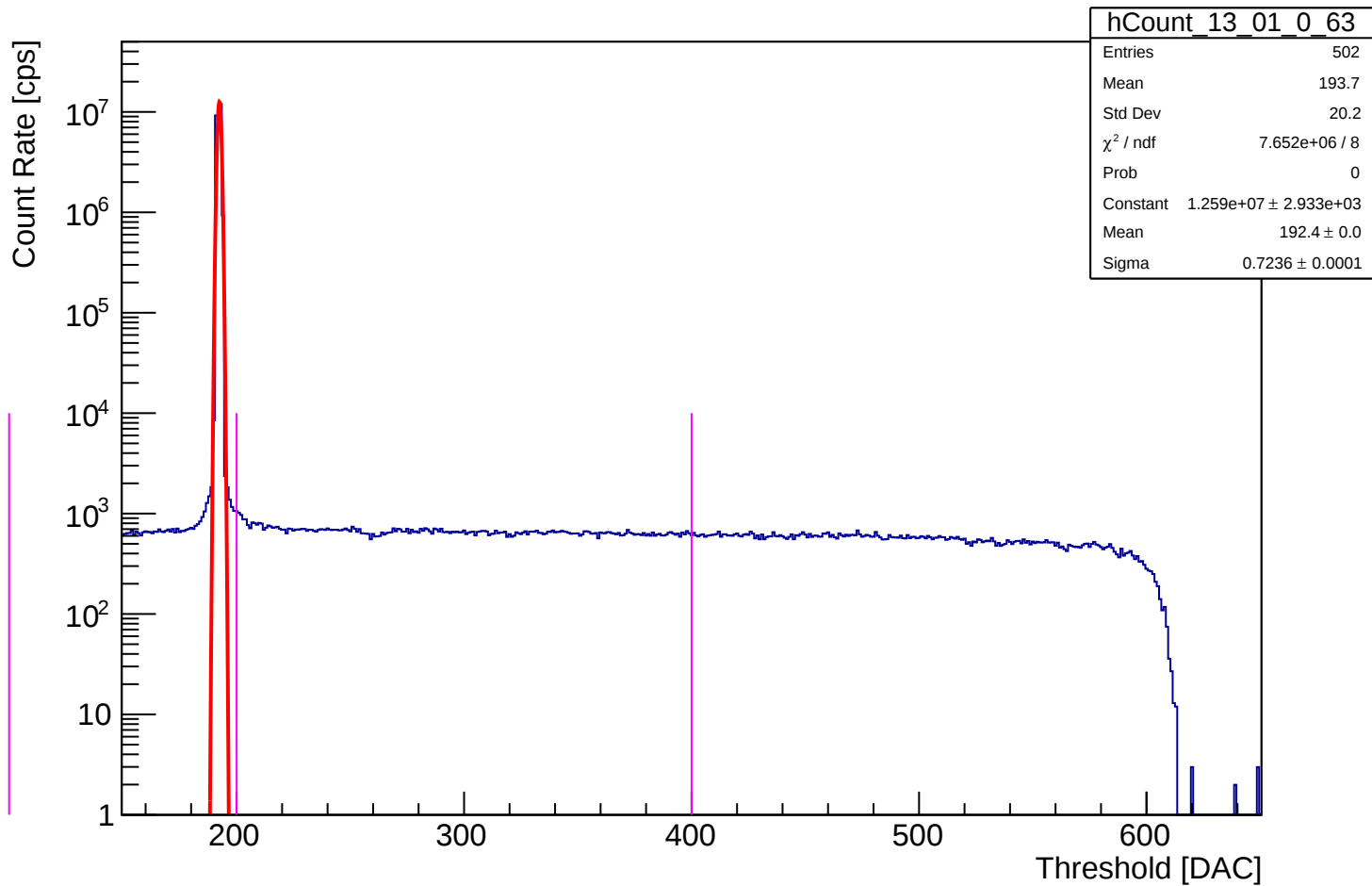


Row 1 Tile 1 Pmt 4 Pixel 62 Slot 13 Fiber 1 Asic 0 Channel 62

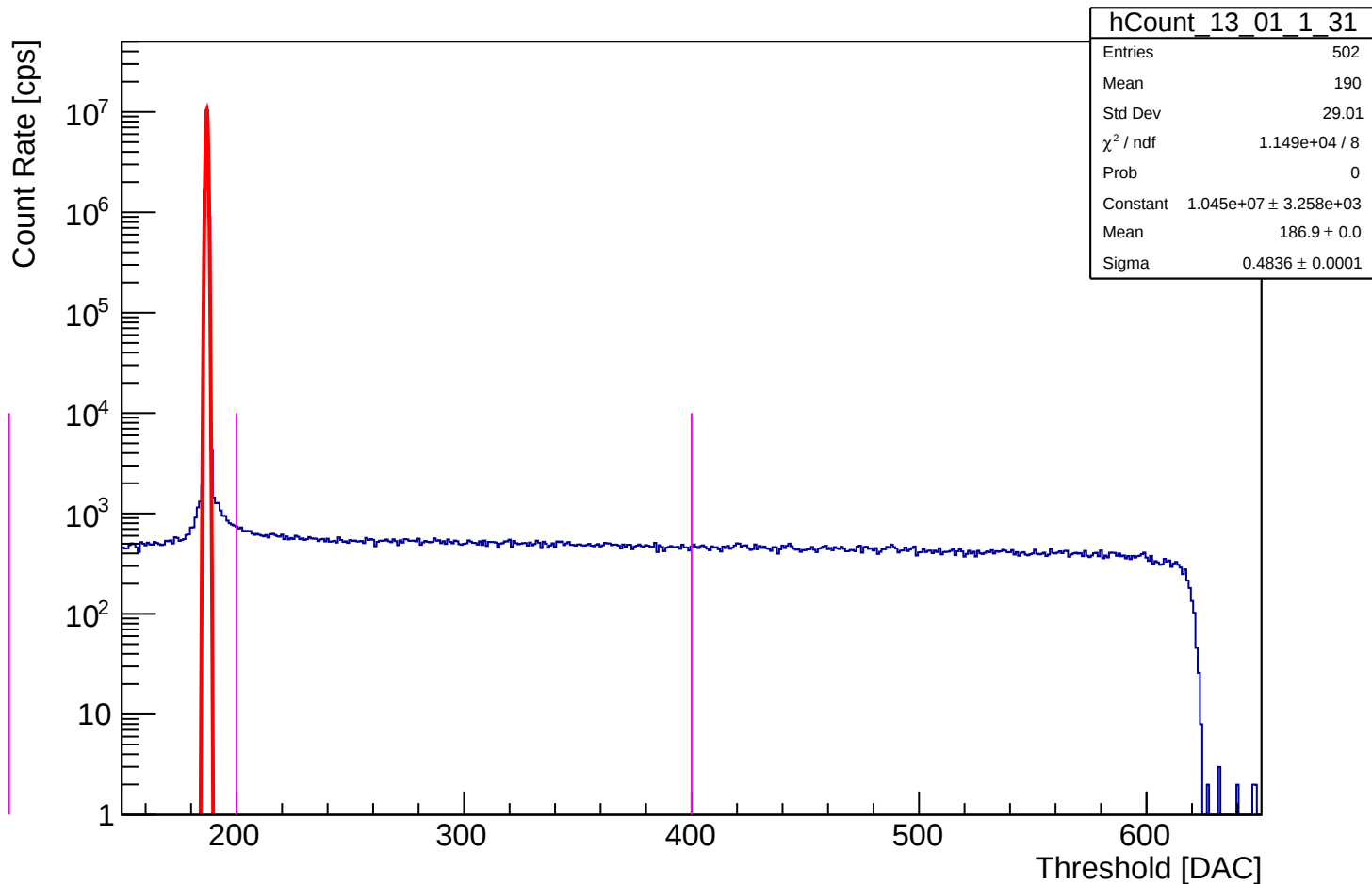




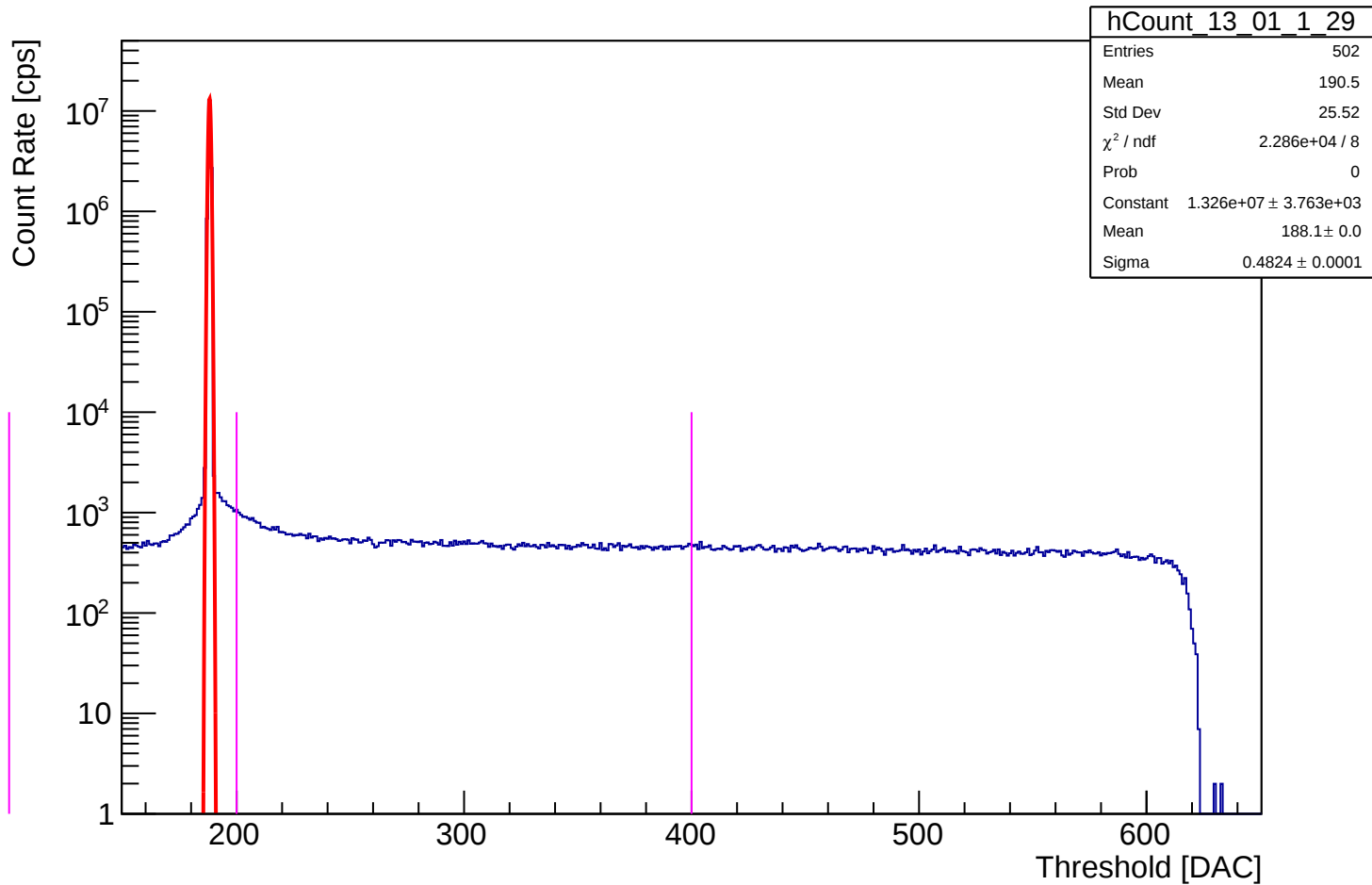
Row 1 Tile 1 Pmt 4 Pixel 64 Slot 13 Fiber 1 Asic 0 Channel 63



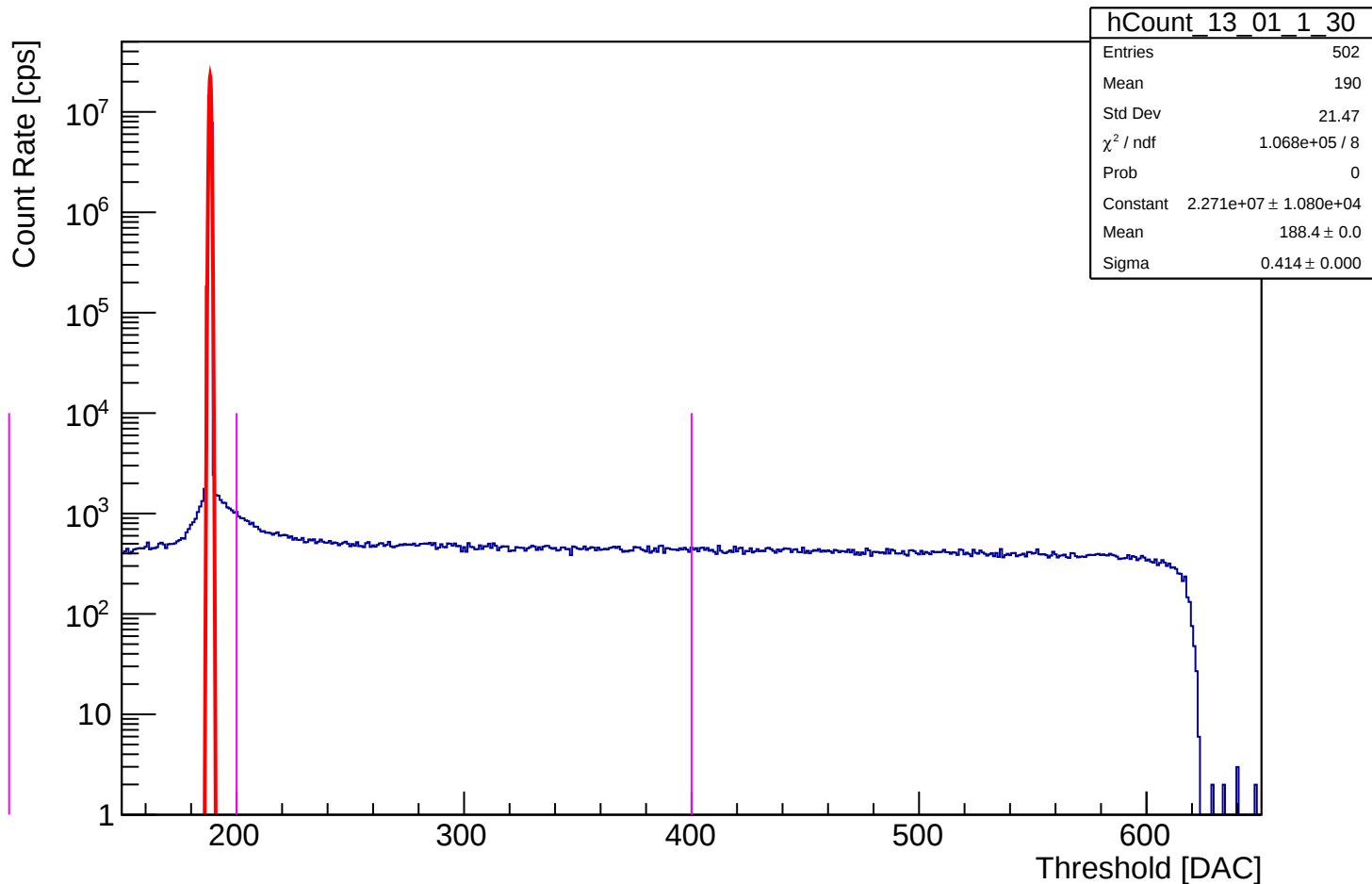
Row 1 Tile 1 Pmt 5 Pixel 1 Slot 13 Fiber 1 Asic 1 Channel 31



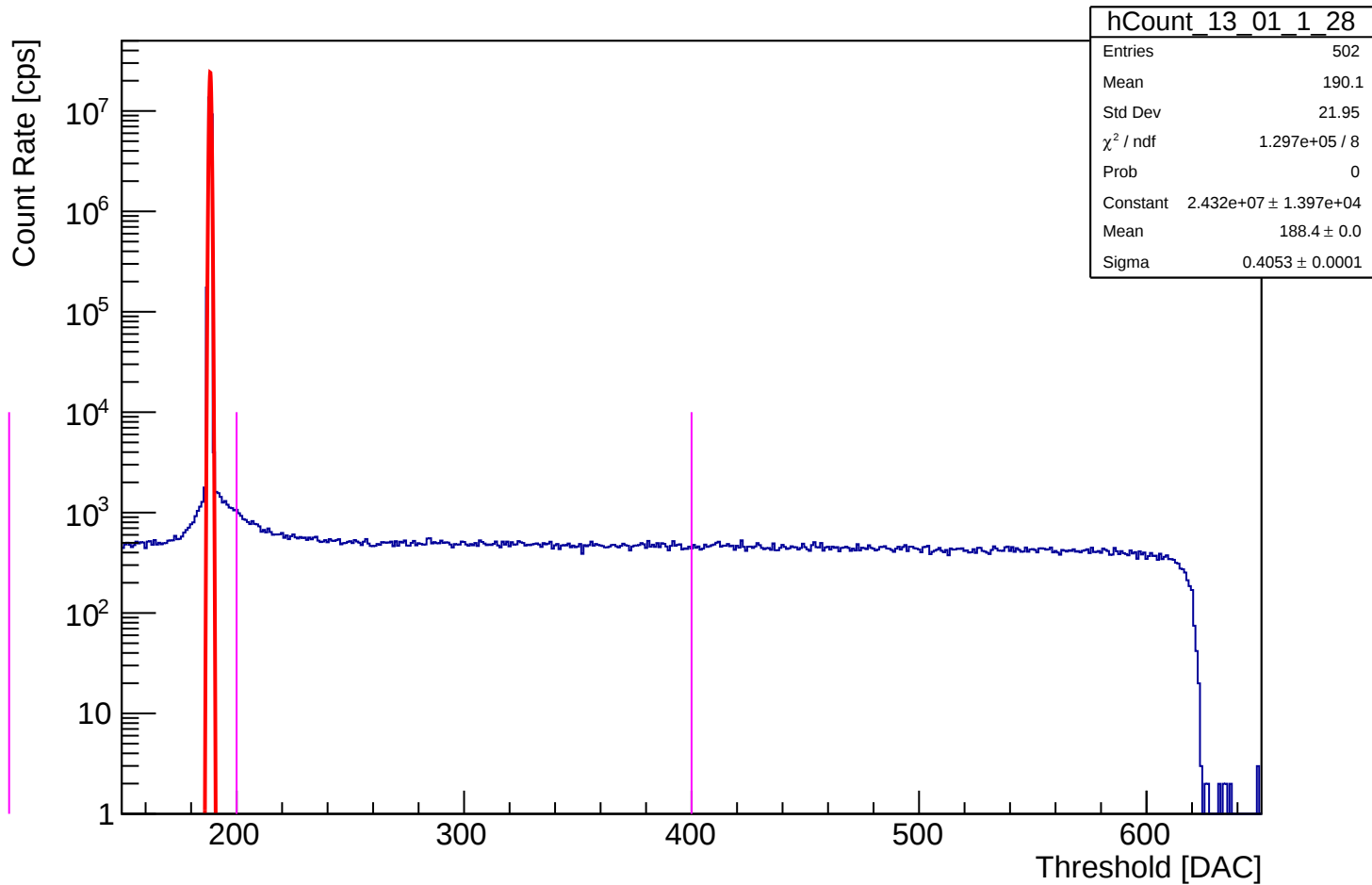
Row 1 Tile 1 Pmt 5 Pixel 2 Slot 13 Fiber 1 Asic 1 Channel 29



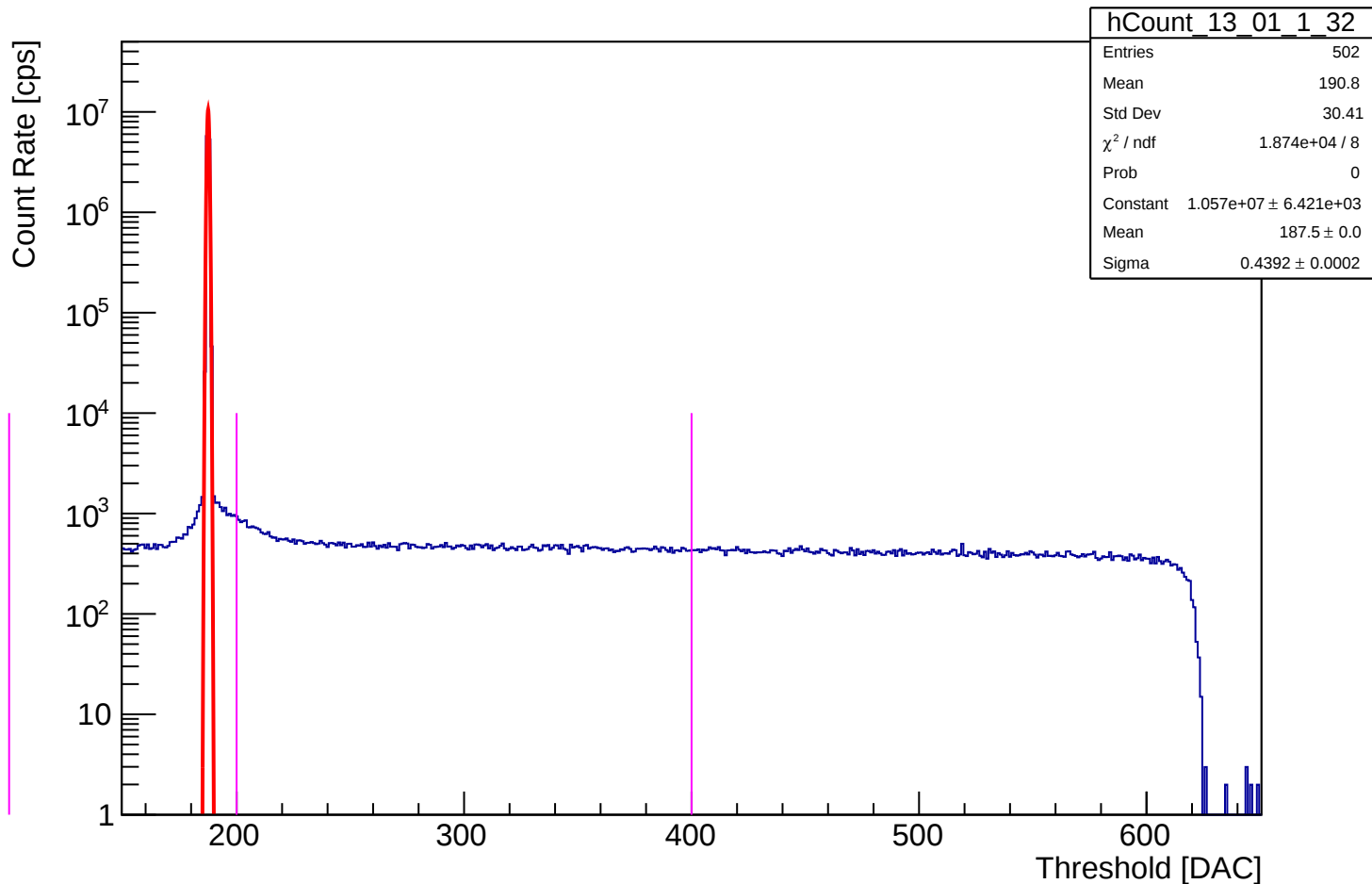
Row 1 Tile 1 Pmt 5 Pixel 3 Slot 13 Fiber 1 Asic 1 Channel 30



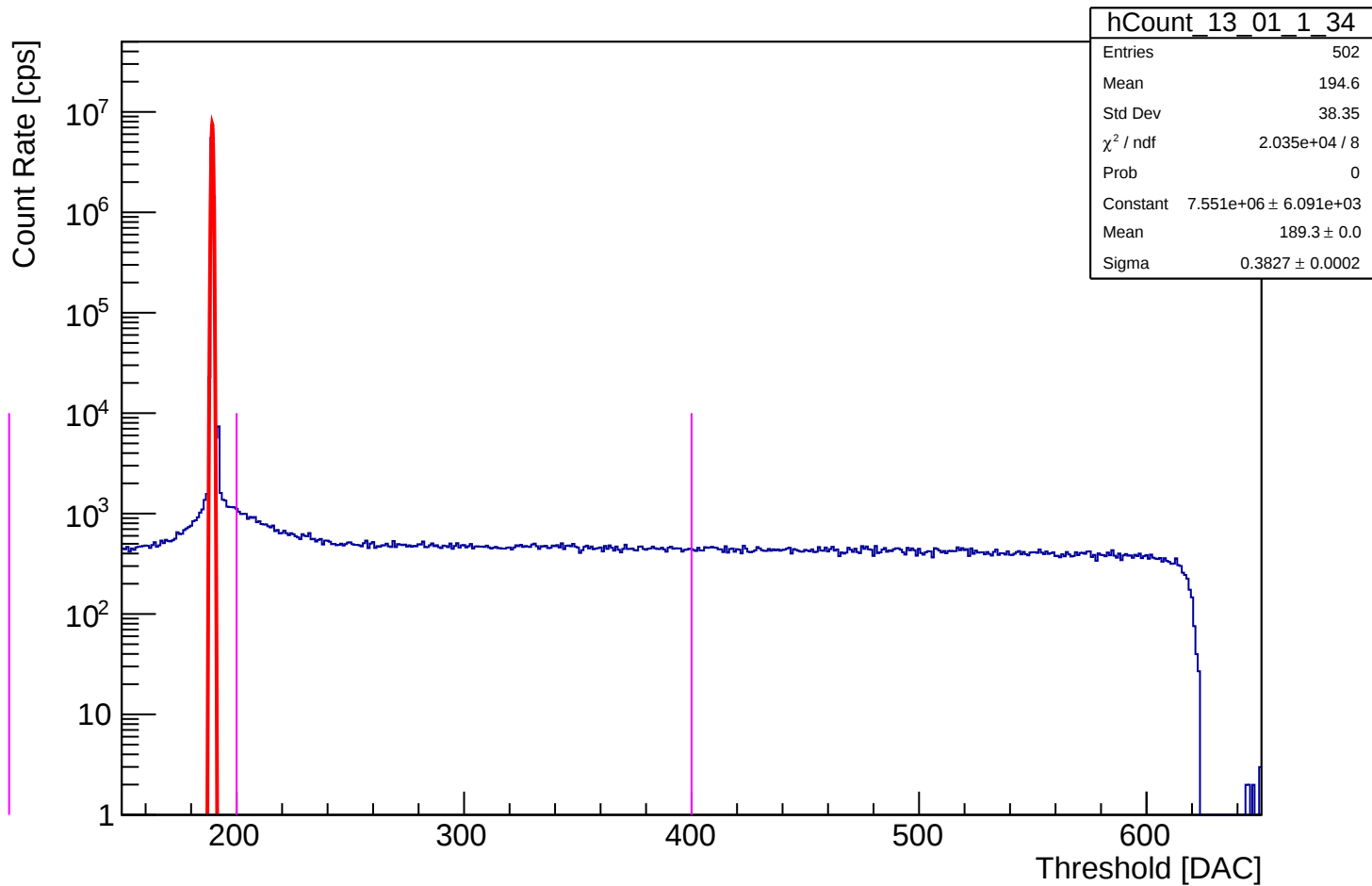
Row 1 Tile 1 Pmt 5 Pixel 4 Slot 13 Fiber 1 Asic 1 Channel 28



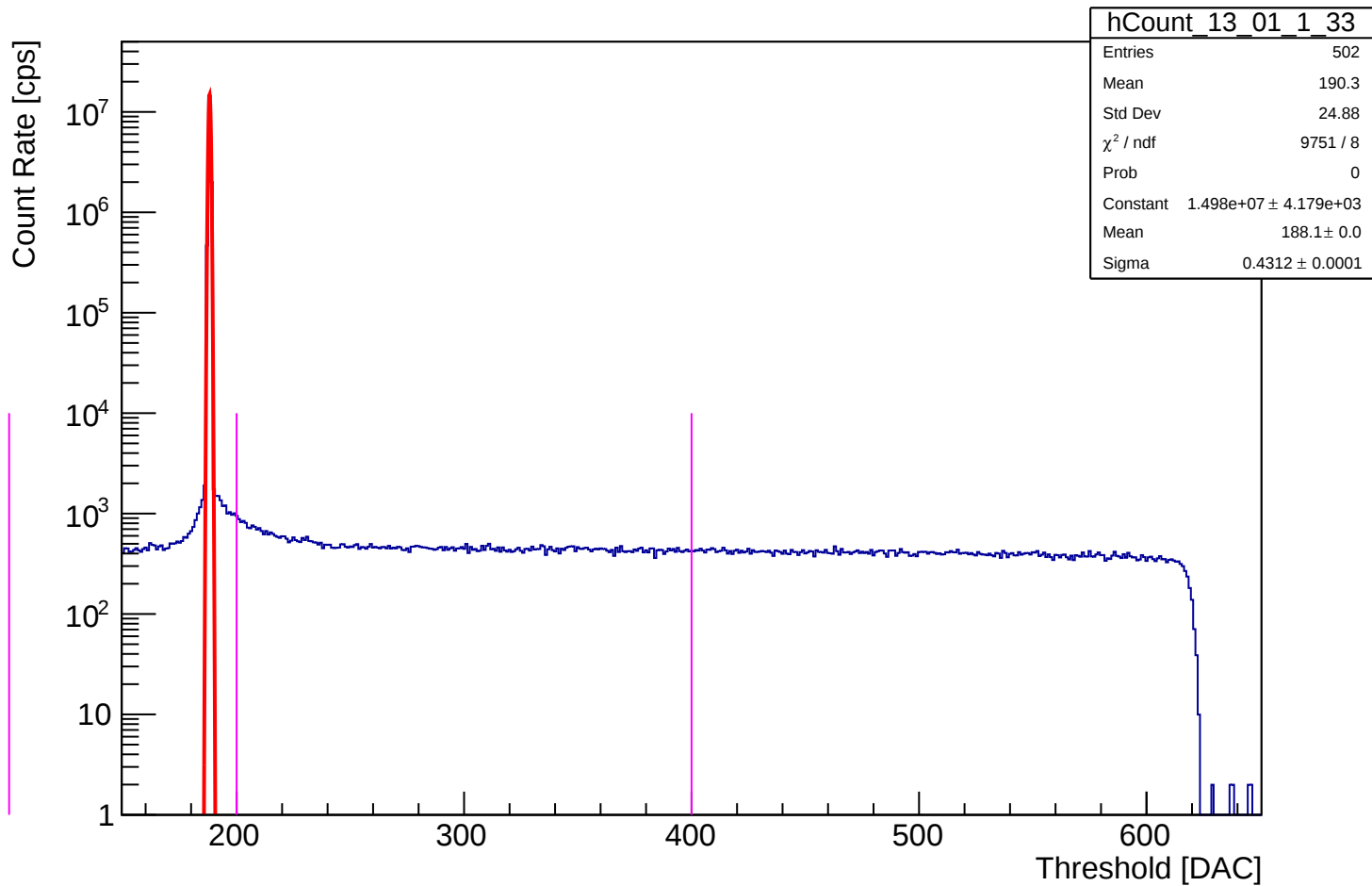
Row 1 Tile 1 Pmt 5 Pixel 5 Slot 13 Fiber 1 Asic 1 Channel 32



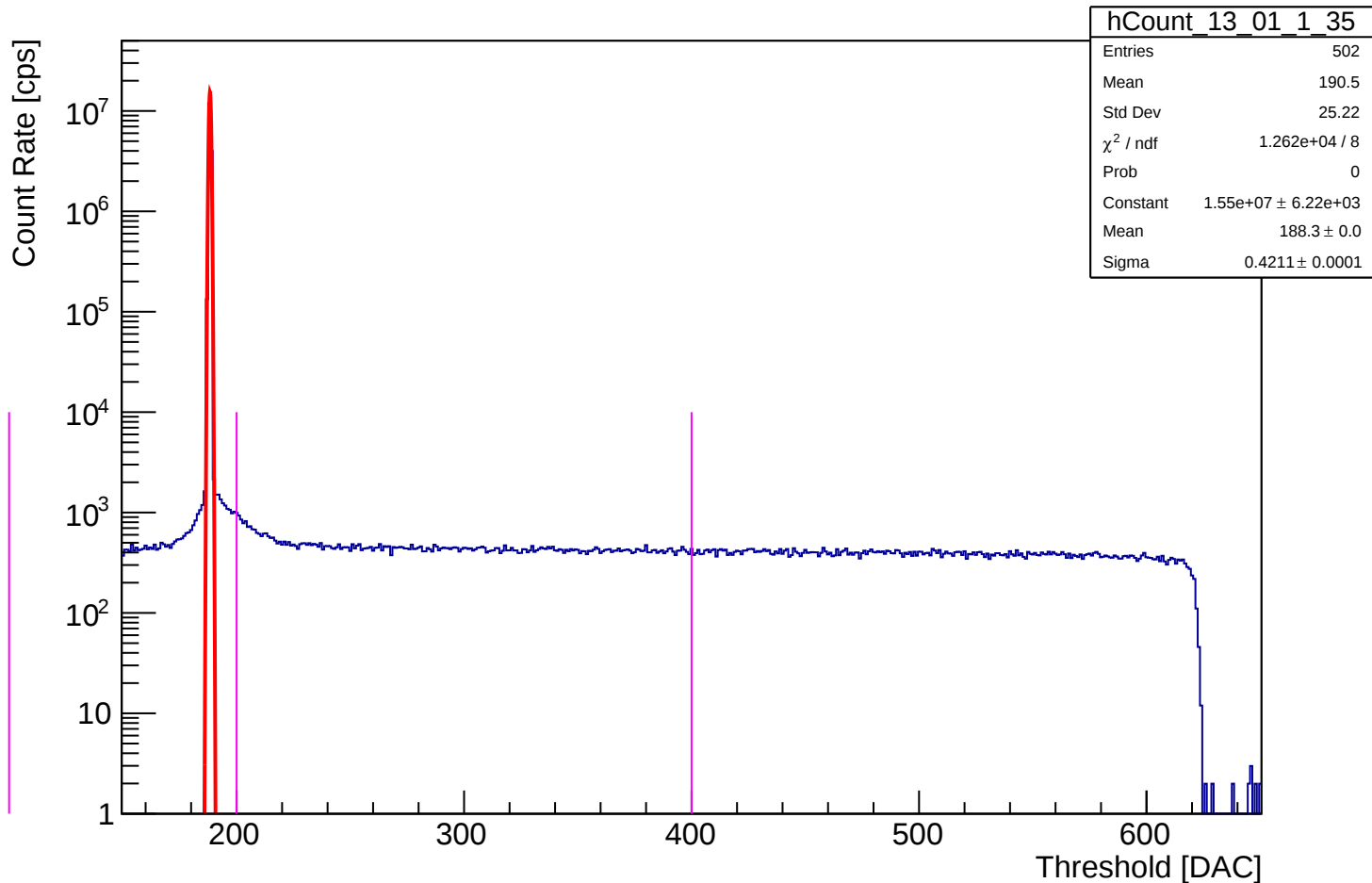
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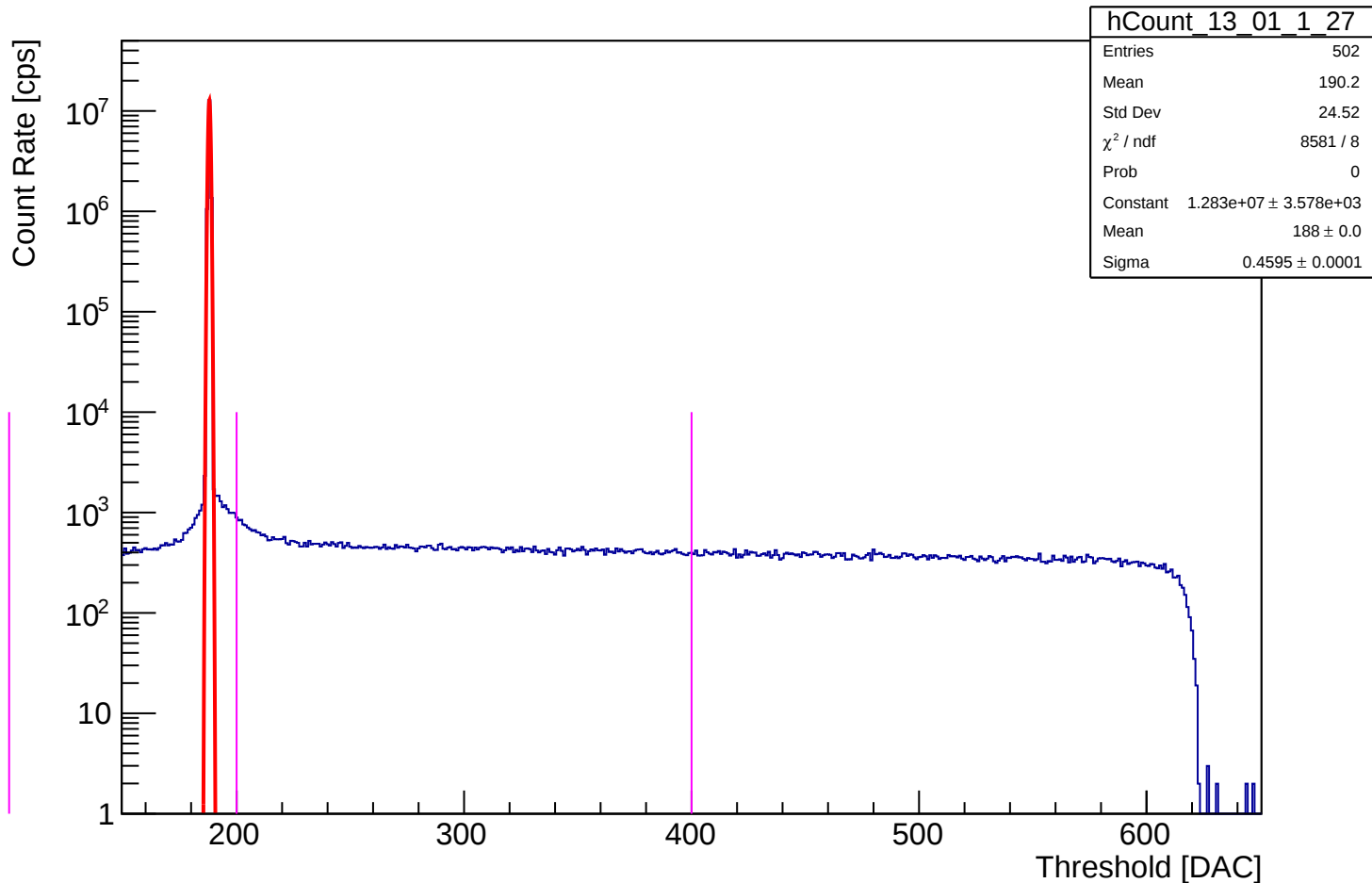
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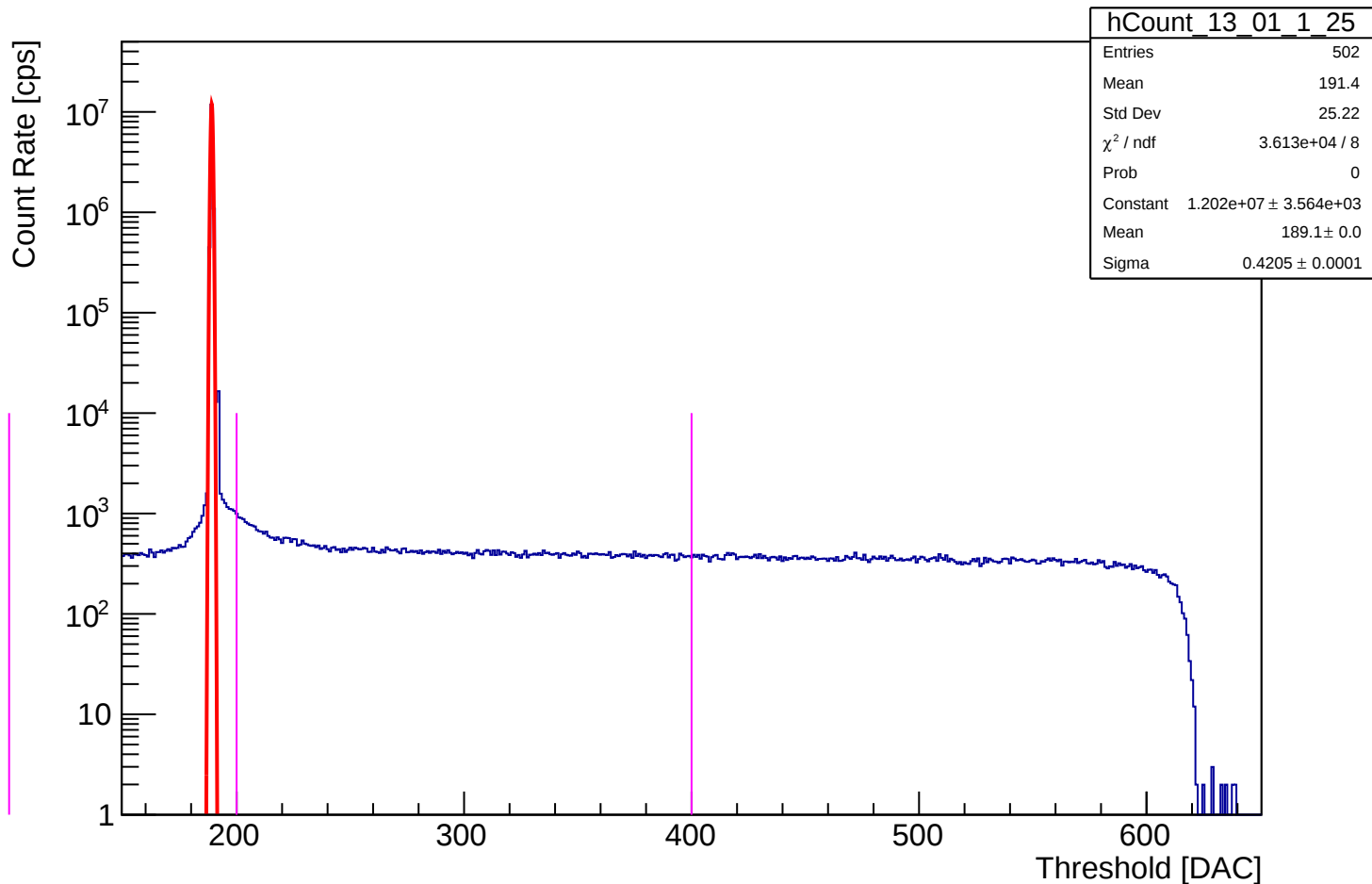
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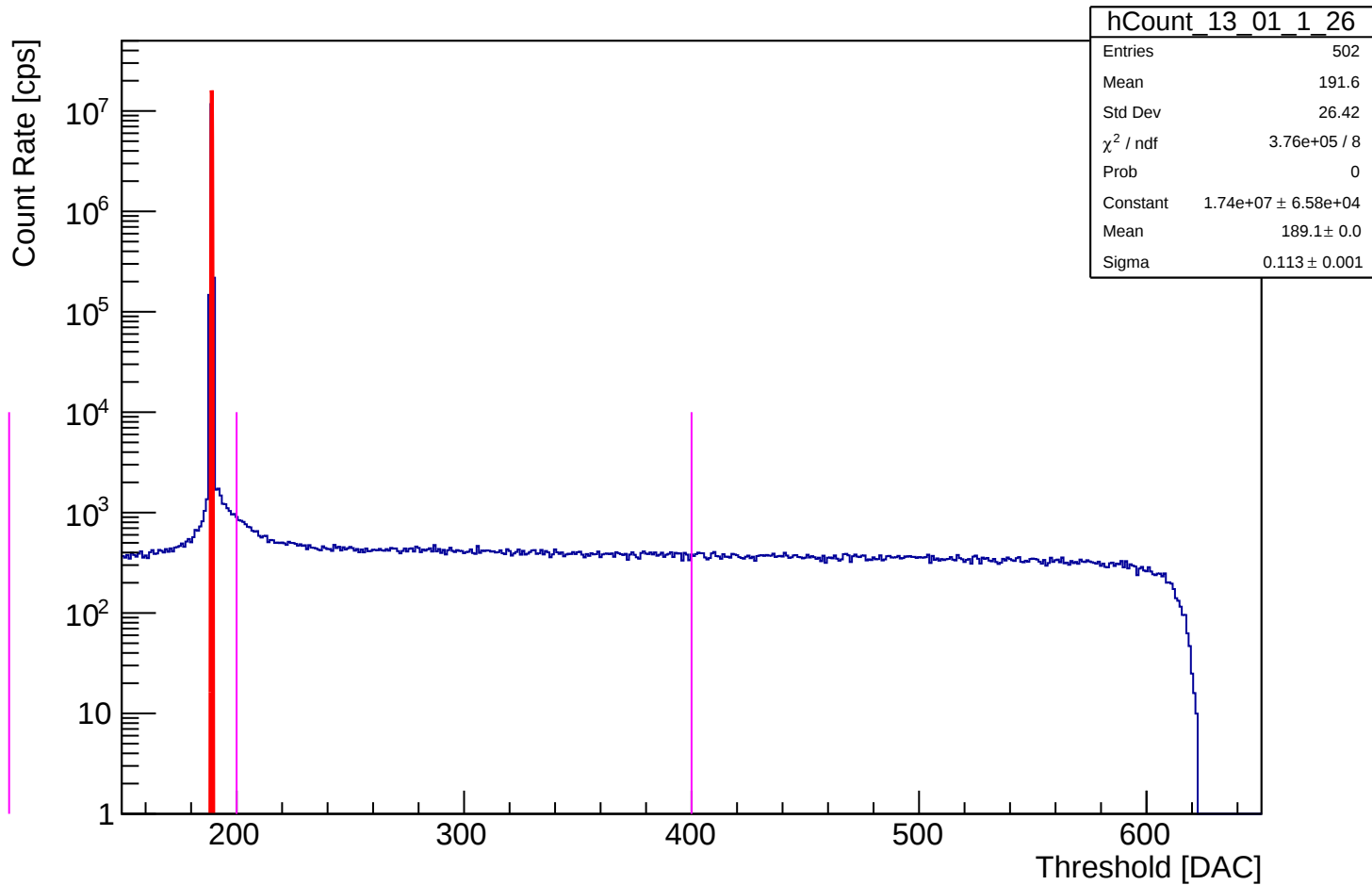
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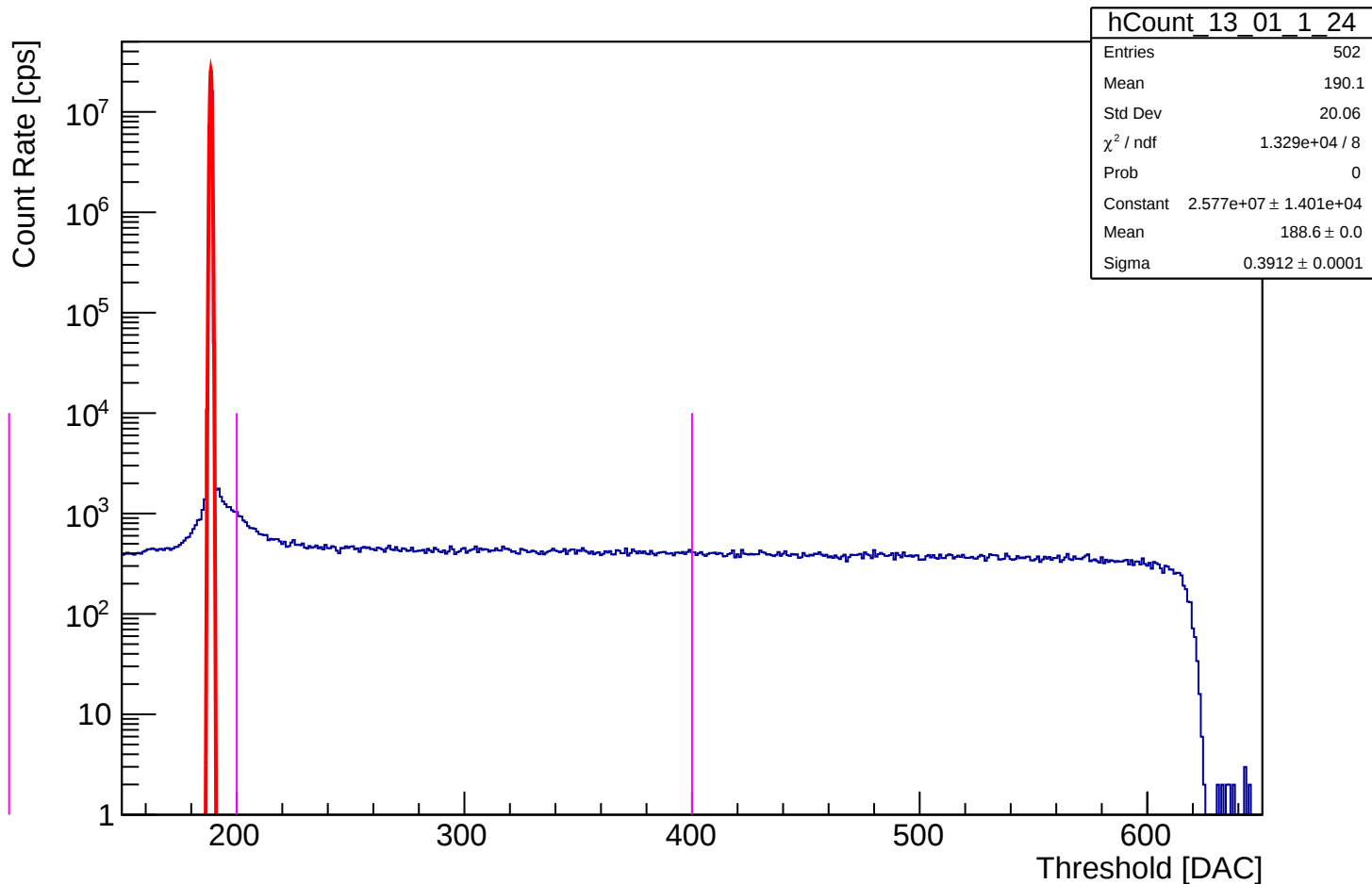
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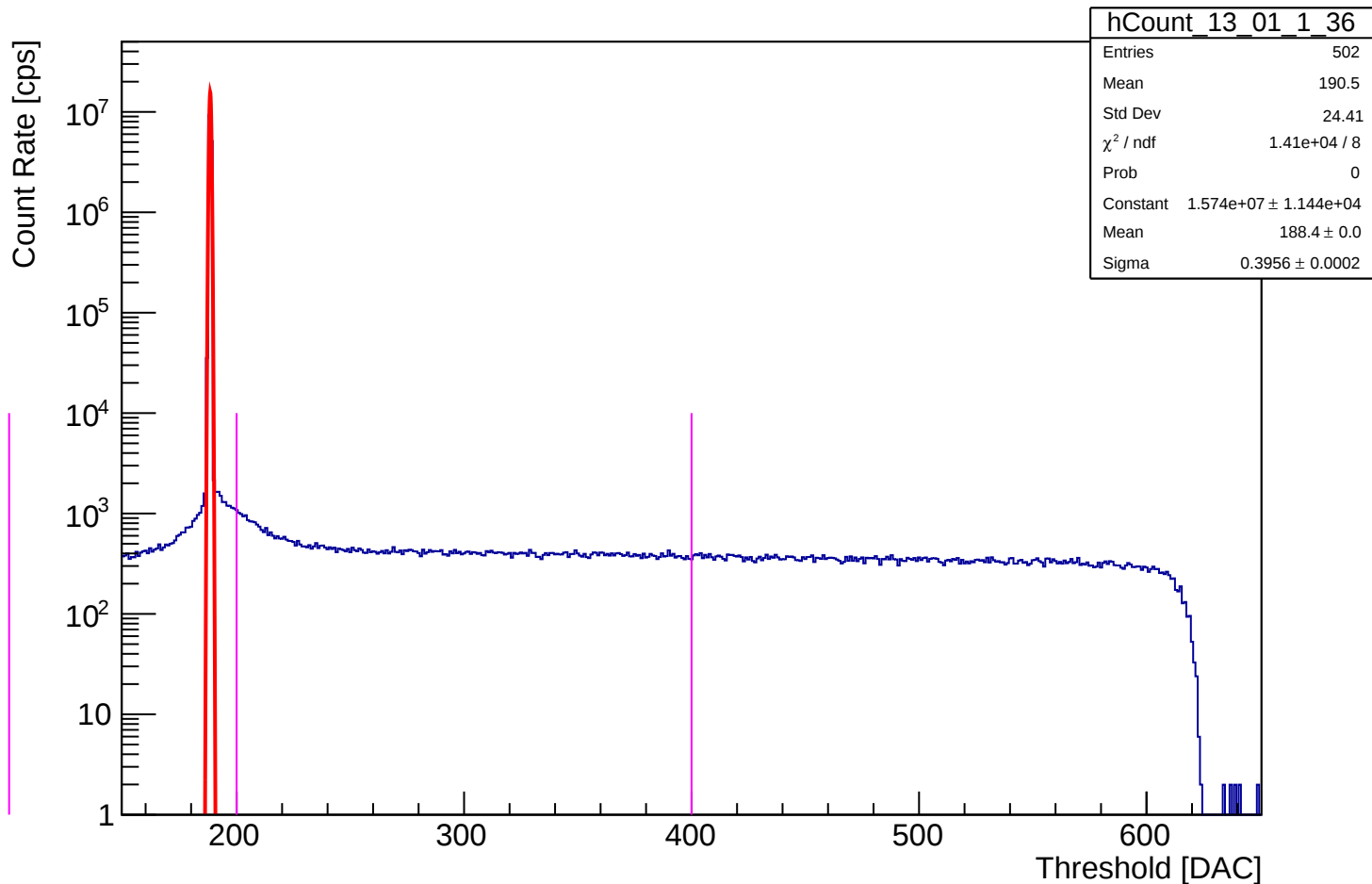
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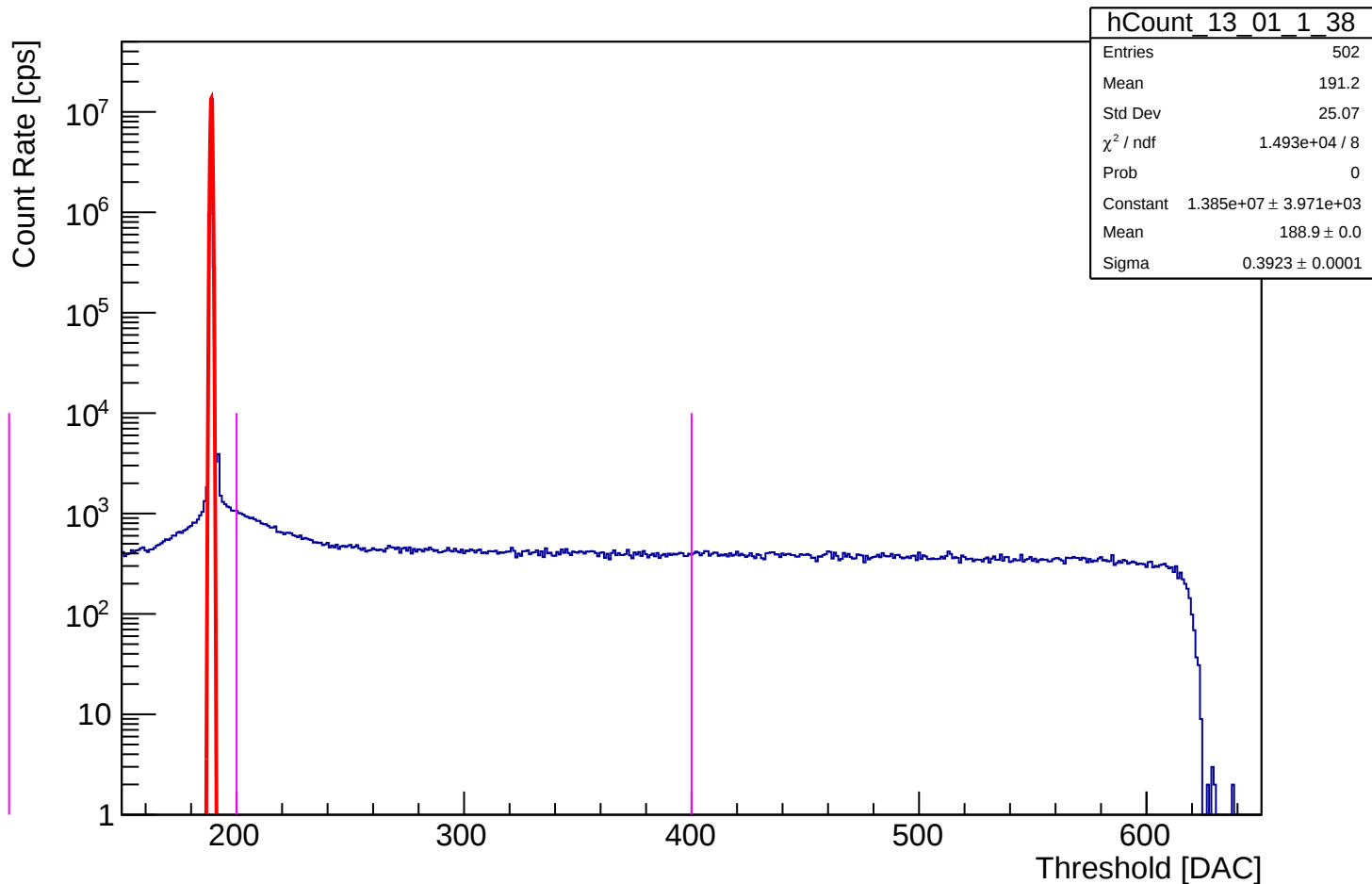
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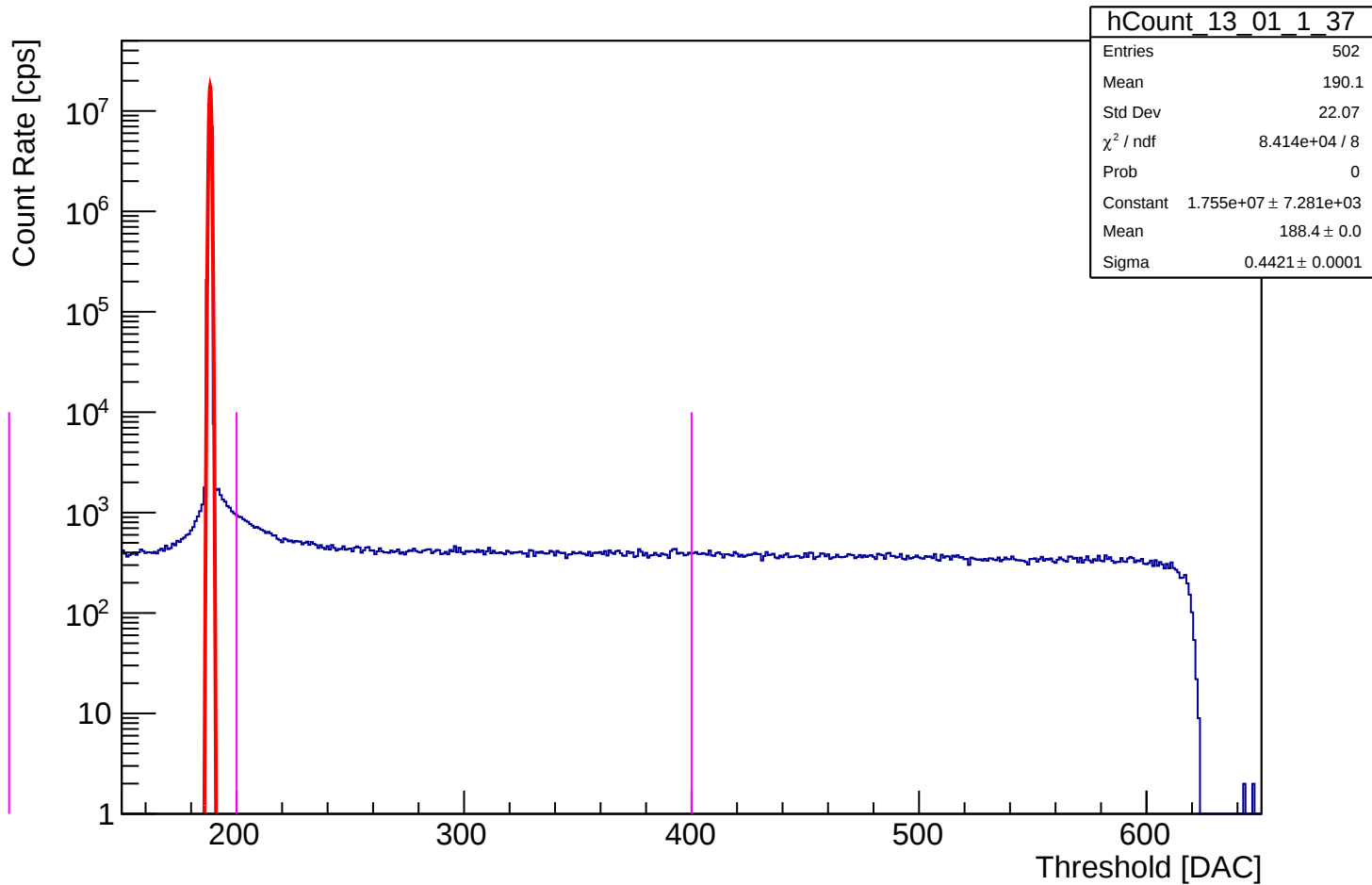
Row 1 Tile 1 Pmt 5 Pixel 13 Slot 13 Fiber 1 Asic 1 Channel 36



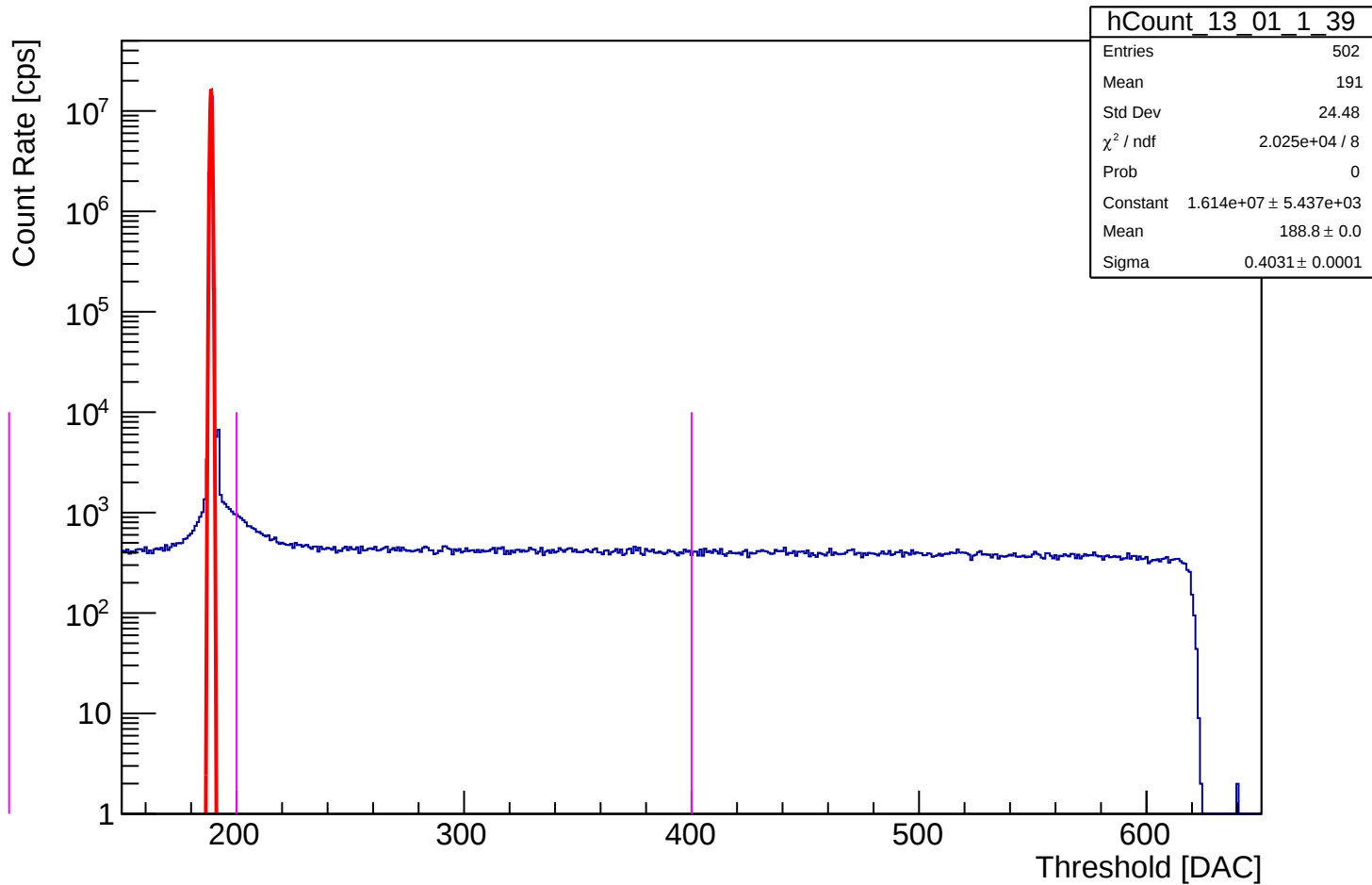
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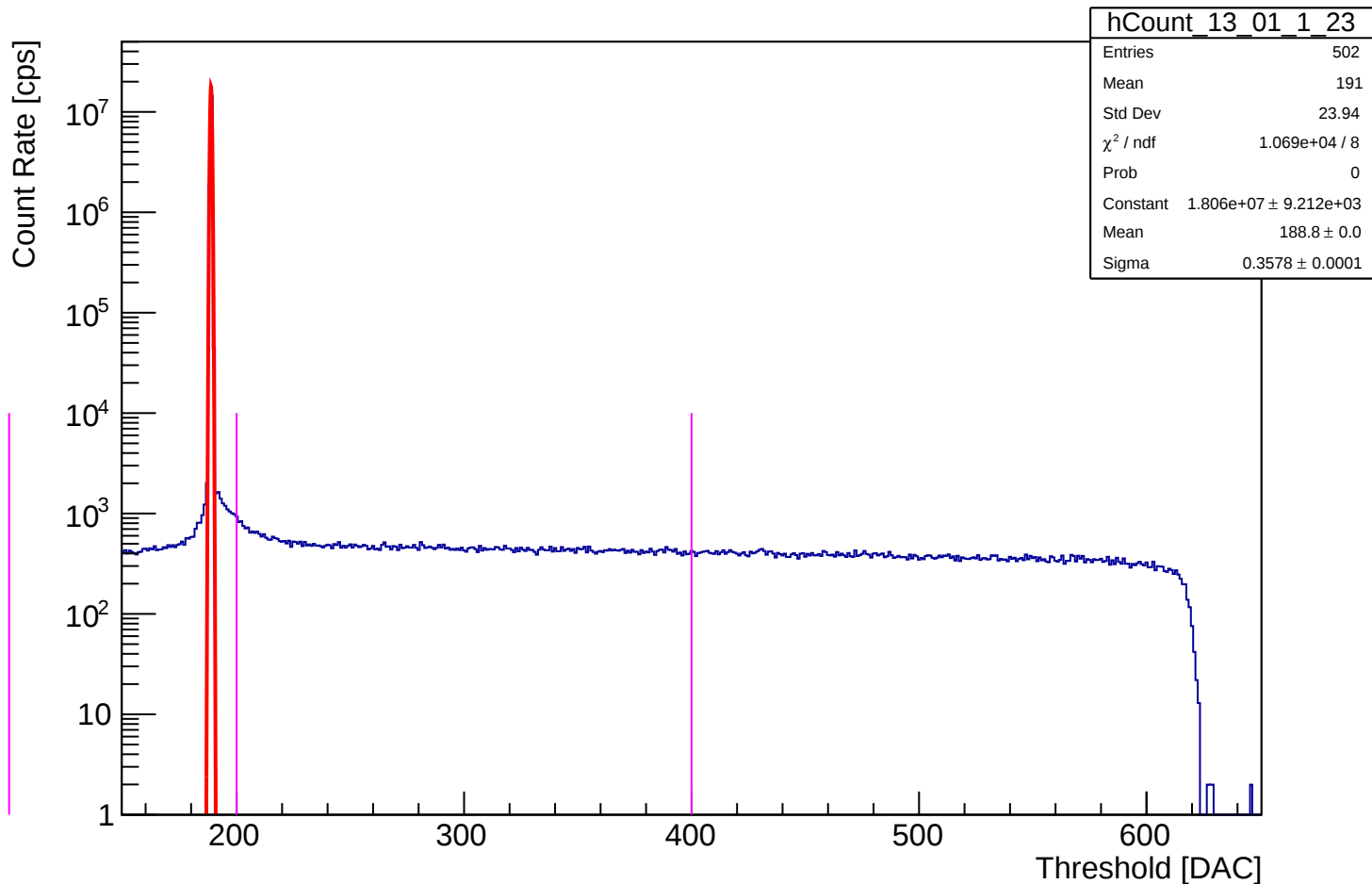
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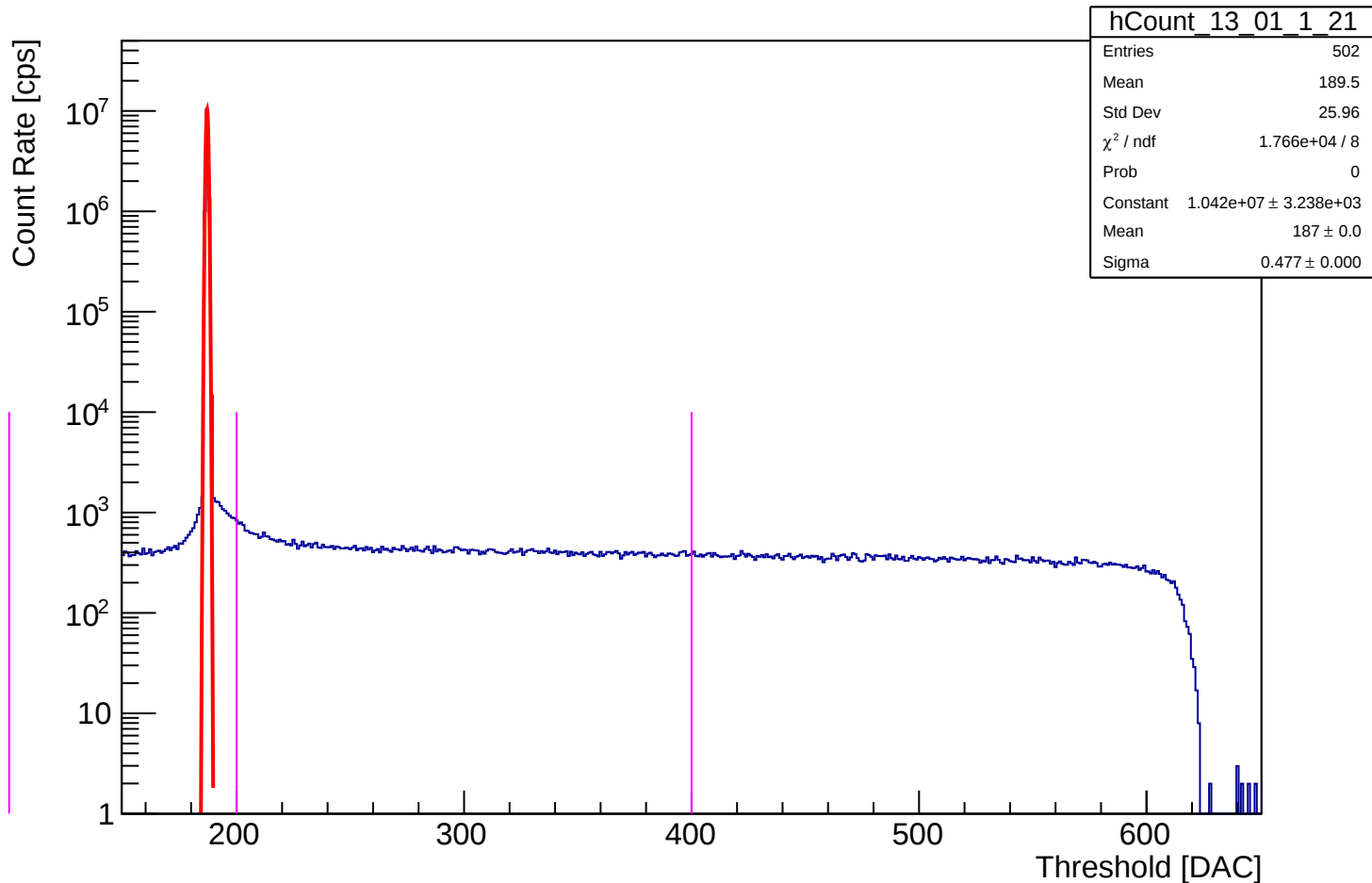
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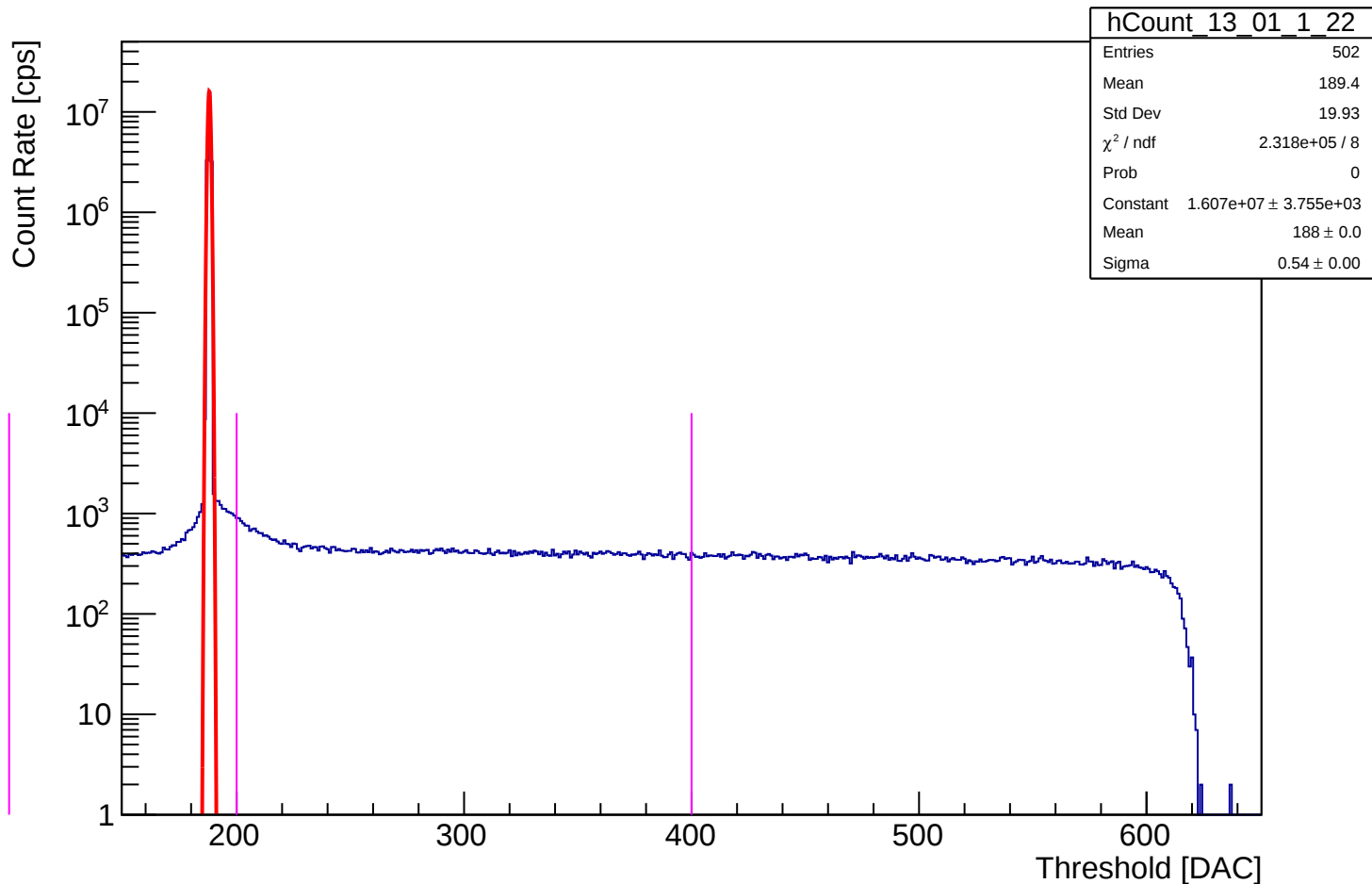
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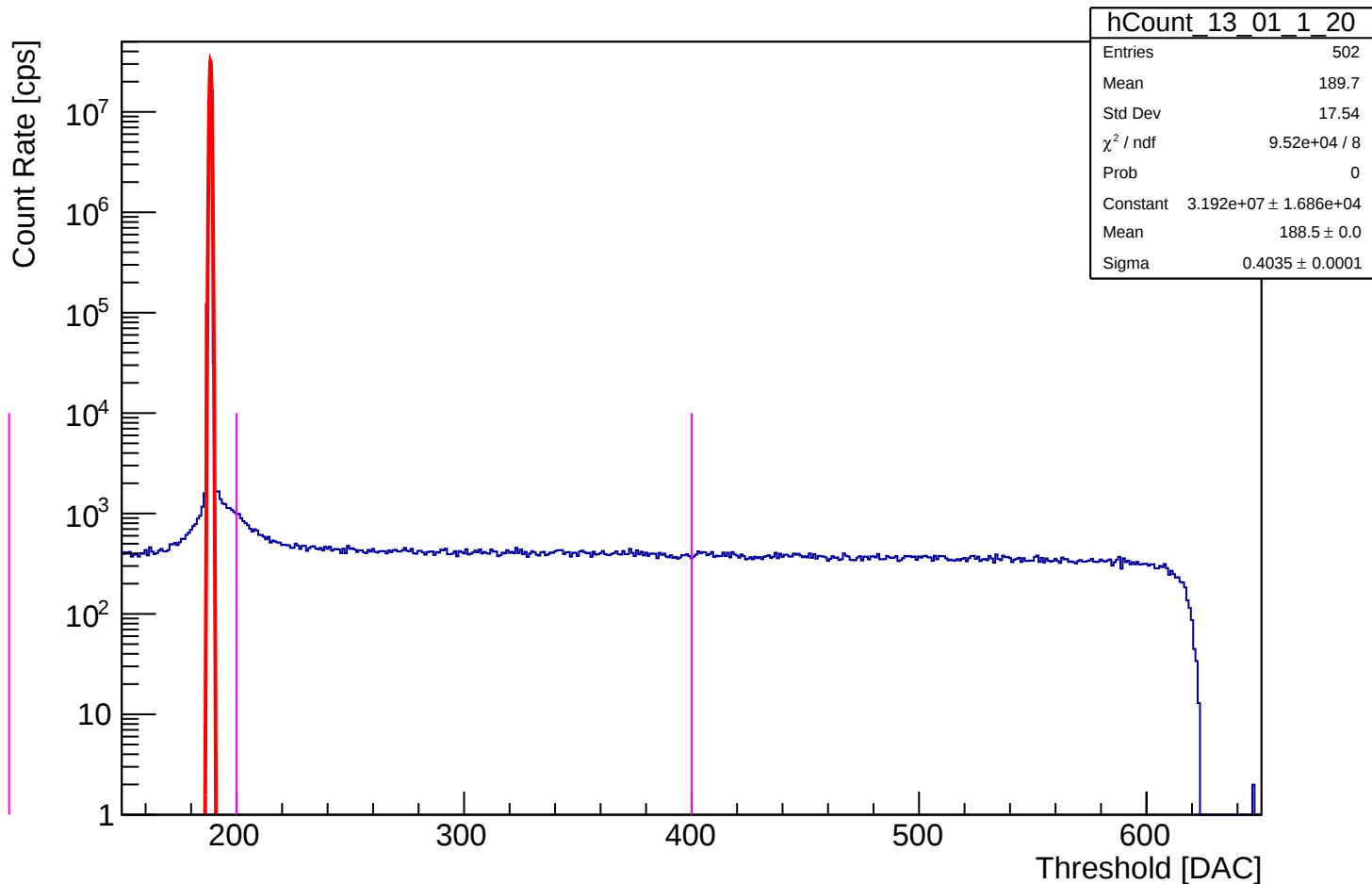
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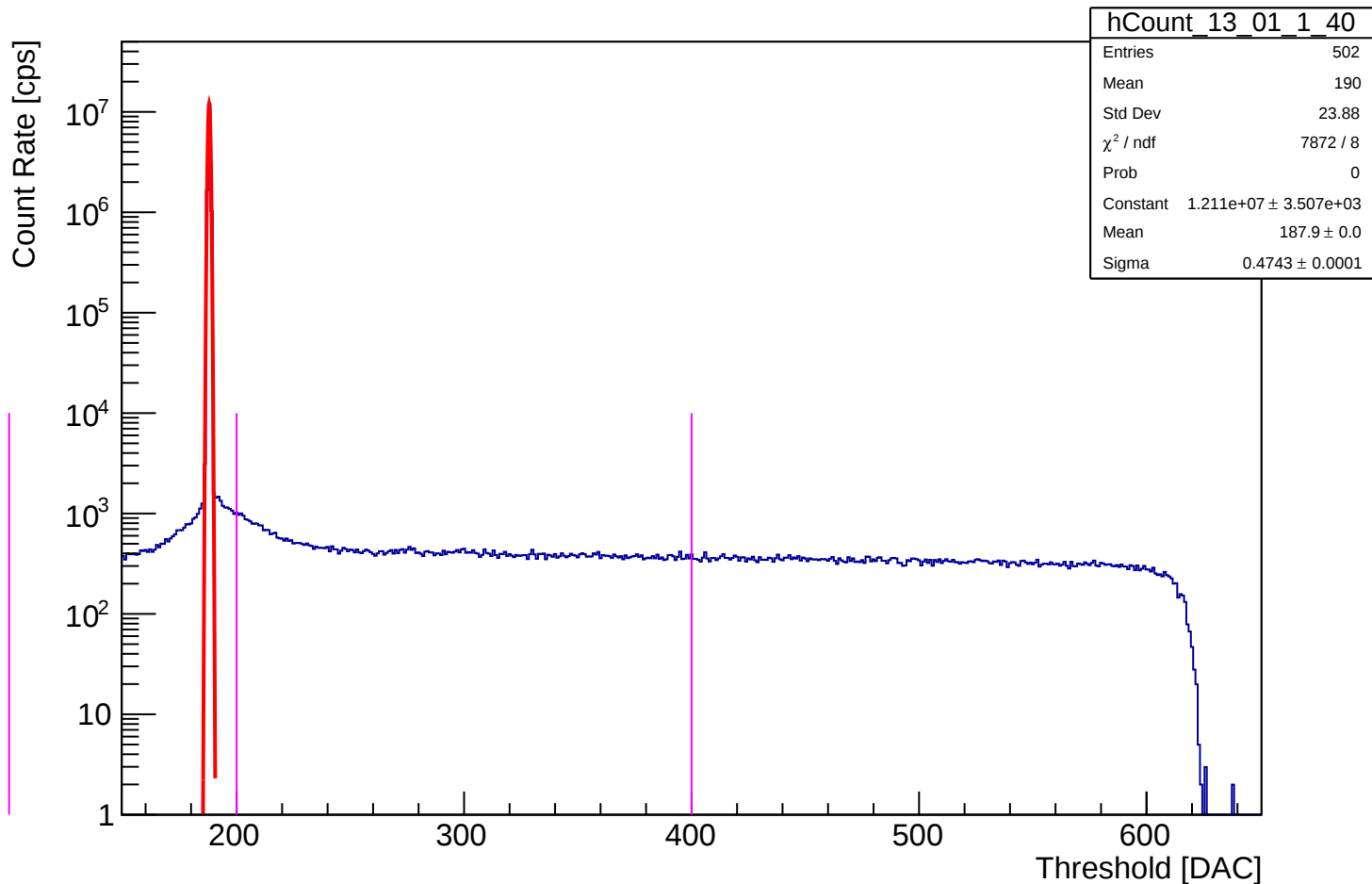
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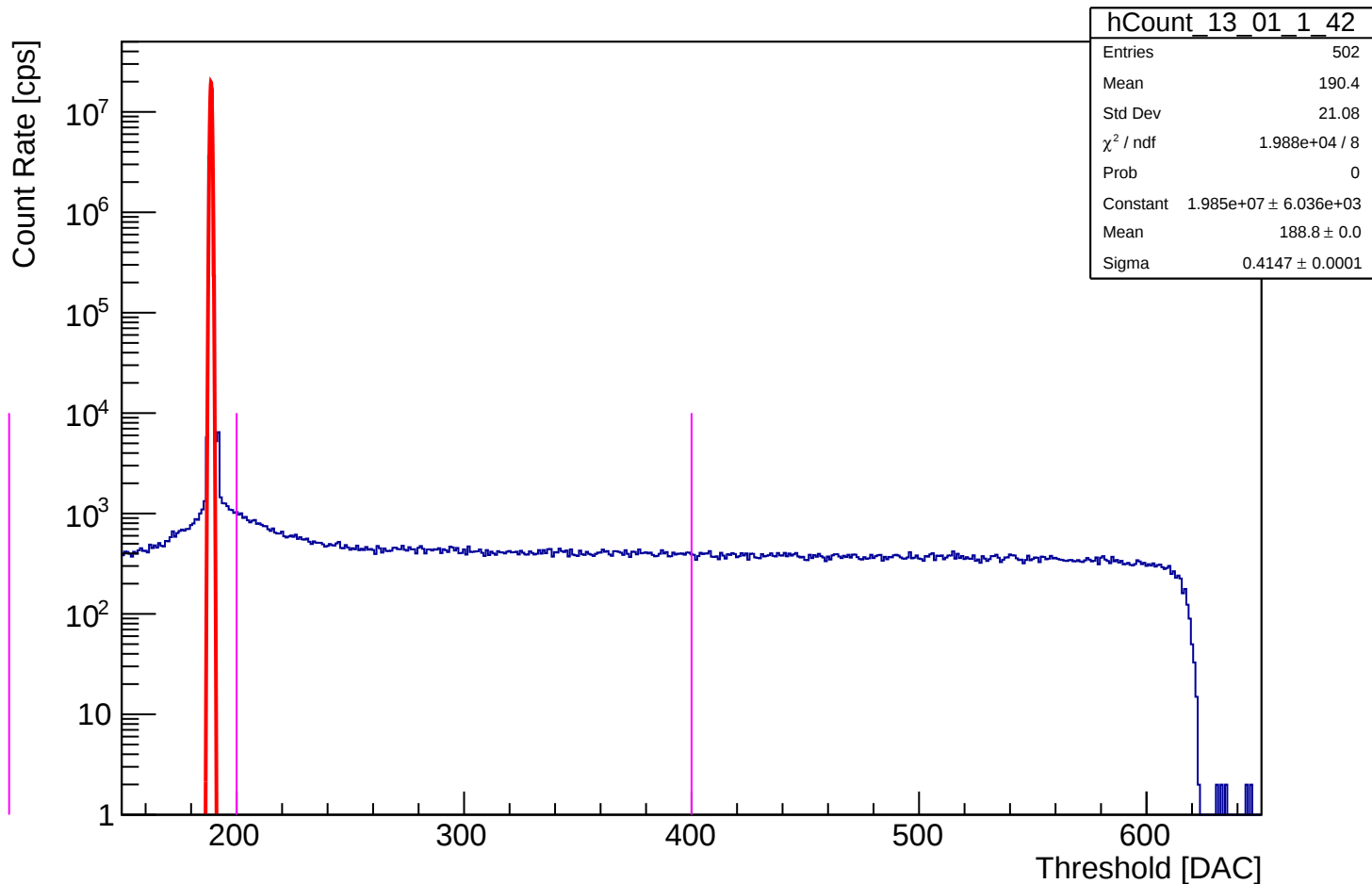
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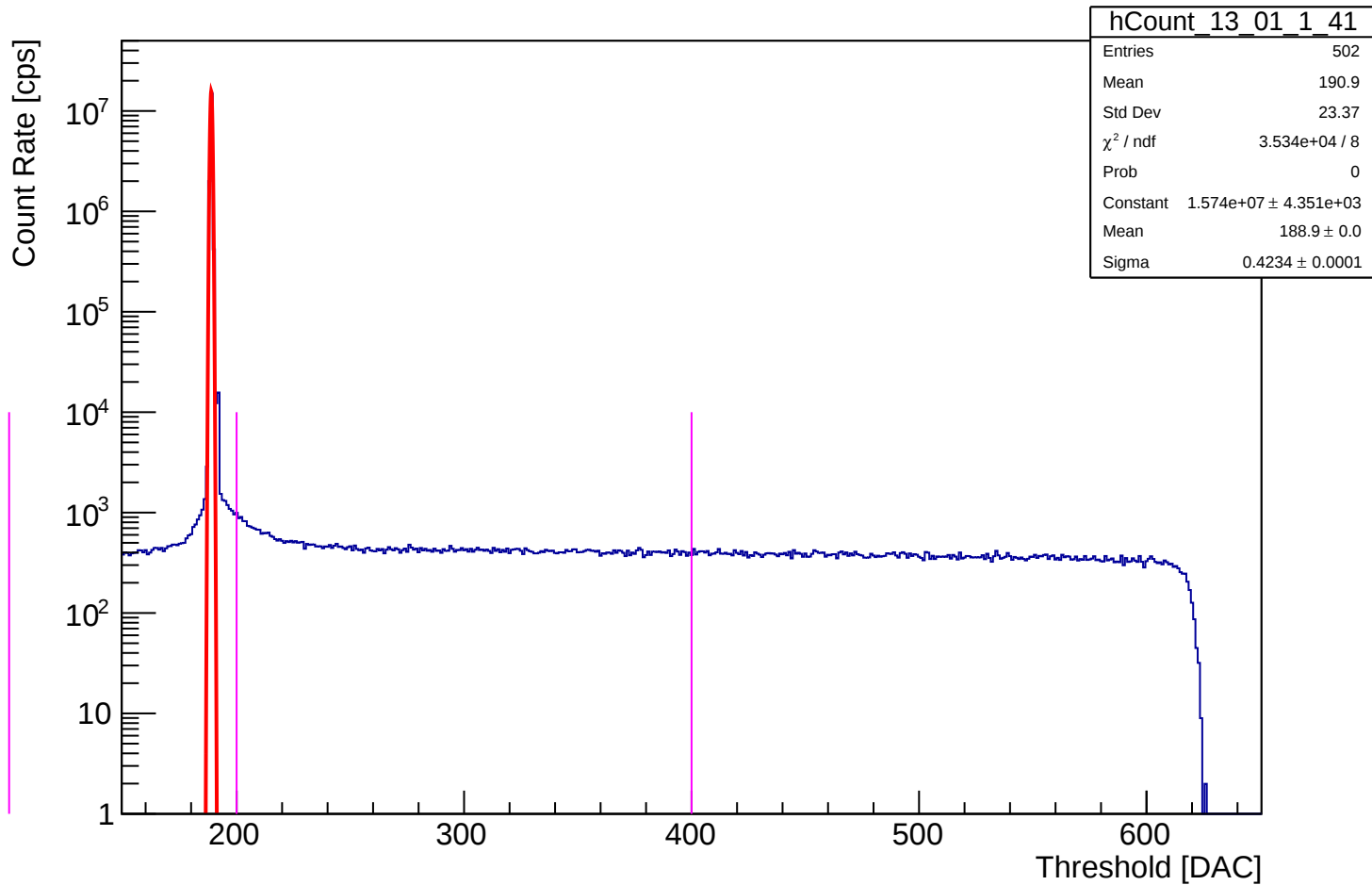
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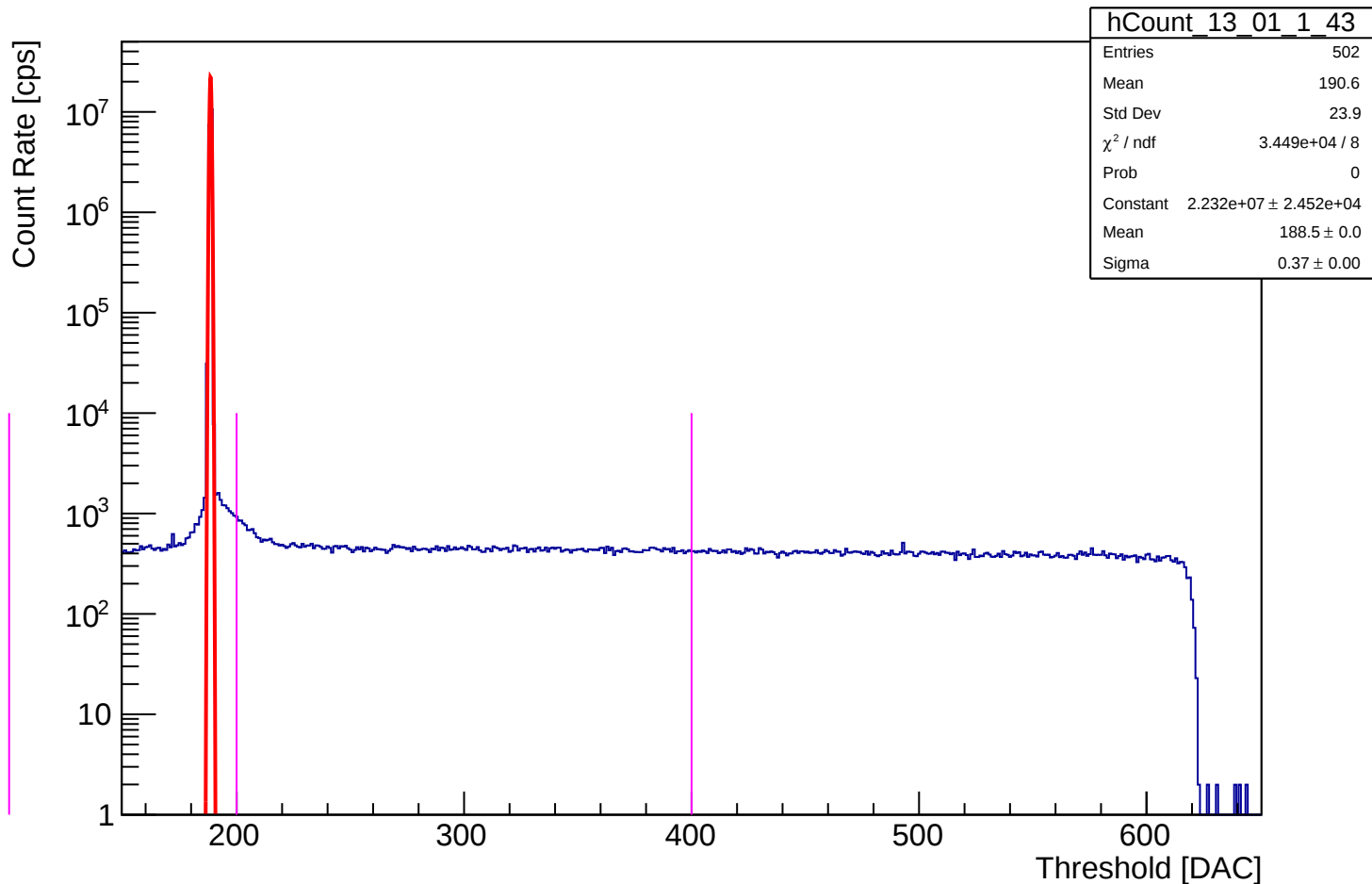
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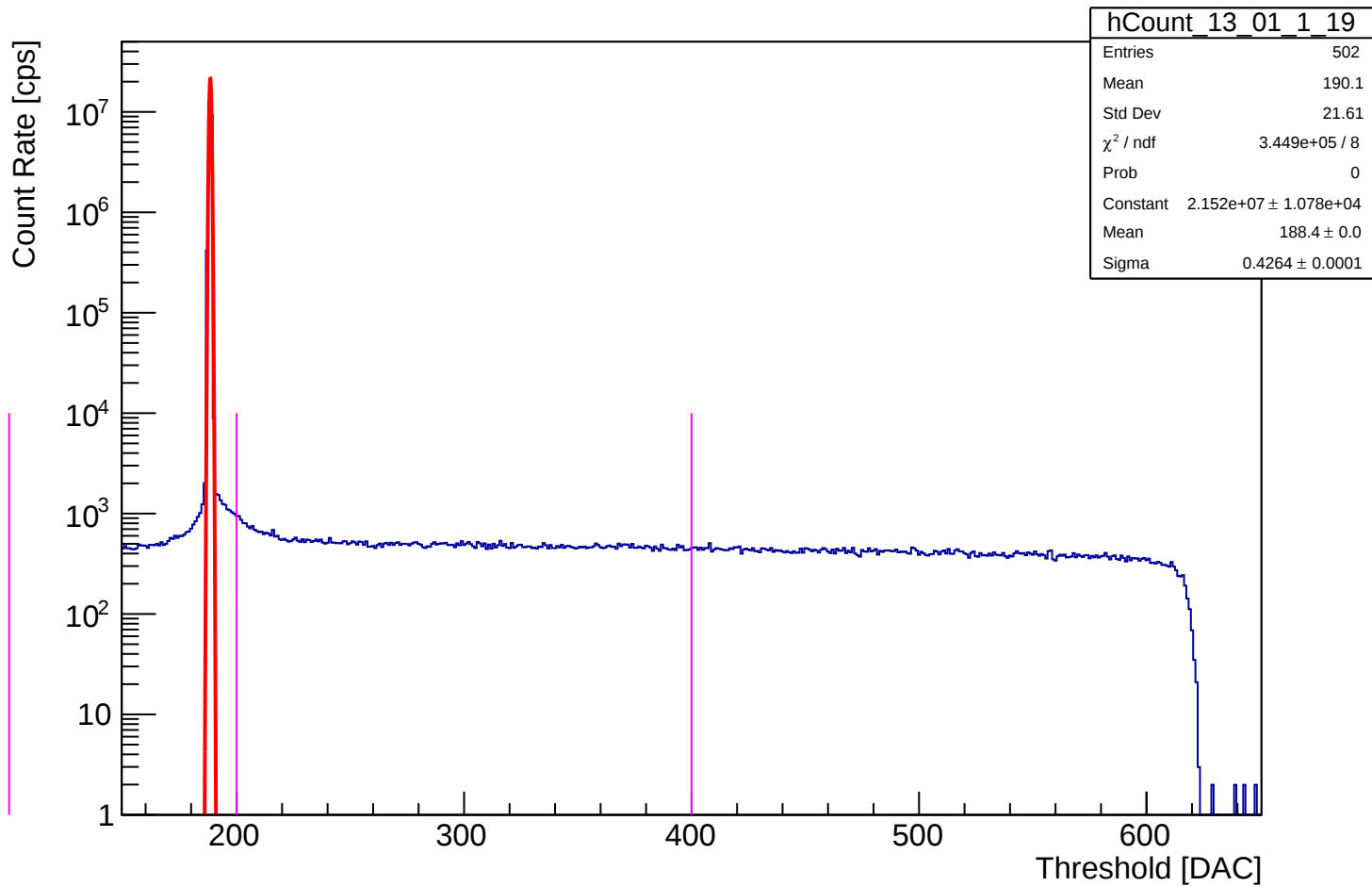


Row 1 Tile 1 Pmt 5 Pixel 23 Slot 13 Fiber 1 Asic 1 Channel 41

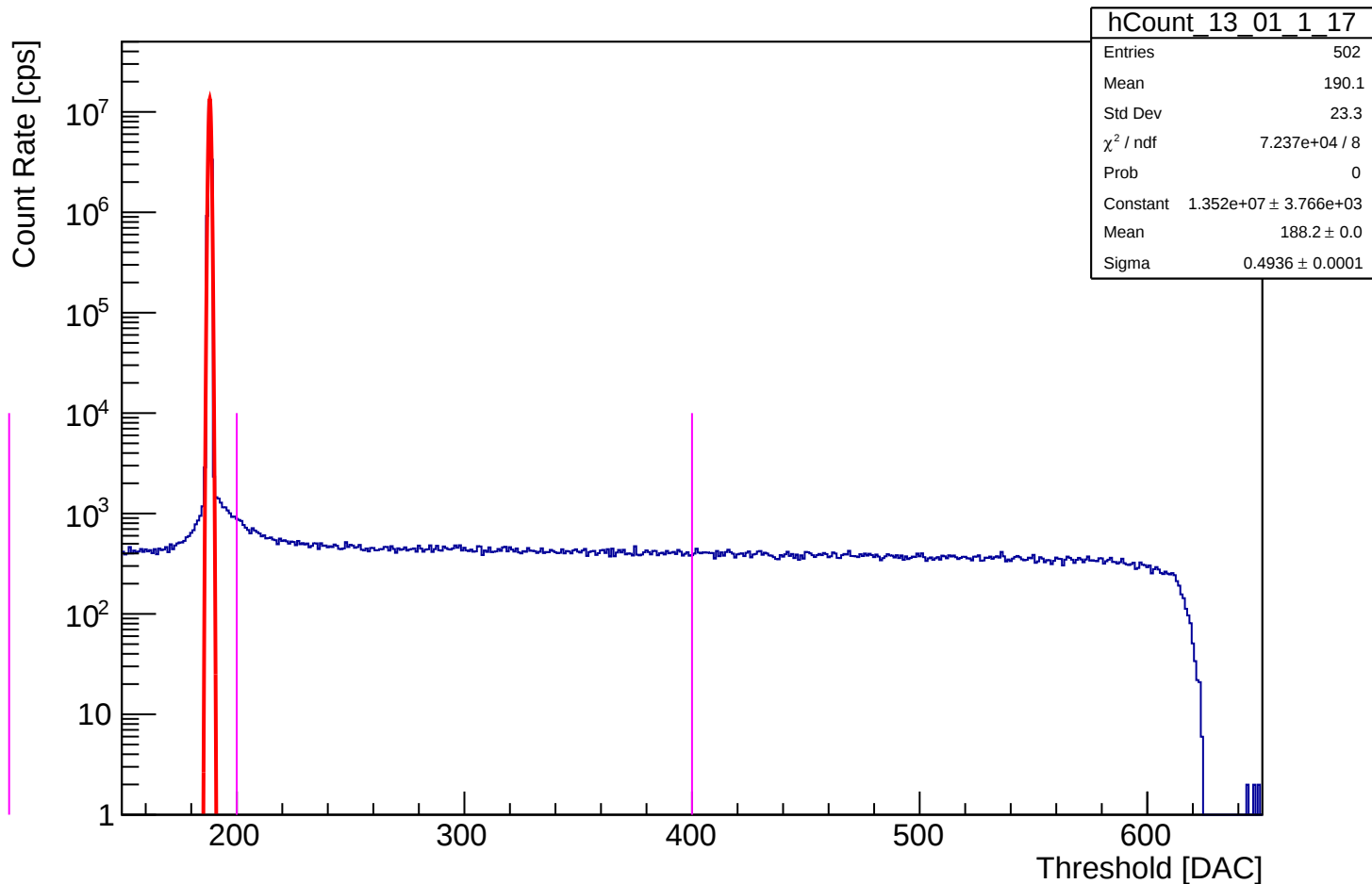


Row 1 Tile 1 Pmt 5 Pixel 24 Slot 13 Fiber 1 Asic 1 Channel 43

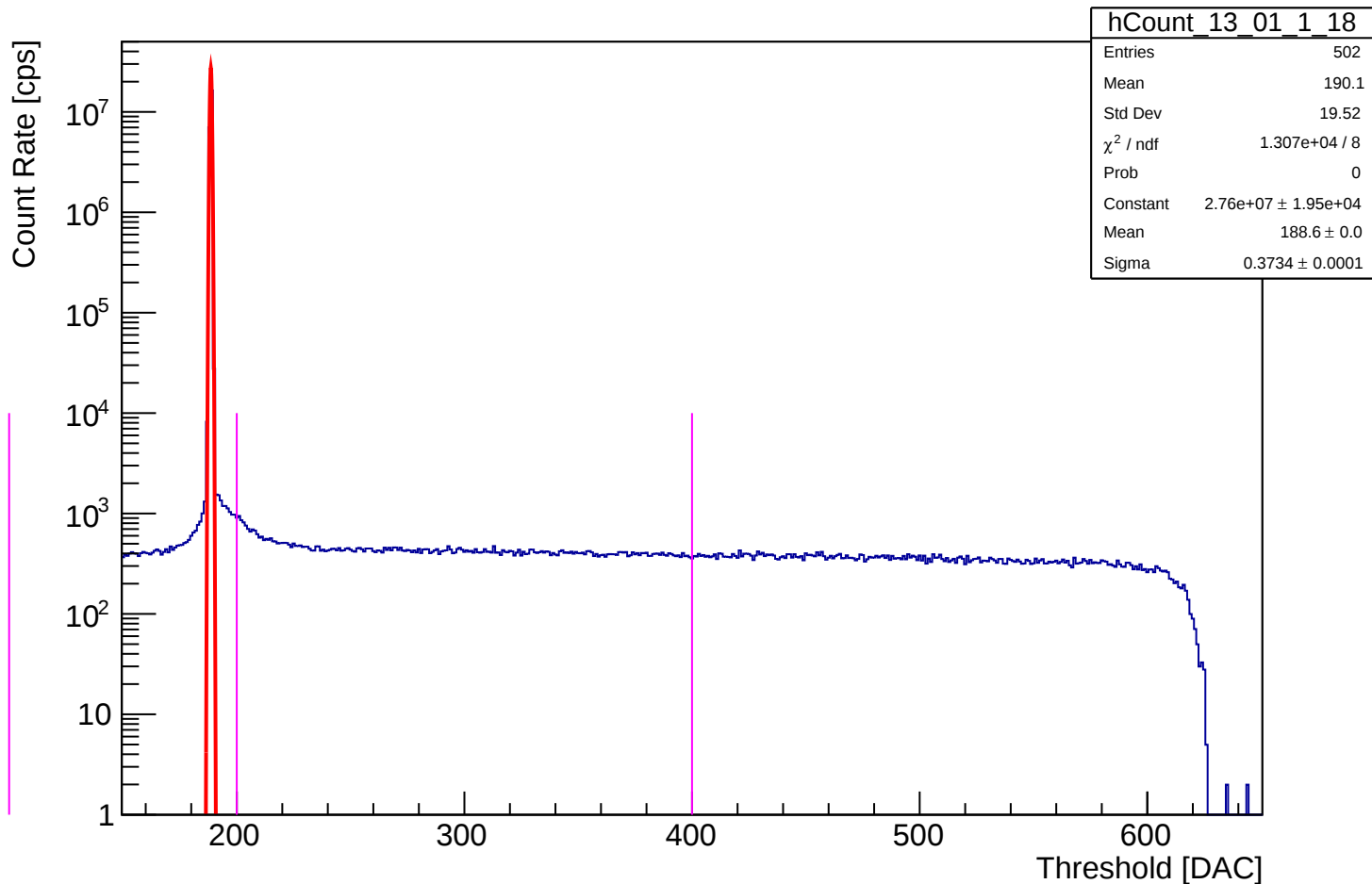




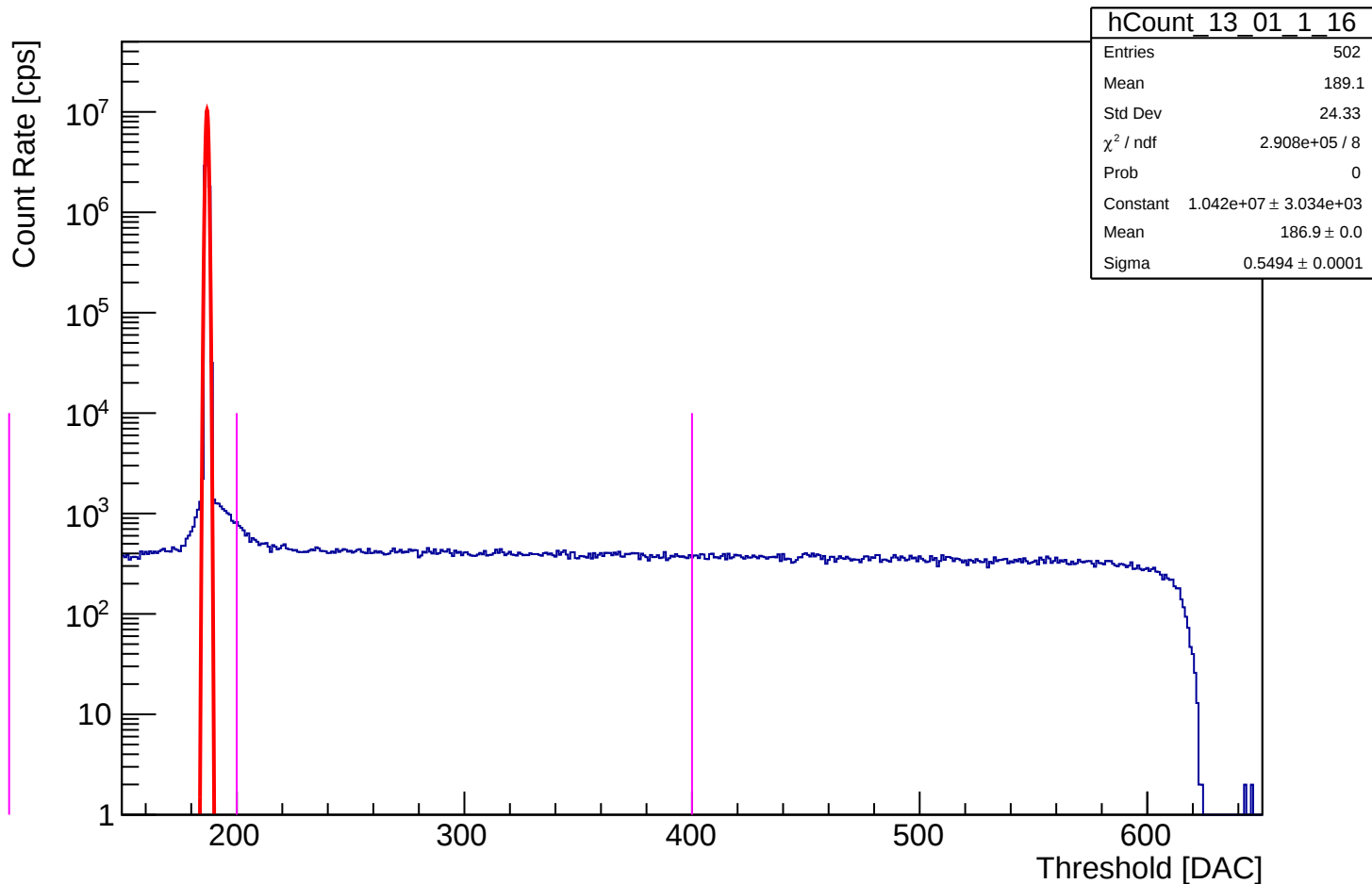
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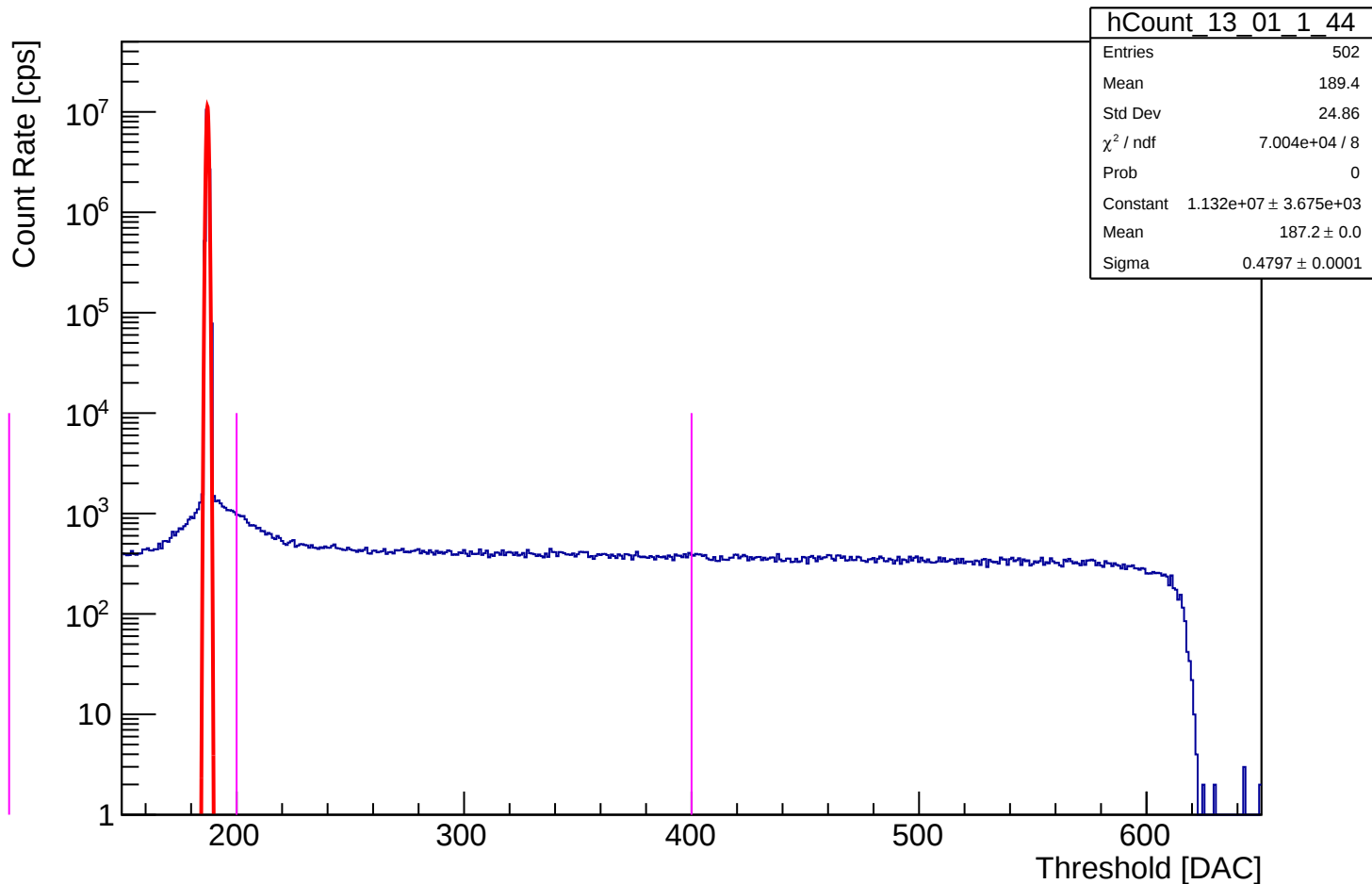
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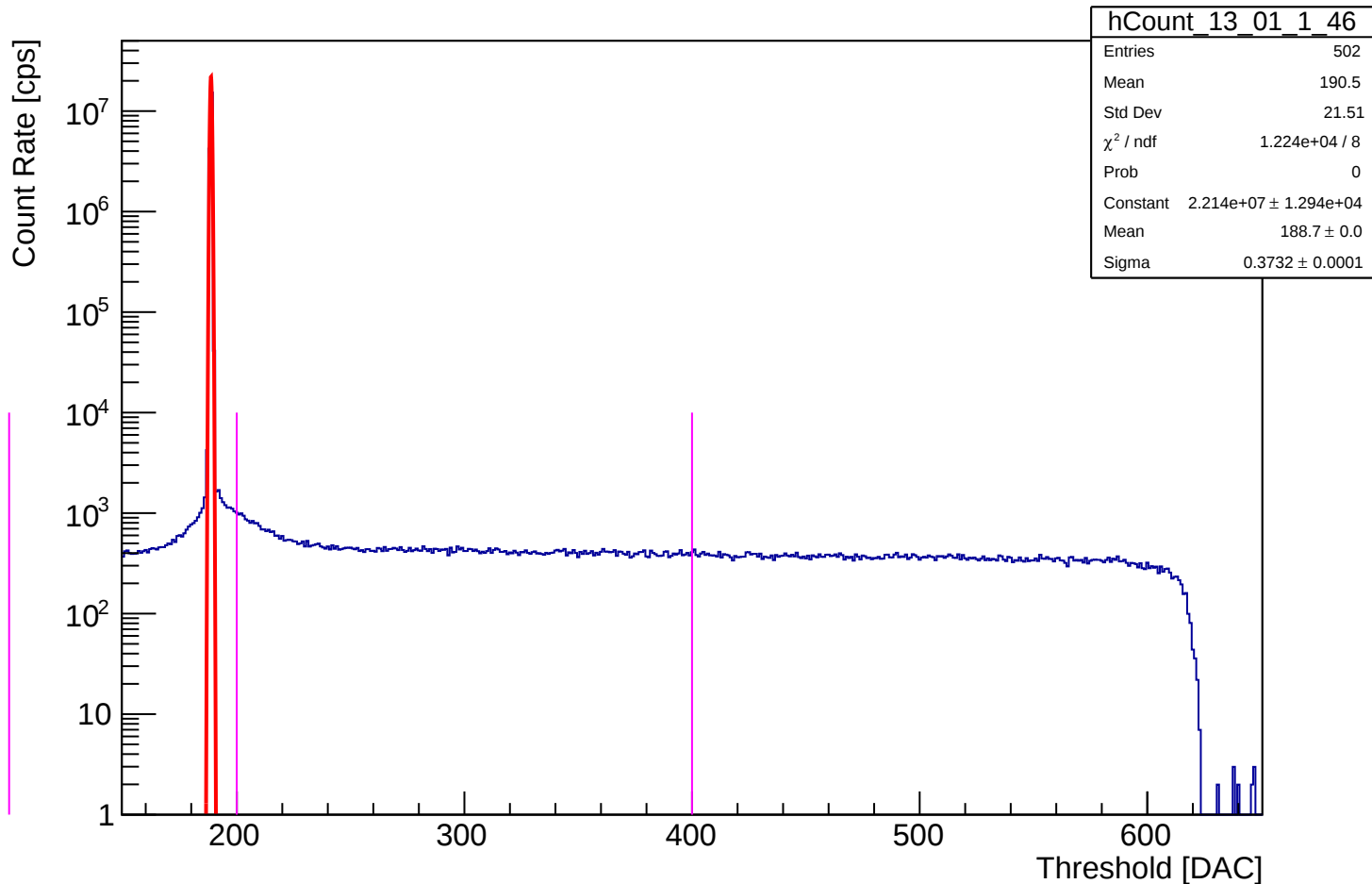
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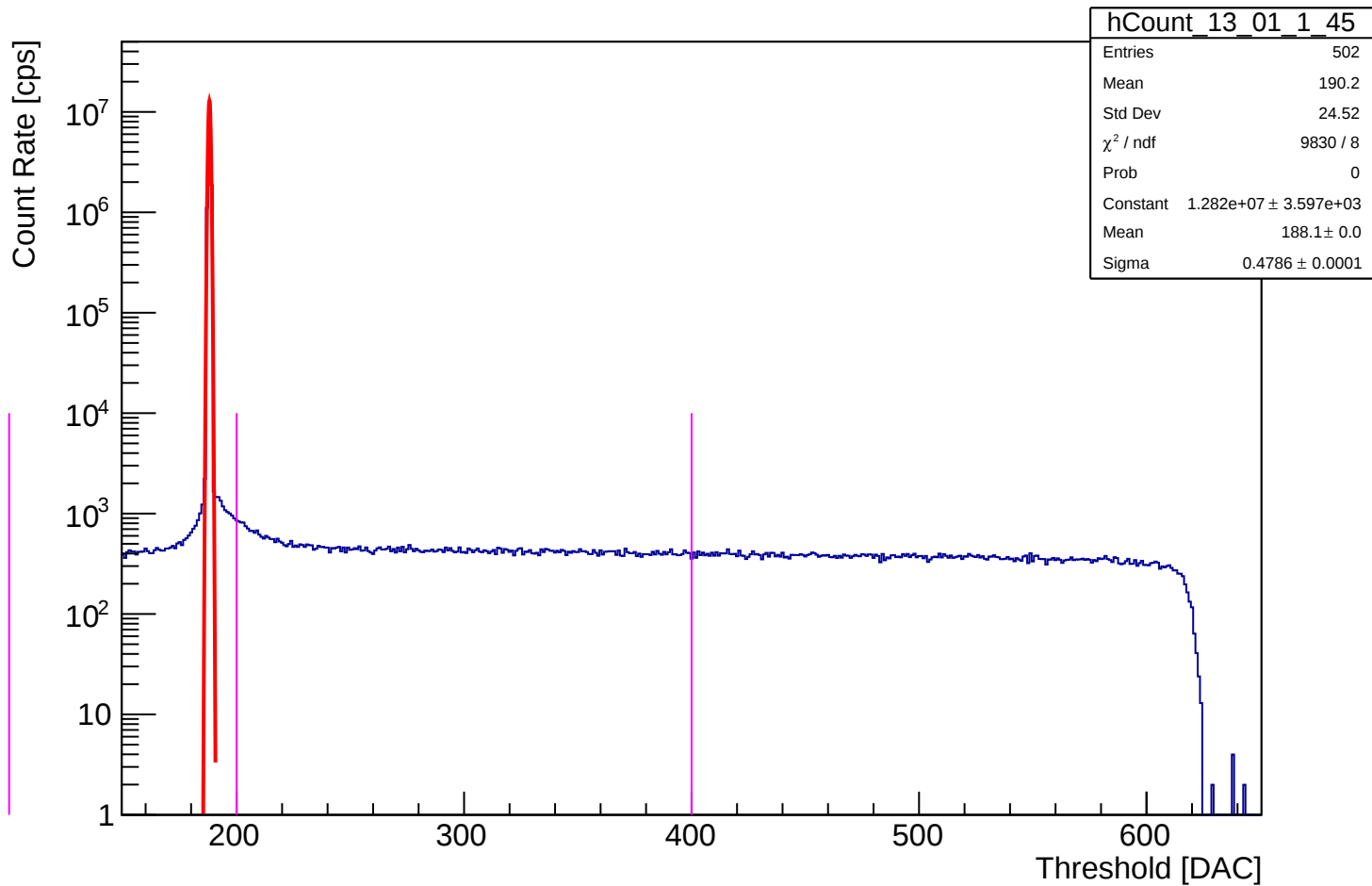
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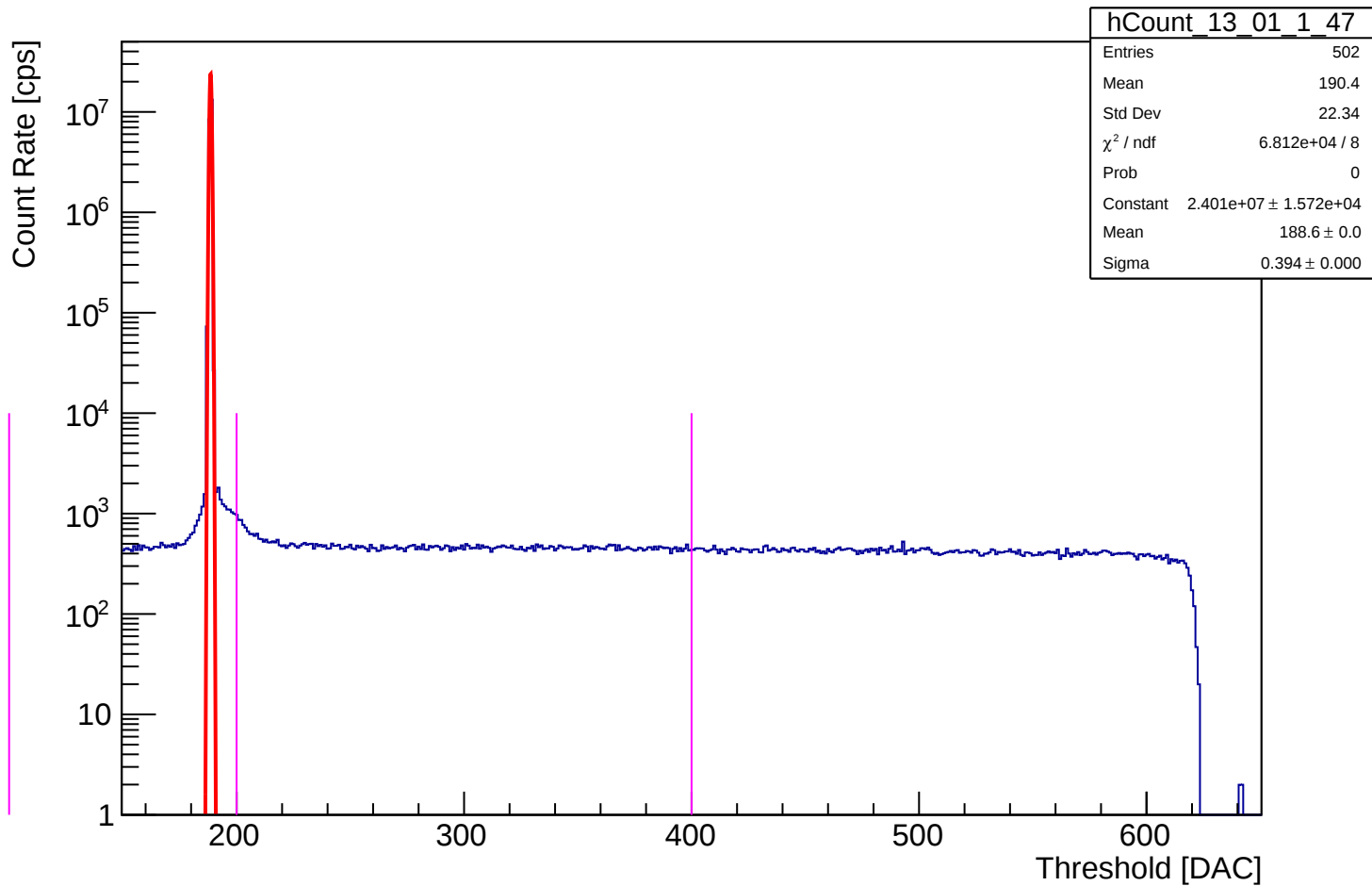
Row 1 Tile 1 Pmt 5 Pixel 30 Slot 13 Fiber 1 Asic 1 Channel 46



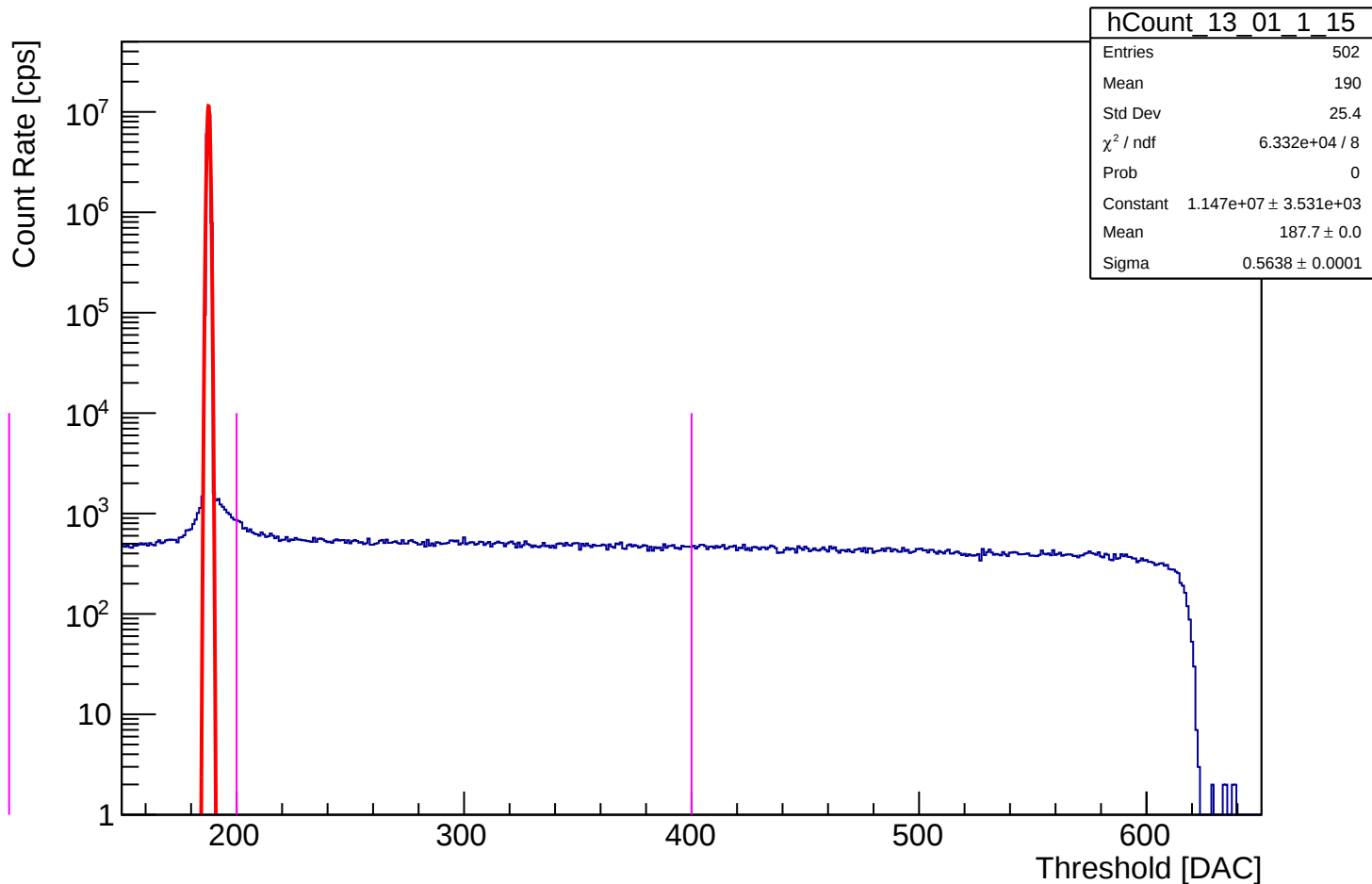
Row 1 Tile 1 Pmt 5 Pixel 31 Slot 13 Fiber 1 Asic 1 Channel 45



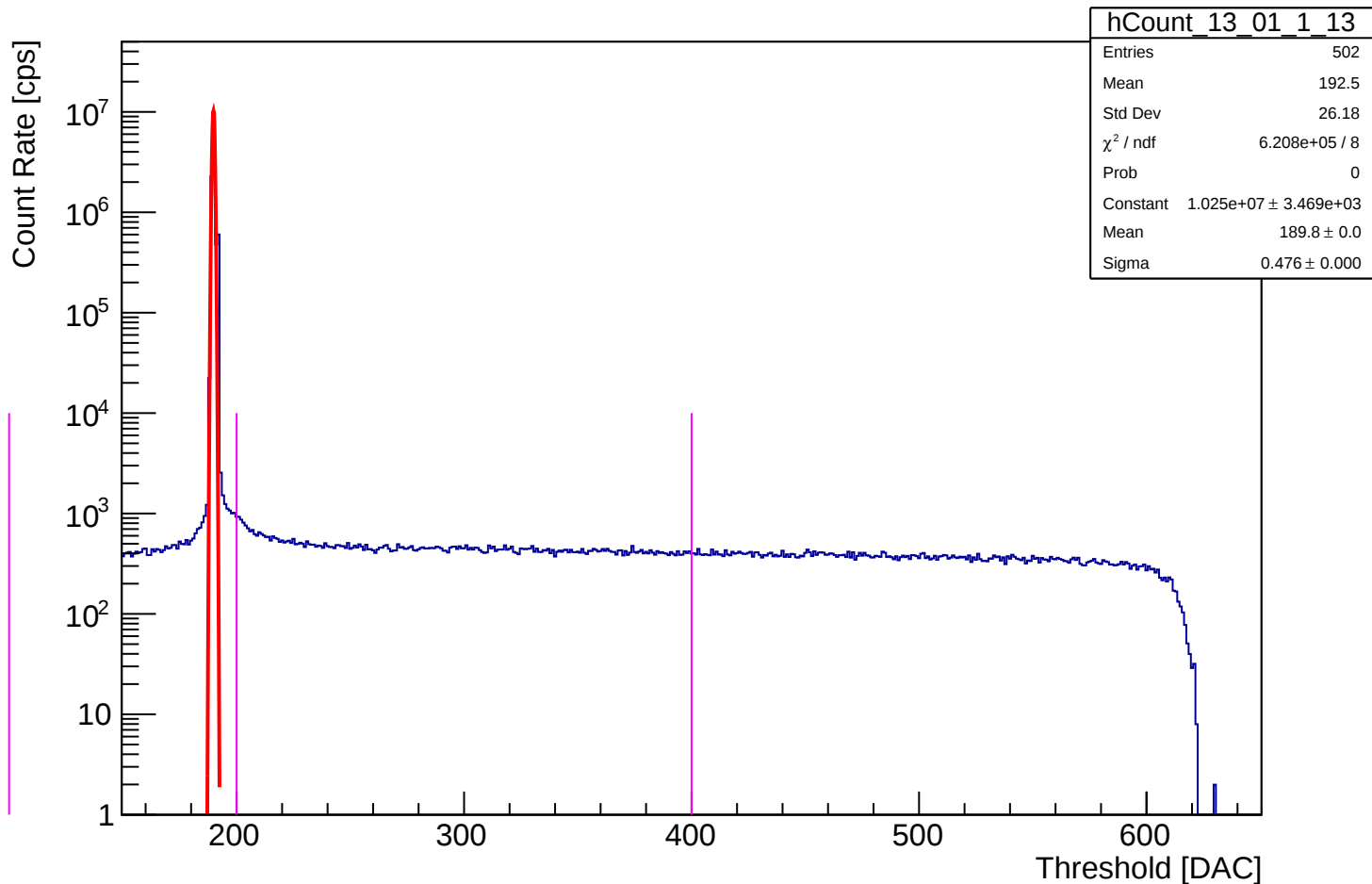
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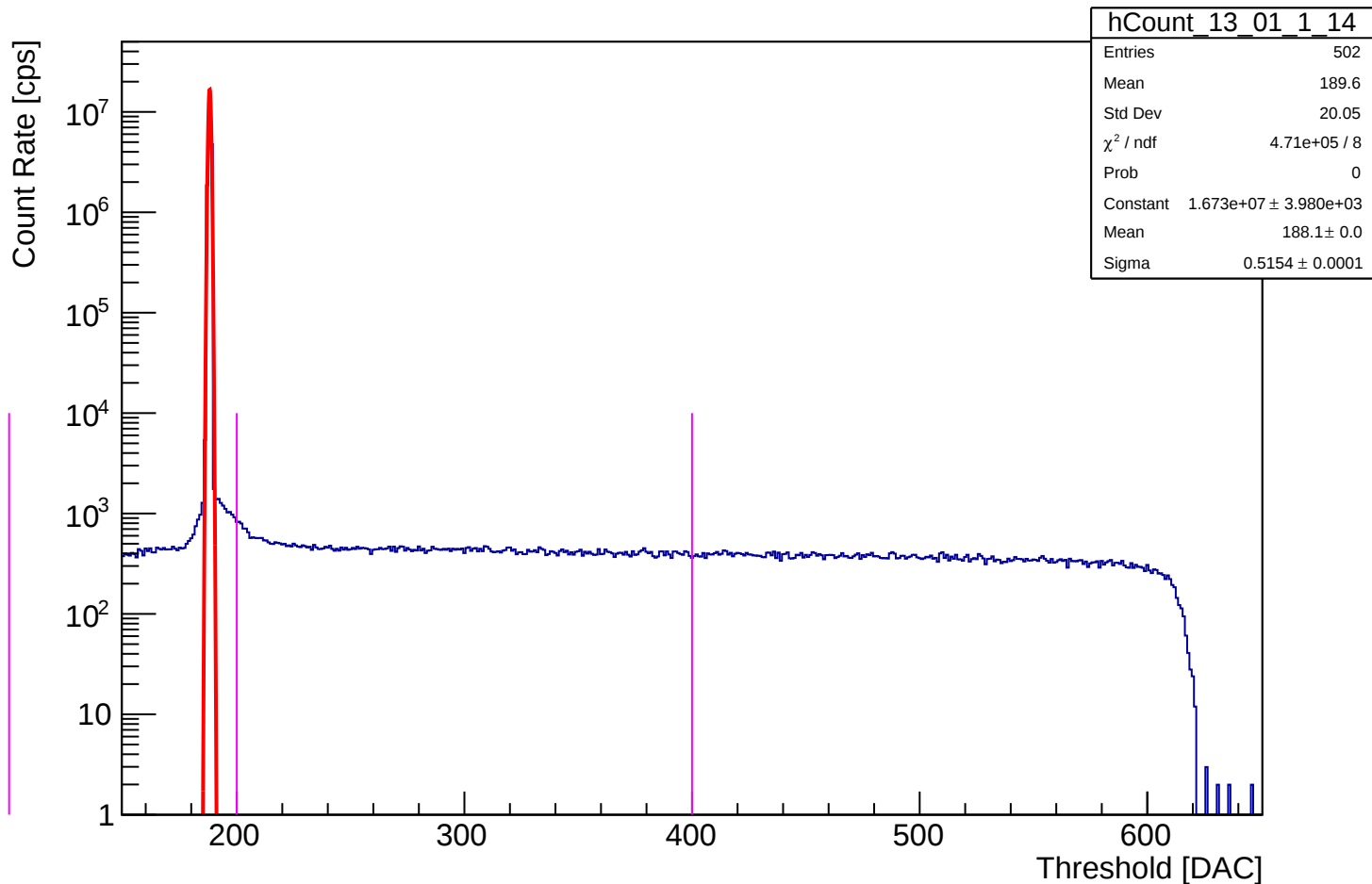
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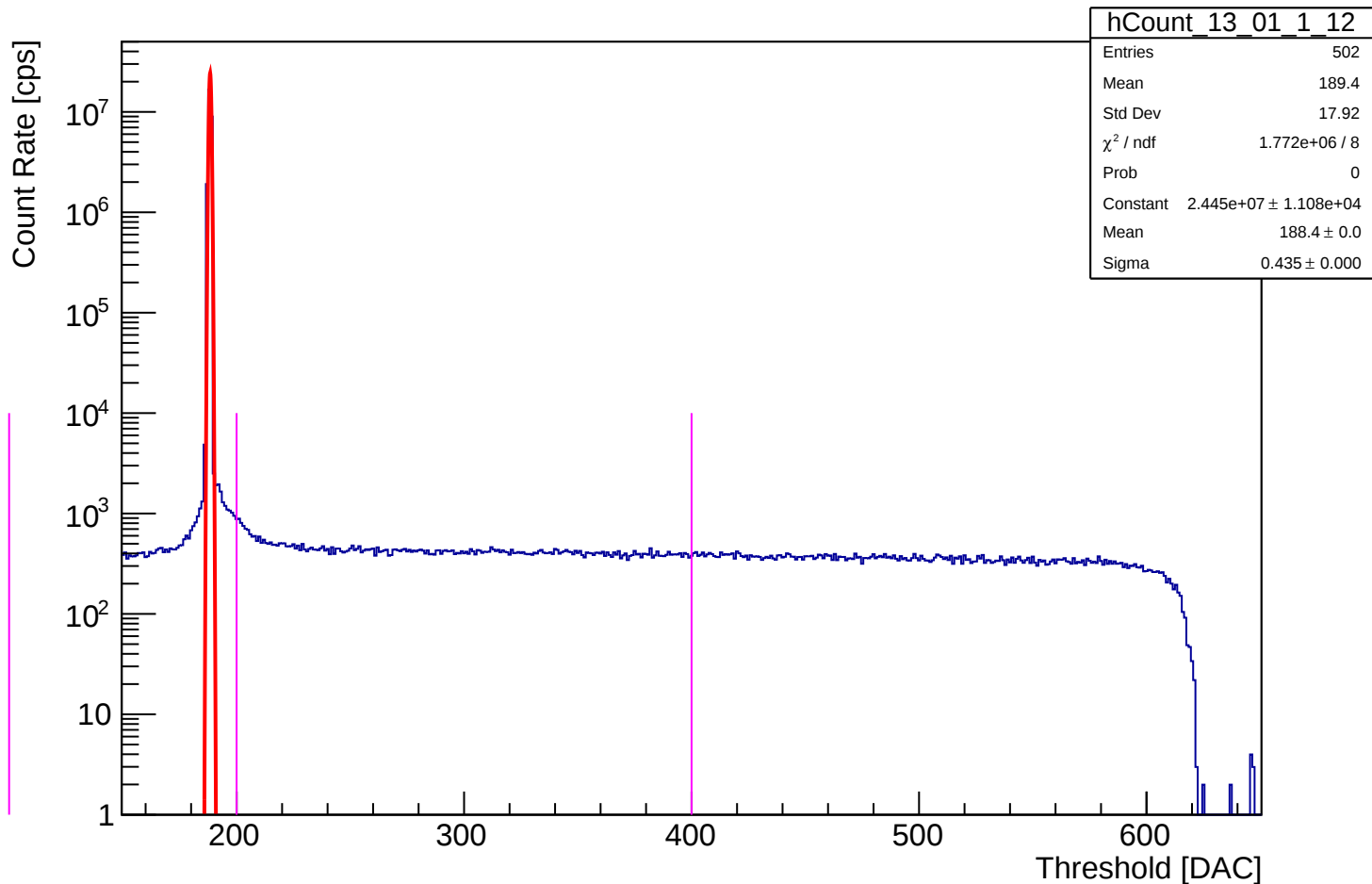
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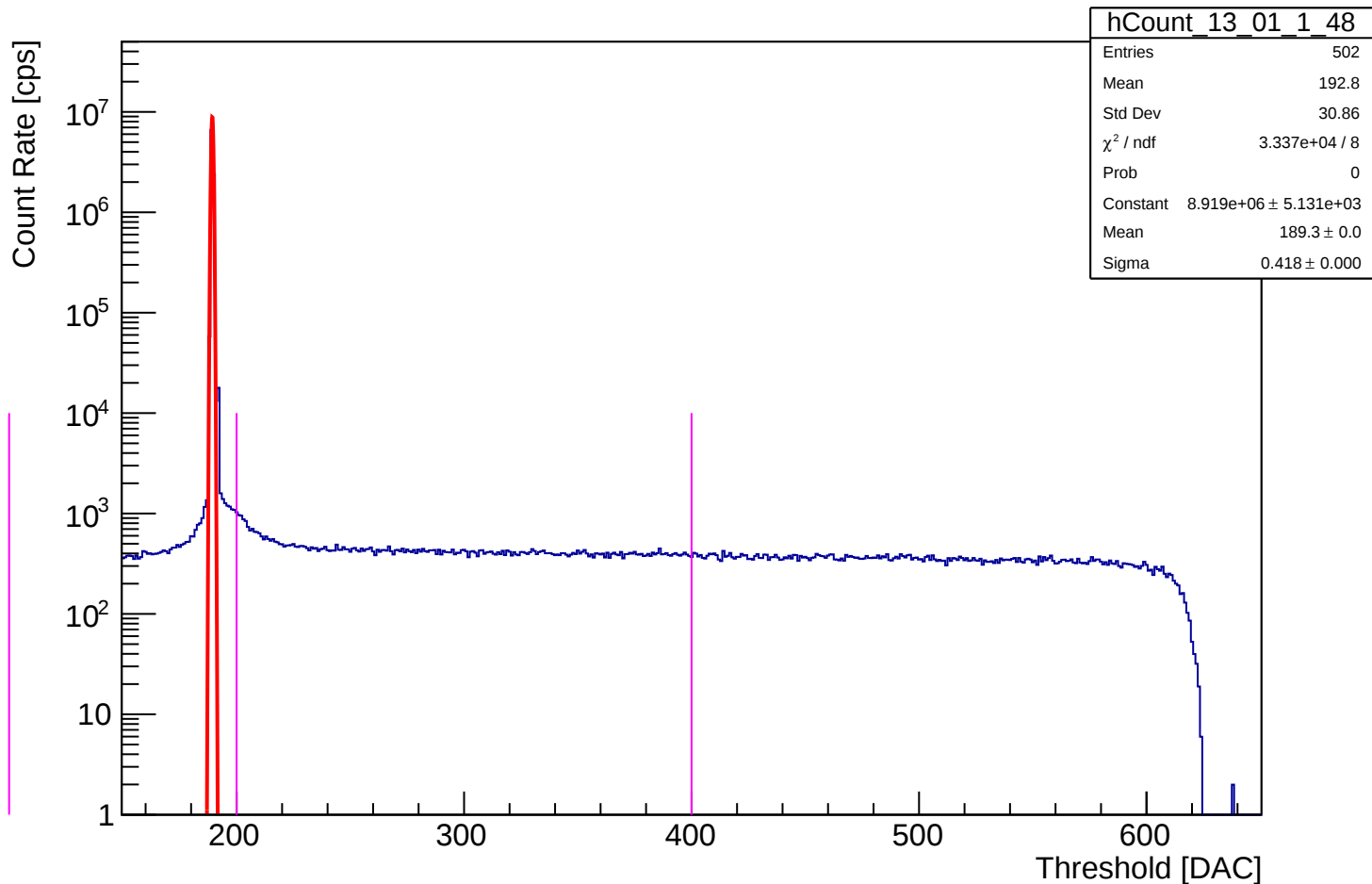
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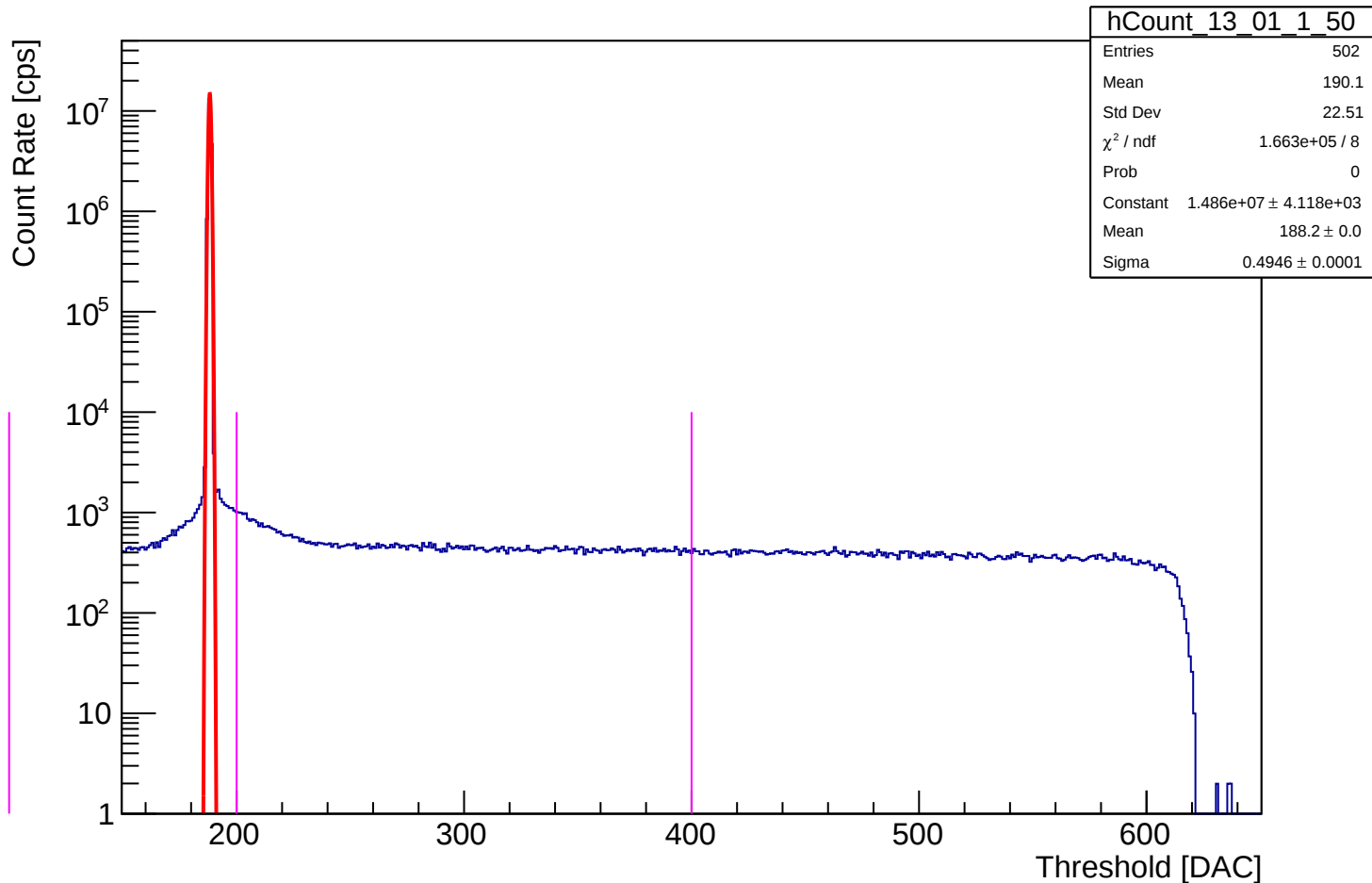
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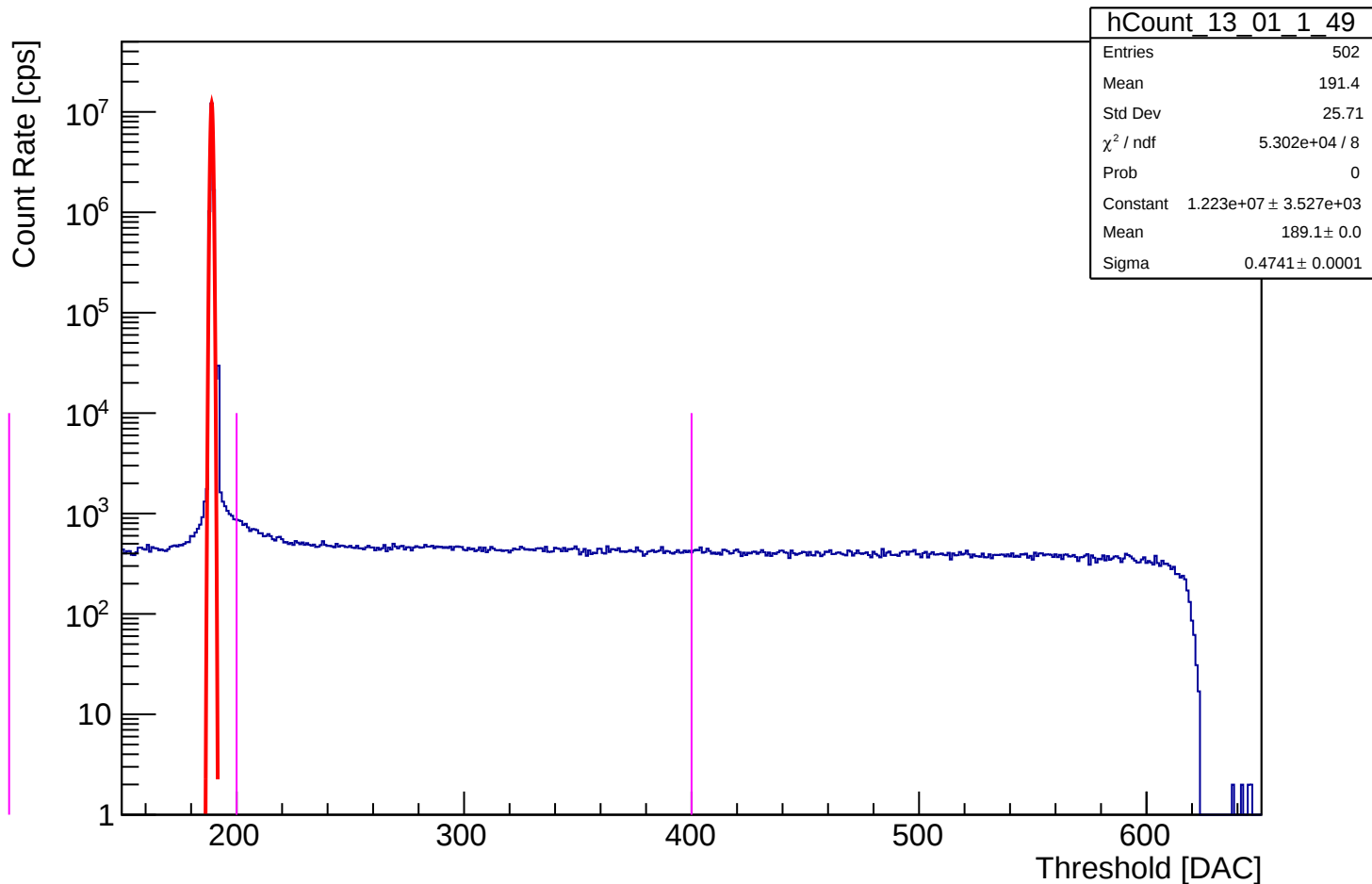
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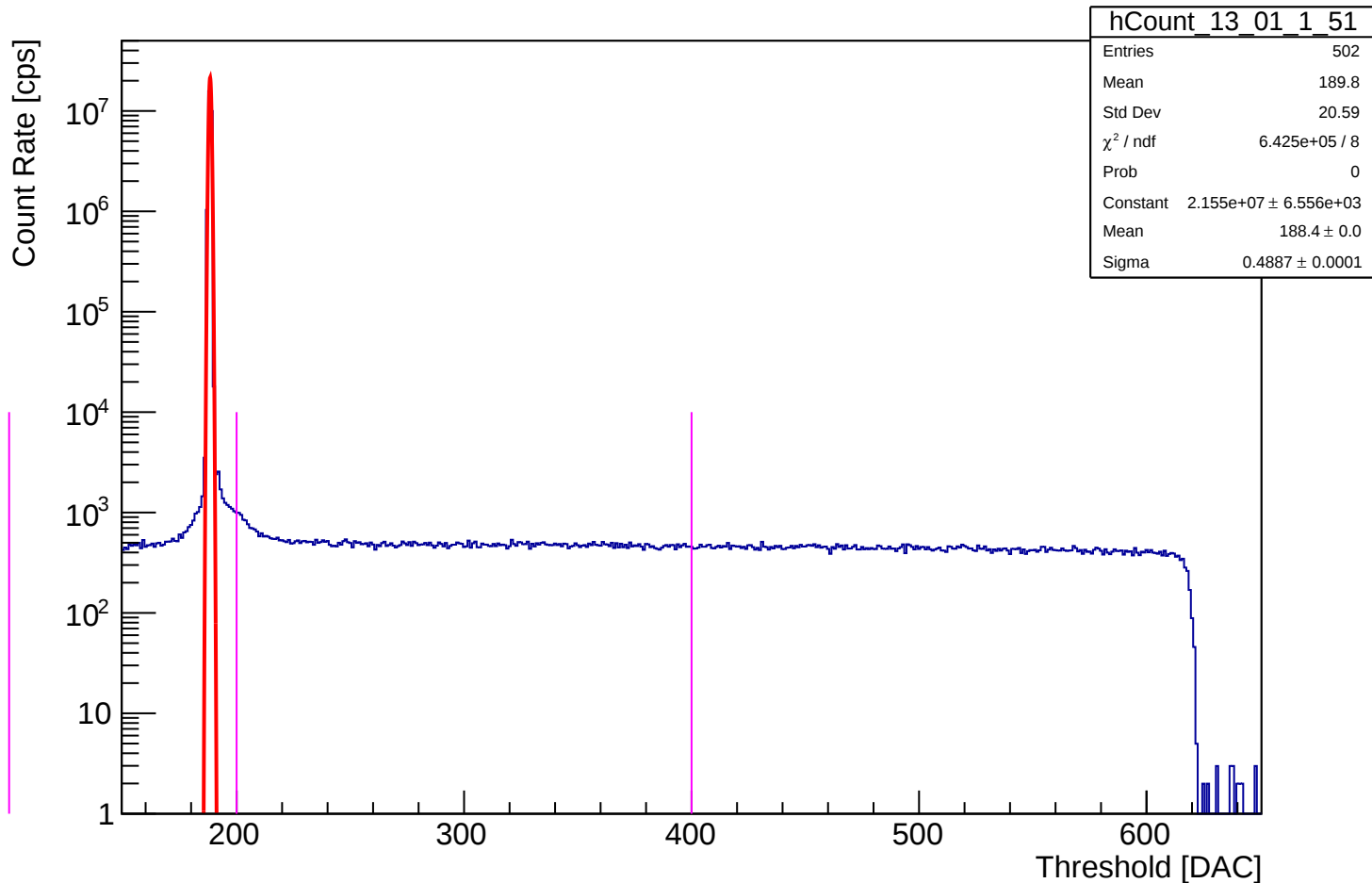
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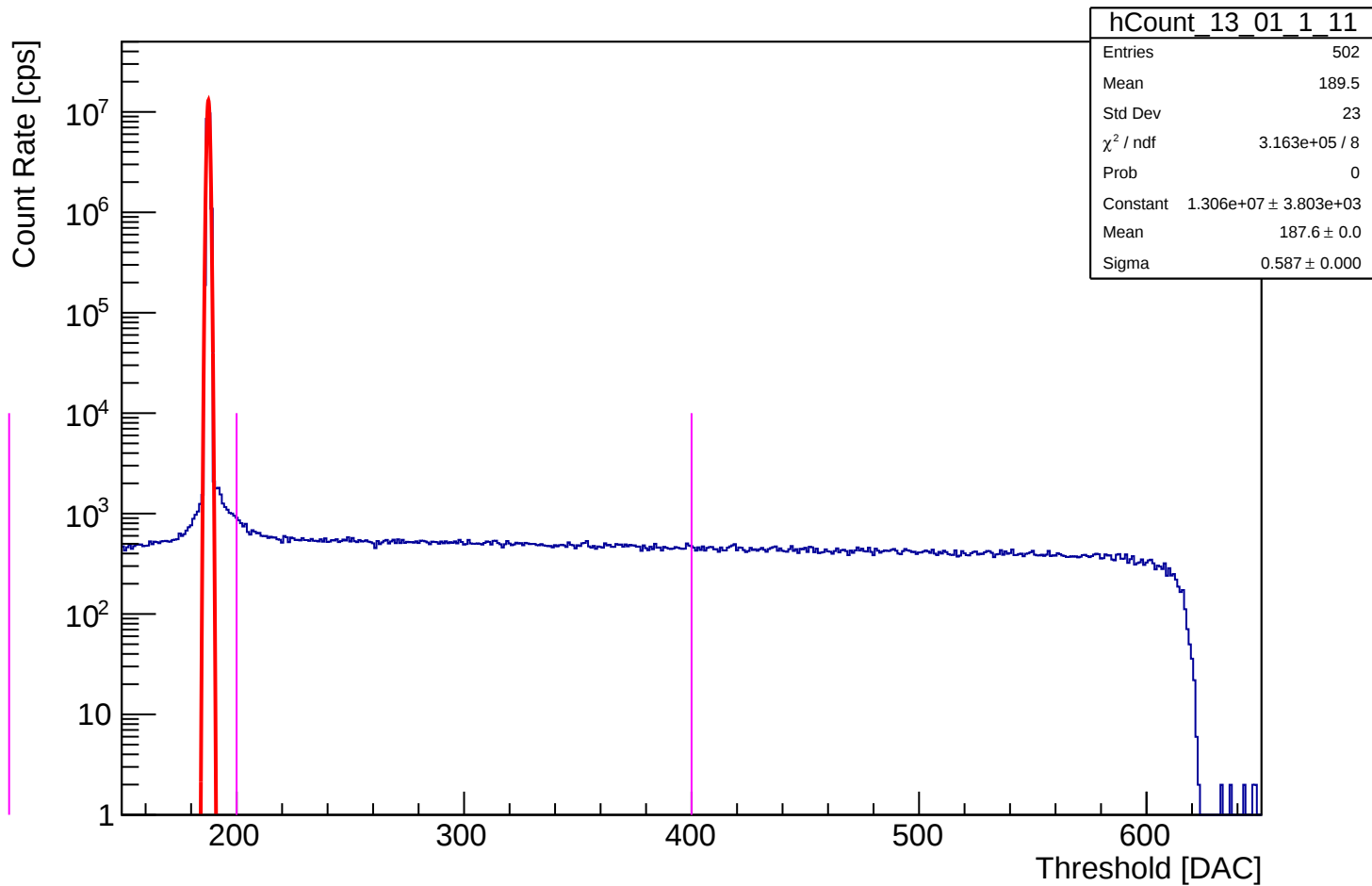
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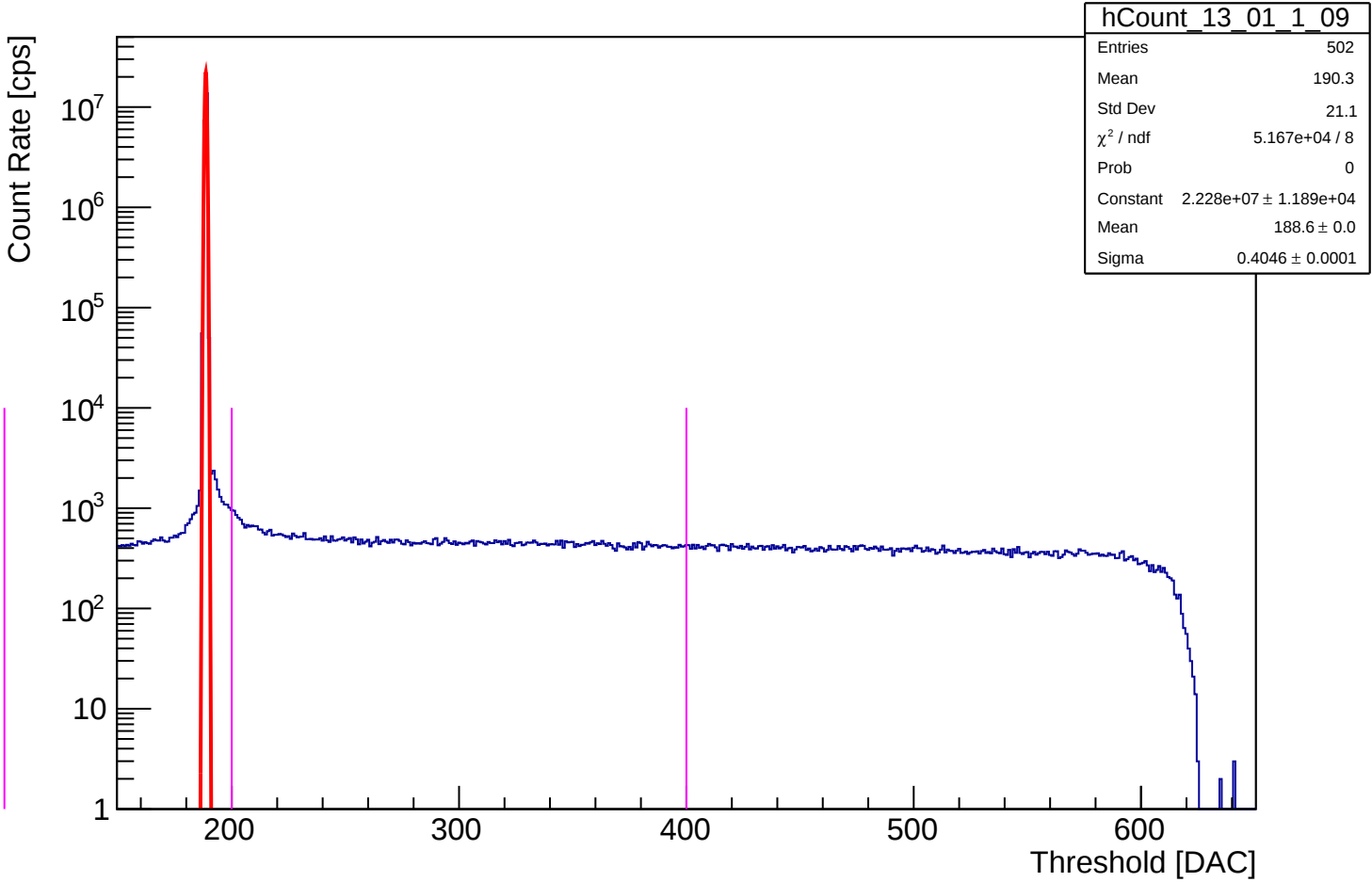


Row 1 Tile 1 Pmt 5 Pixel 40 Slot 13 Fiber 1 Asic 1 Channel 51

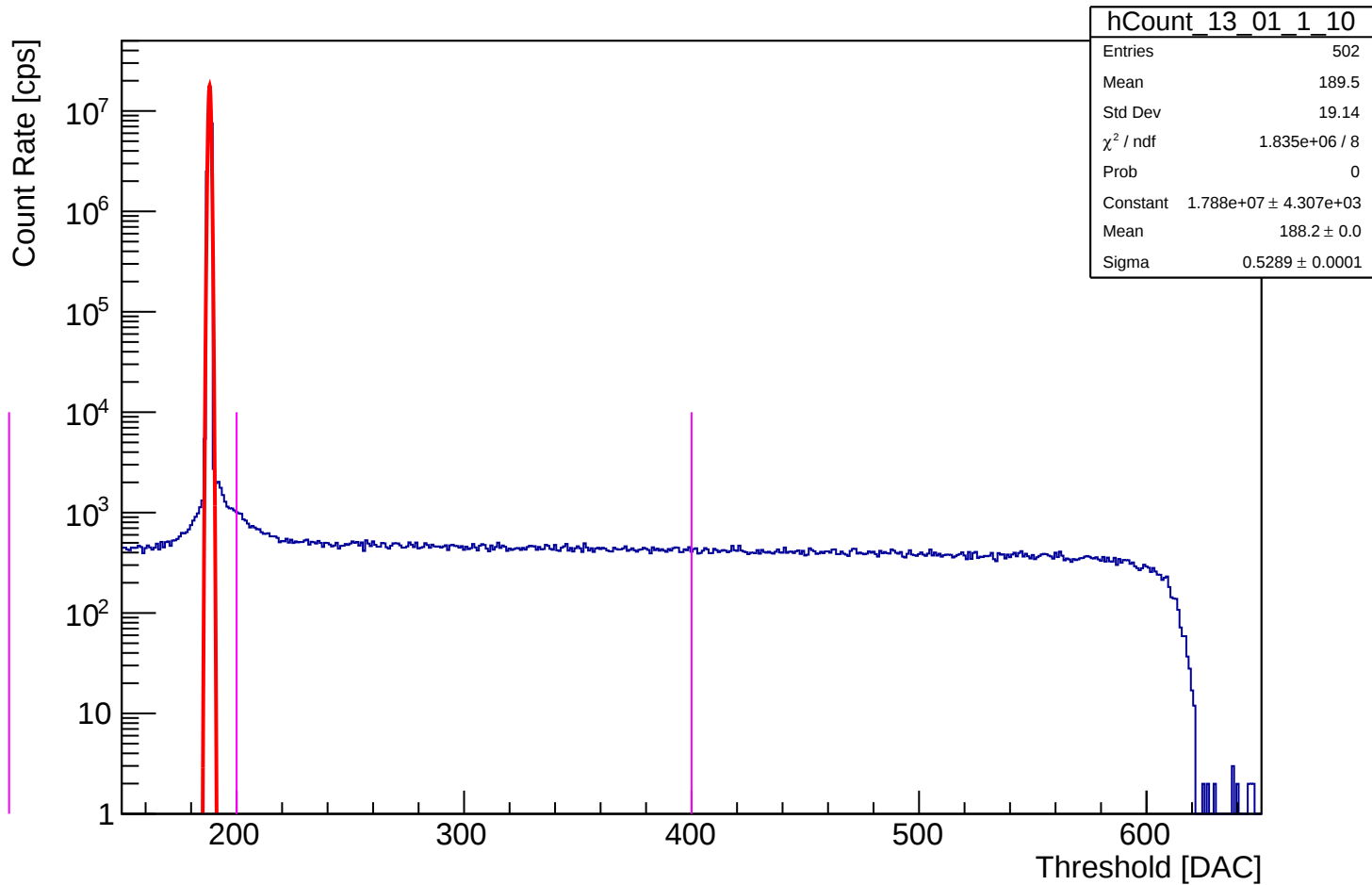


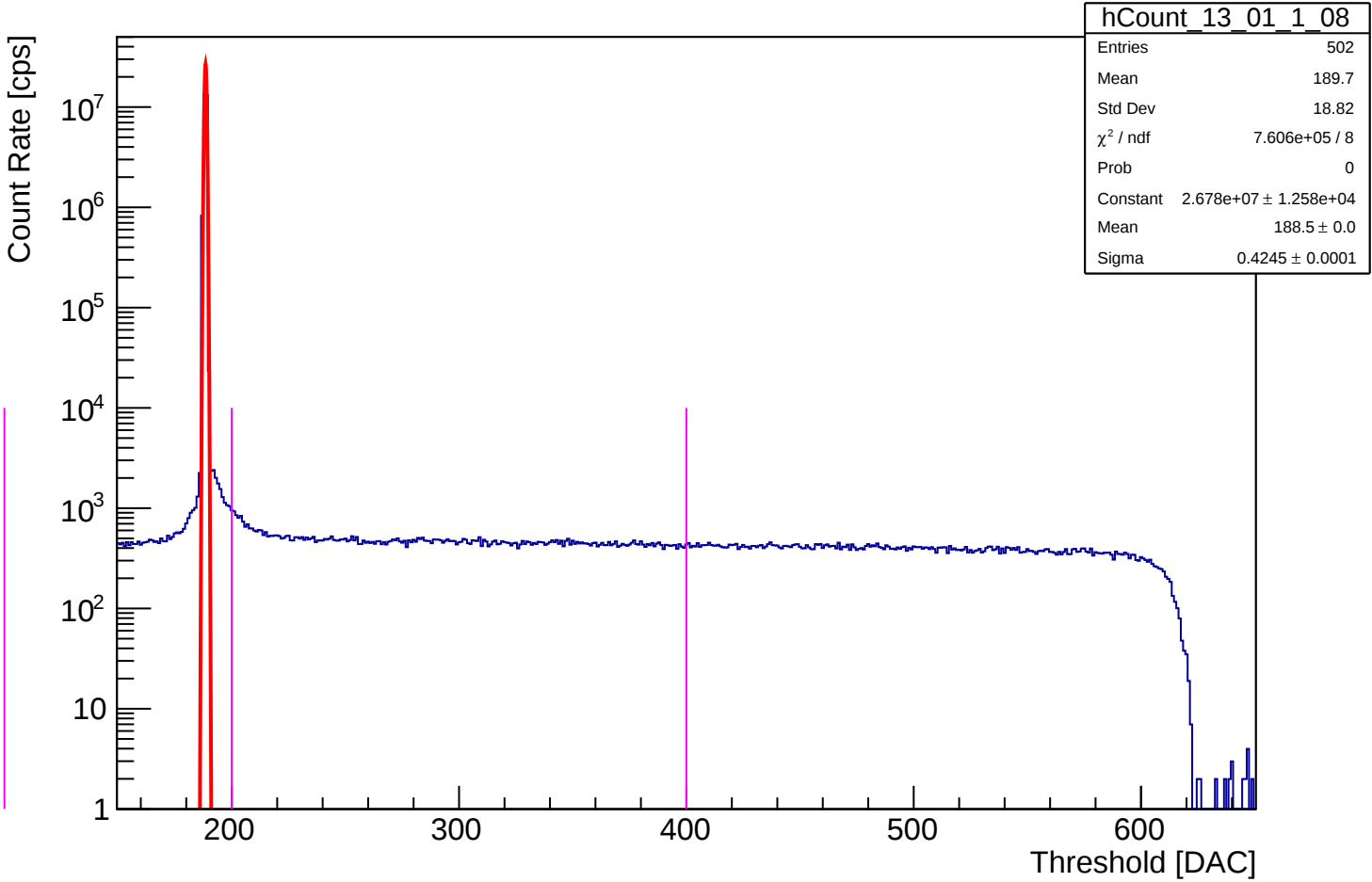
Row 1 Tile 1 Pmt 5 Pixel 41 Slot 13 Fiber 1 Asic 1 Channel 11



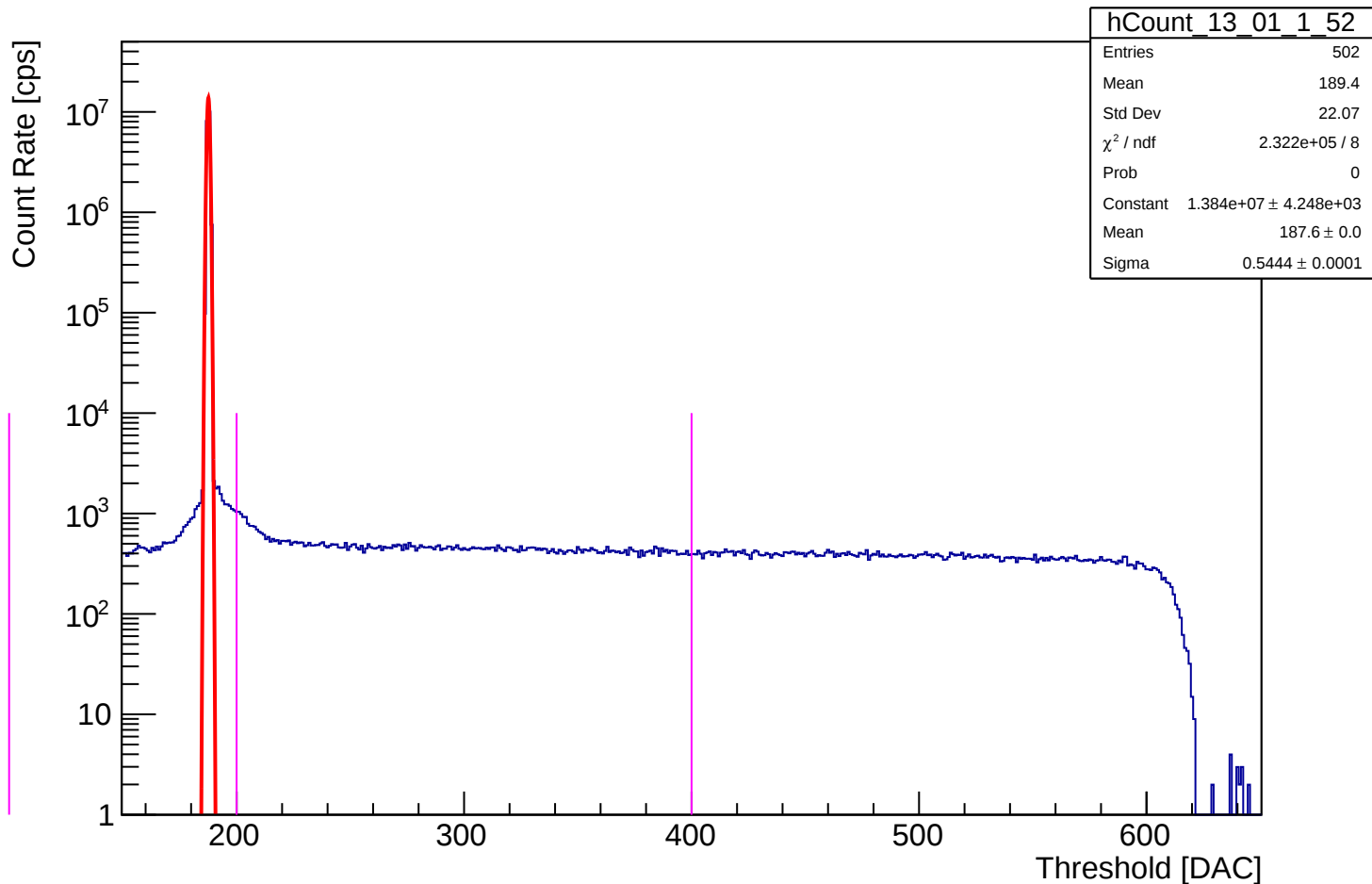


Row 1 Tile 1 Pmt 5 Pixel 43 Slot 13 Fiber 1 Asic 1 Channel 10

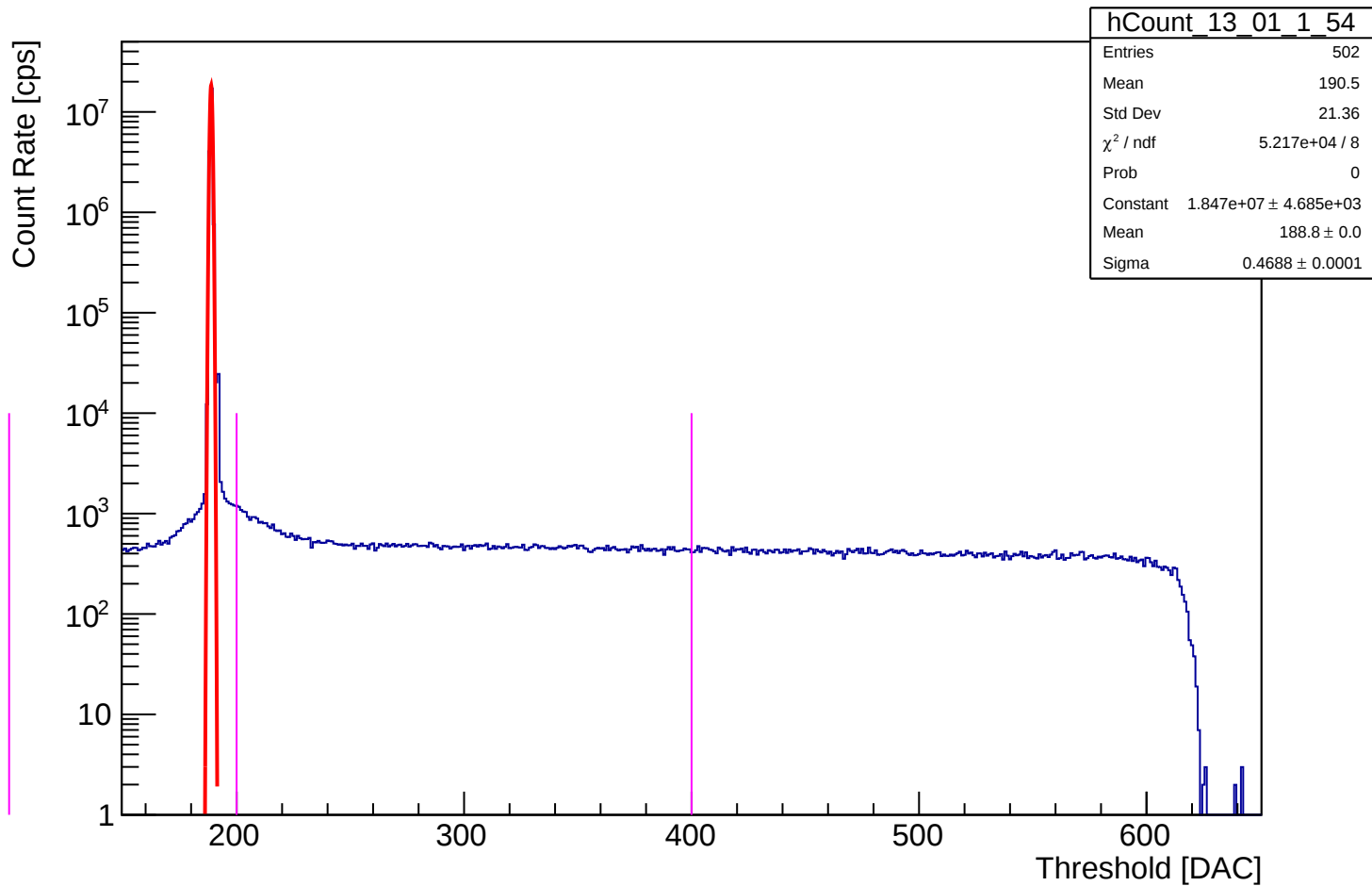




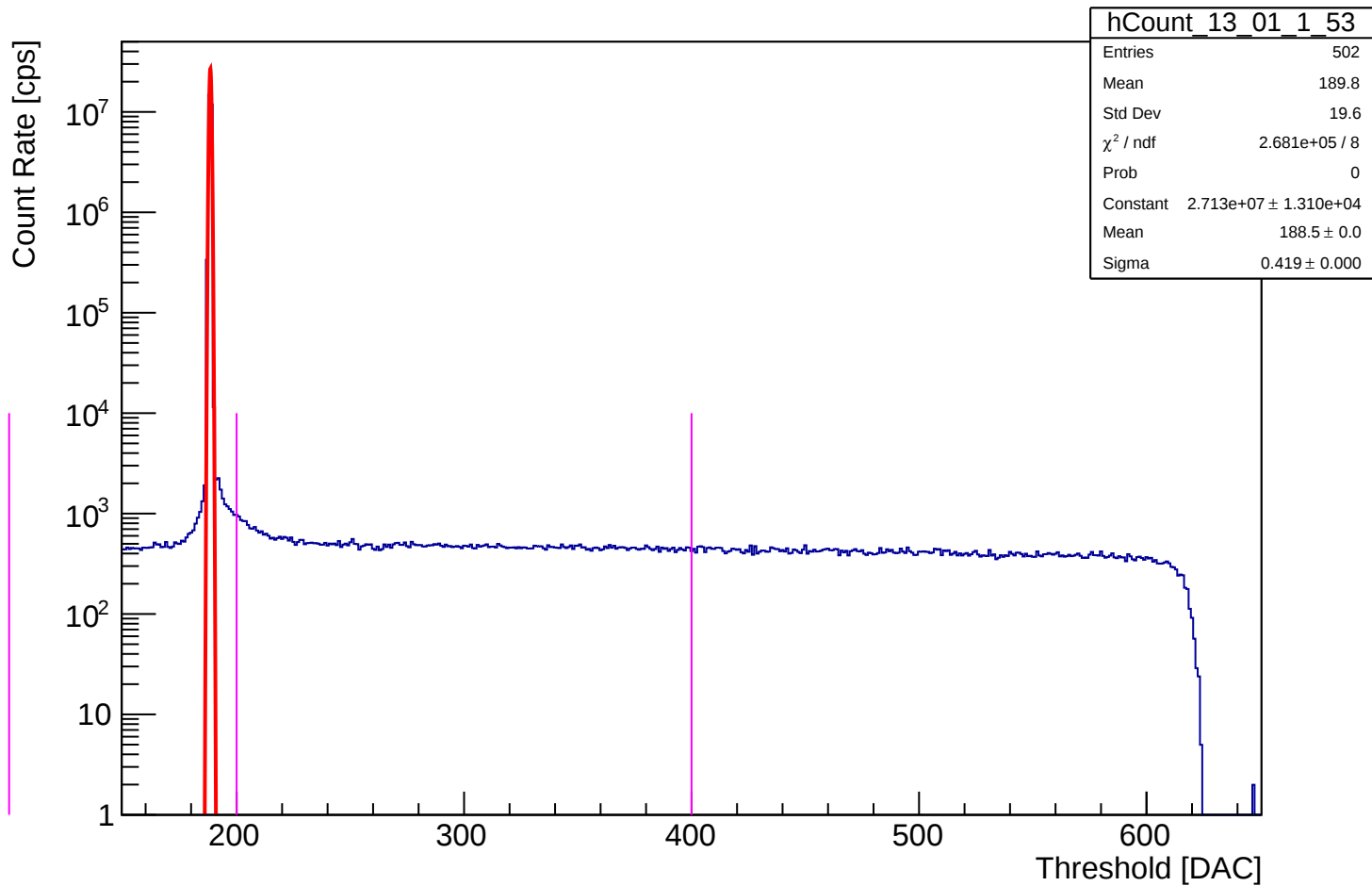
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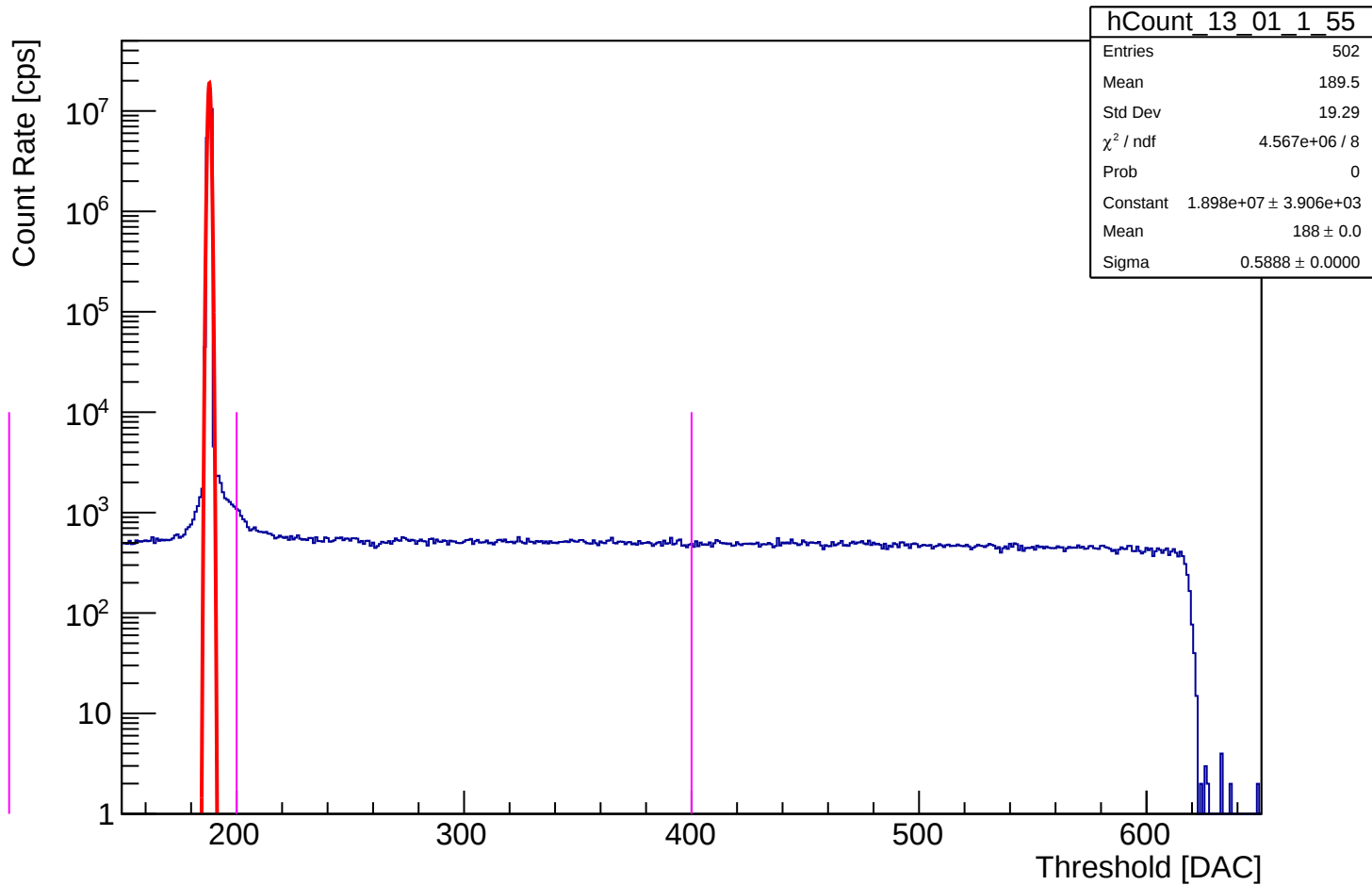
Row 1 Tile 1 Pmt 5 Pixel 46 Slot 13 Fiber 1 Asic 1 Channel 54

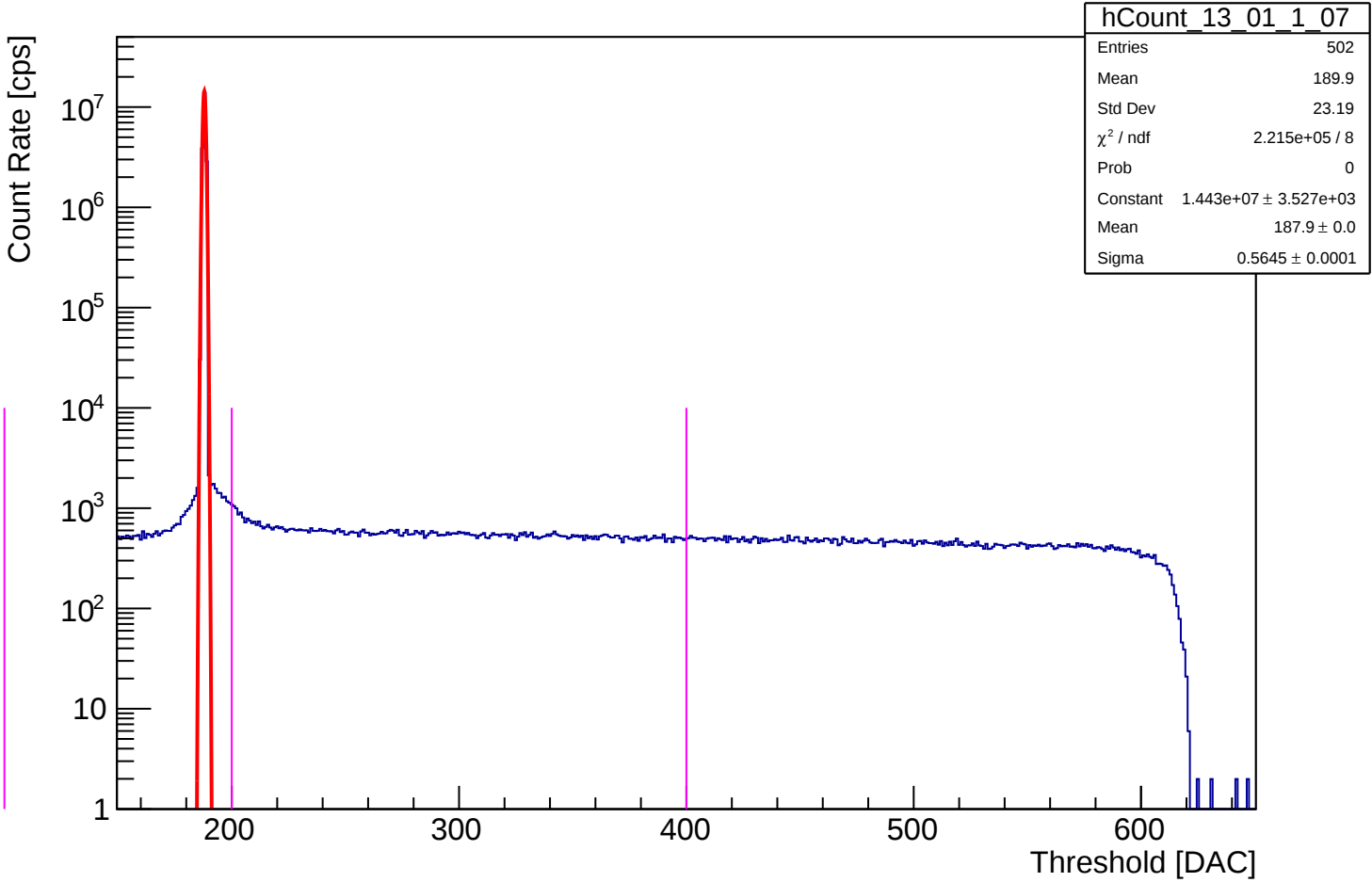


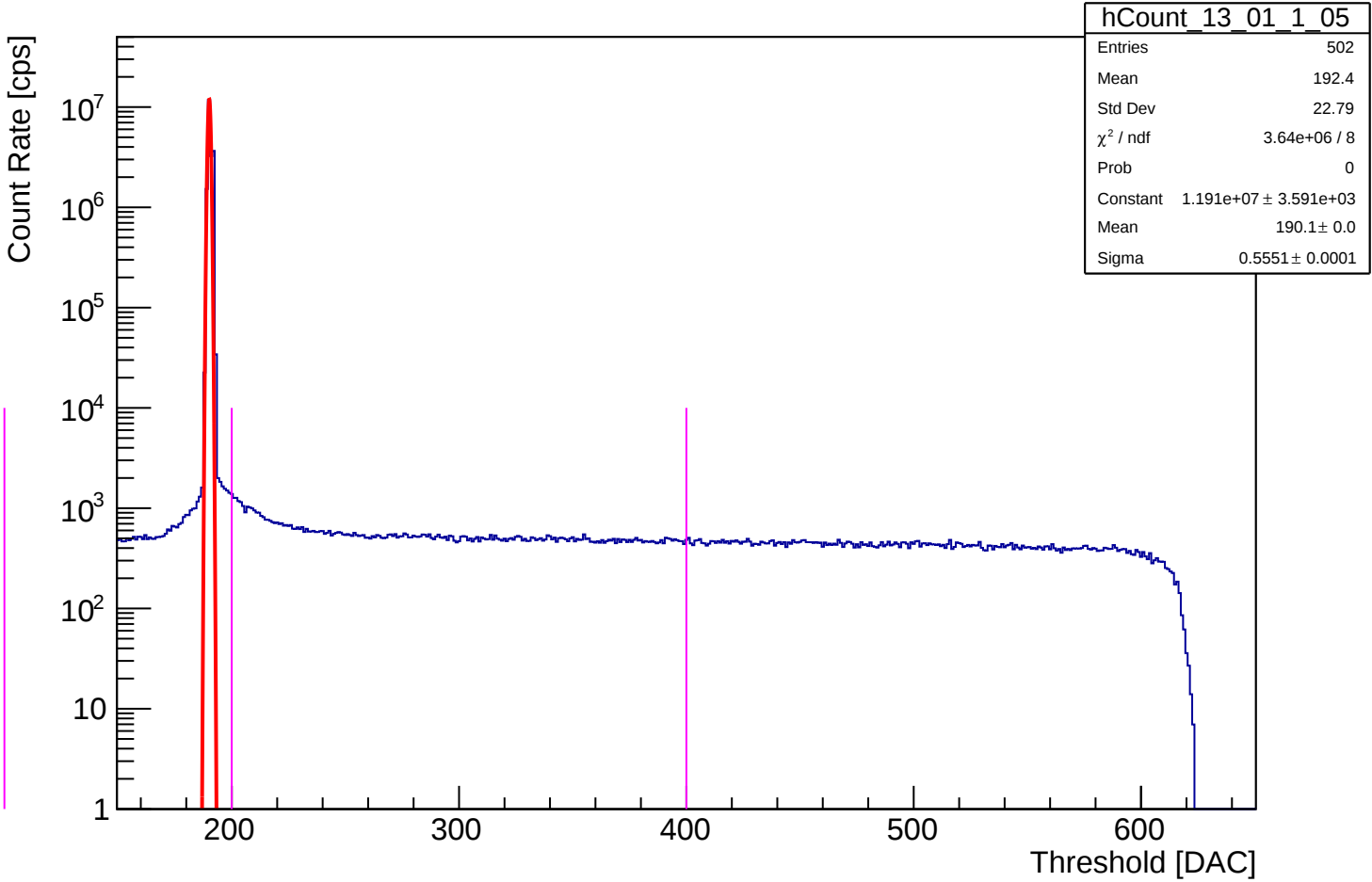
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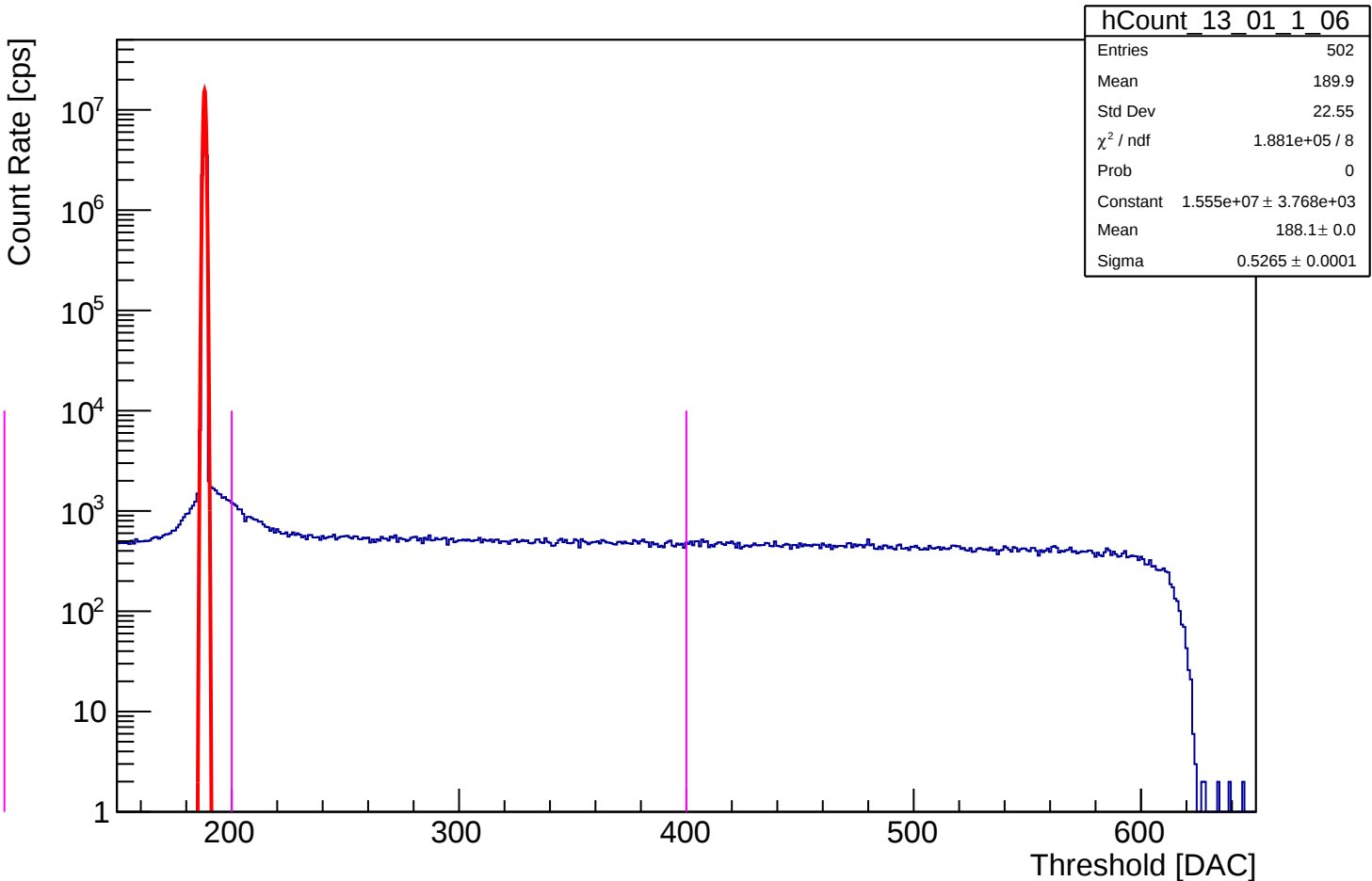


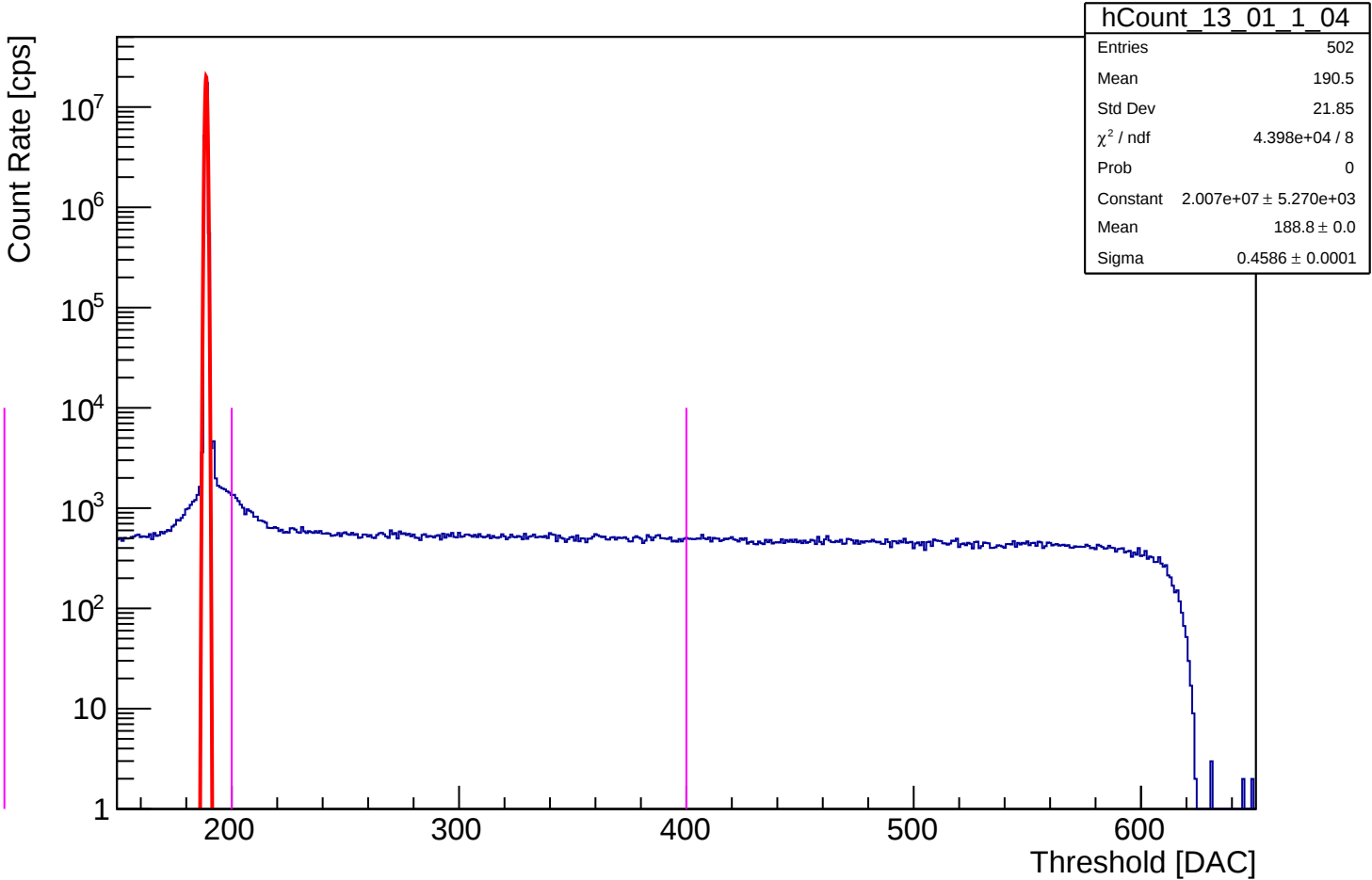
Row 1 Tile 1 Pmt 5 Pixel 48 Slot 13 Fiber 1 Asic 1 Channel 55

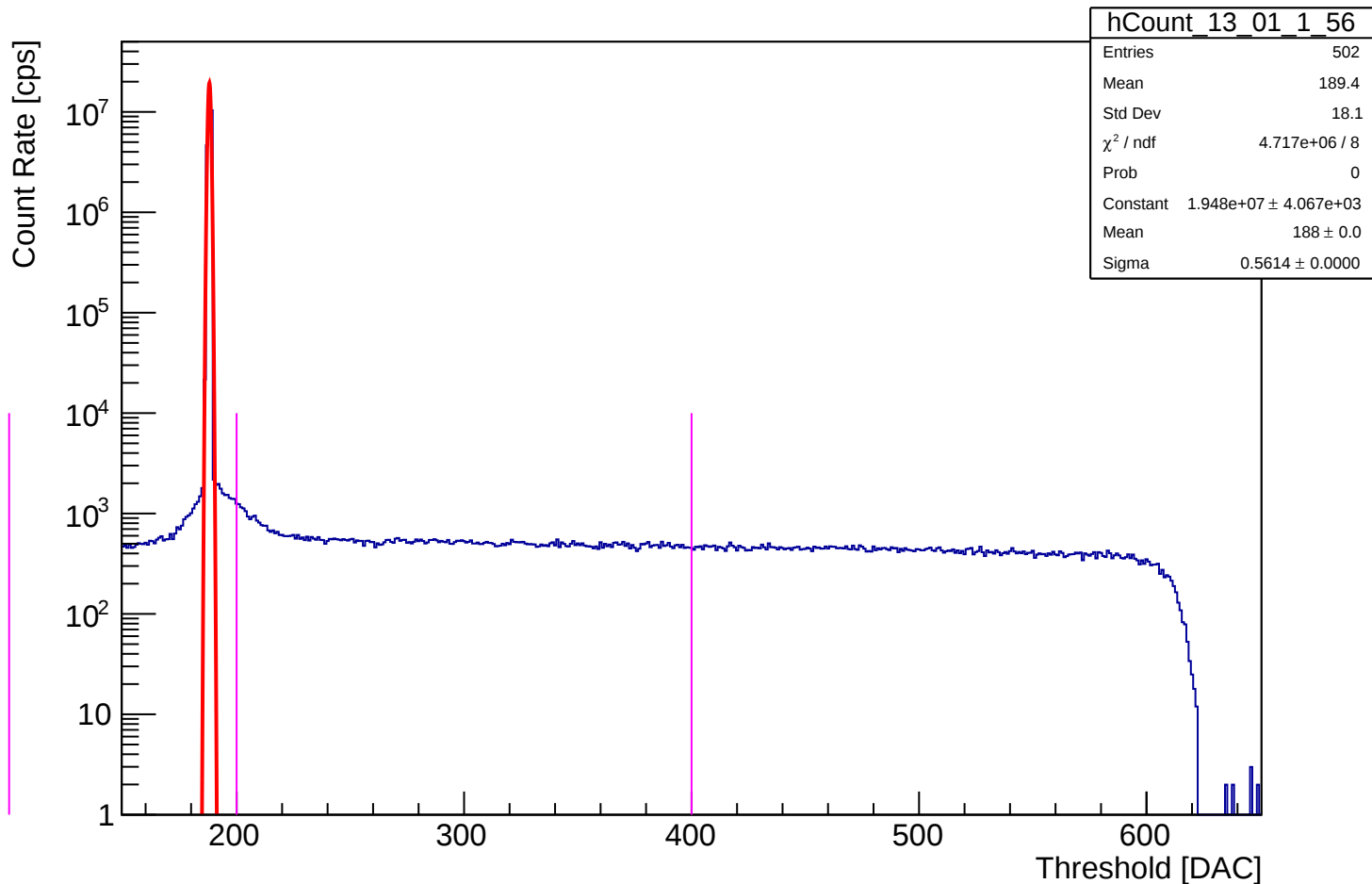




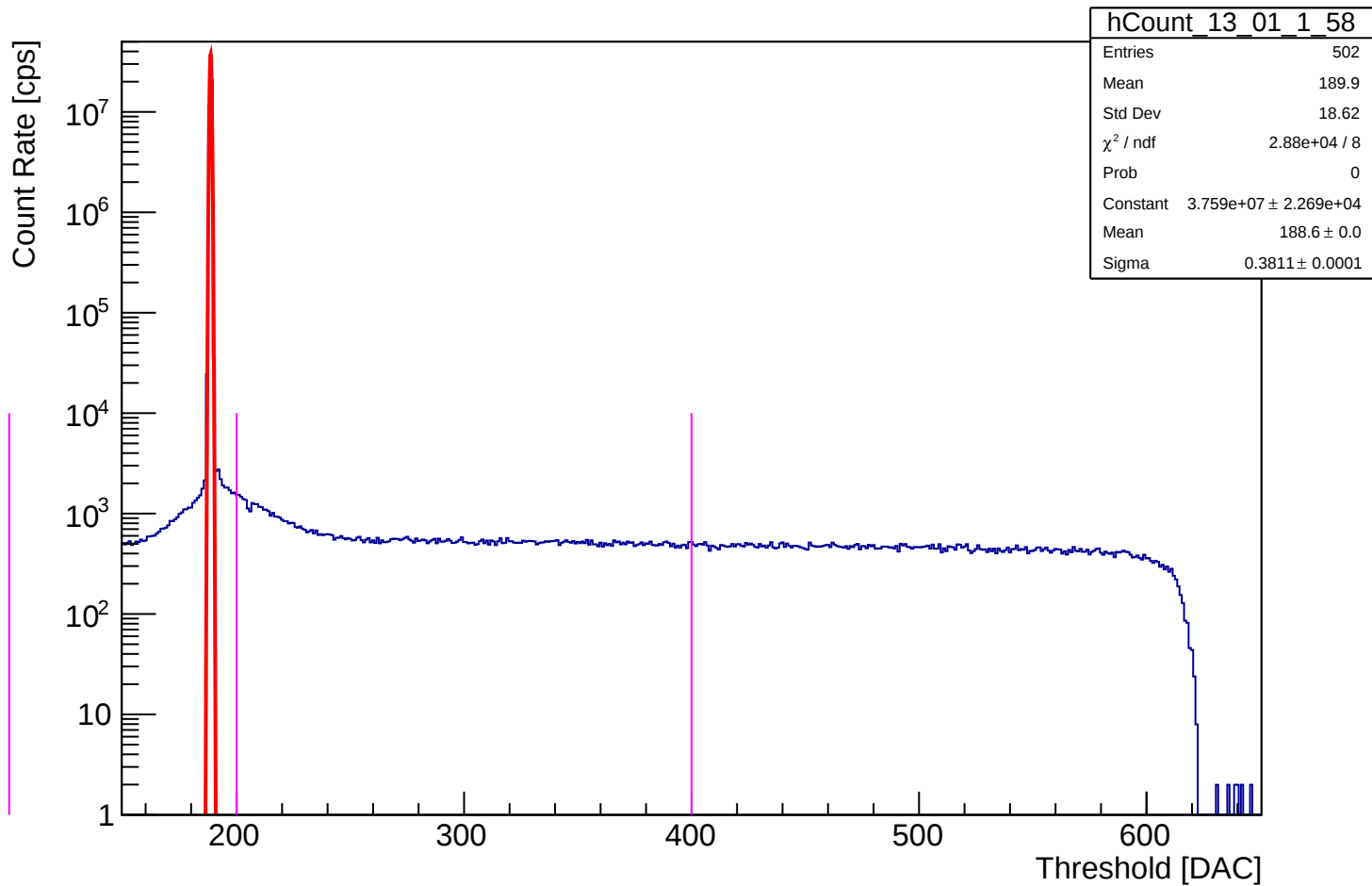




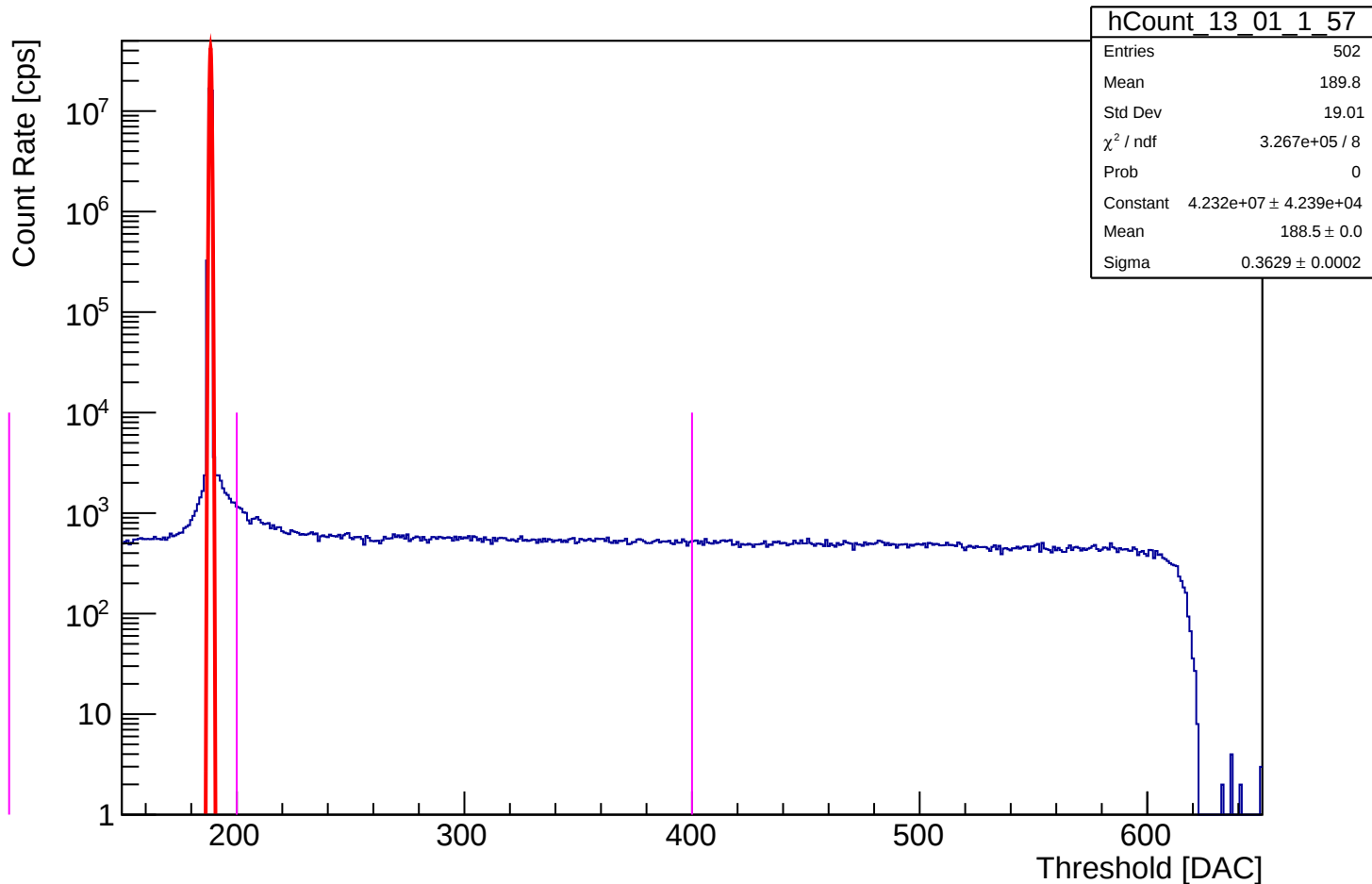




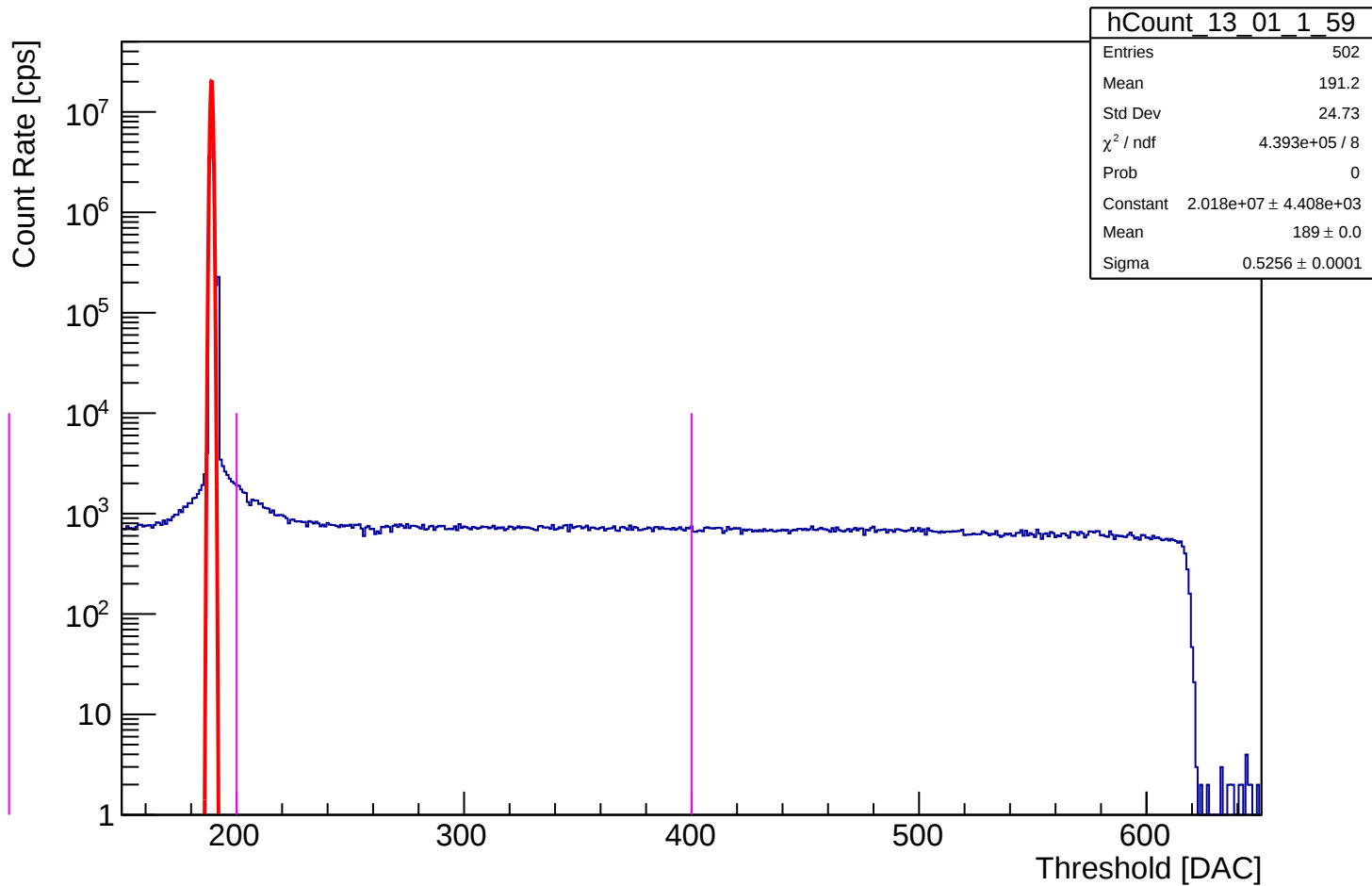
Row 1 Tile 1 Pmt 5 Pixel 54 Slot 13 Fiber 1 Asic 1 Channel 58

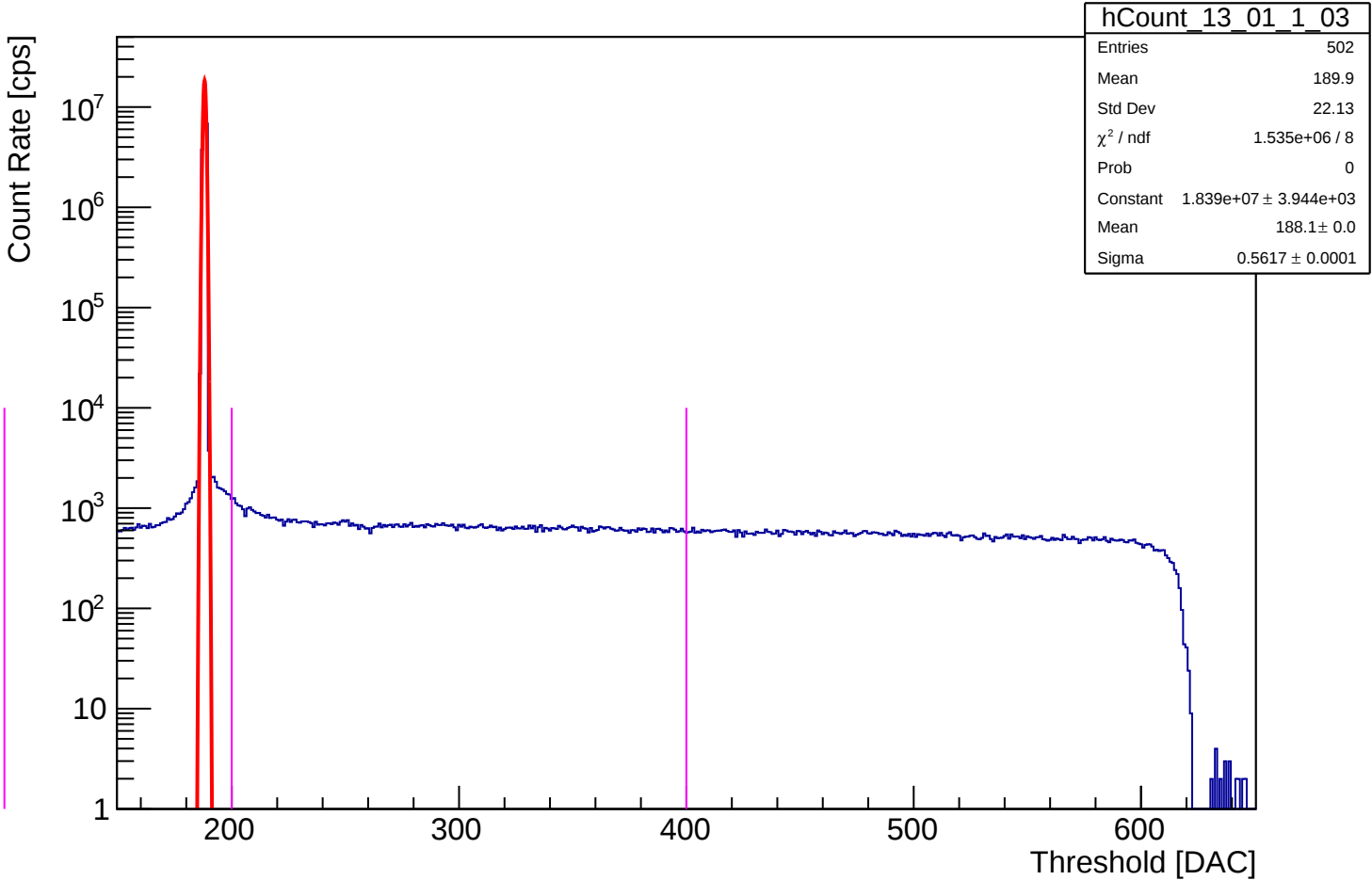


Row 1 Tile 1 Pmt 5 Pixel 55 Slot 13 Fiber 1 Asic 1 Channel 57

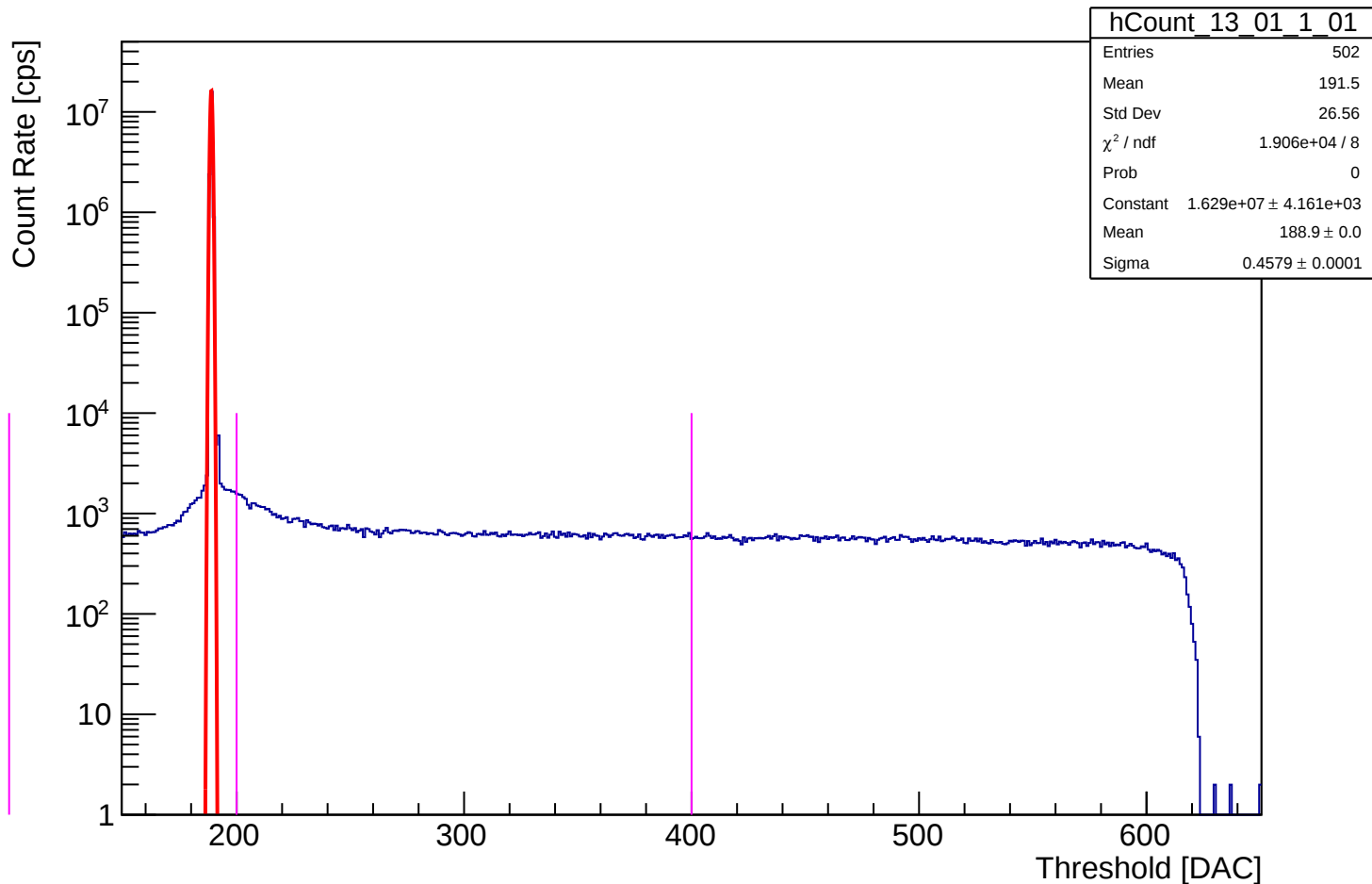


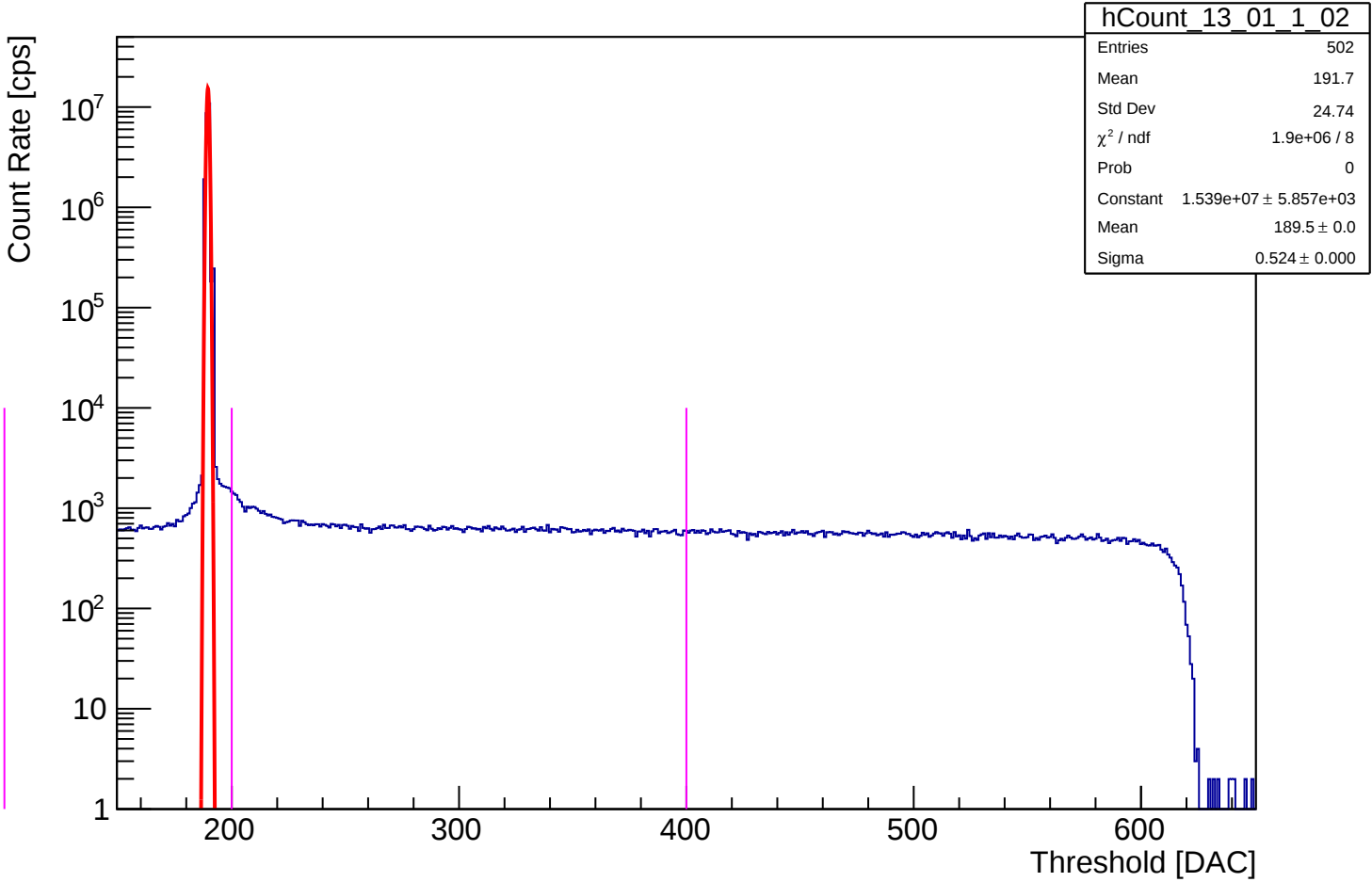
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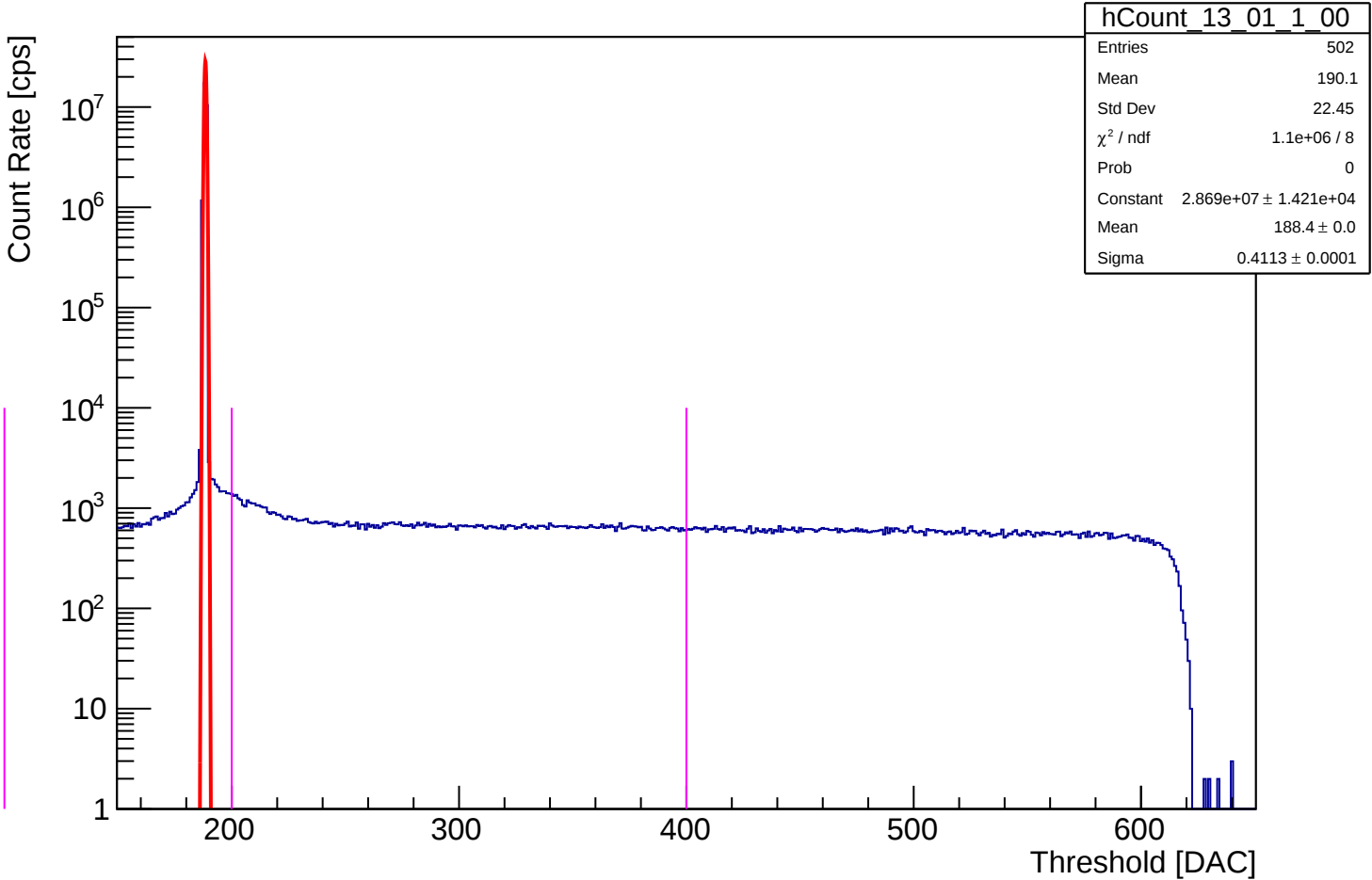




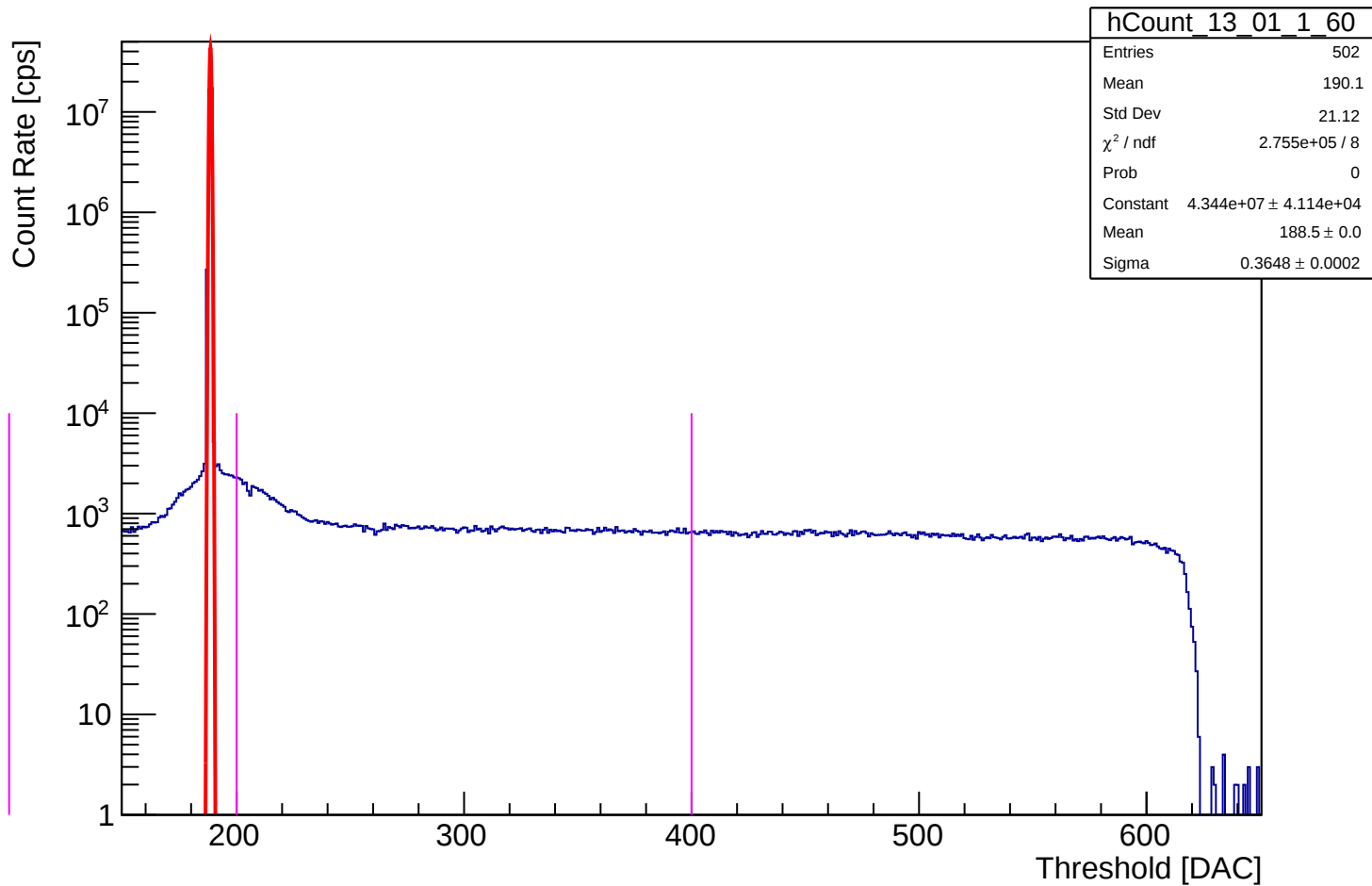
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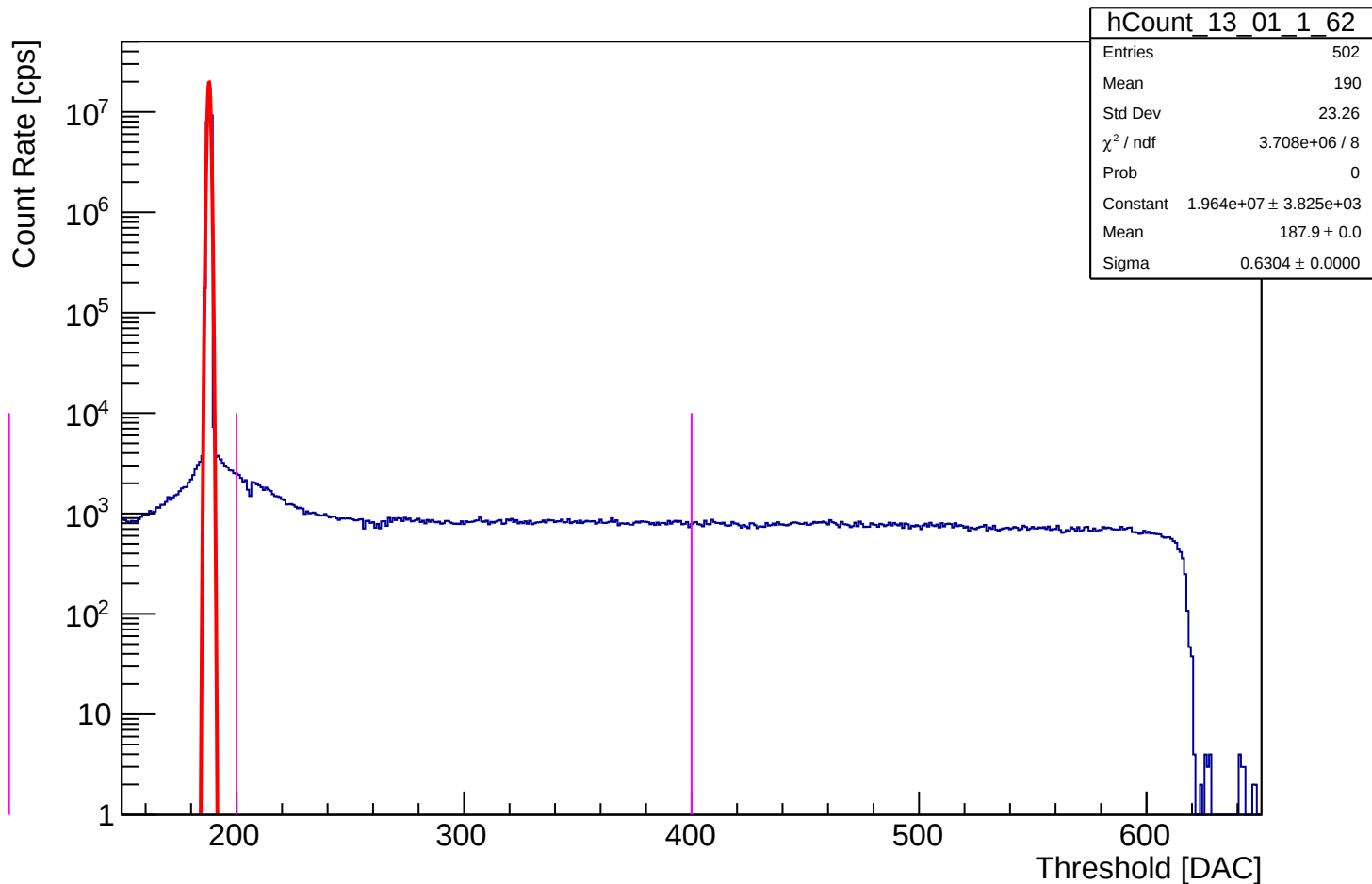




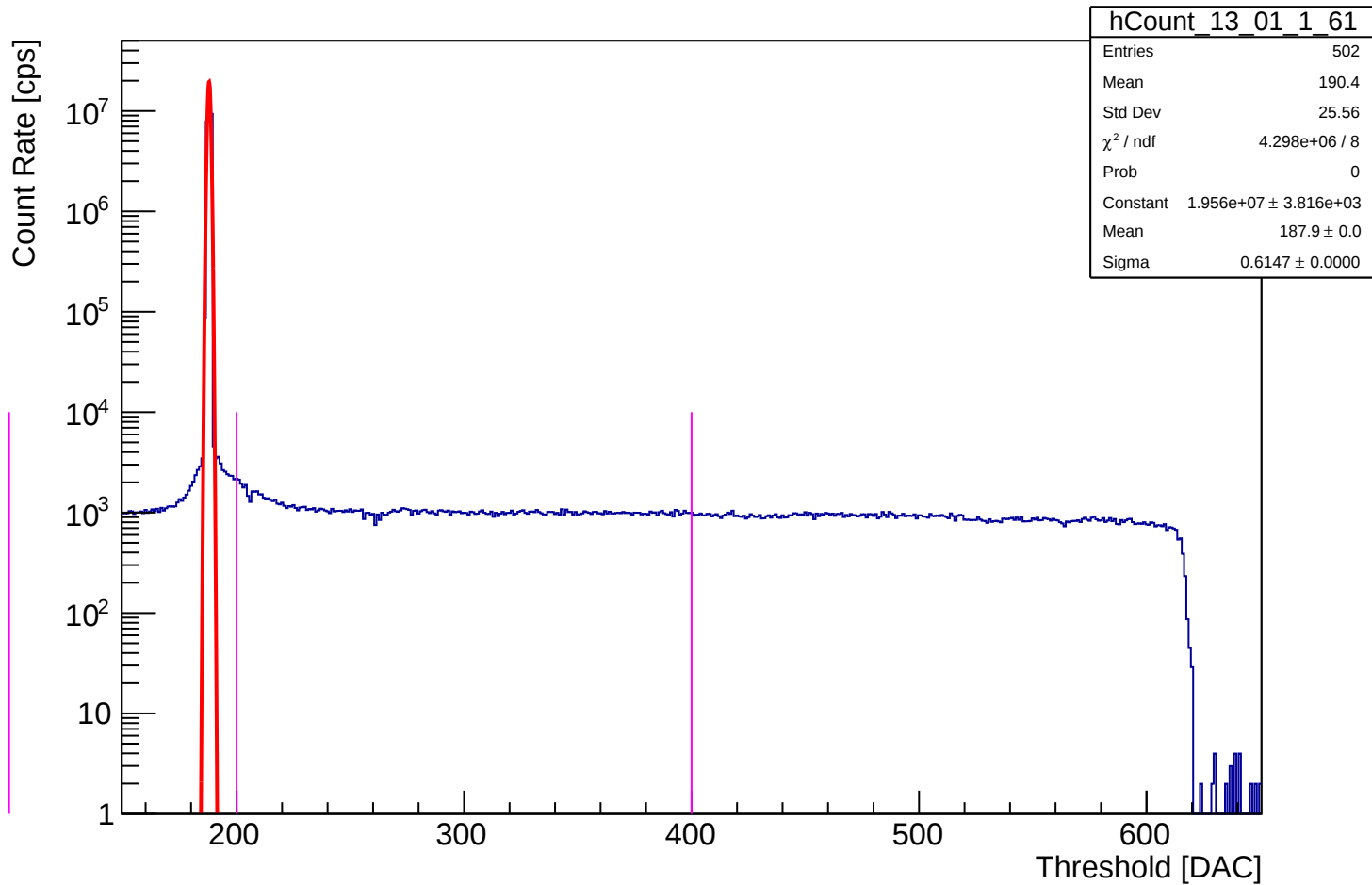
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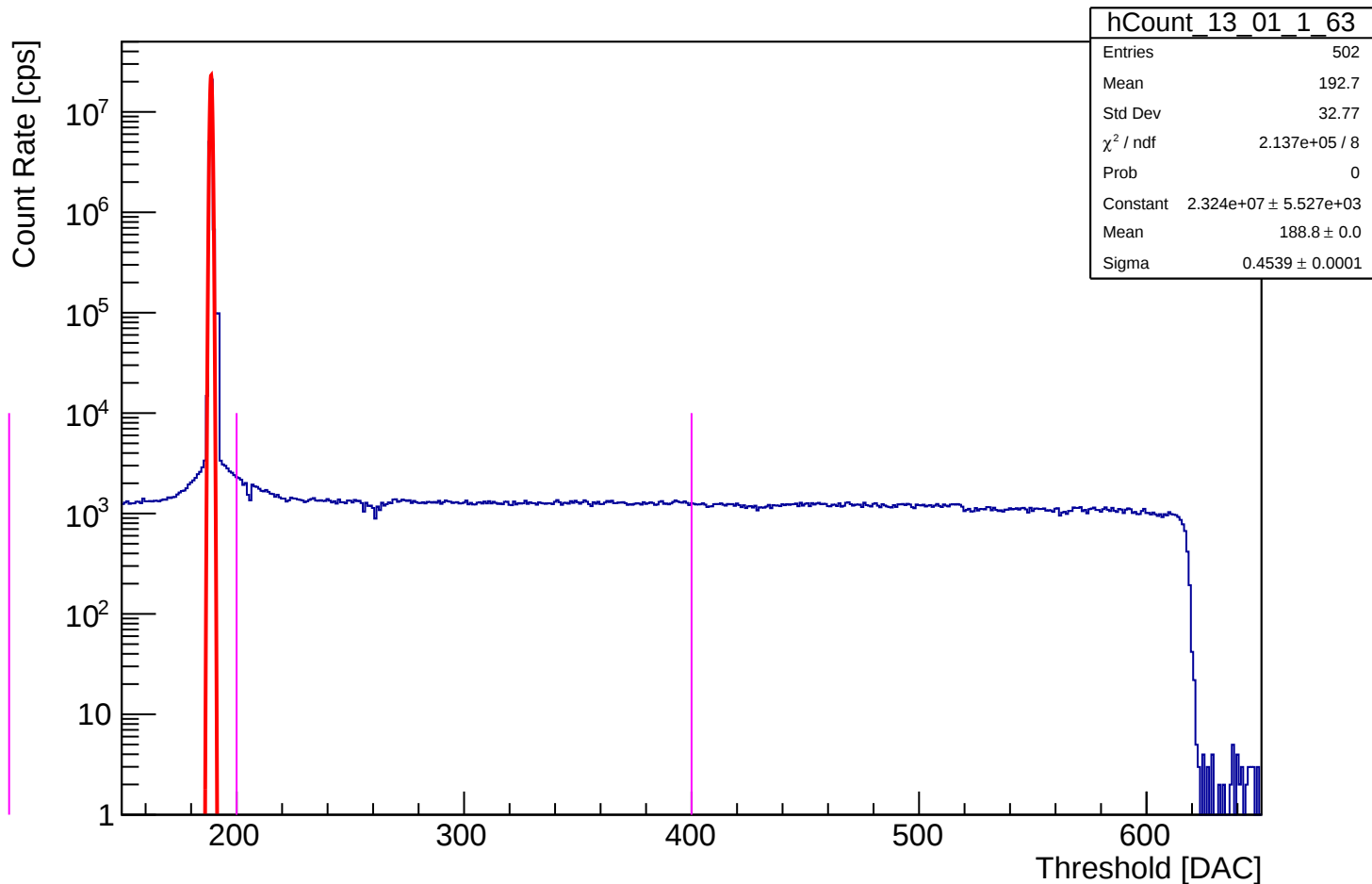
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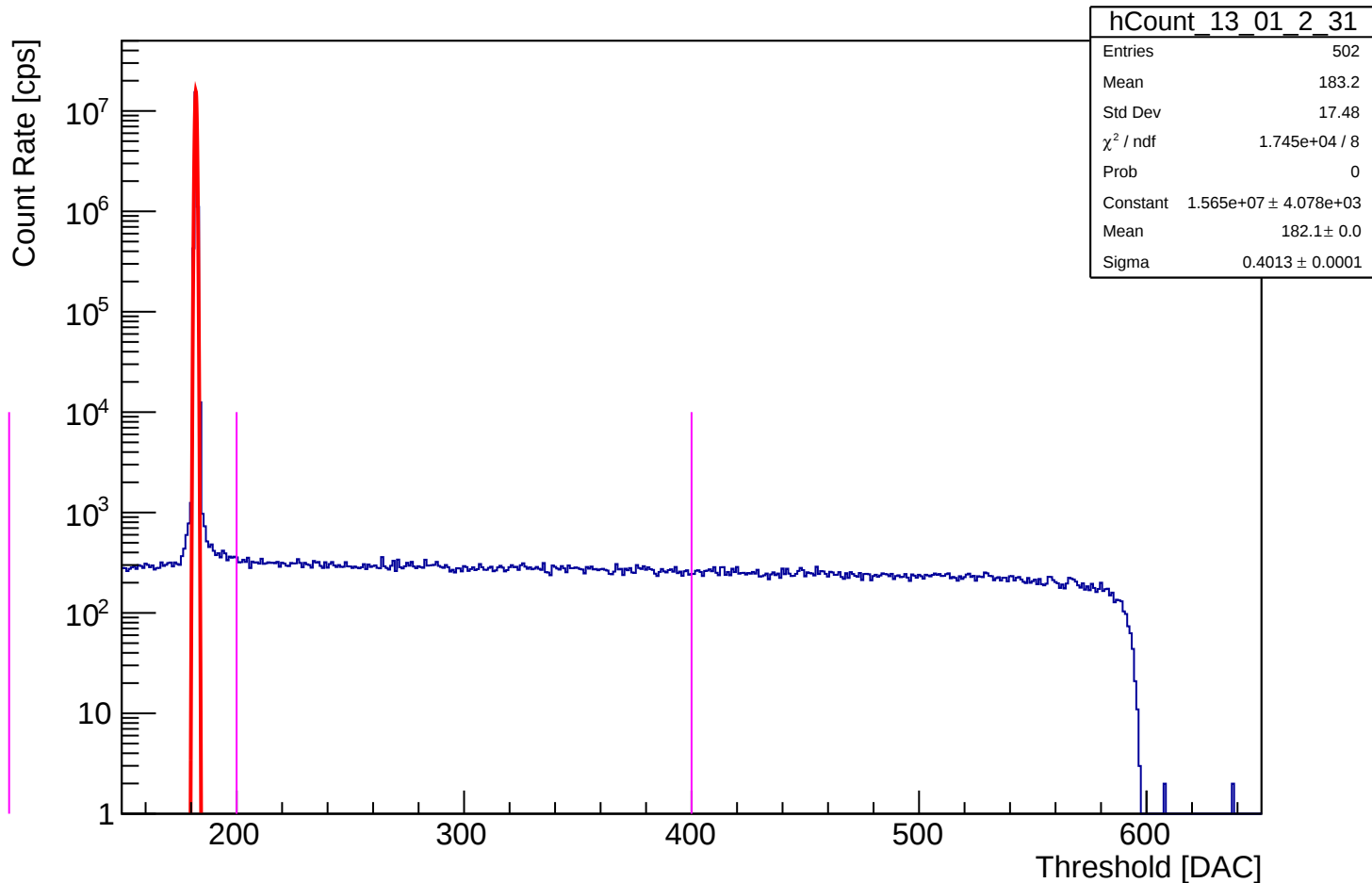
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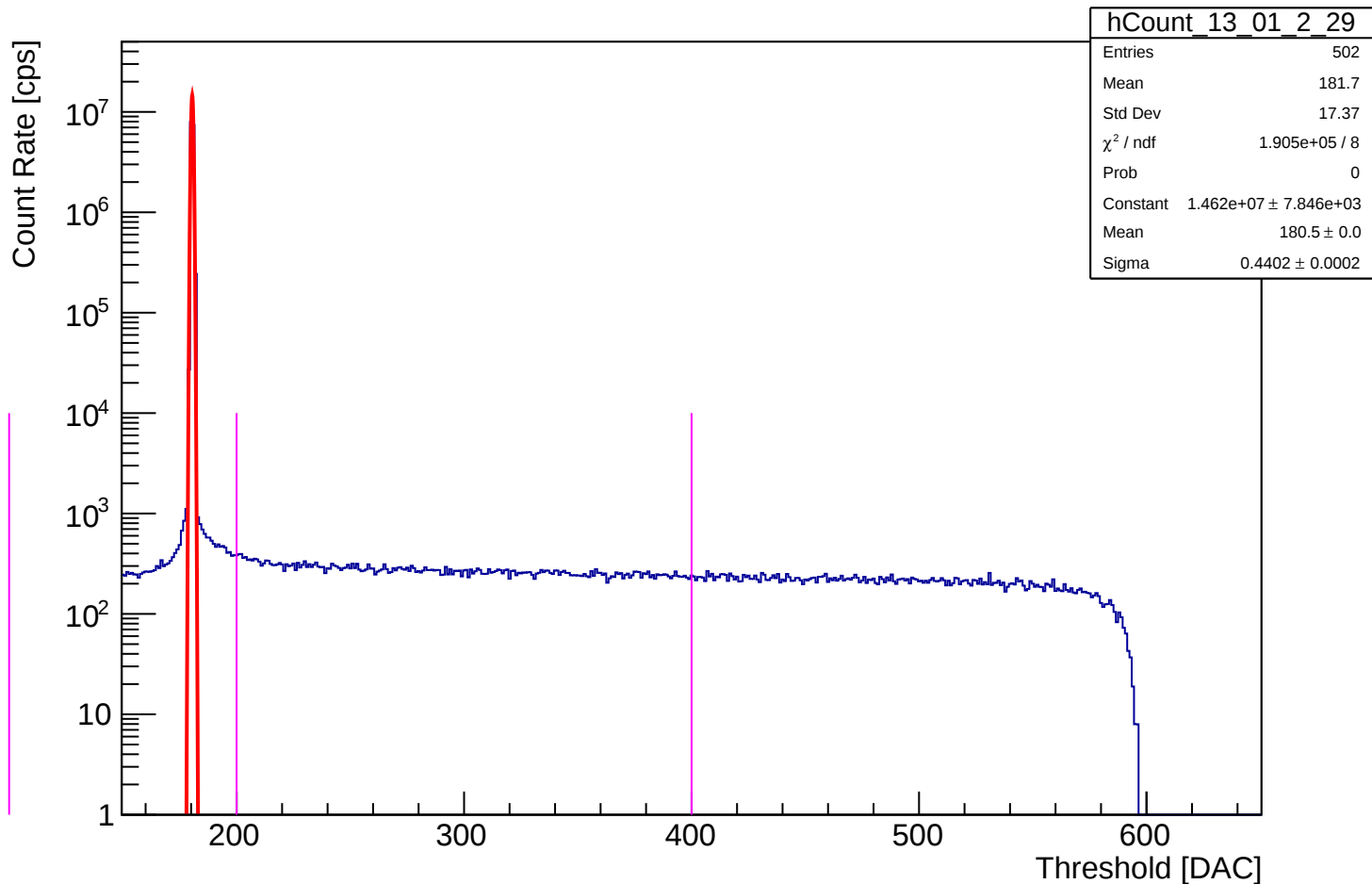
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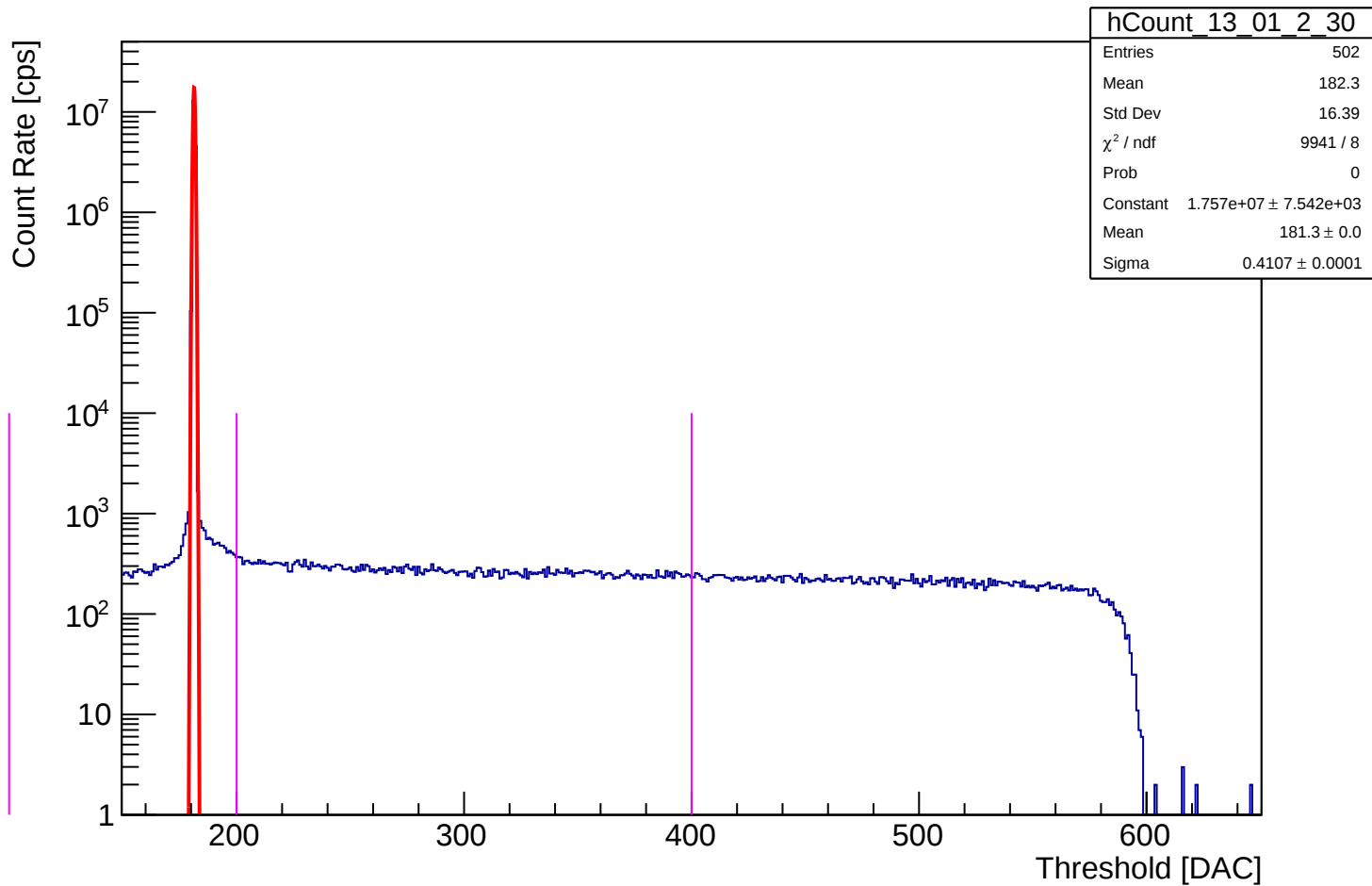
Row 1 Tile 1 Pmt 6 Pixel 1 Slot 13 Fiber 1 Asic 2 Channel 31



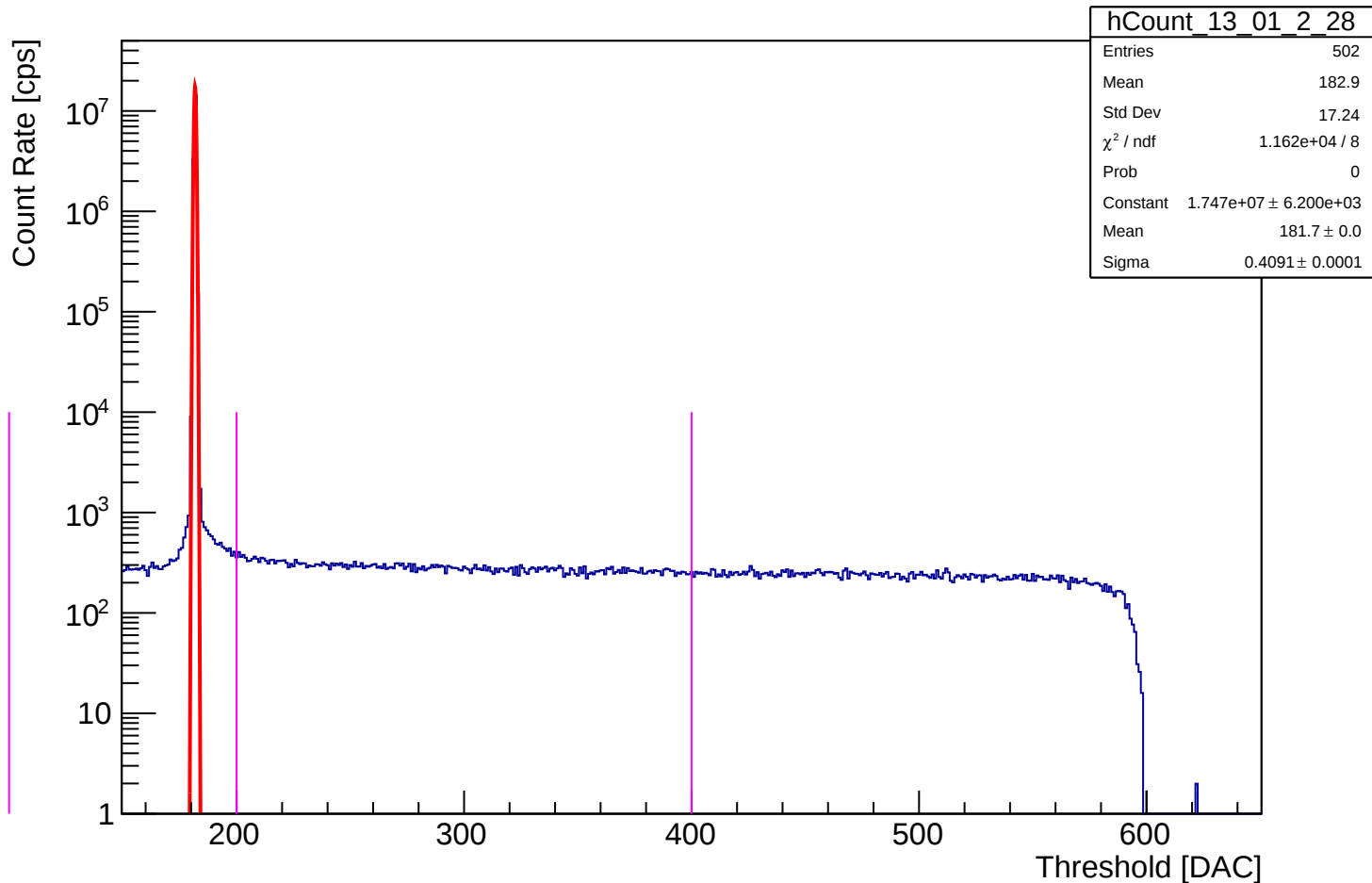
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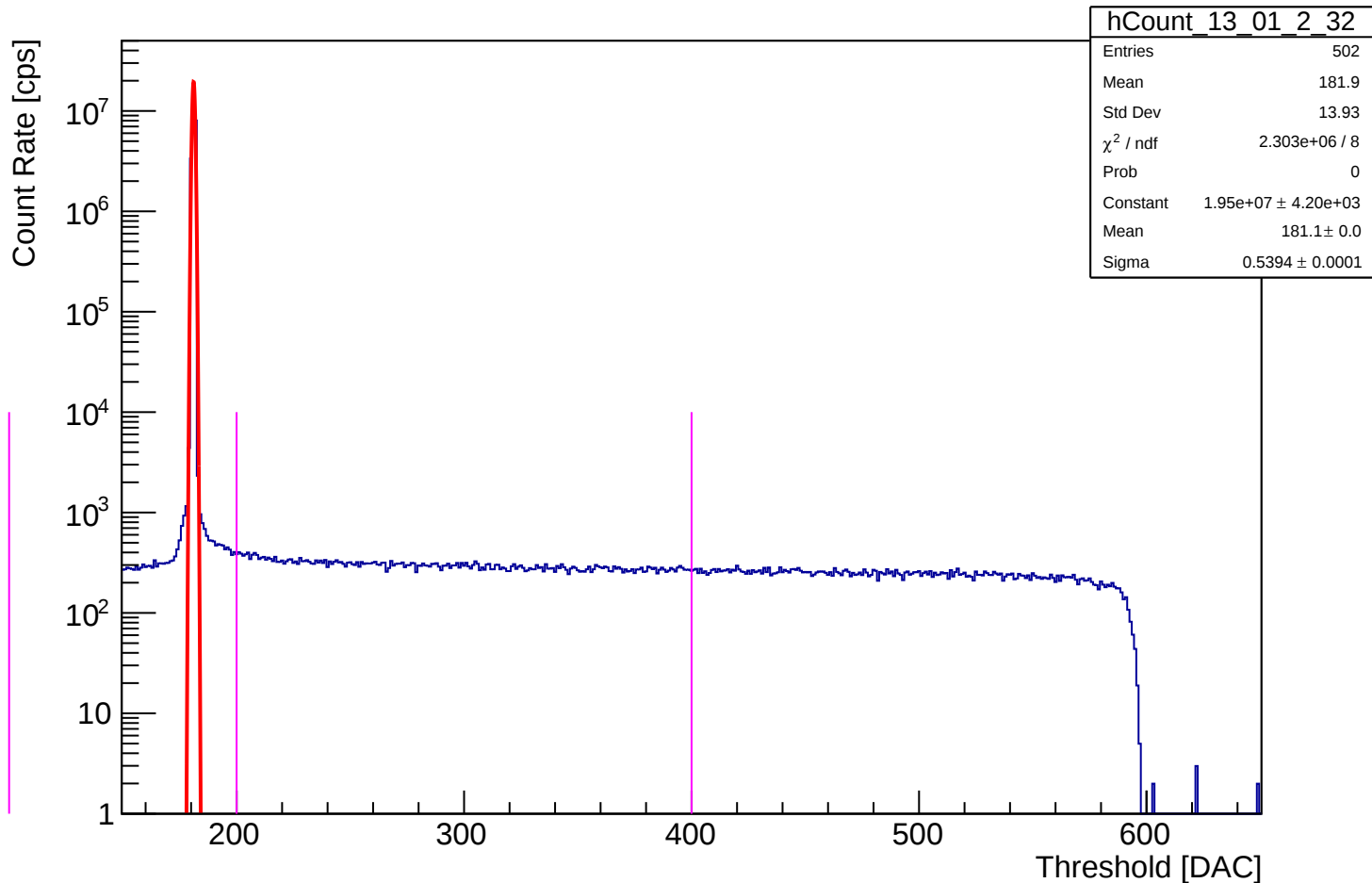
Row 1 Tile 1 Pmt 6 Pixel 3 Slot 13 Fiber 1 Asic 2 Channel 30



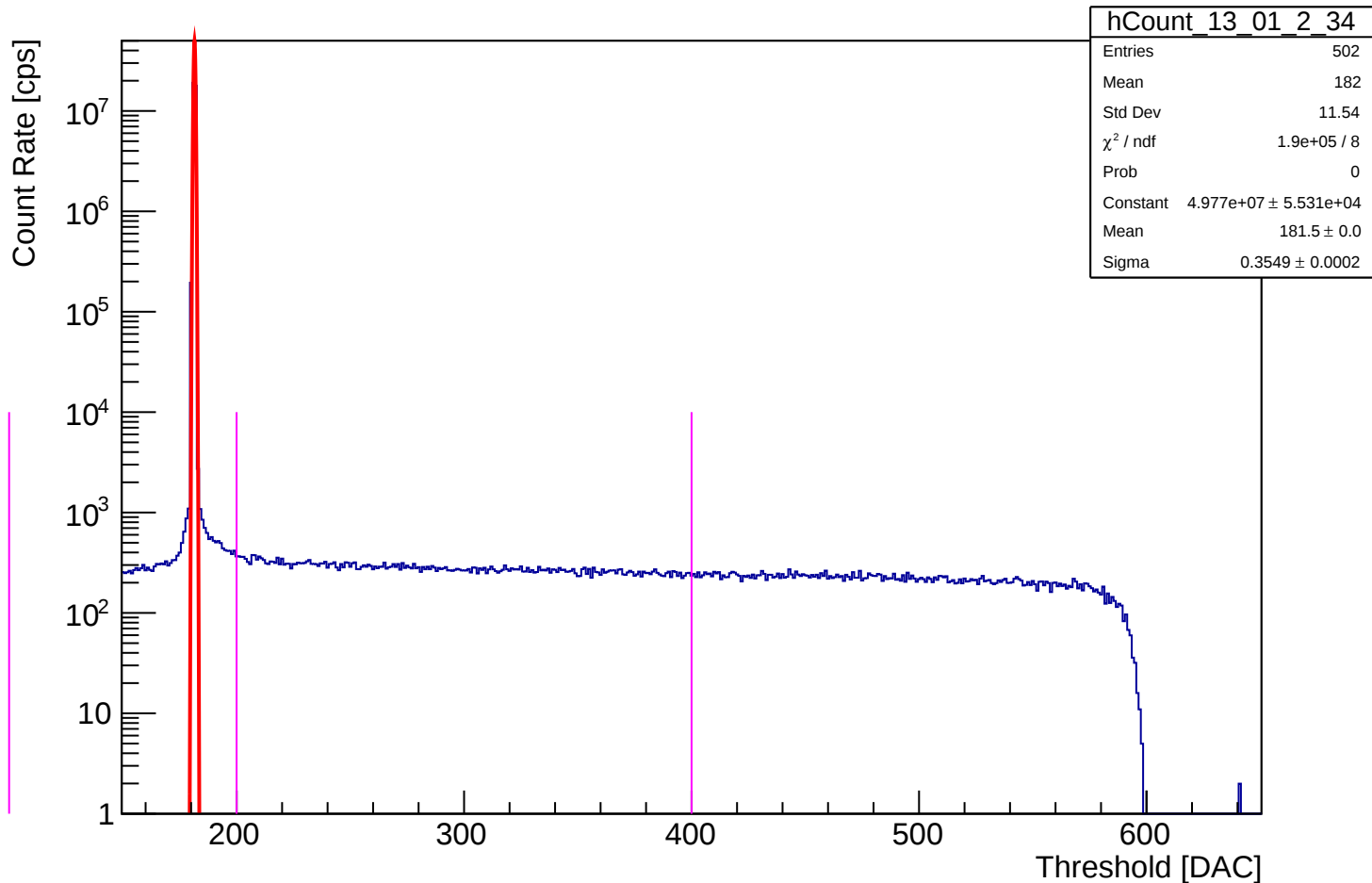
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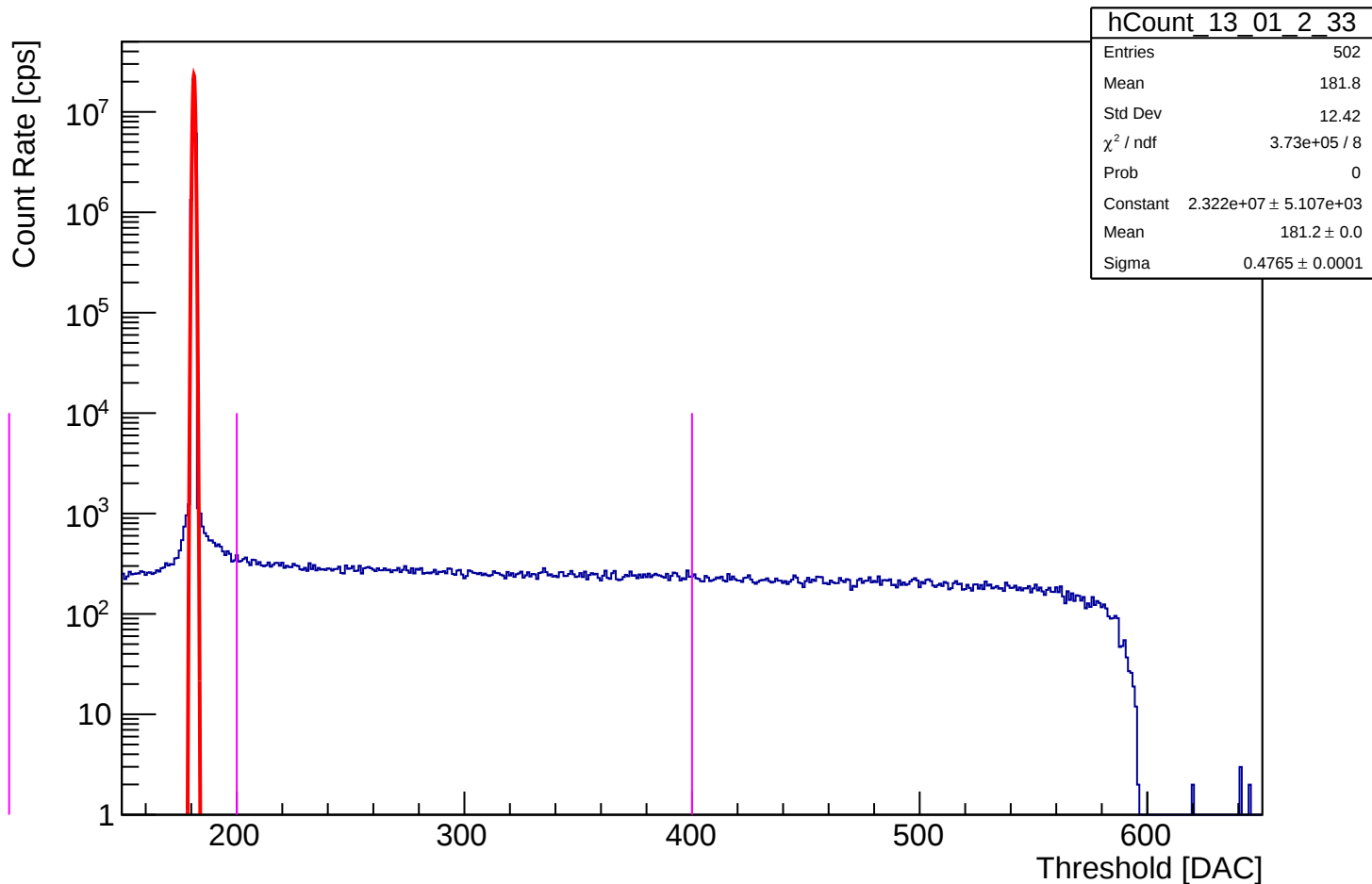
Row 1 Tile 1 Pmt 6 Pixel 5 Slot 13 Fiber 1 Asic 2 Channel 32

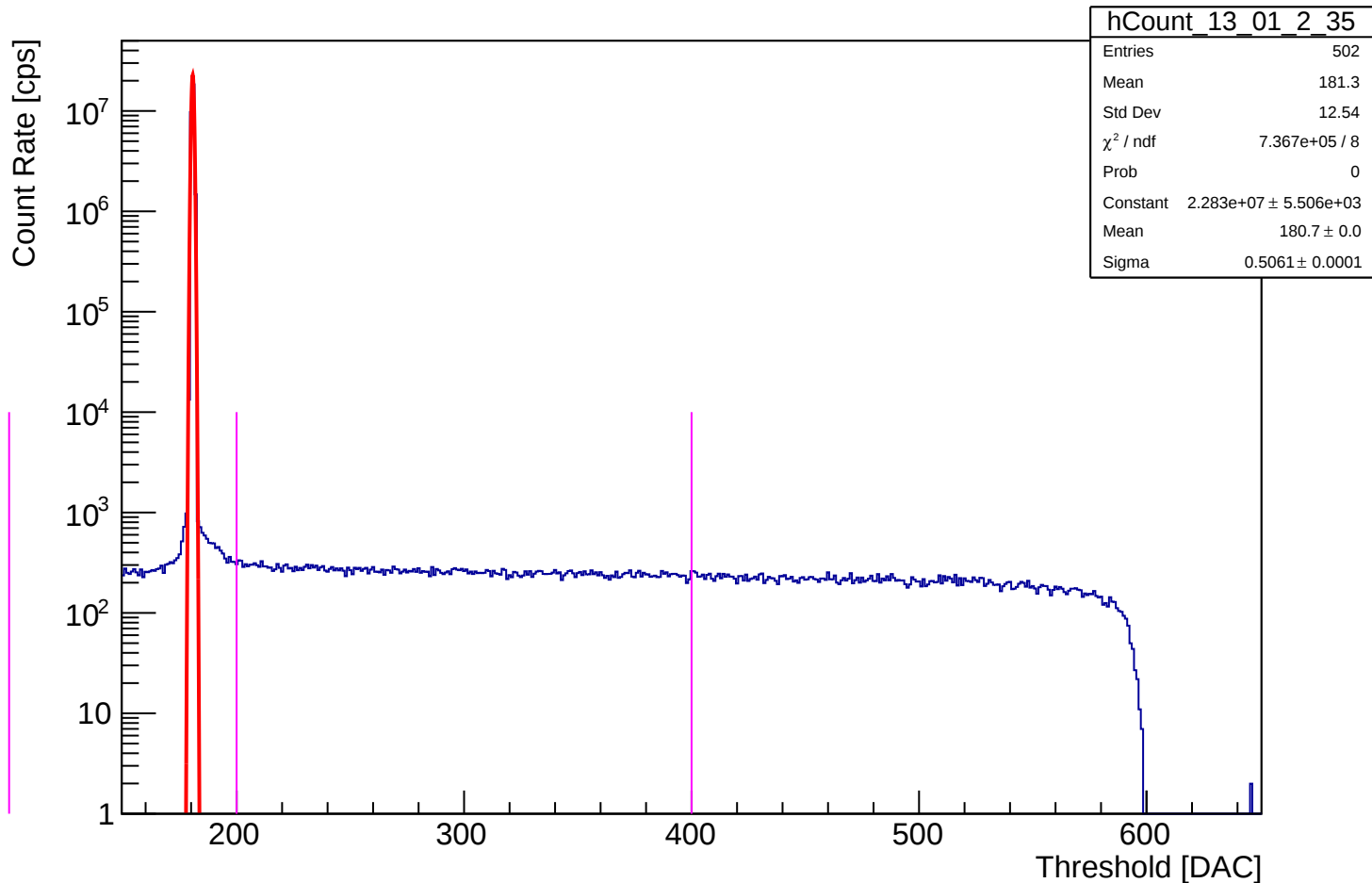


Row 1 Tile 1 Pmt 6 Pixel 6 Slot 13 Fiber 1 Asic 2 Channel 34

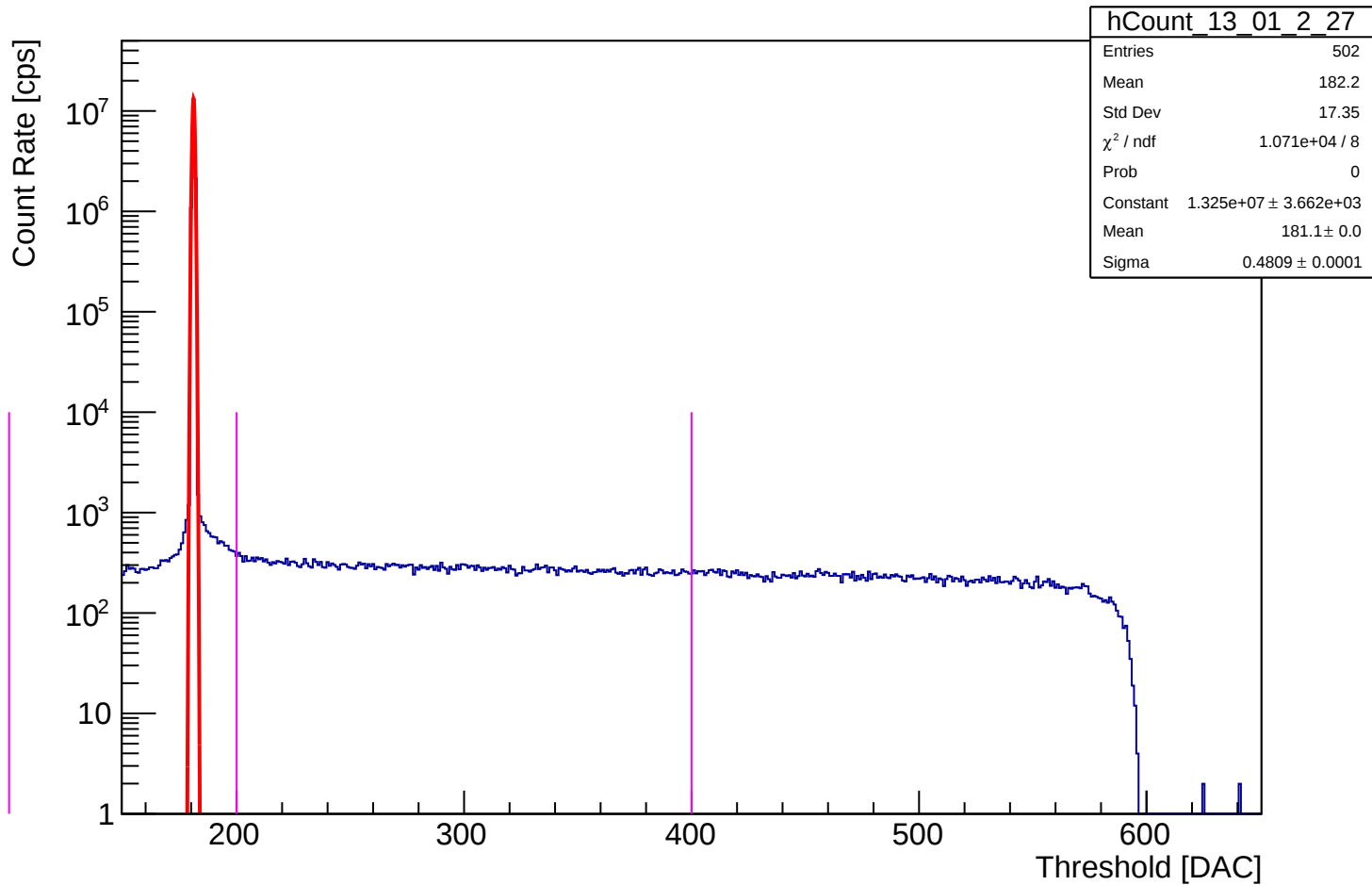


Row 1 Tile 1 Pmt 6 Pixel 7 Slot 13 Fiber 1 Asic 2 Channel 33

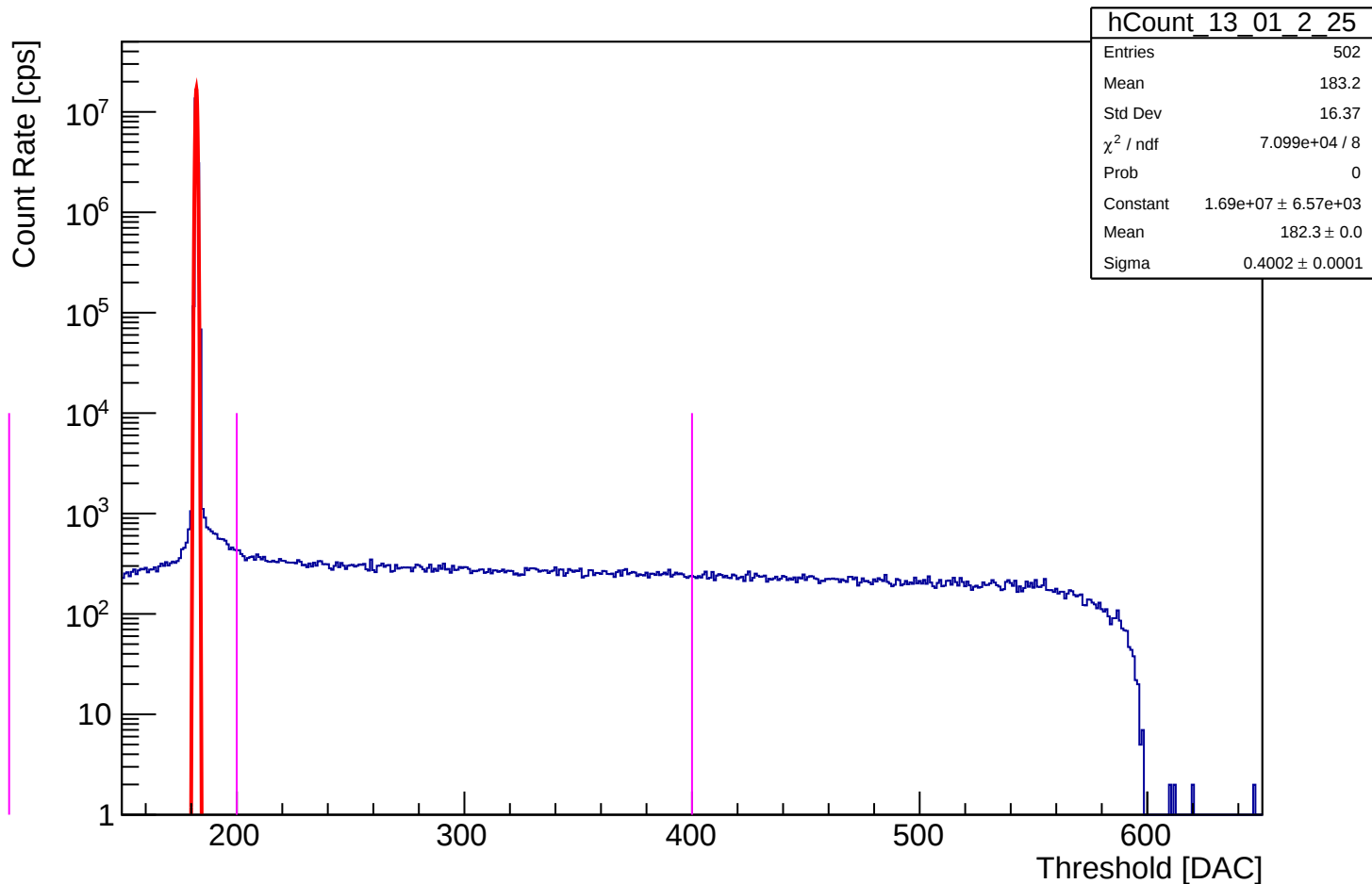




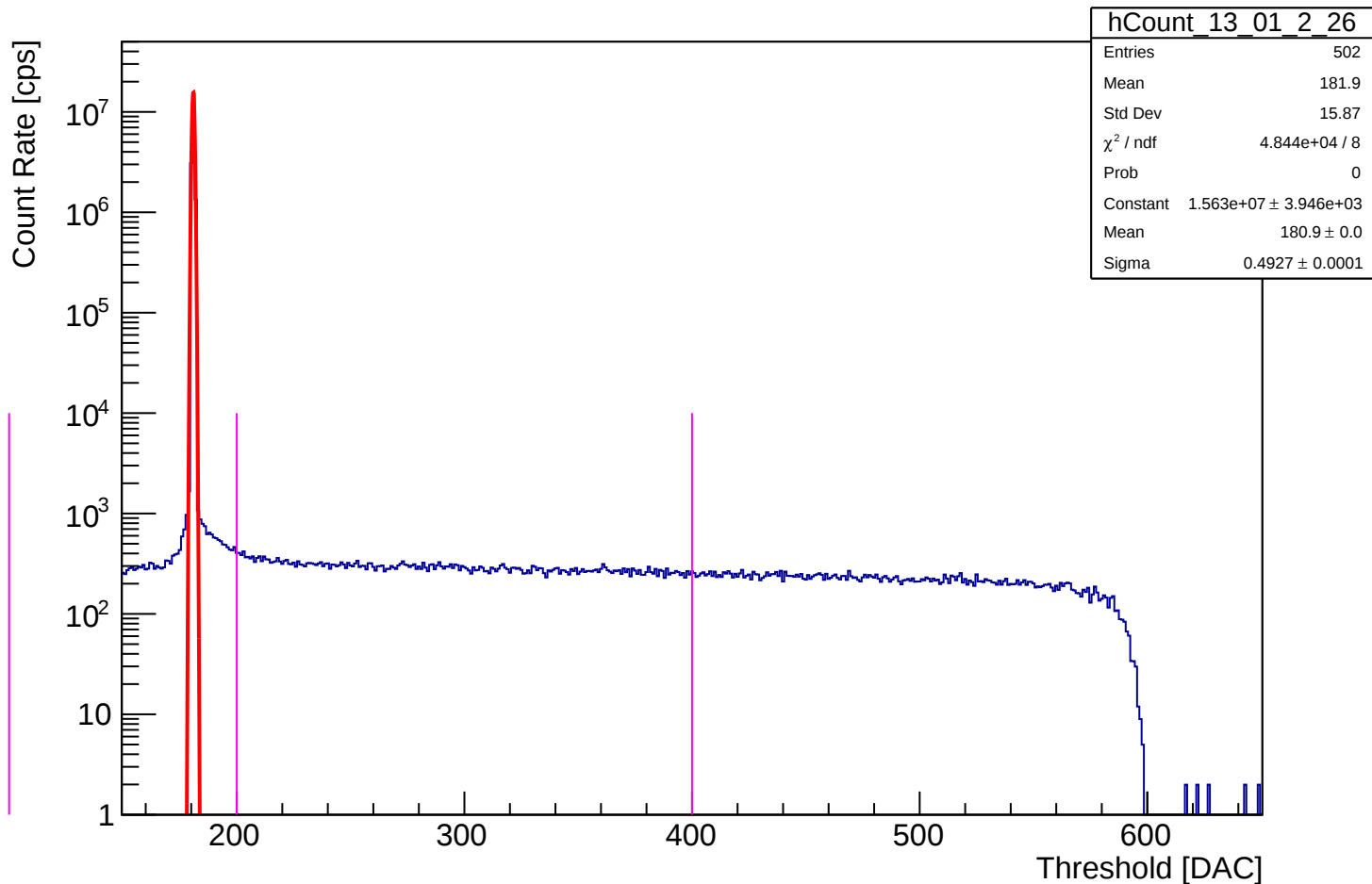
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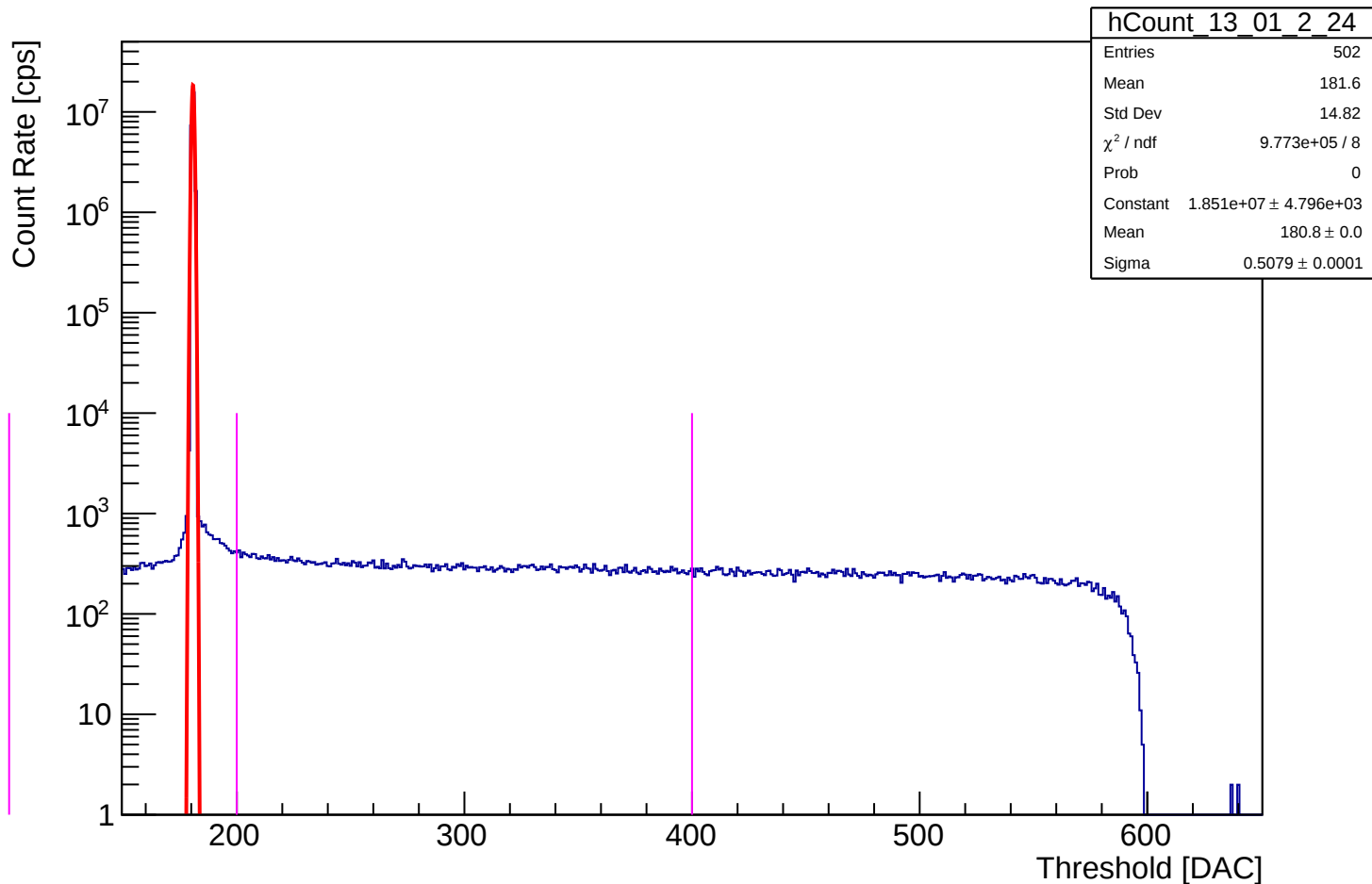
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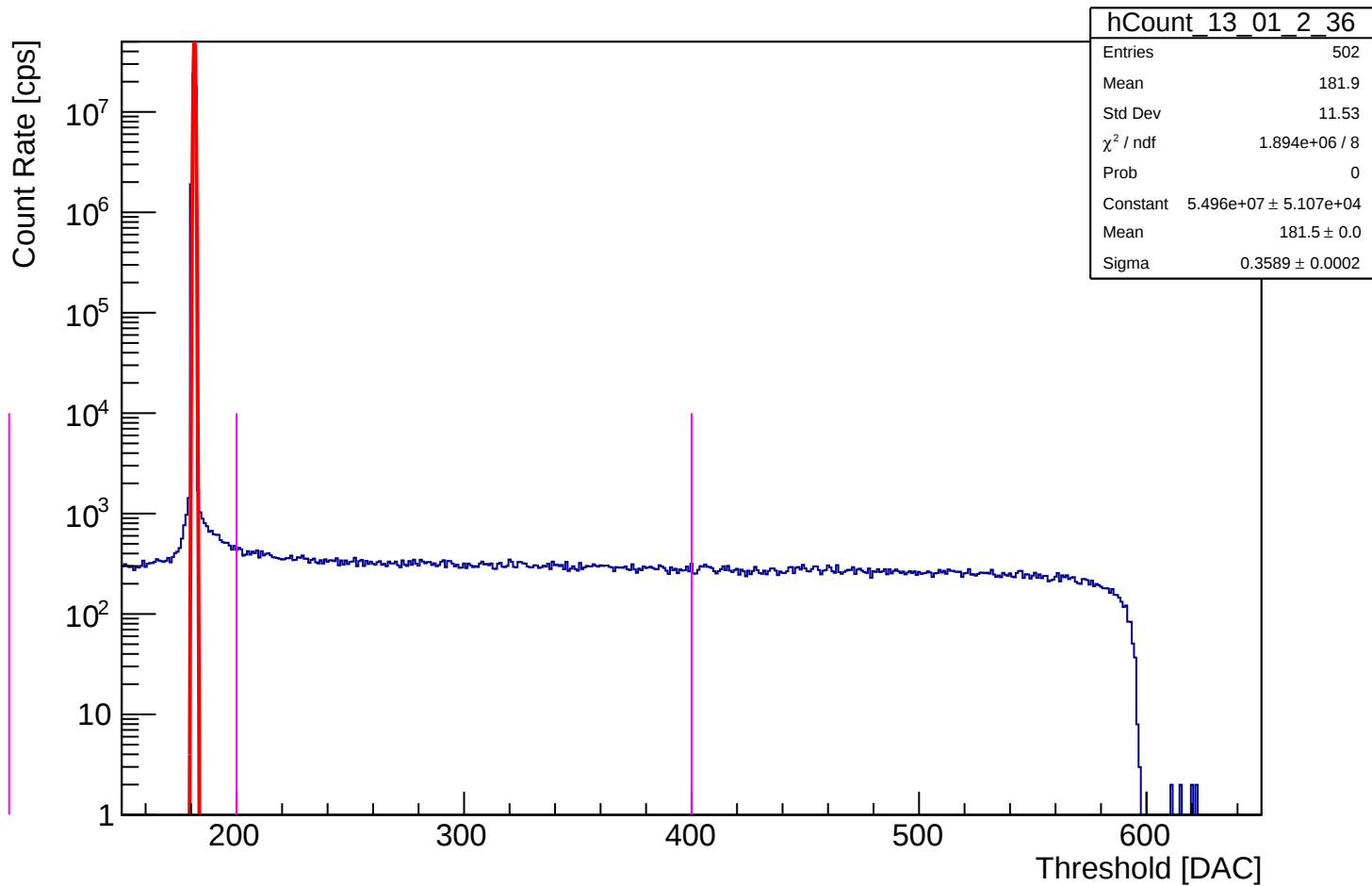
Row 1 Tile 1 Pmt 6 Pixel 11 Slot 13 Fiber 1 Asic 2 Channel 26



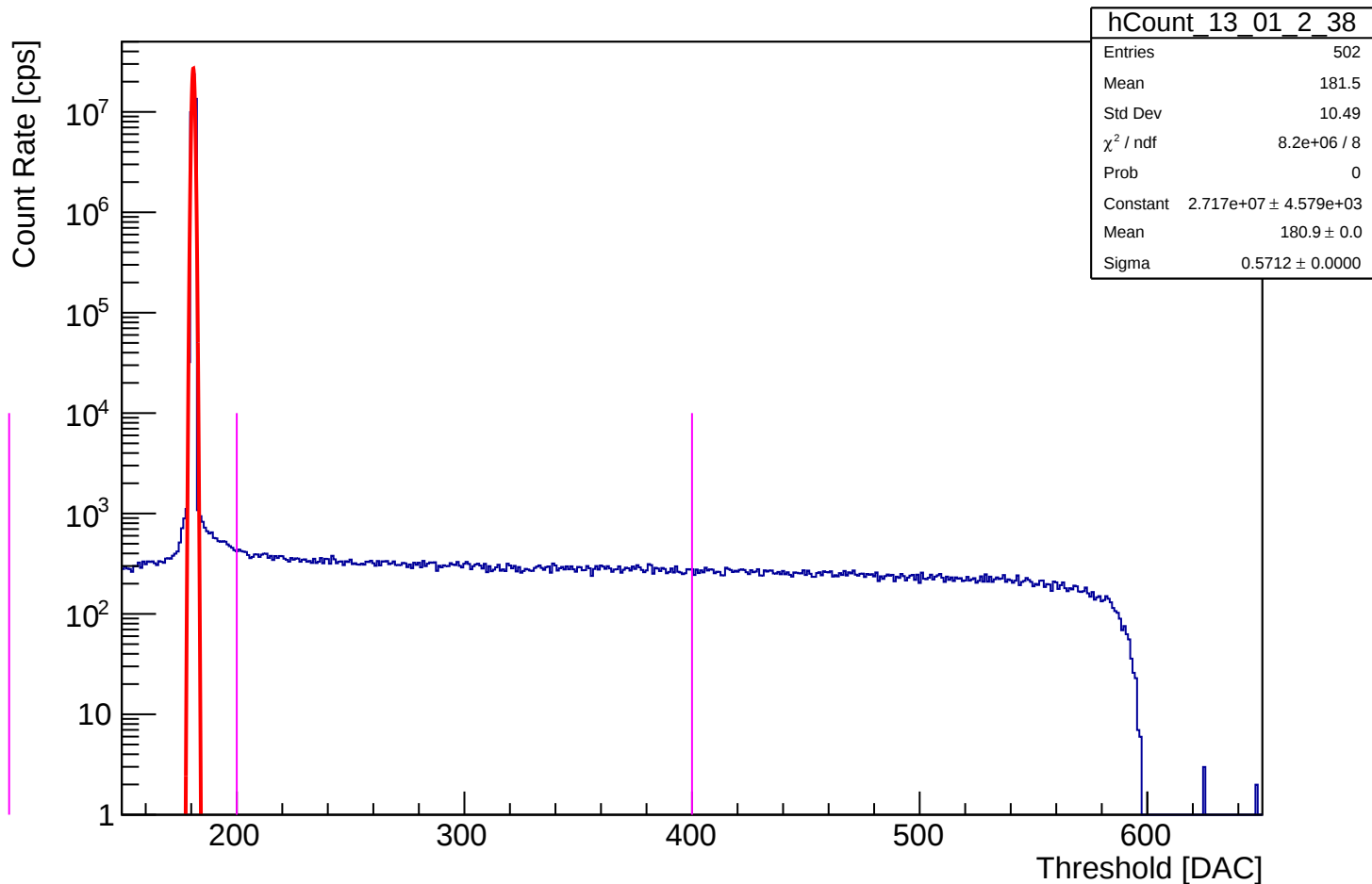
Row 1 Tile 1 Pmt 6 Pixel 12 Slot 13 Fiber 1 Asic 2 Channel 24



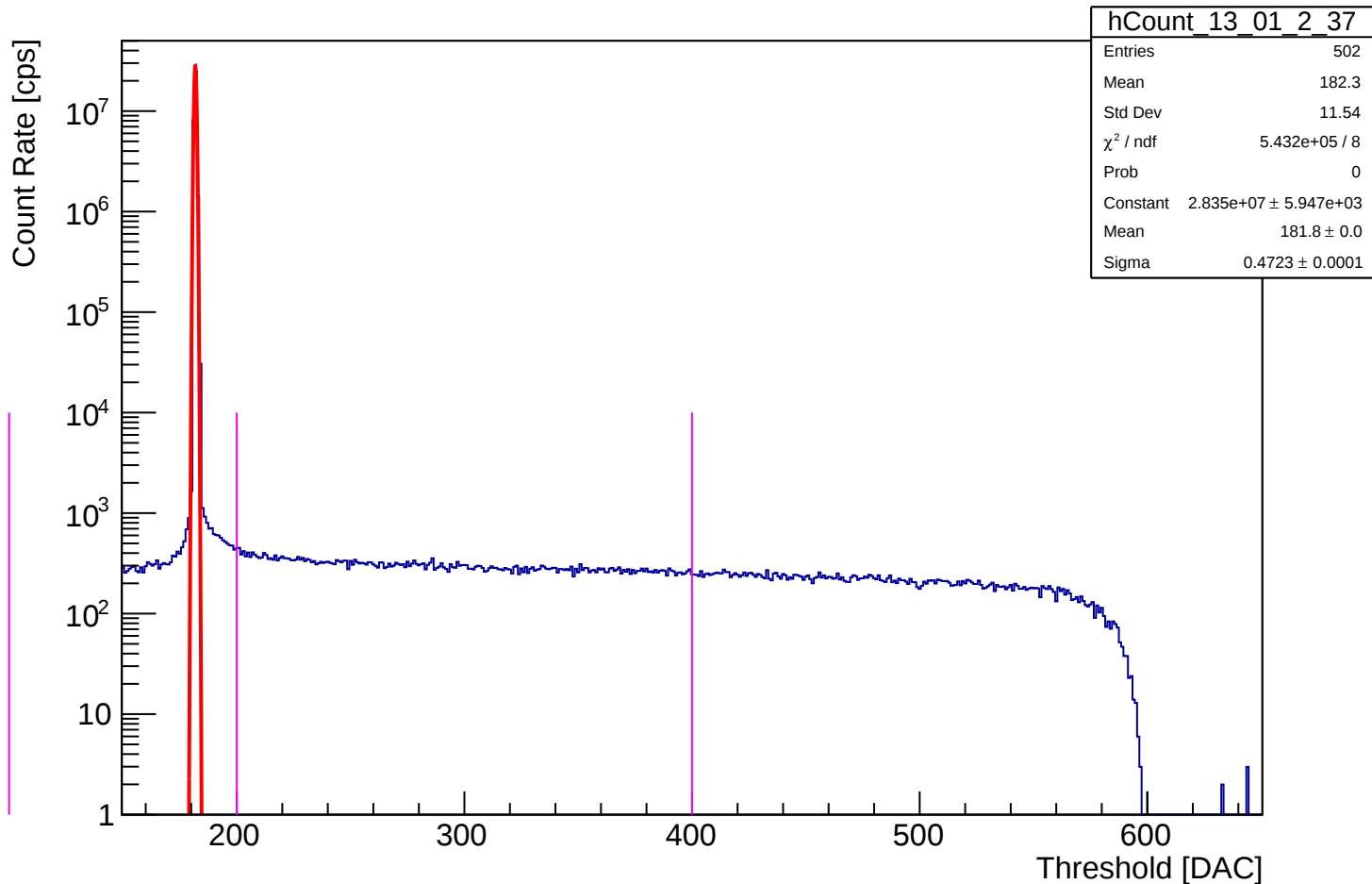
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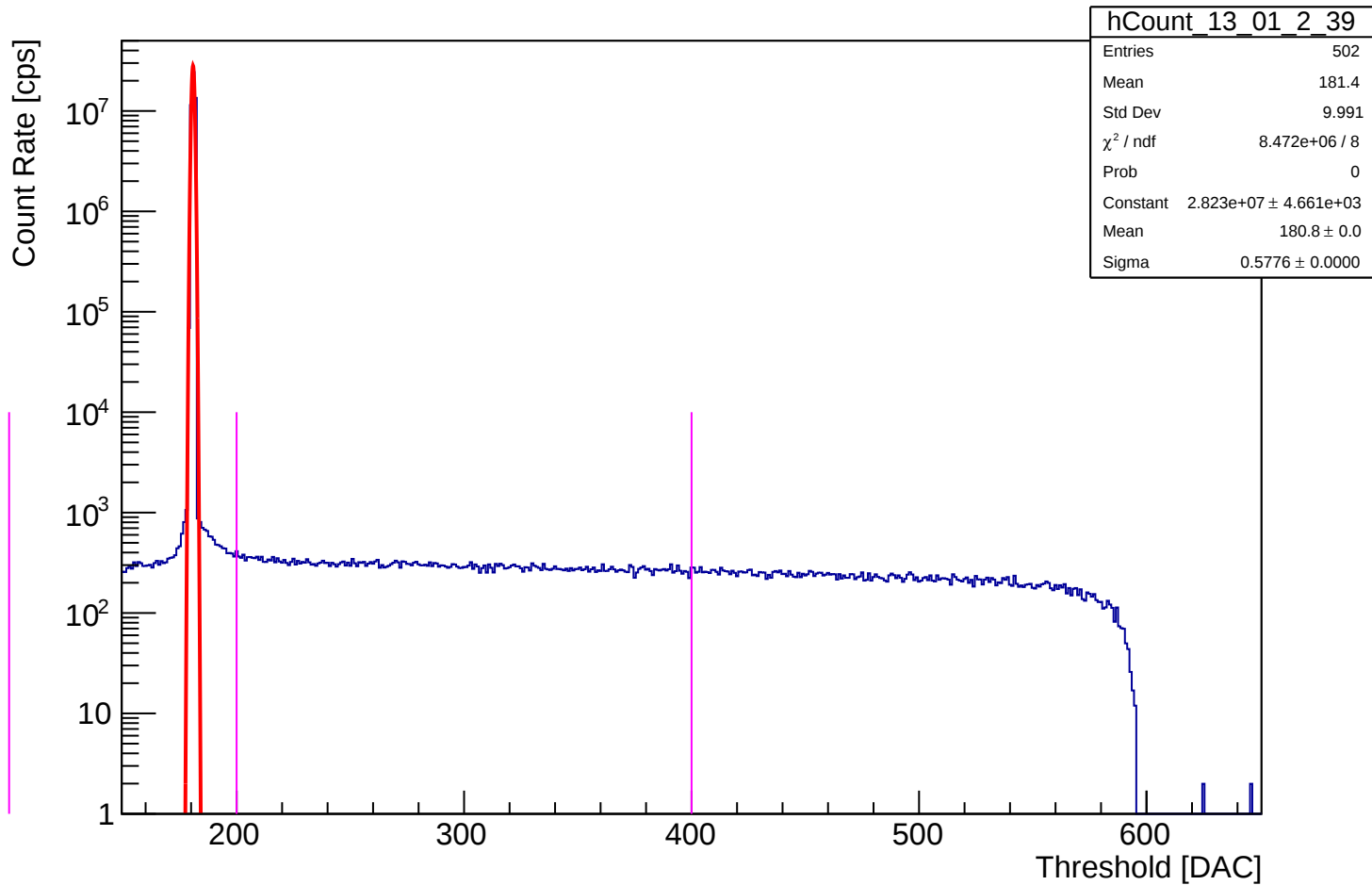
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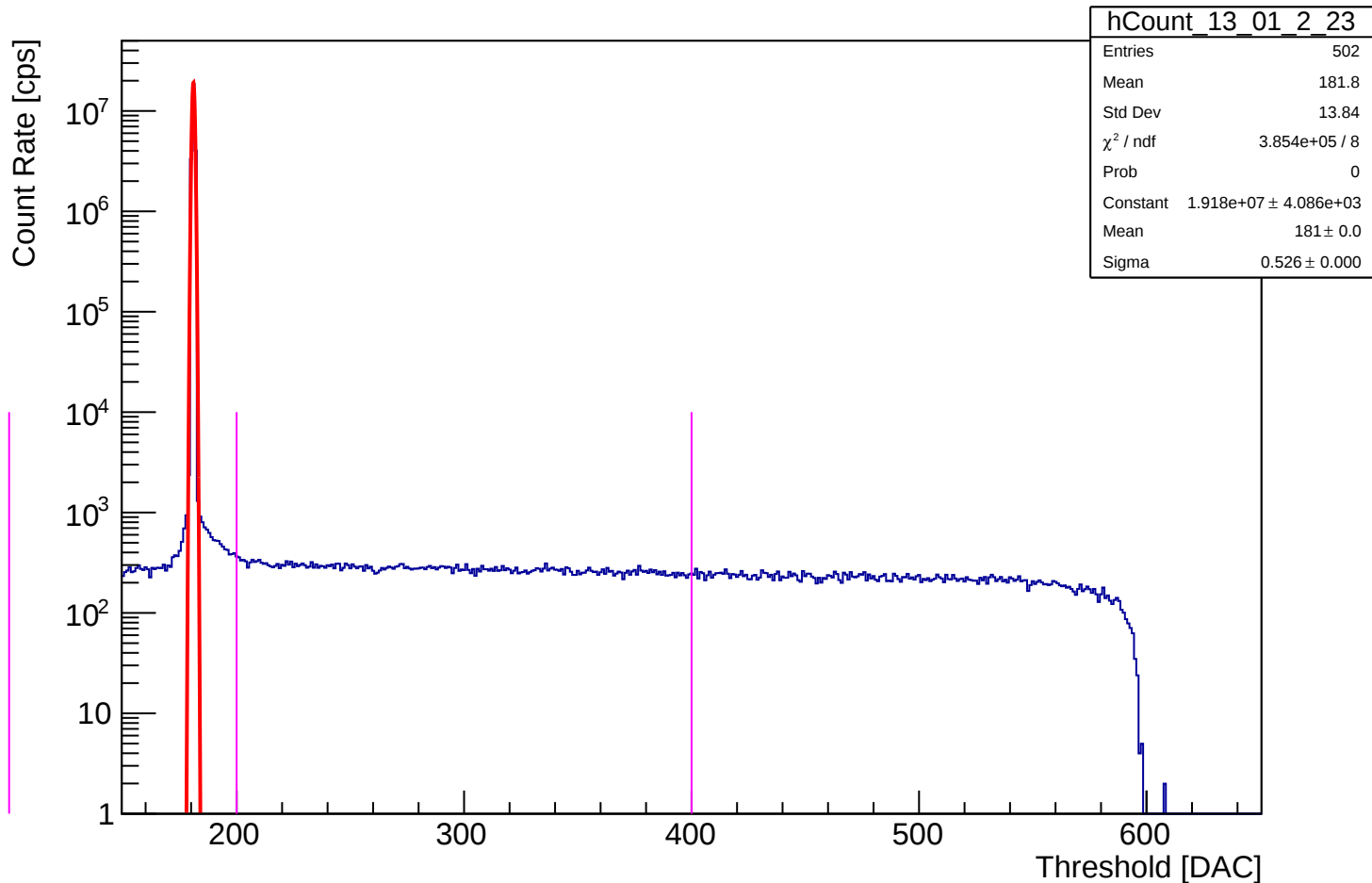
Row 1 Tile 1 Pmt 6 Pixel 15 Slot 13 Fiber 1 Asic 2 Channel 37



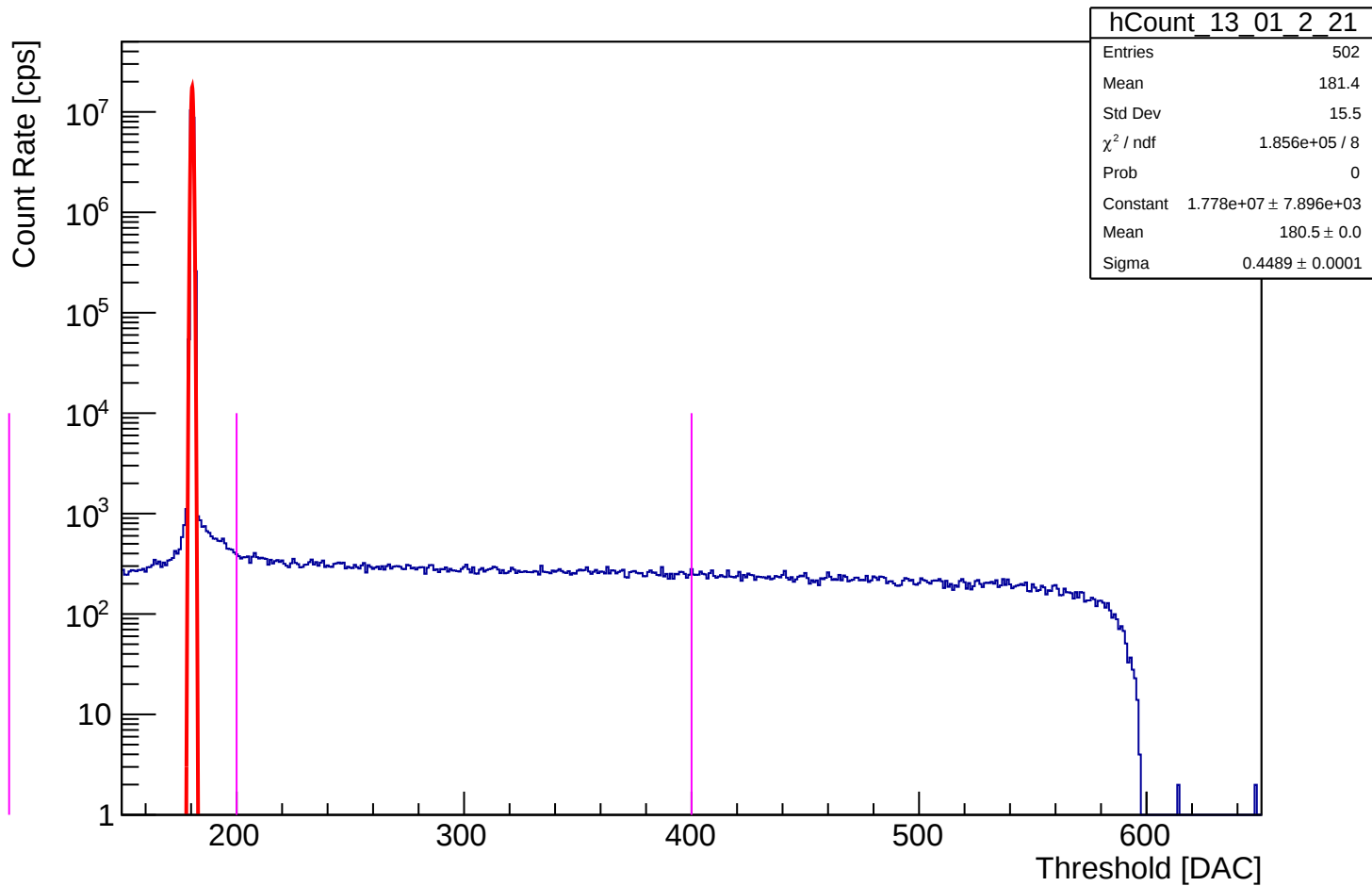
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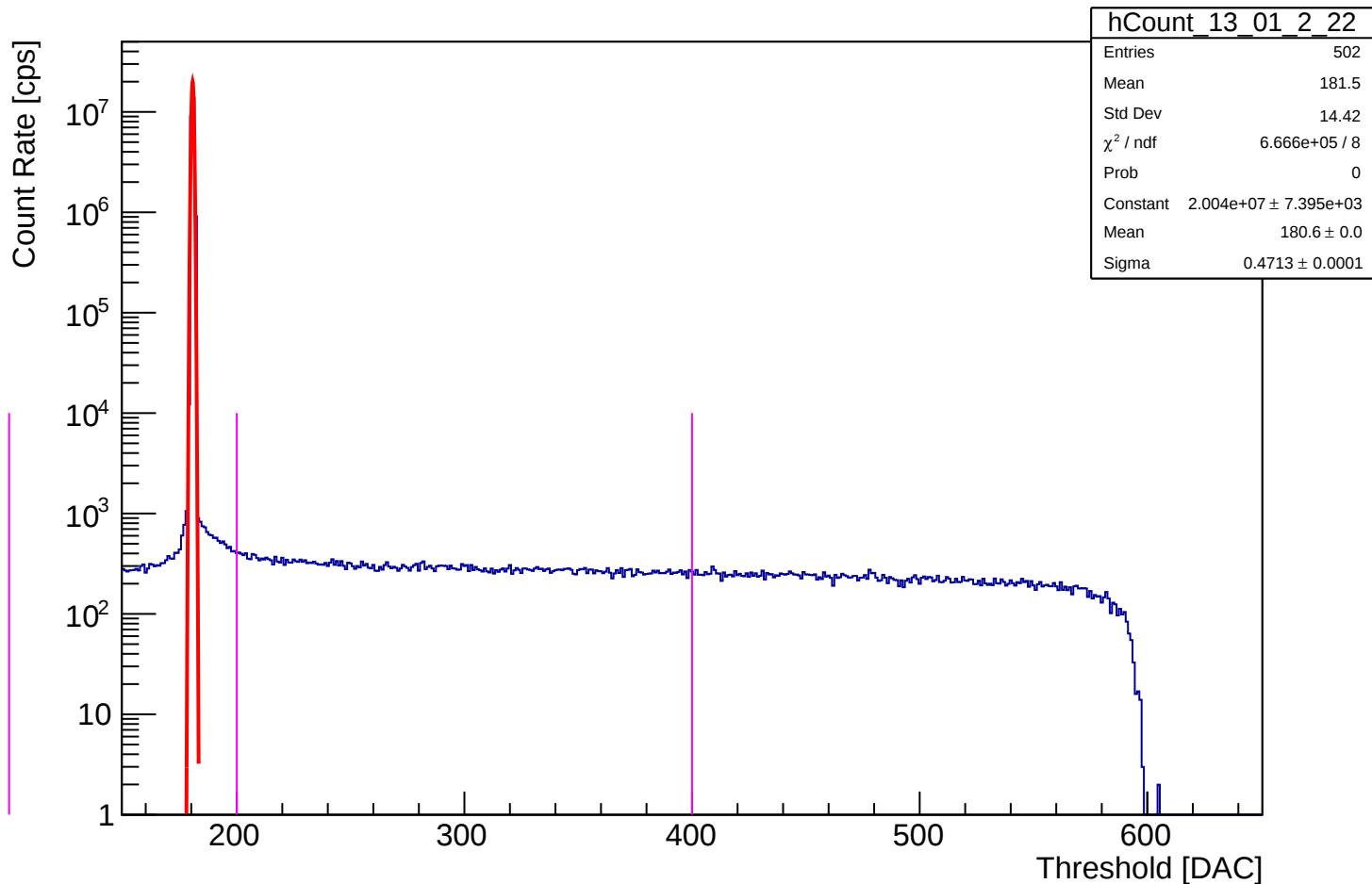
Row 1 Tile 1 Pmt 6 Pixel 17 Slot 13 Fiber 1 Asic 2 Channel 23



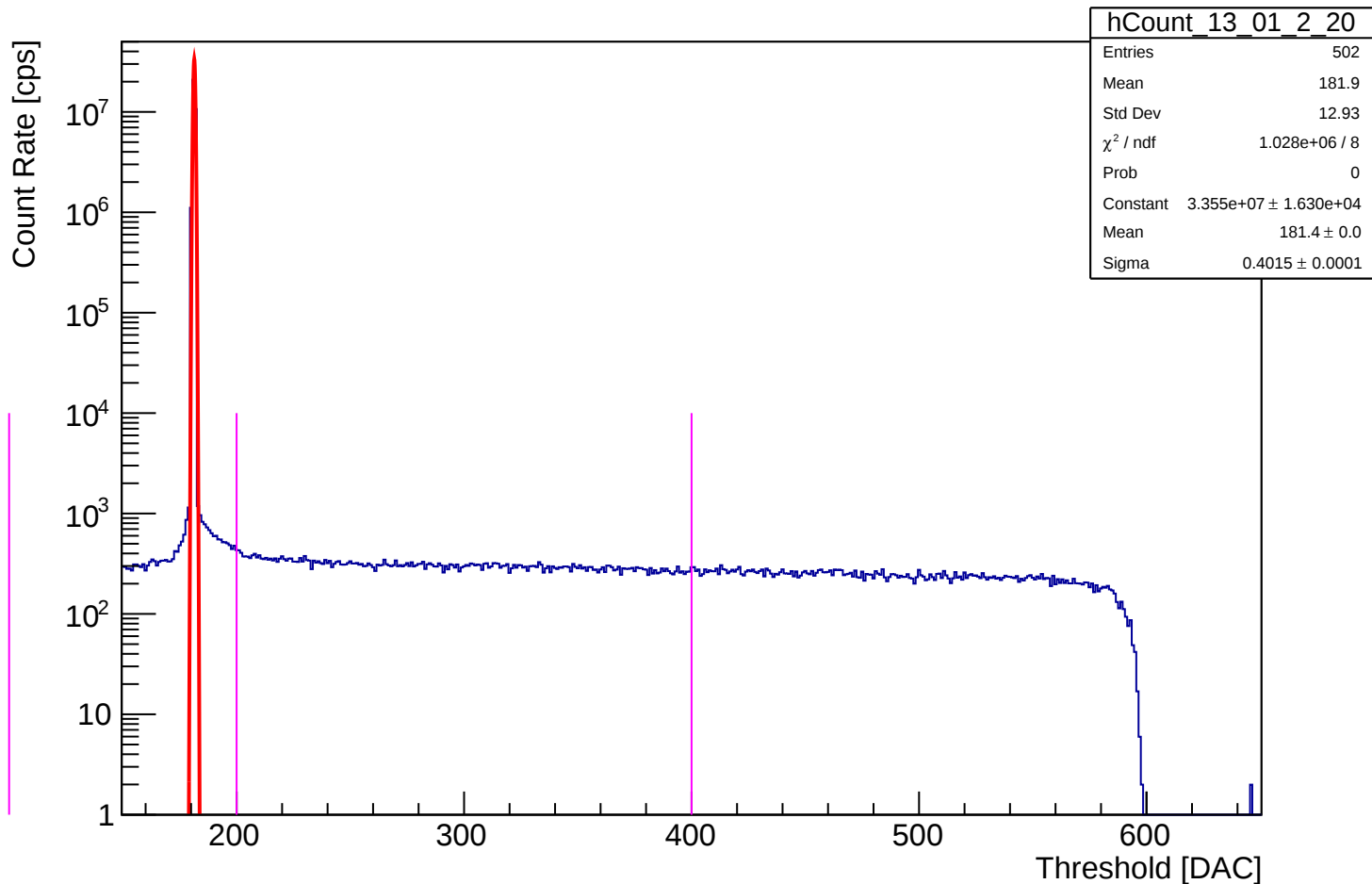
Row 1 Tile 1 Pmt 6 Pixel 18 Slot 13 Fiber 1 Asic 2 Channel 21



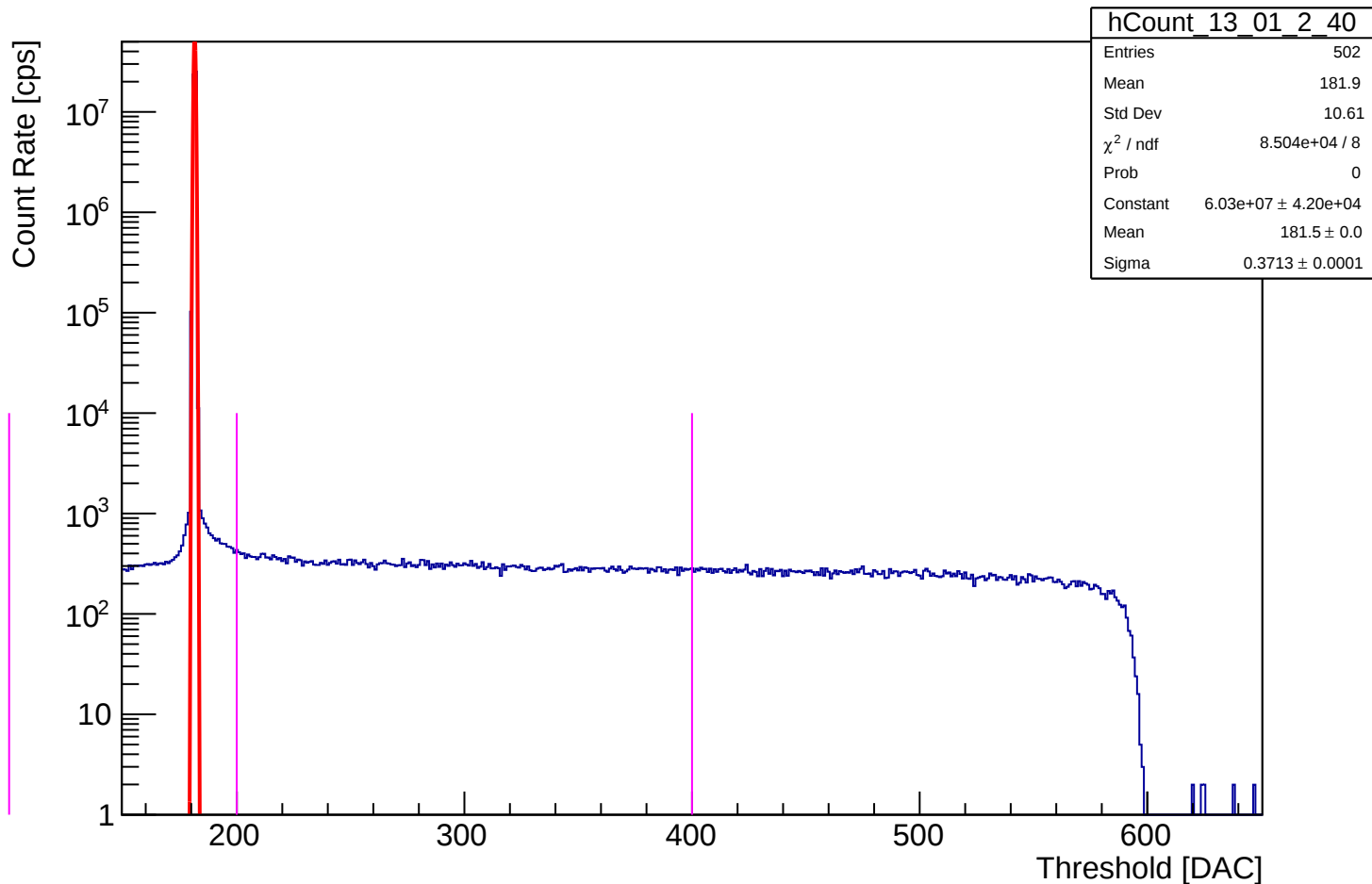
Row 1 Tile 1 Pmt 6 Pixel 19 Slot 13 Fiber 1 Asic 2 Channel 22



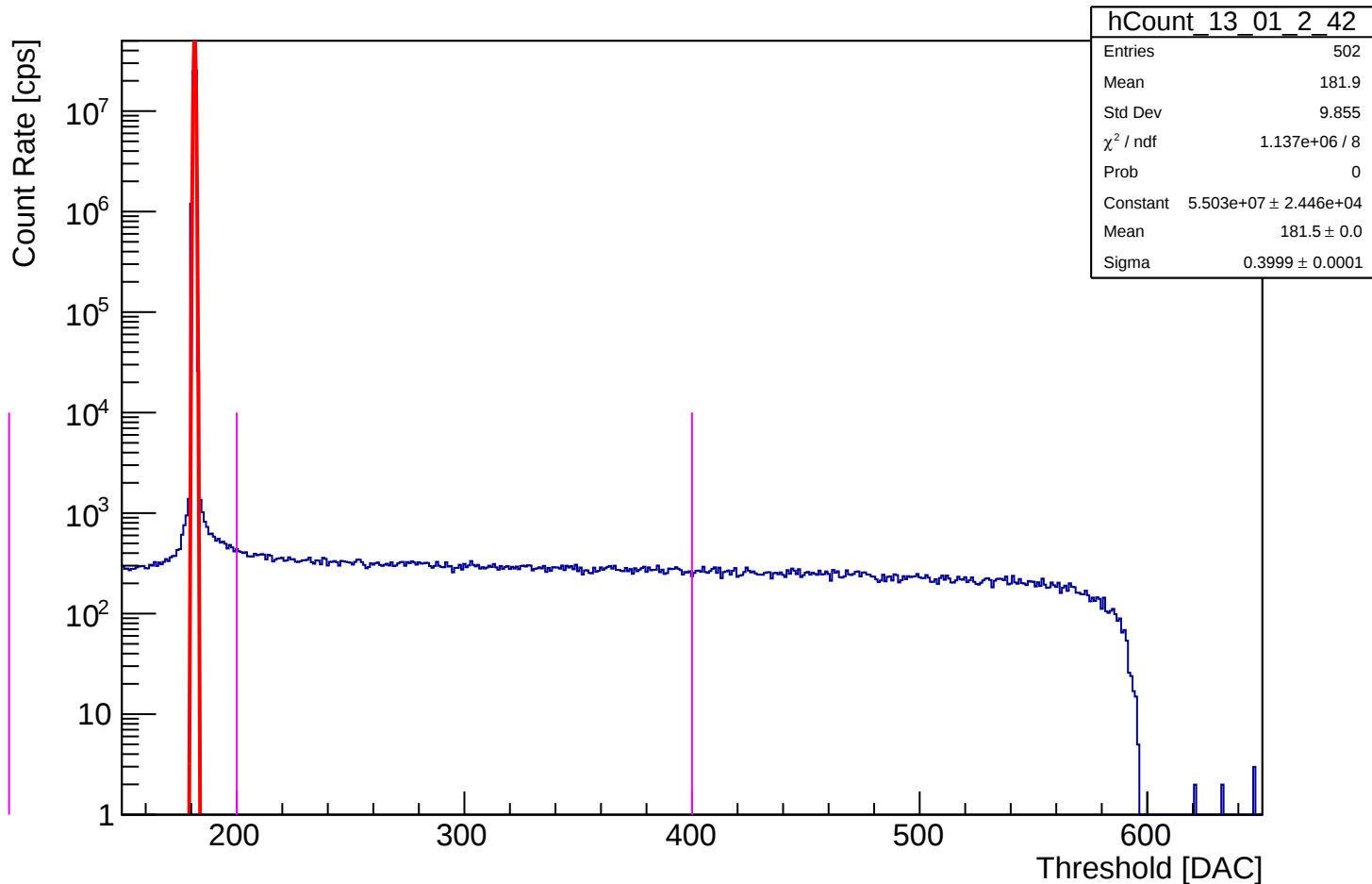
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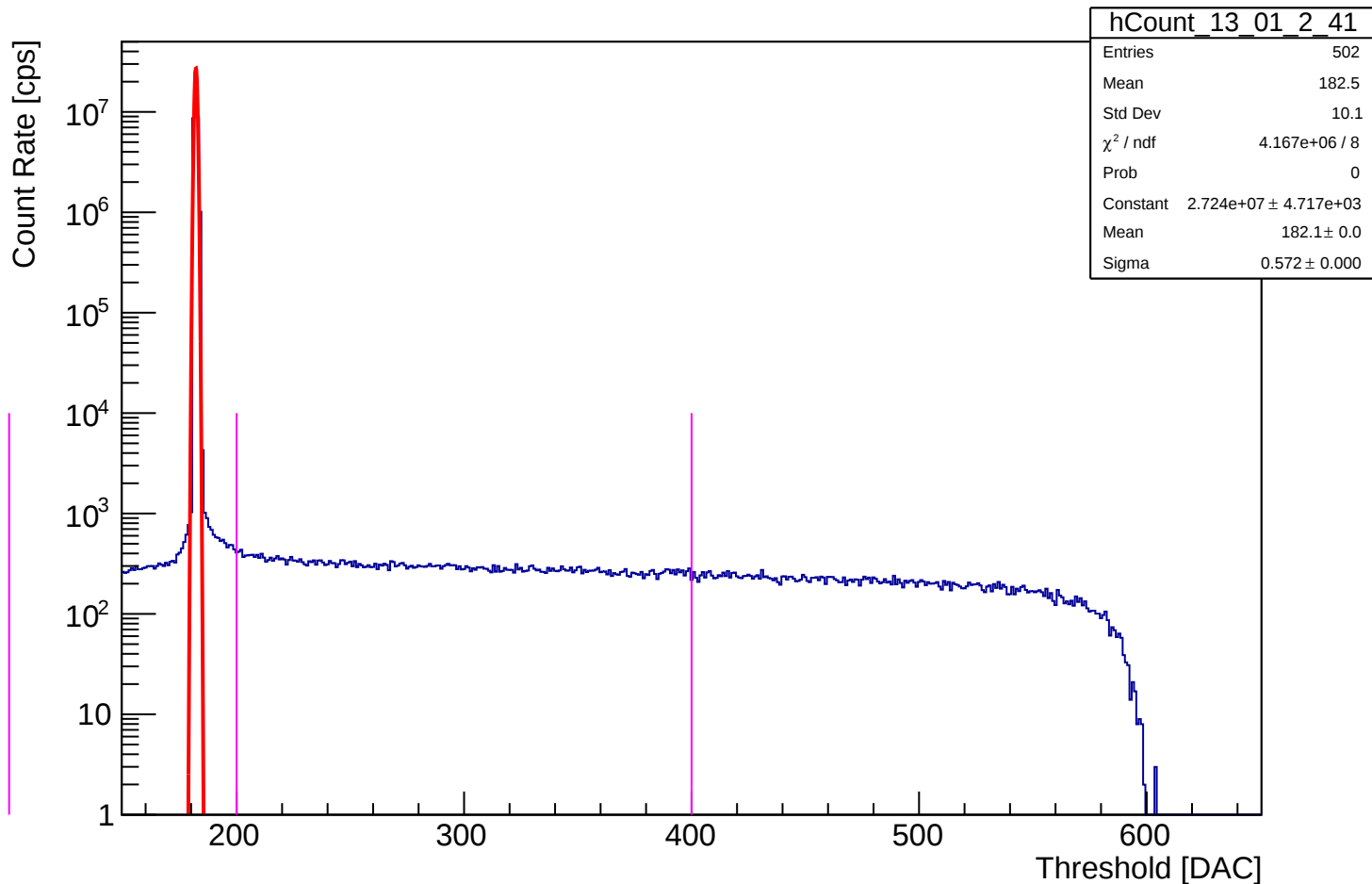
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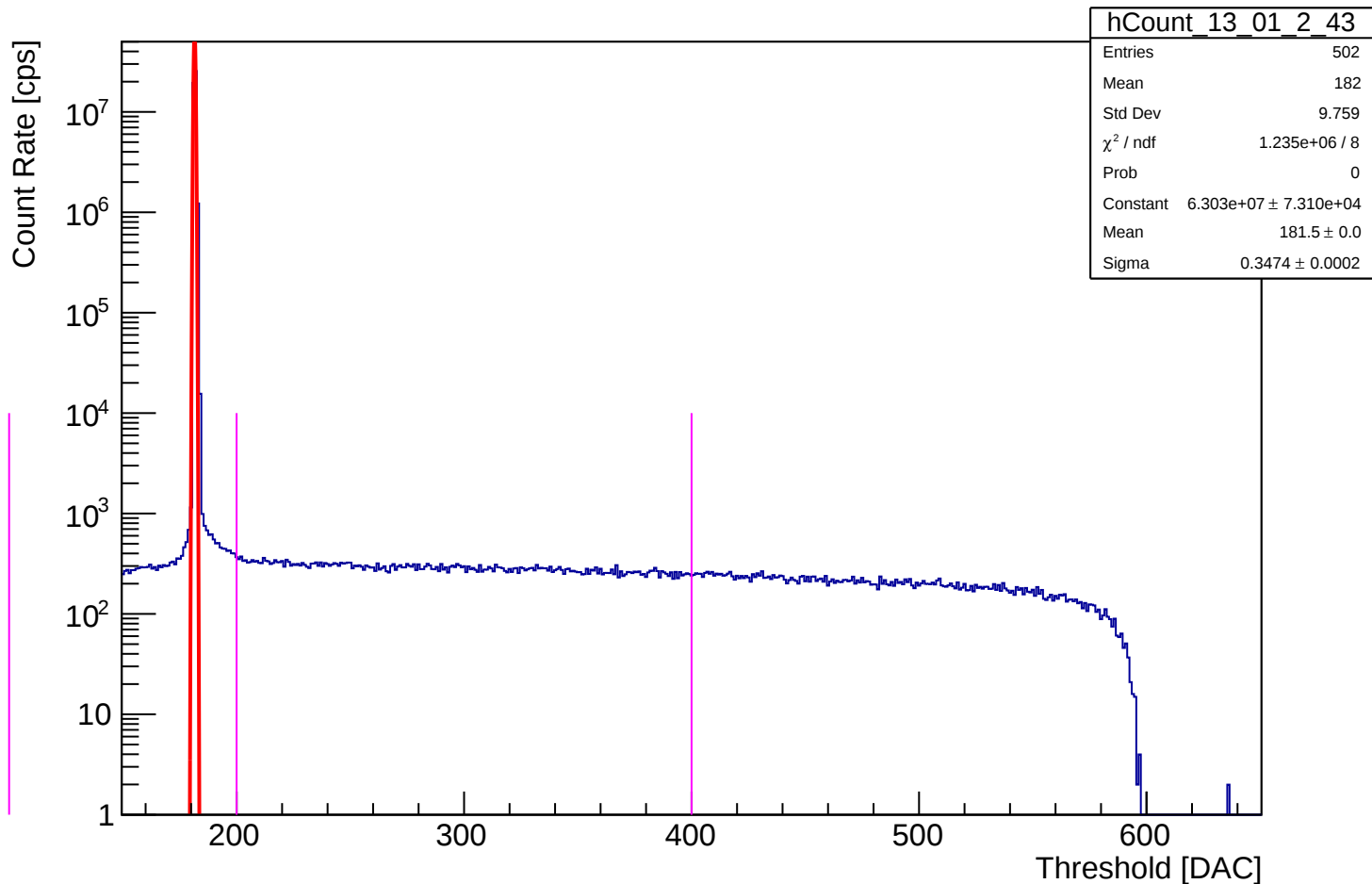
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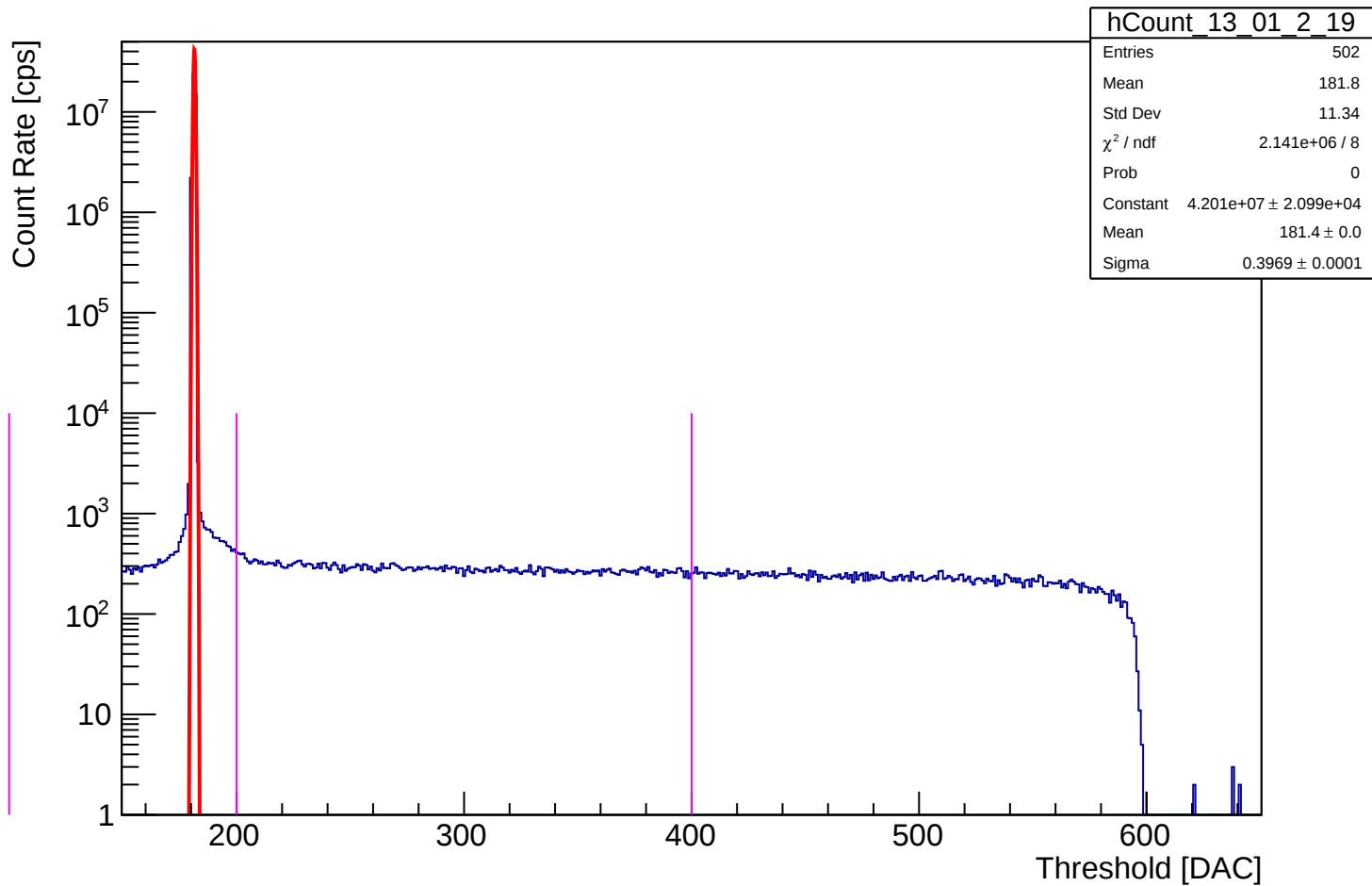
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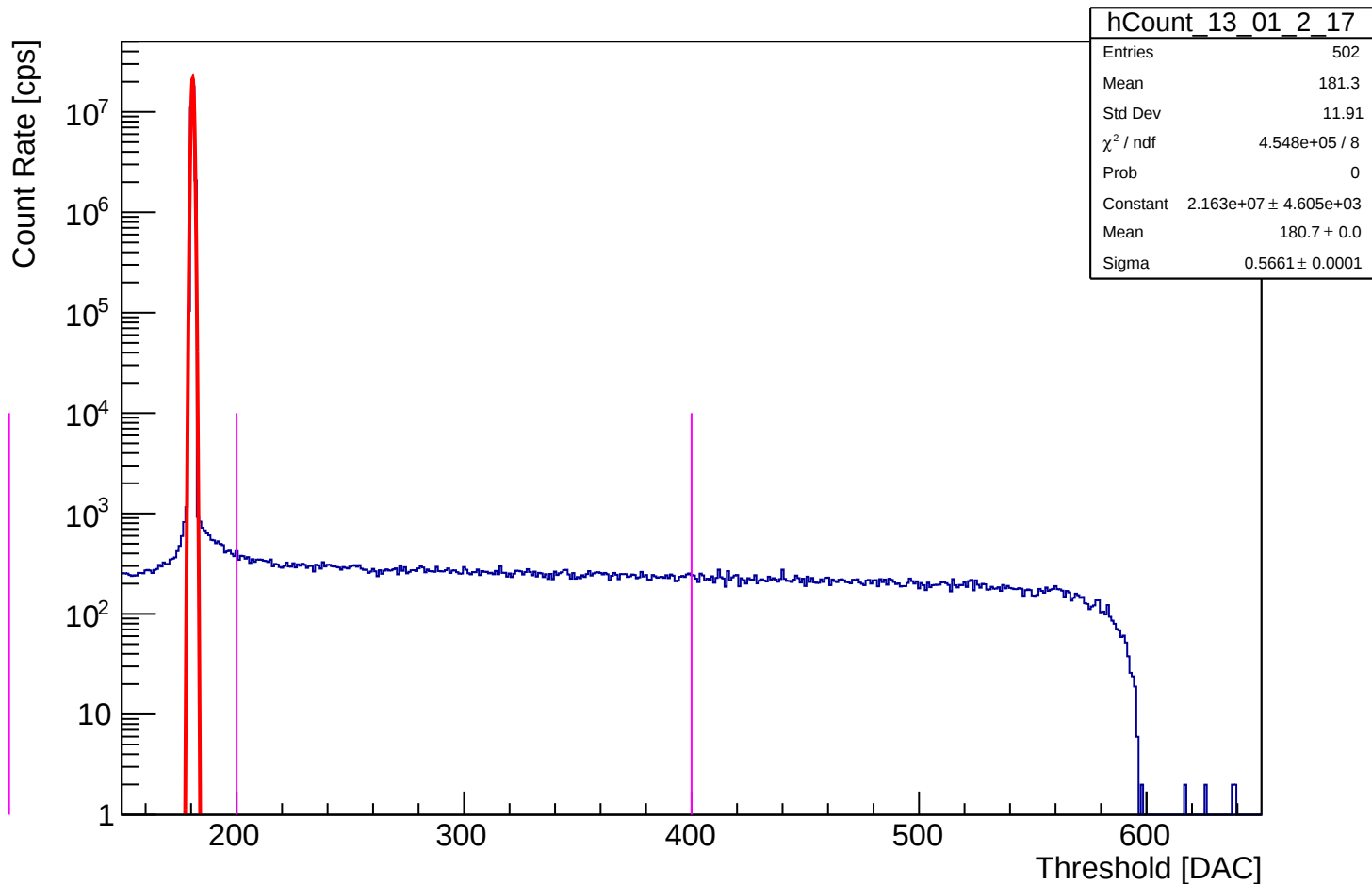
Row 1 Tile 1 Pmt 6 Pixel 24 Slot 13 Fiber 1 Asic 2 Channel 43



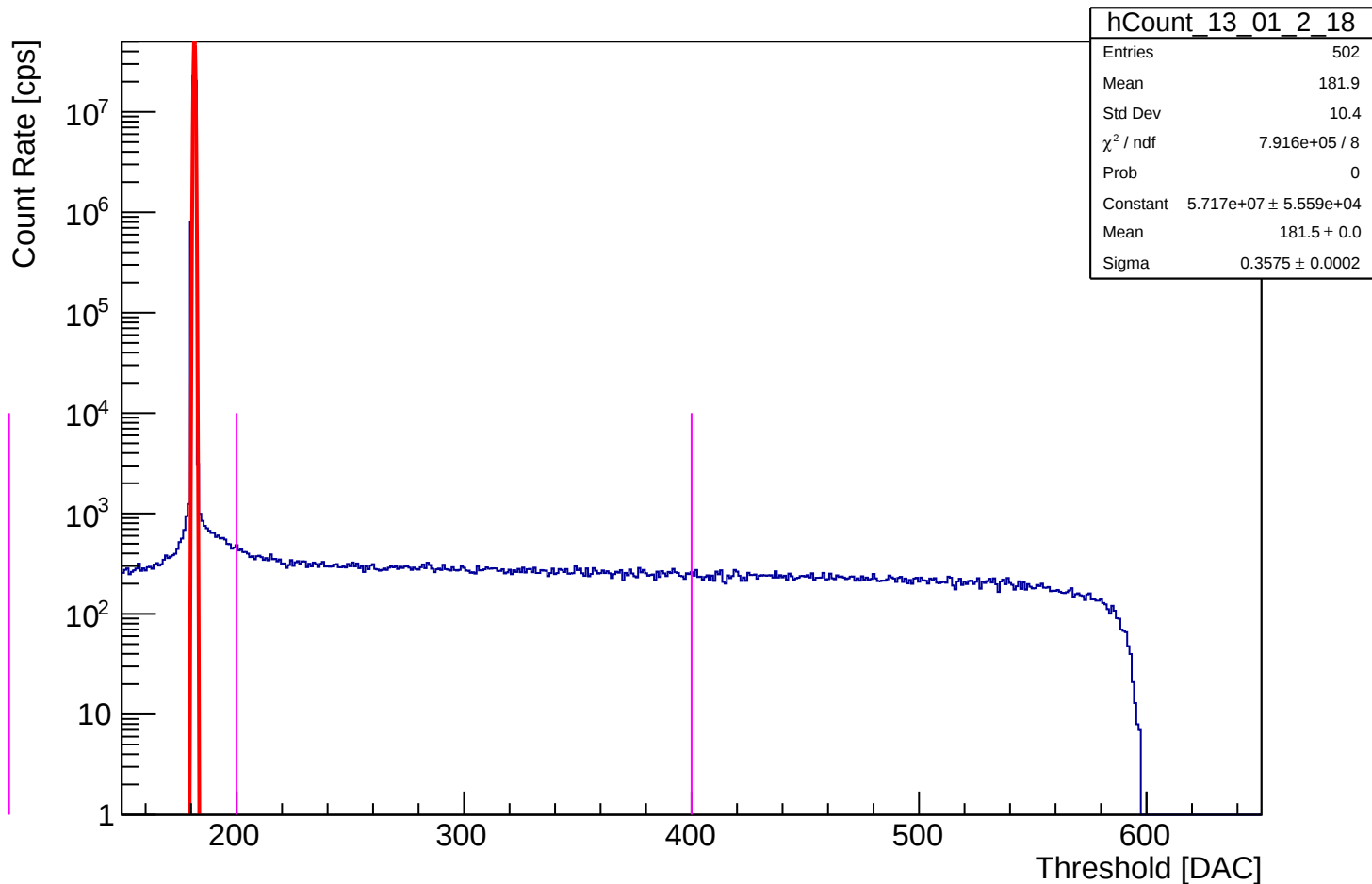
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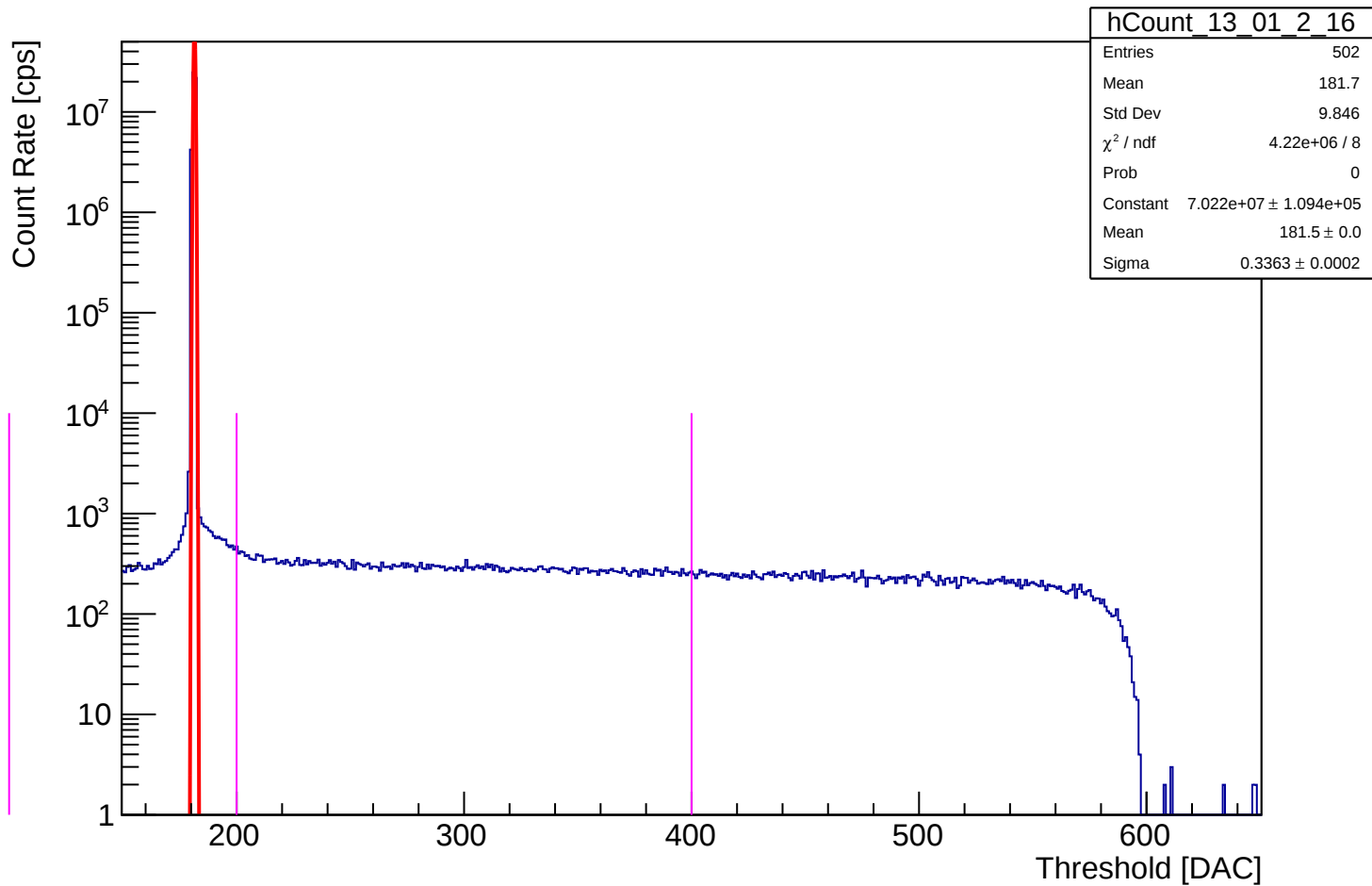
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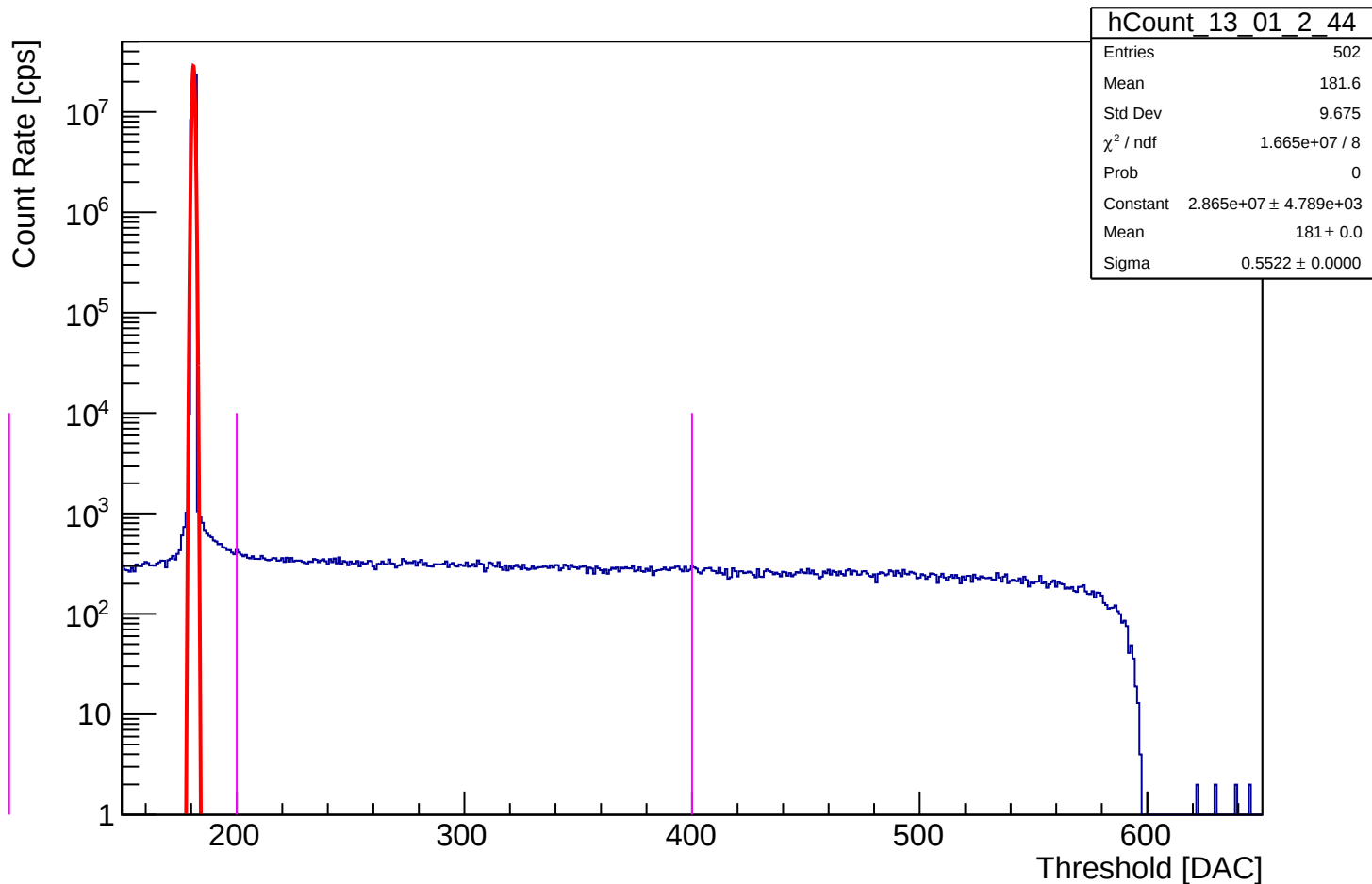
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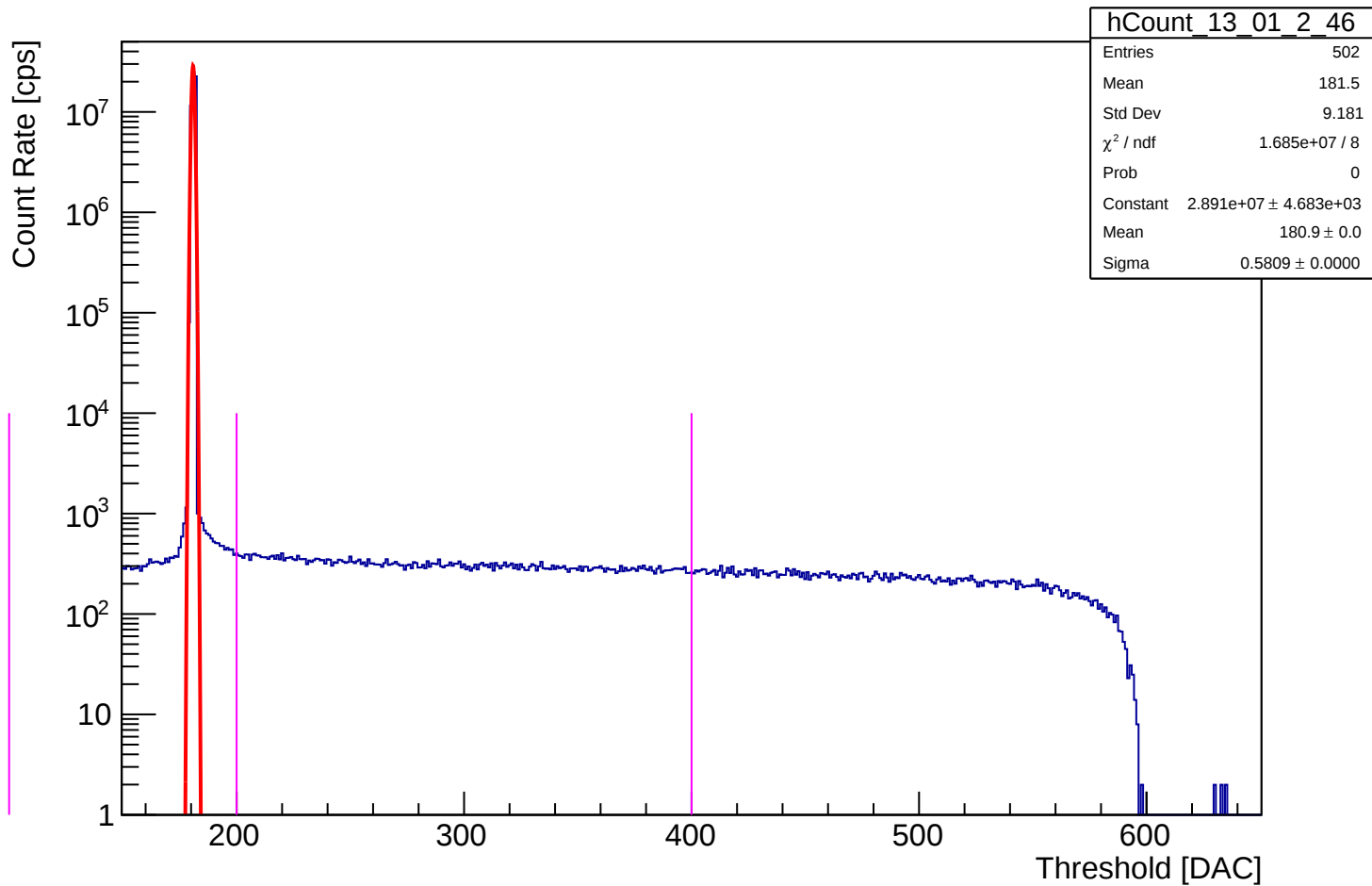
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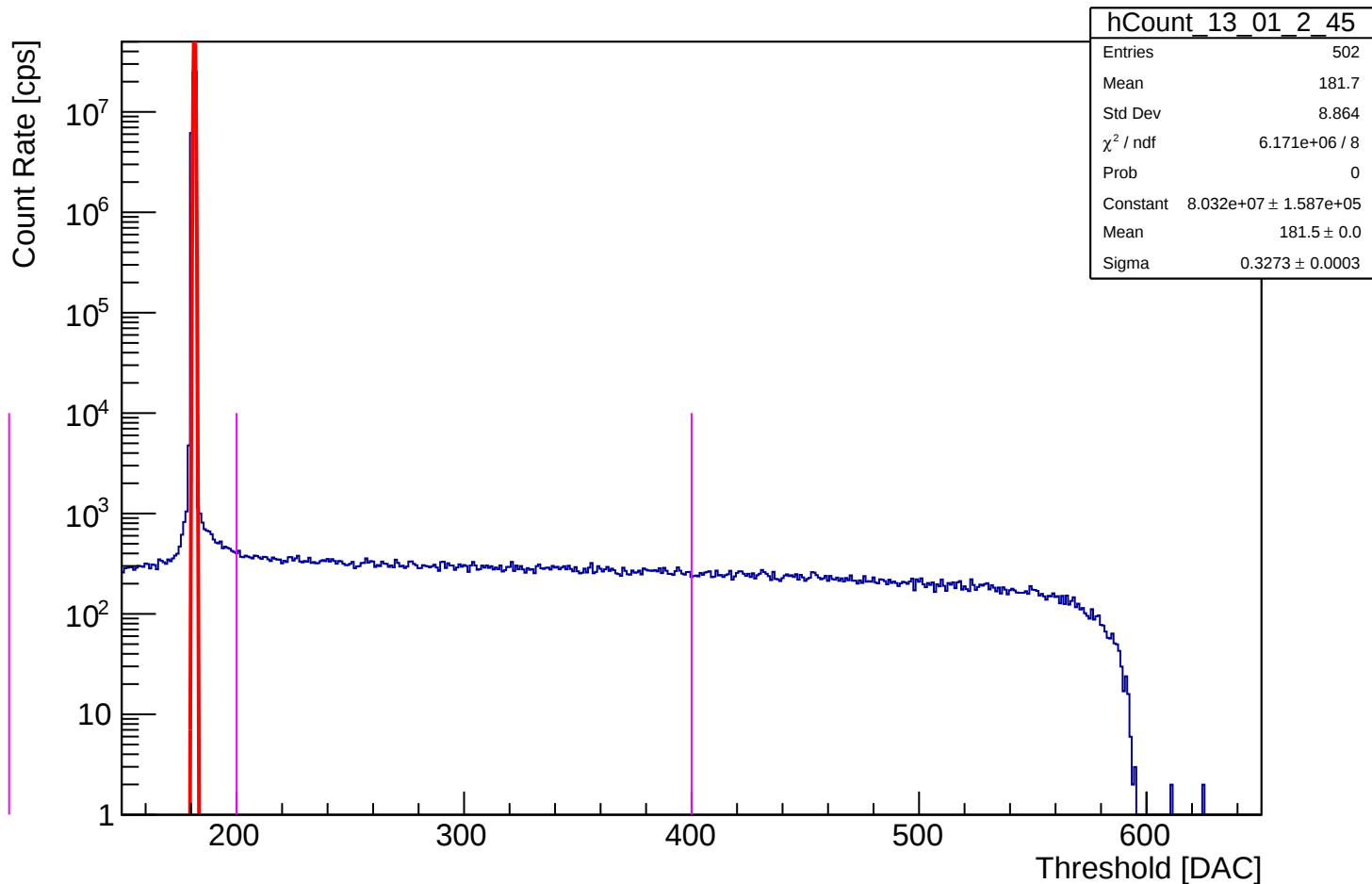
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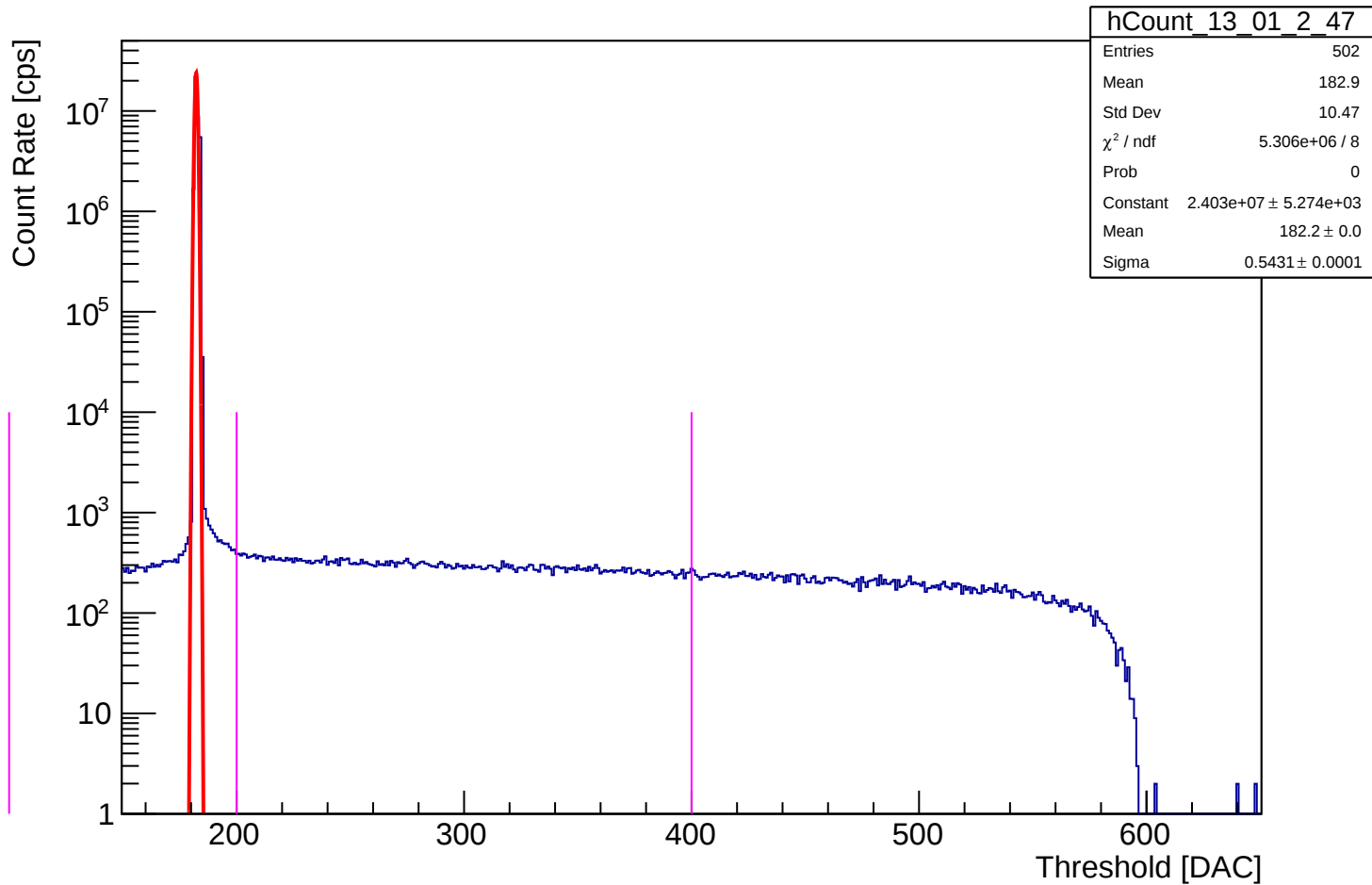
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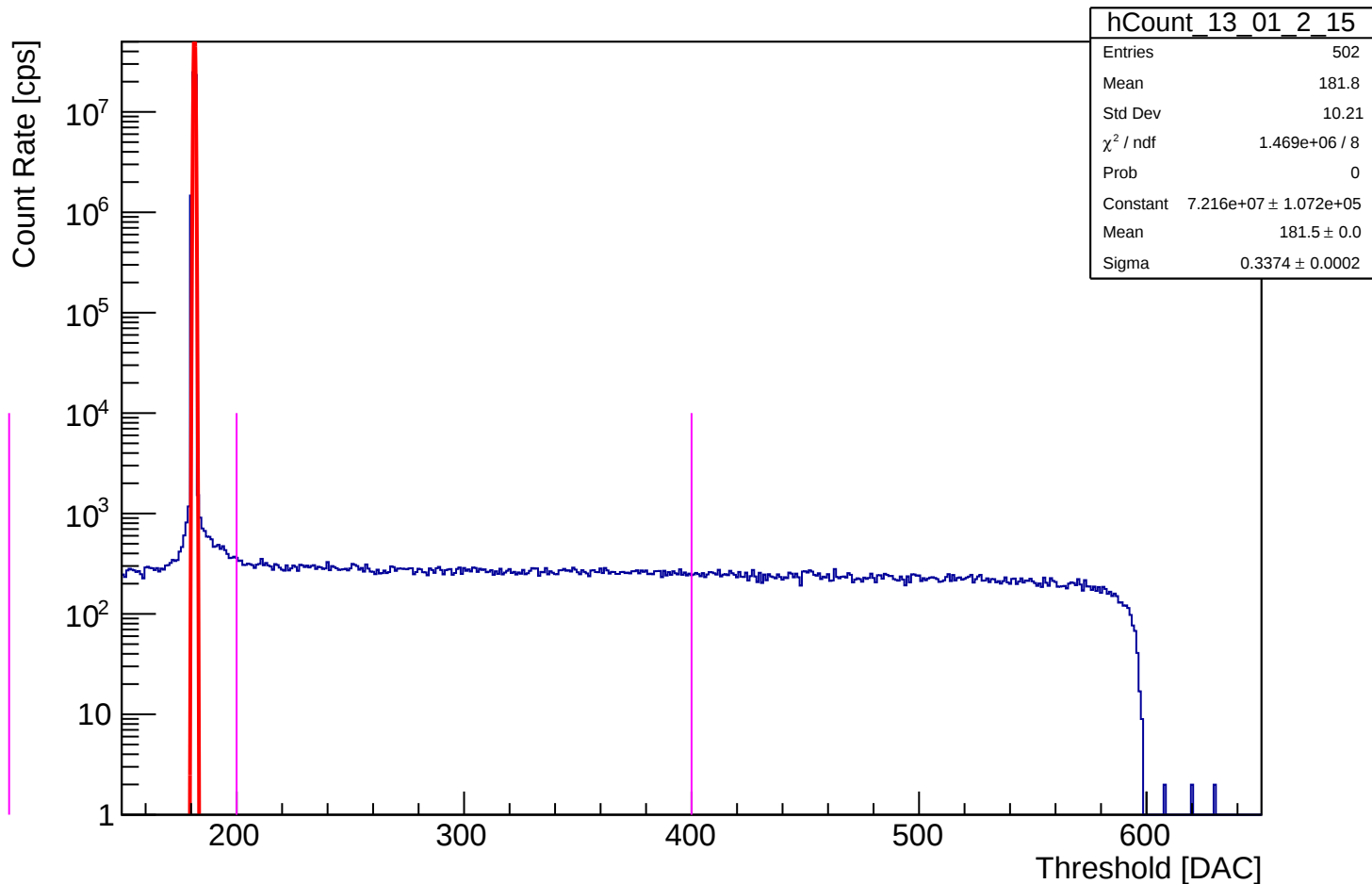
Row 1 Tile 1 Pmt 6 Pixel 31 Slot 13 Fiber 1 Asic 2 Channel 45



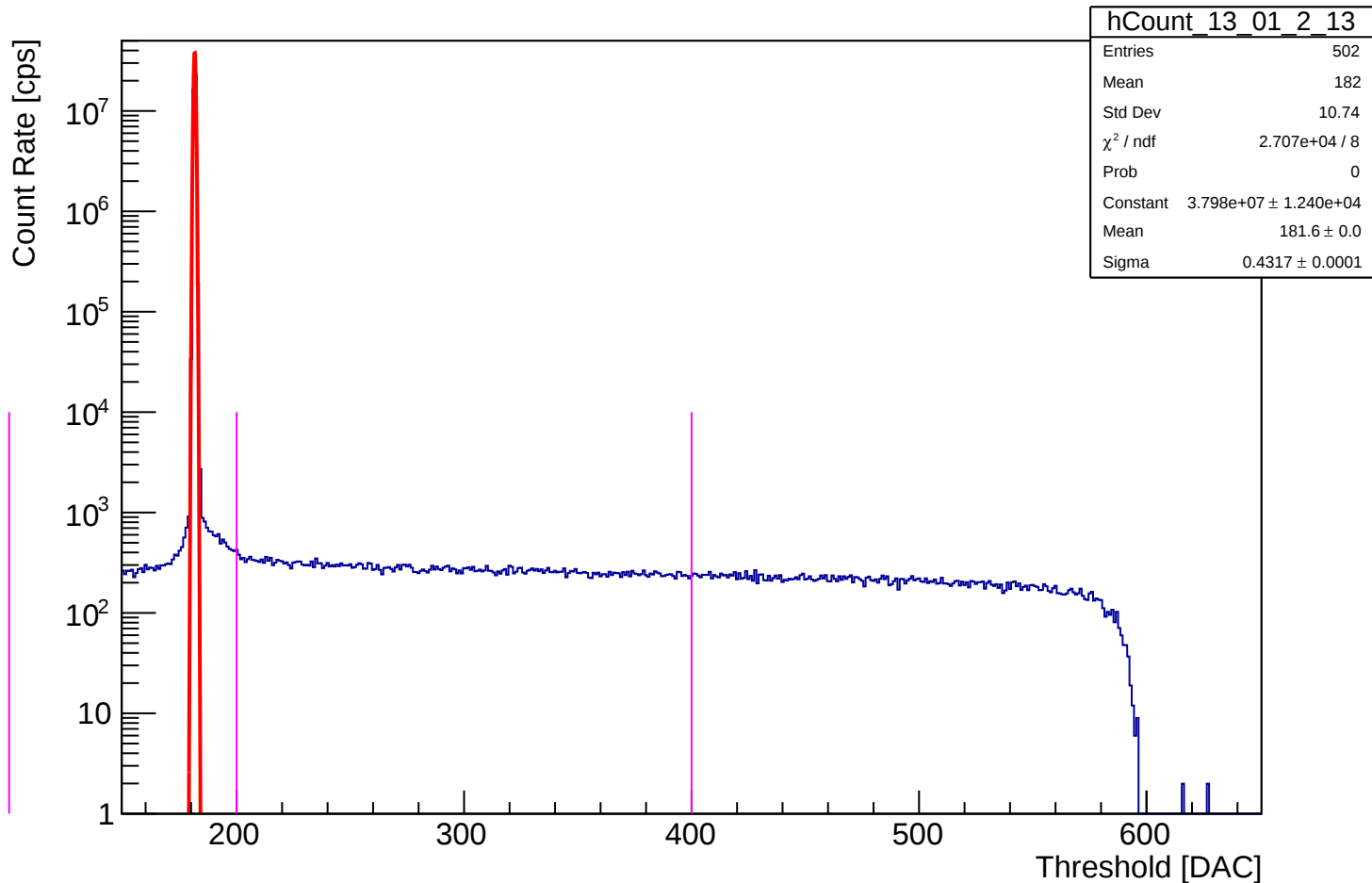
Row 1 Tile 1 Pmt 6 Pixel 32 Slot 13 Fiber 1 Asic 2 Channel 47



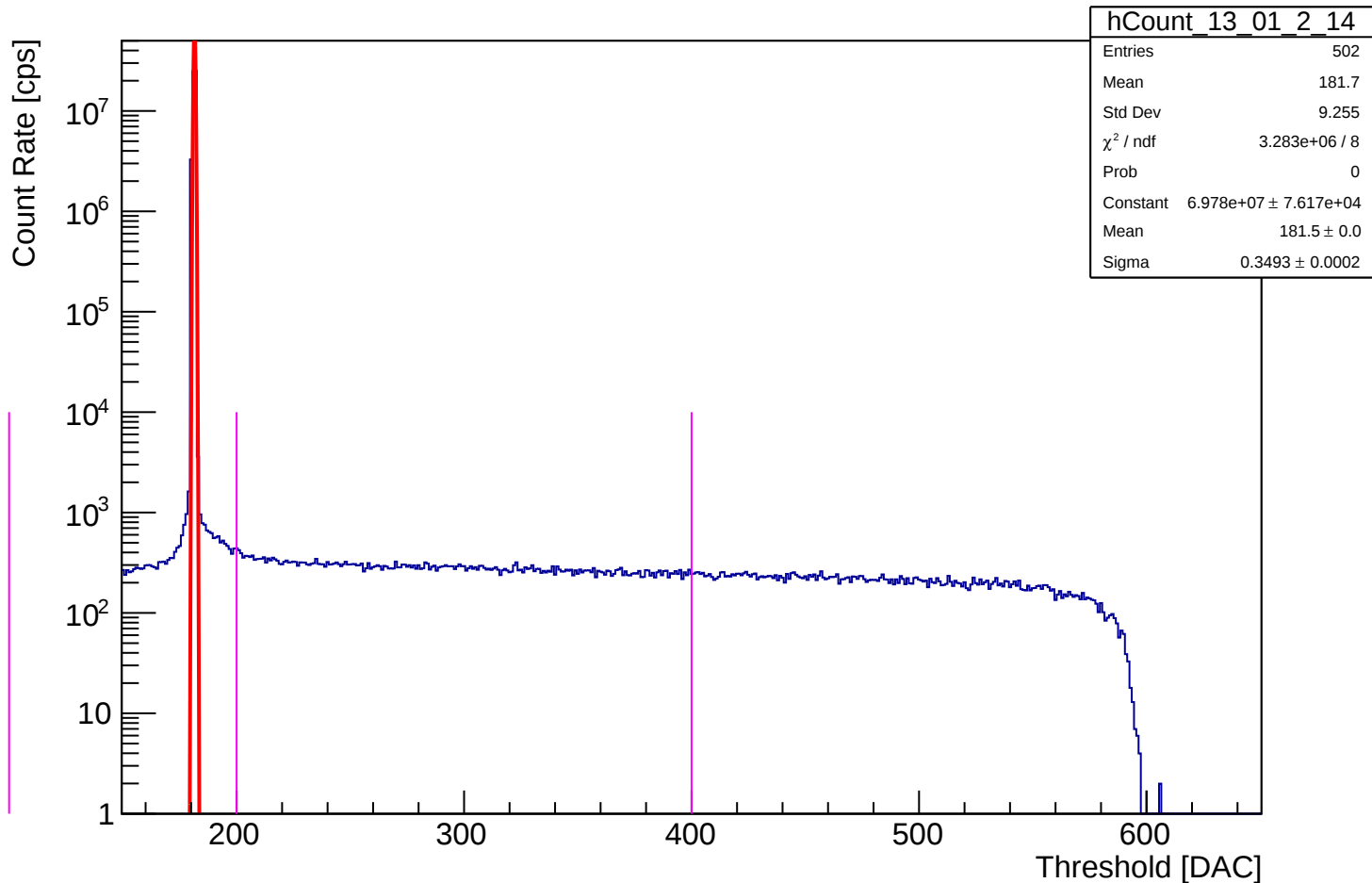
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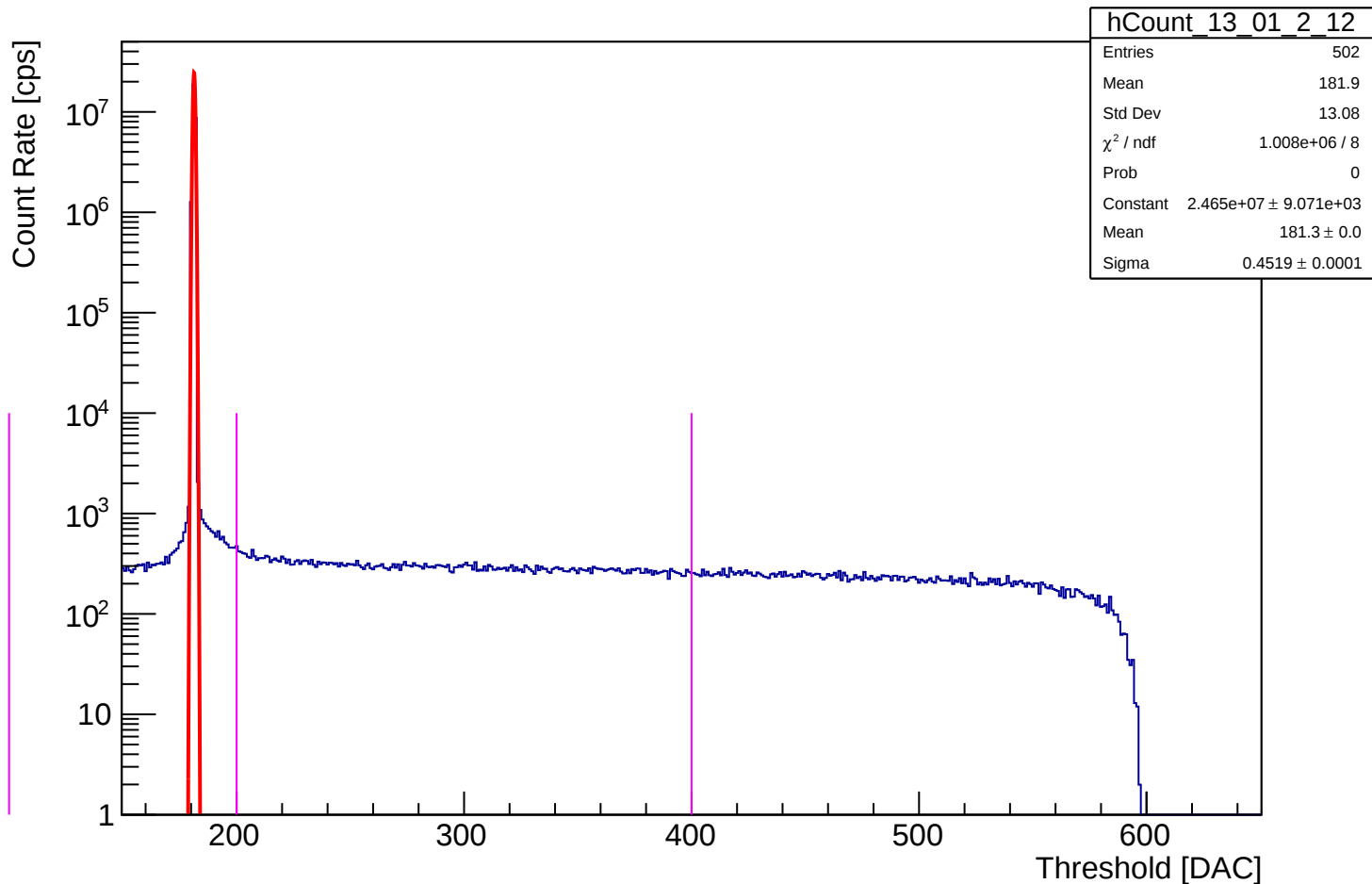
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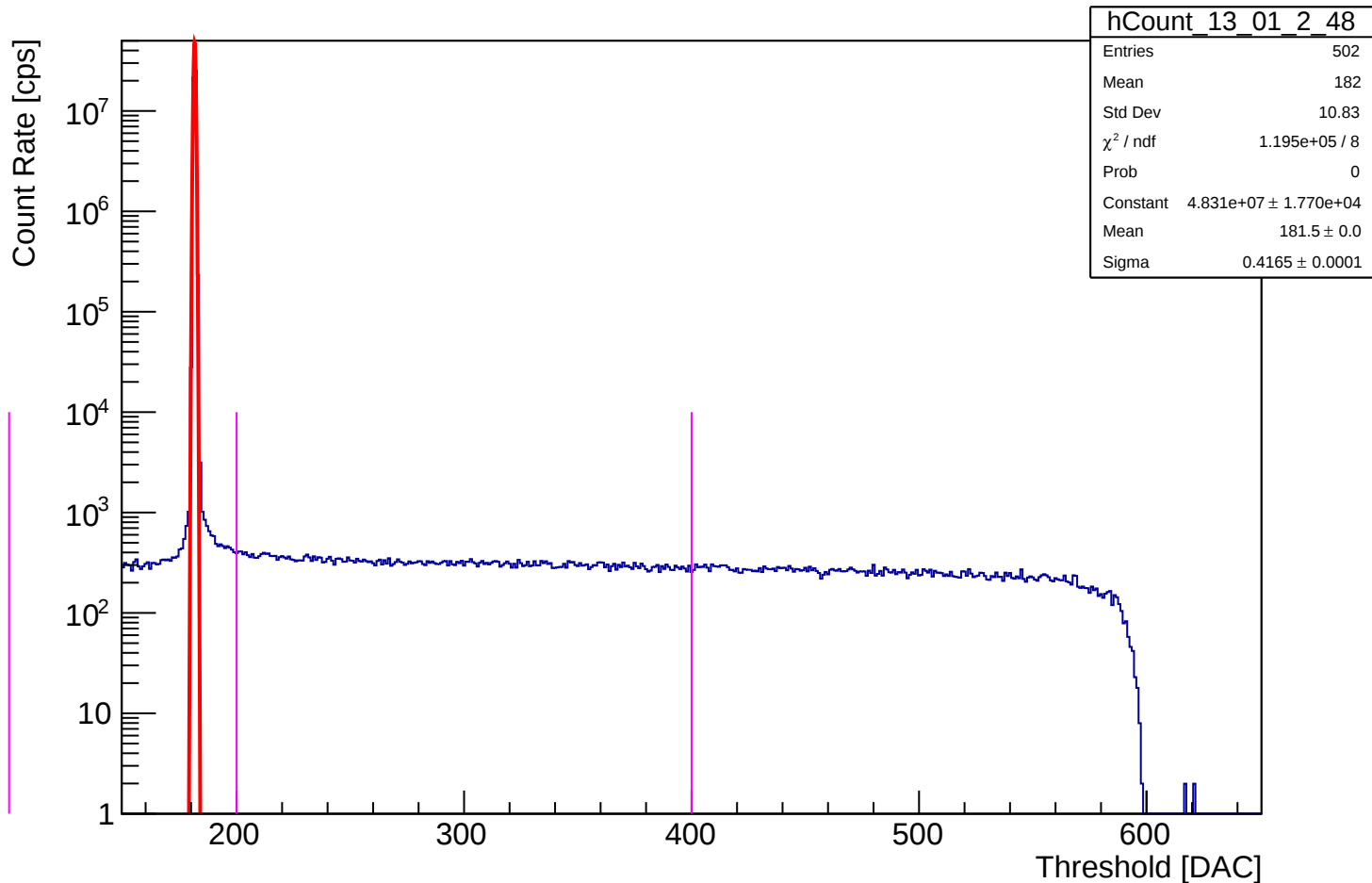
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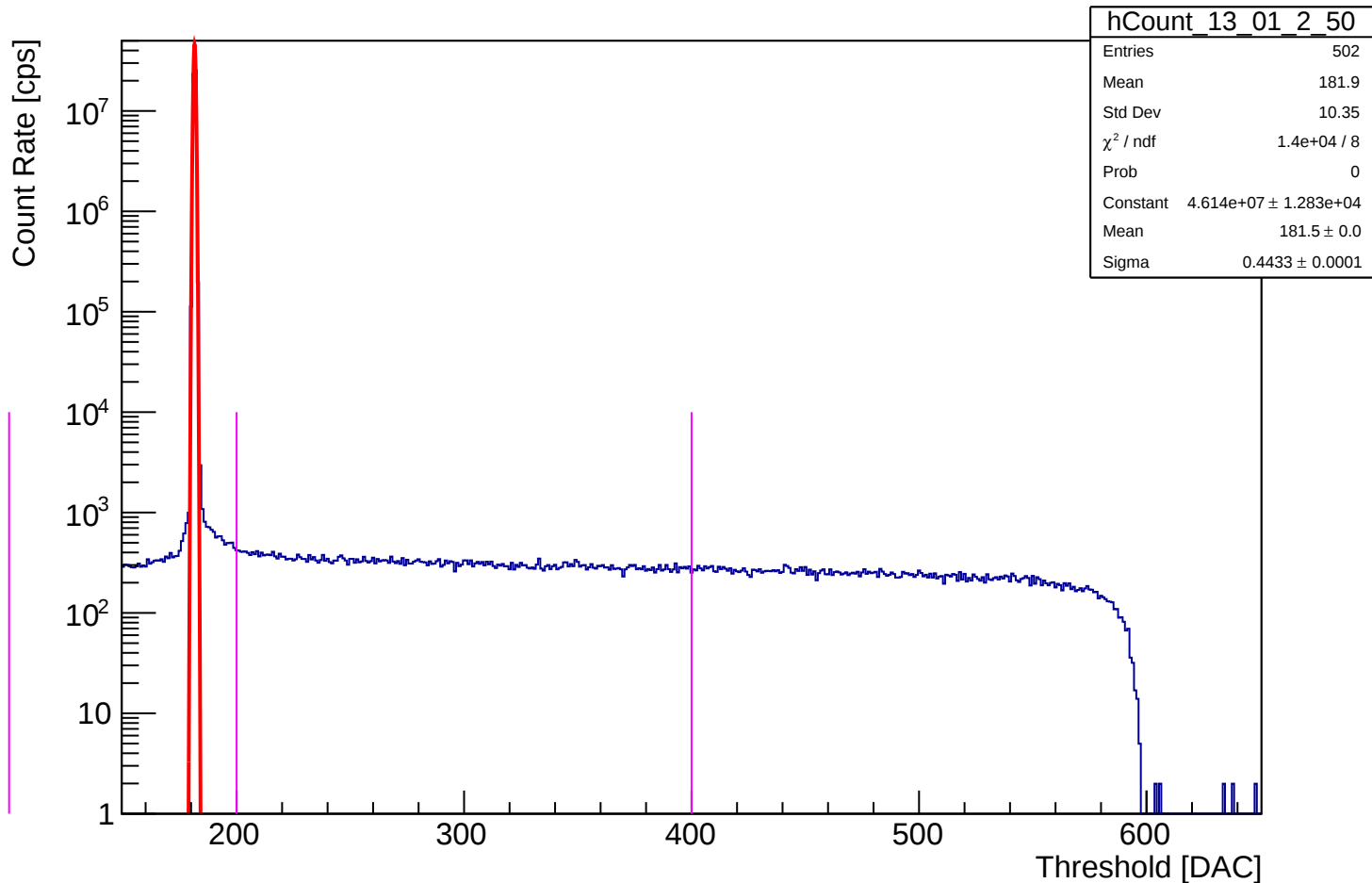
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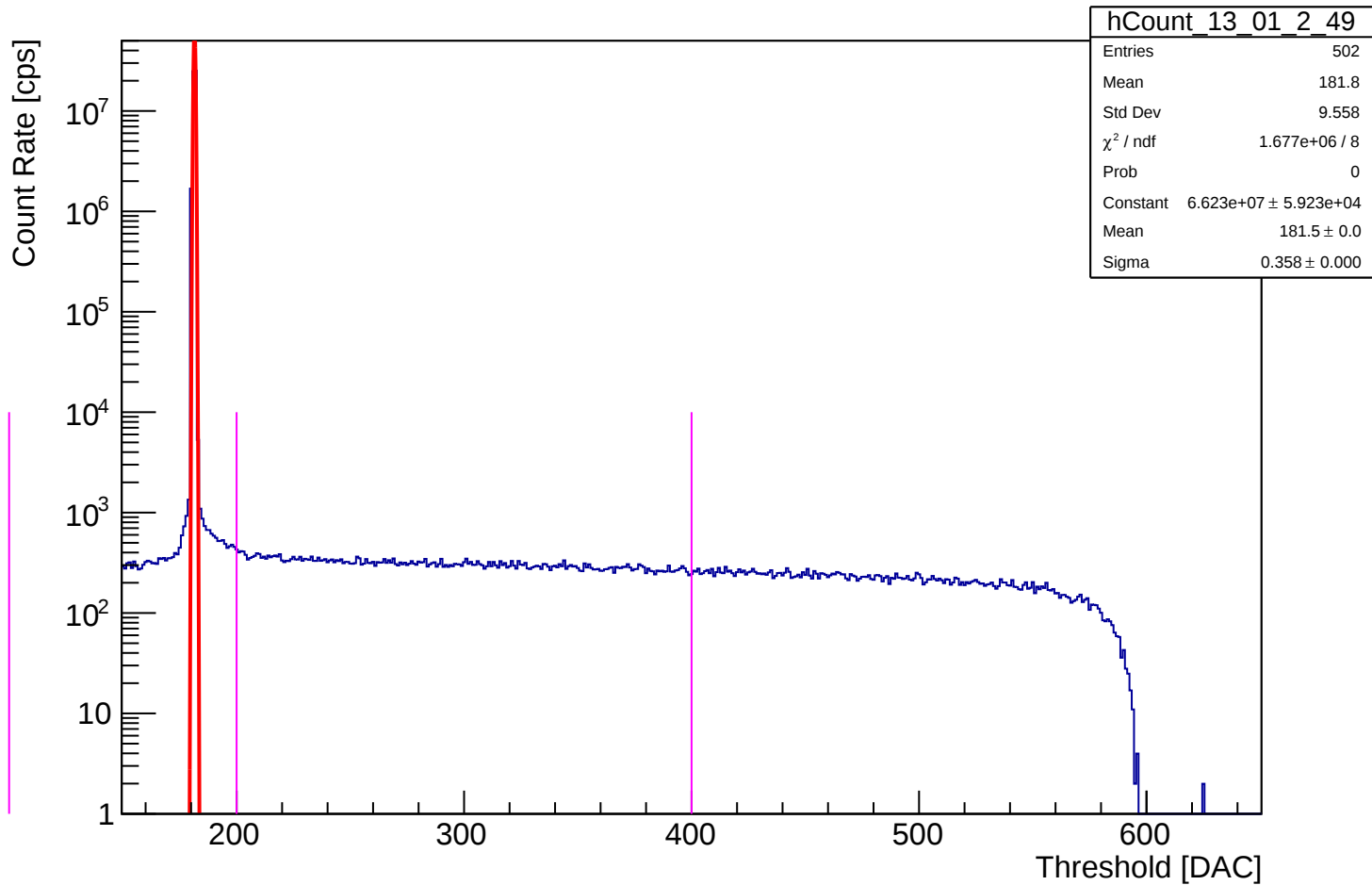
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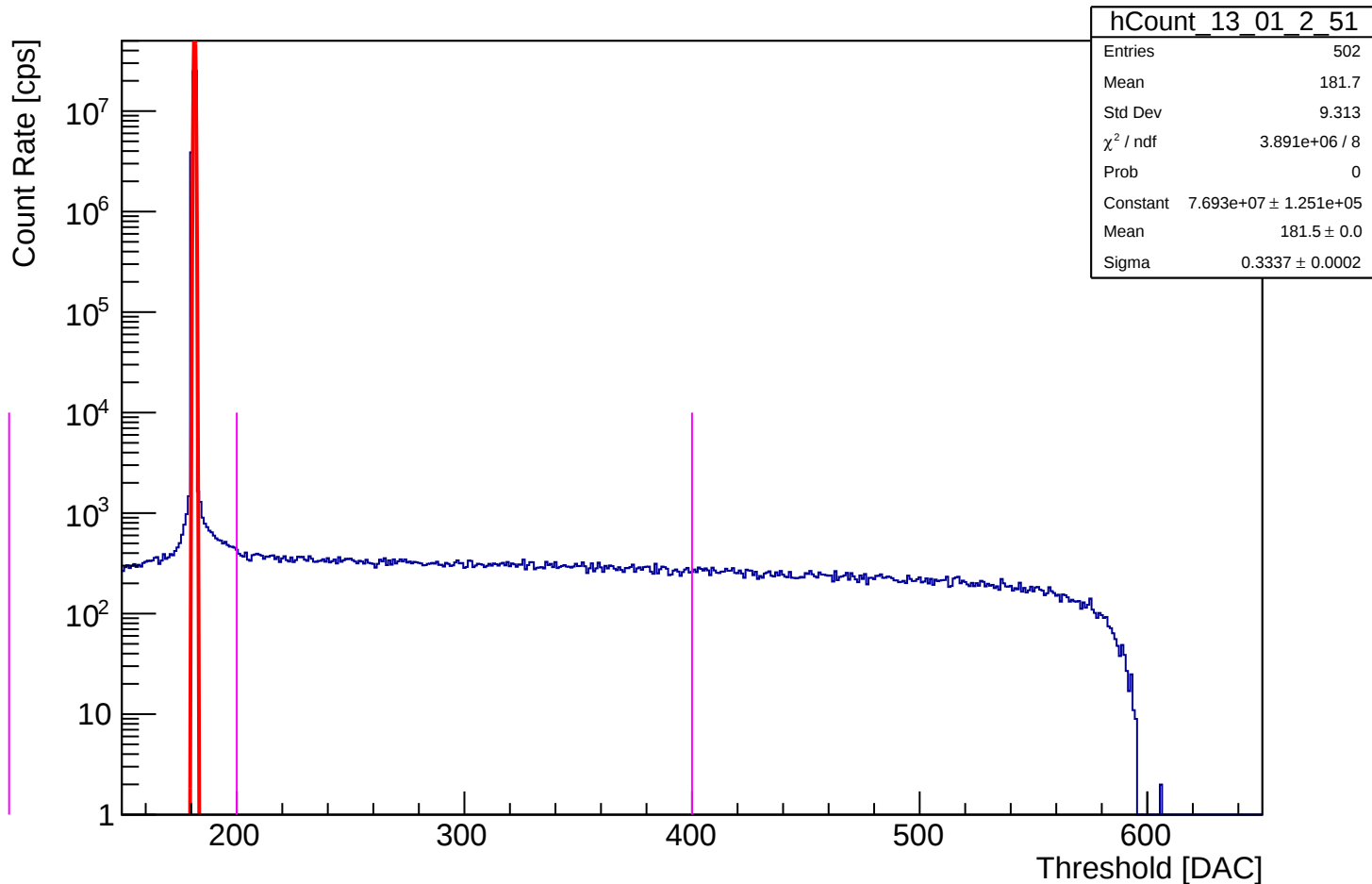
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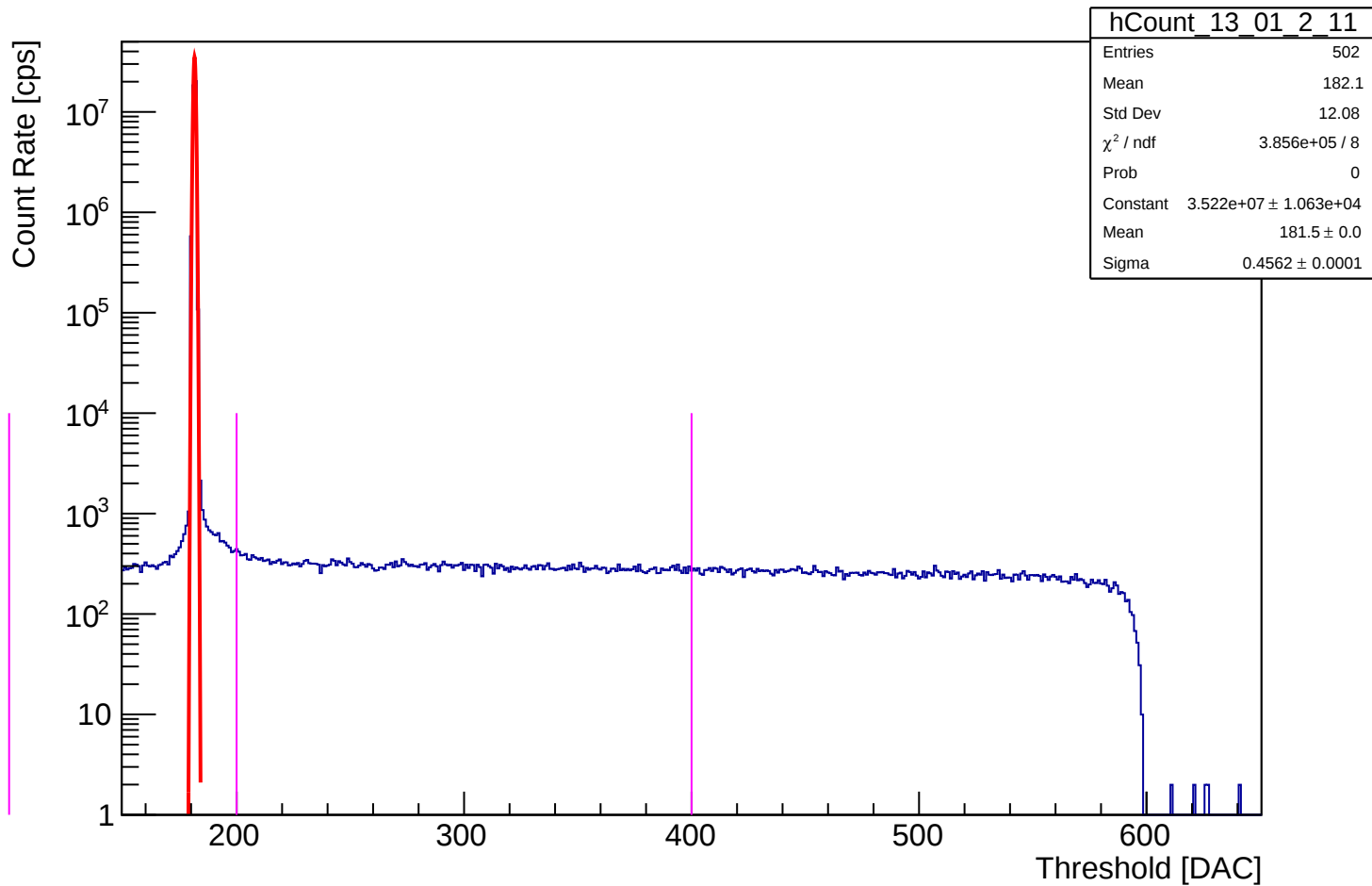
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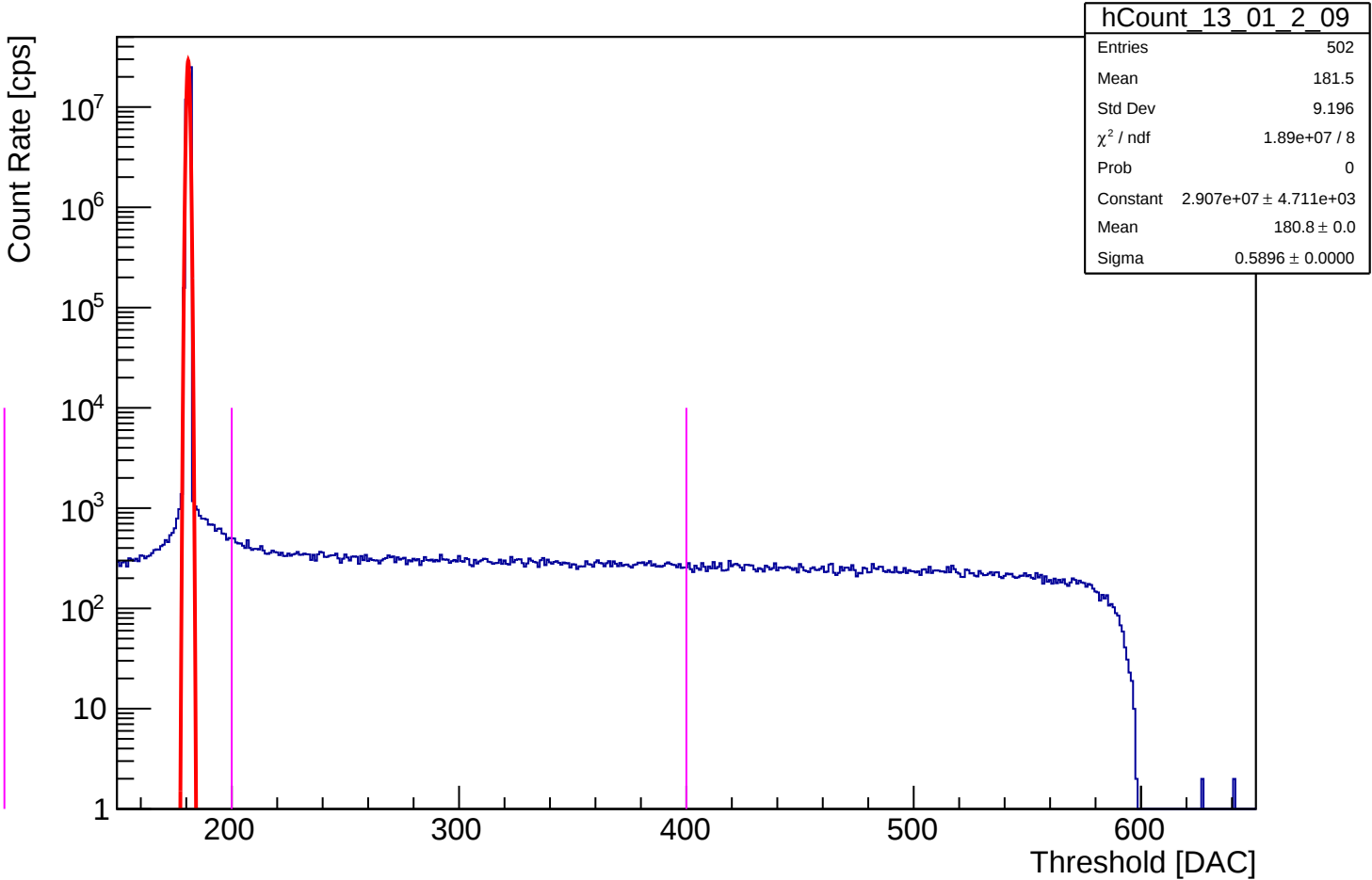


Row 1 Tile 1 Pmt 6 Pixel 40 Slot 13 Fiber 1 Asic 2 Channel 51

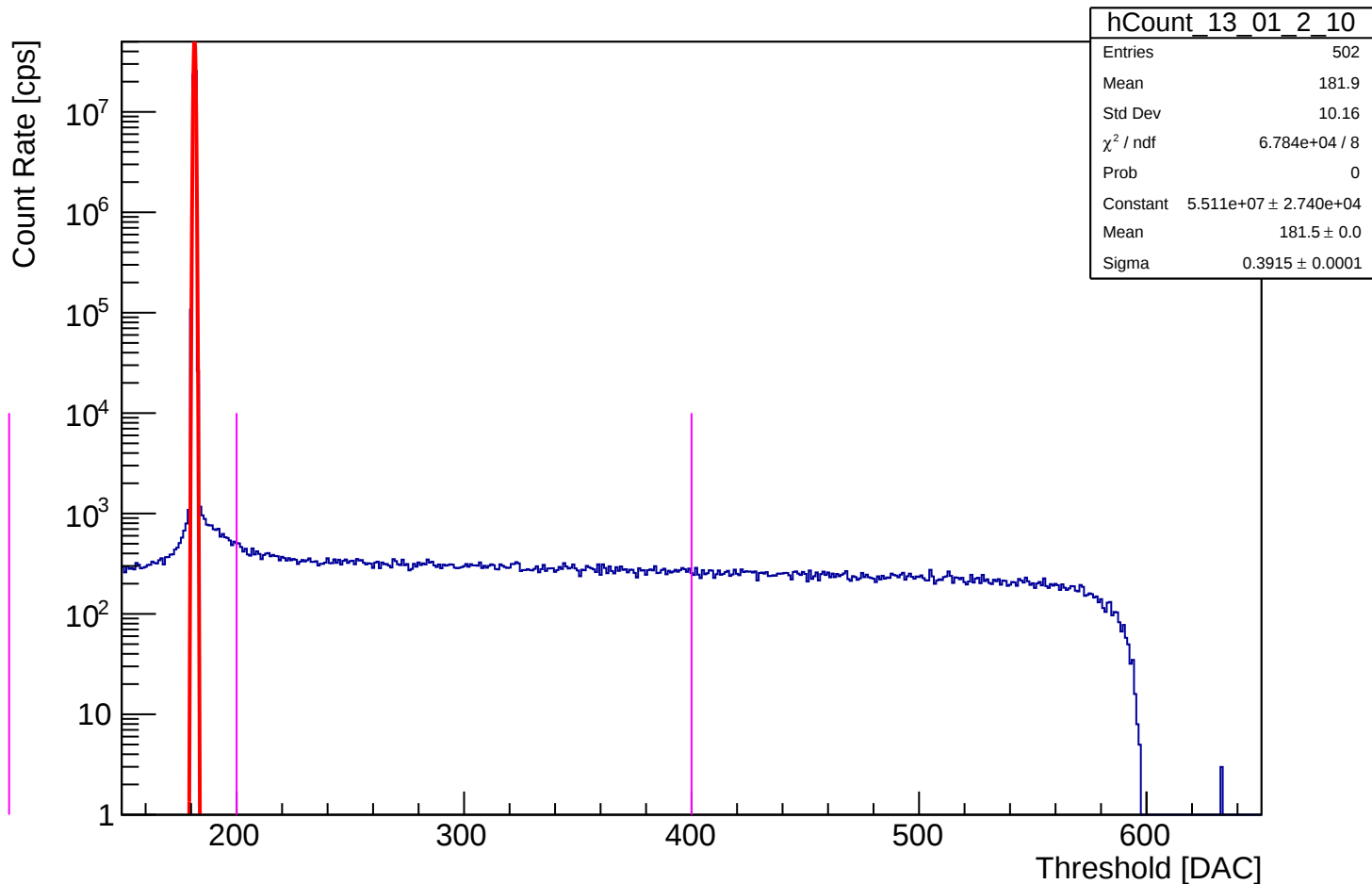


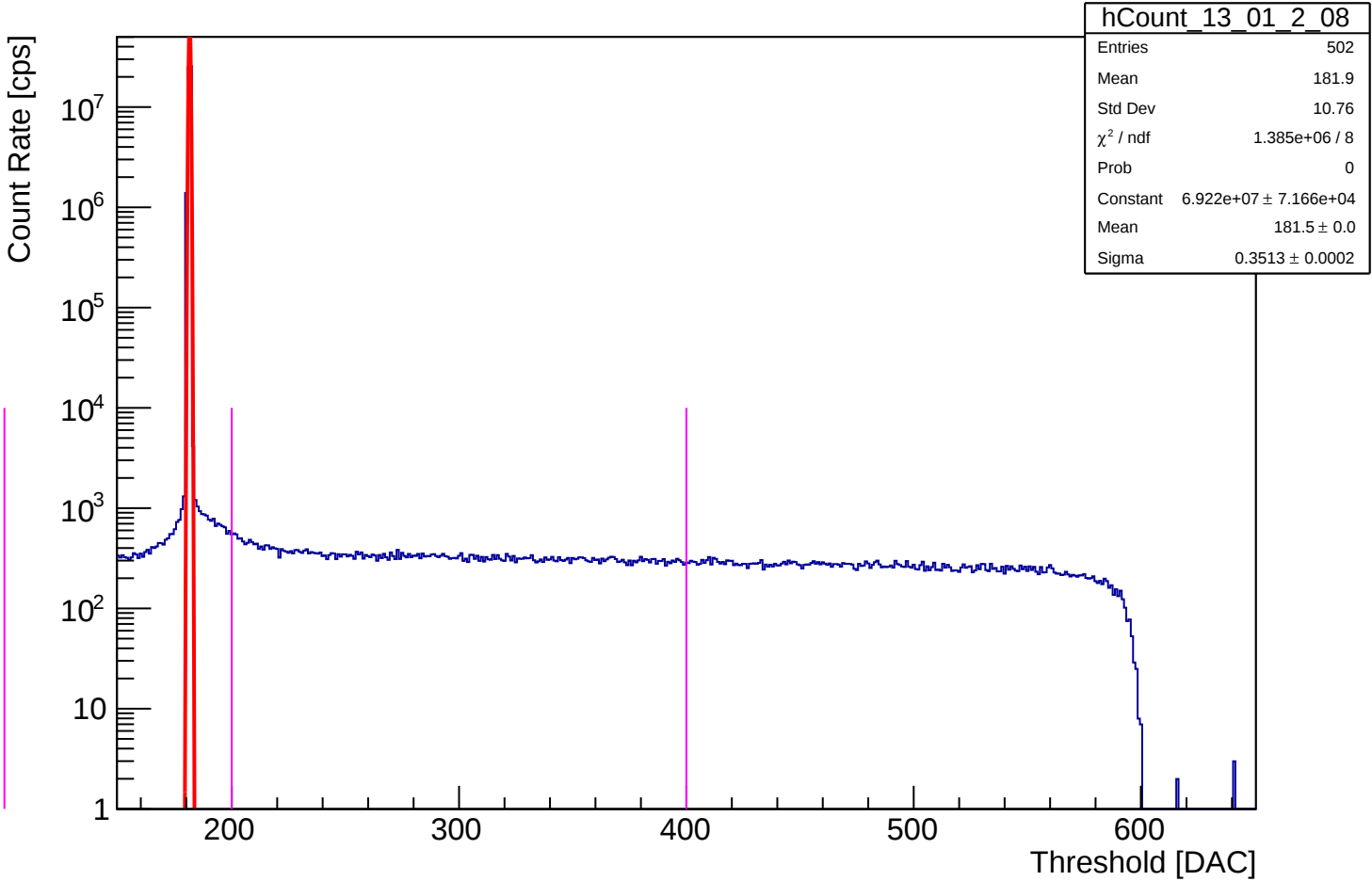
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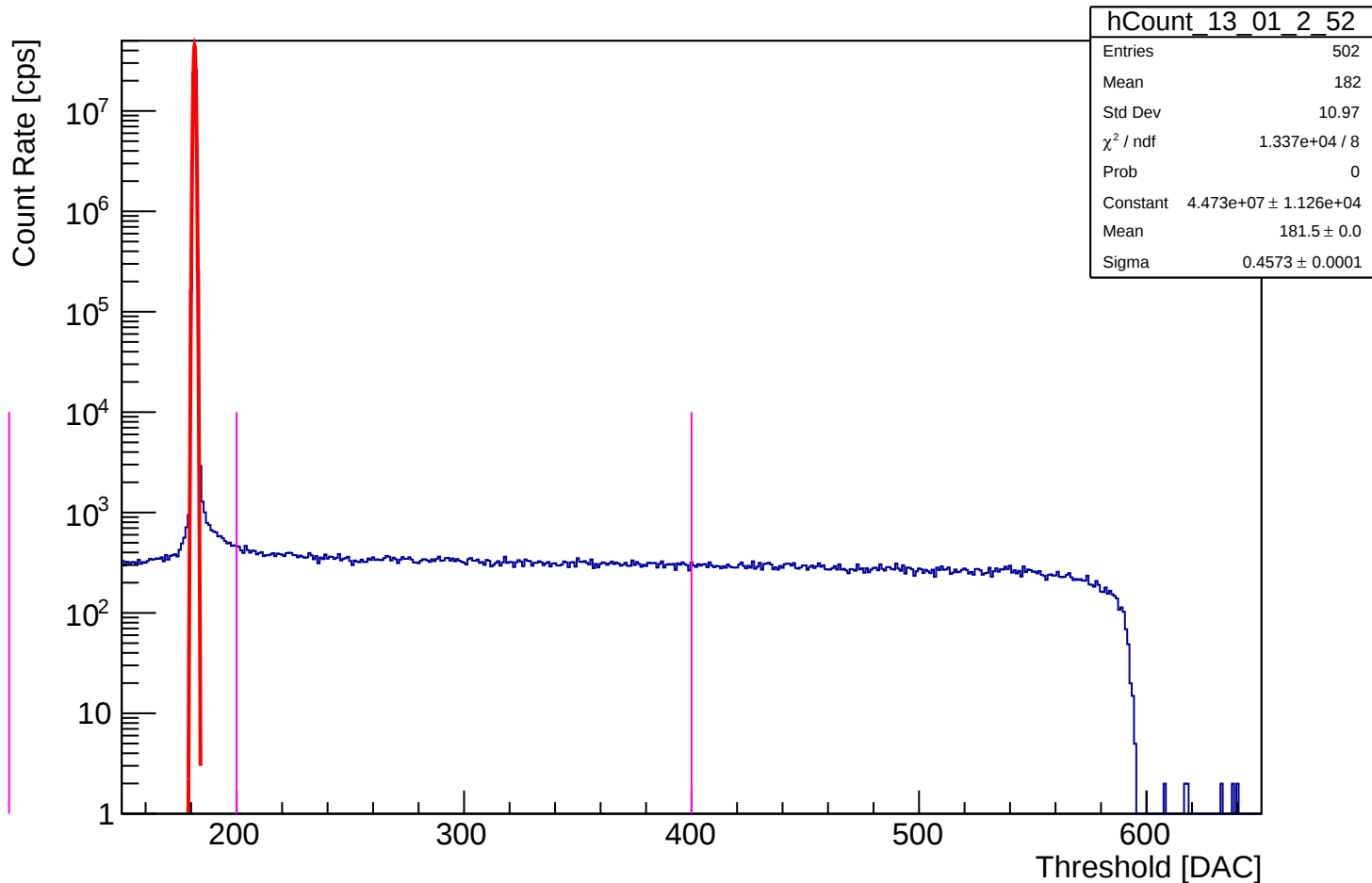


Row 1 Tile 1 Pmt 6 Pixel 43 Slot 13 Fiber 1 Asic 2 Channel 10

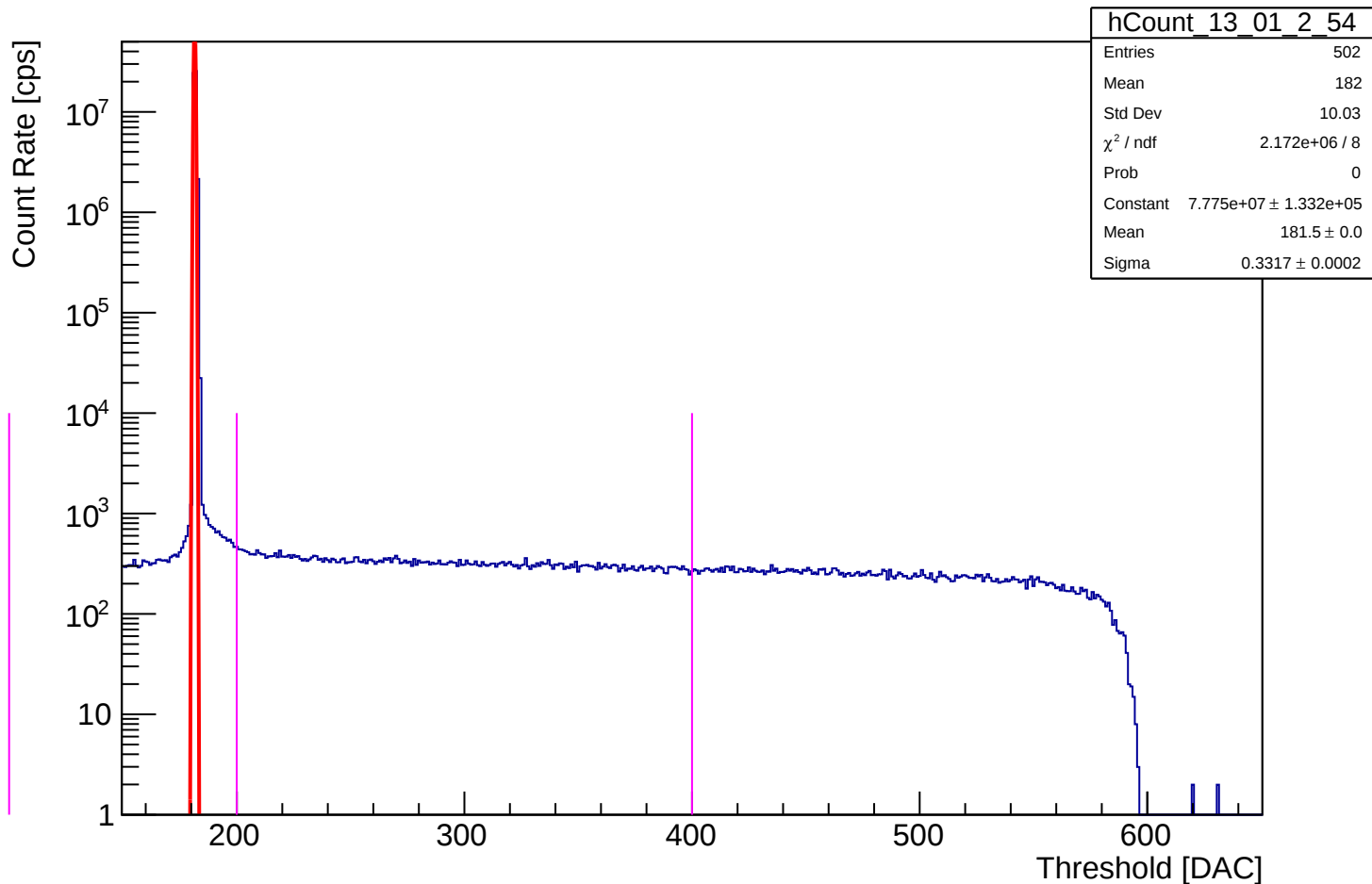




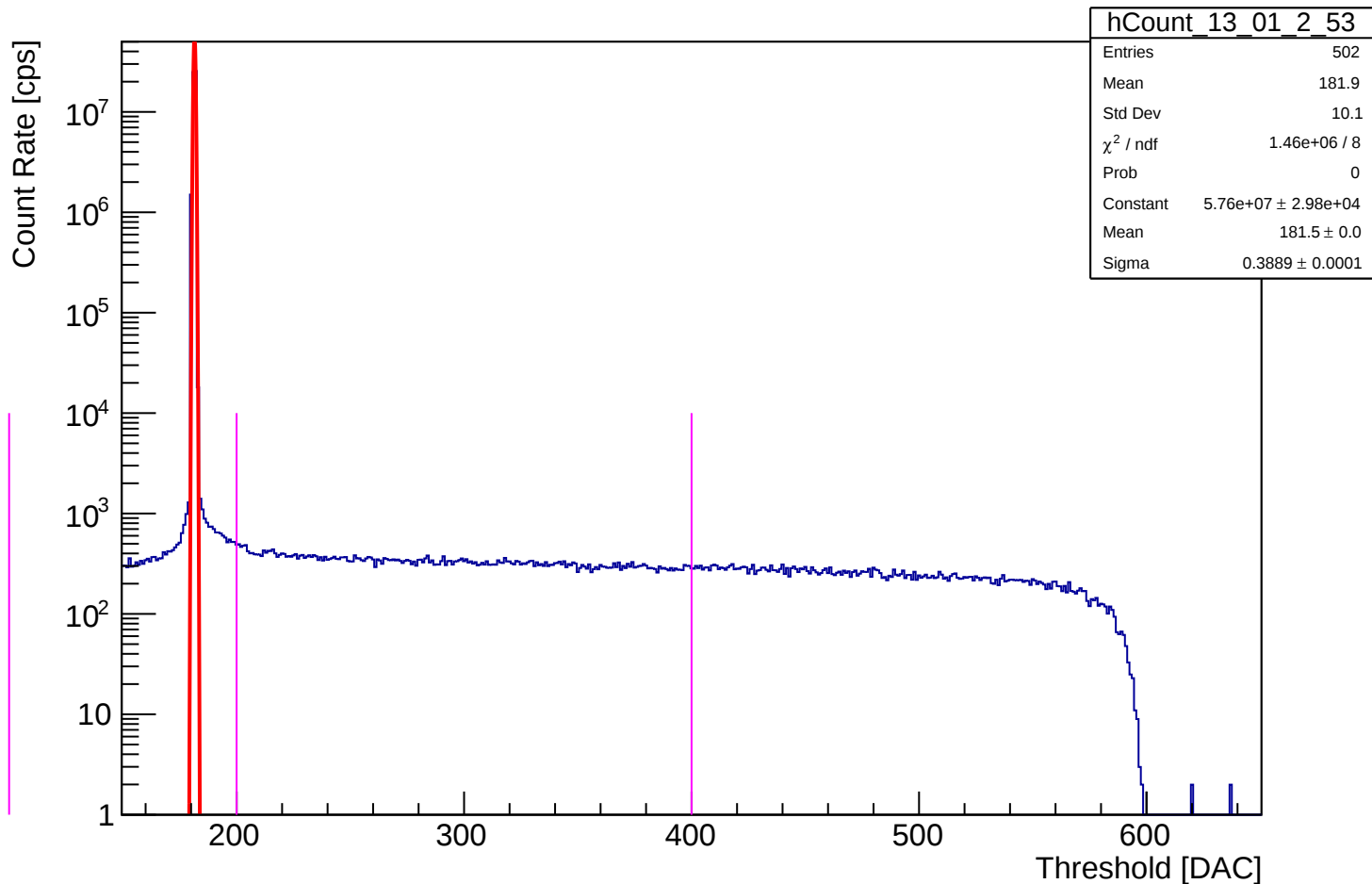
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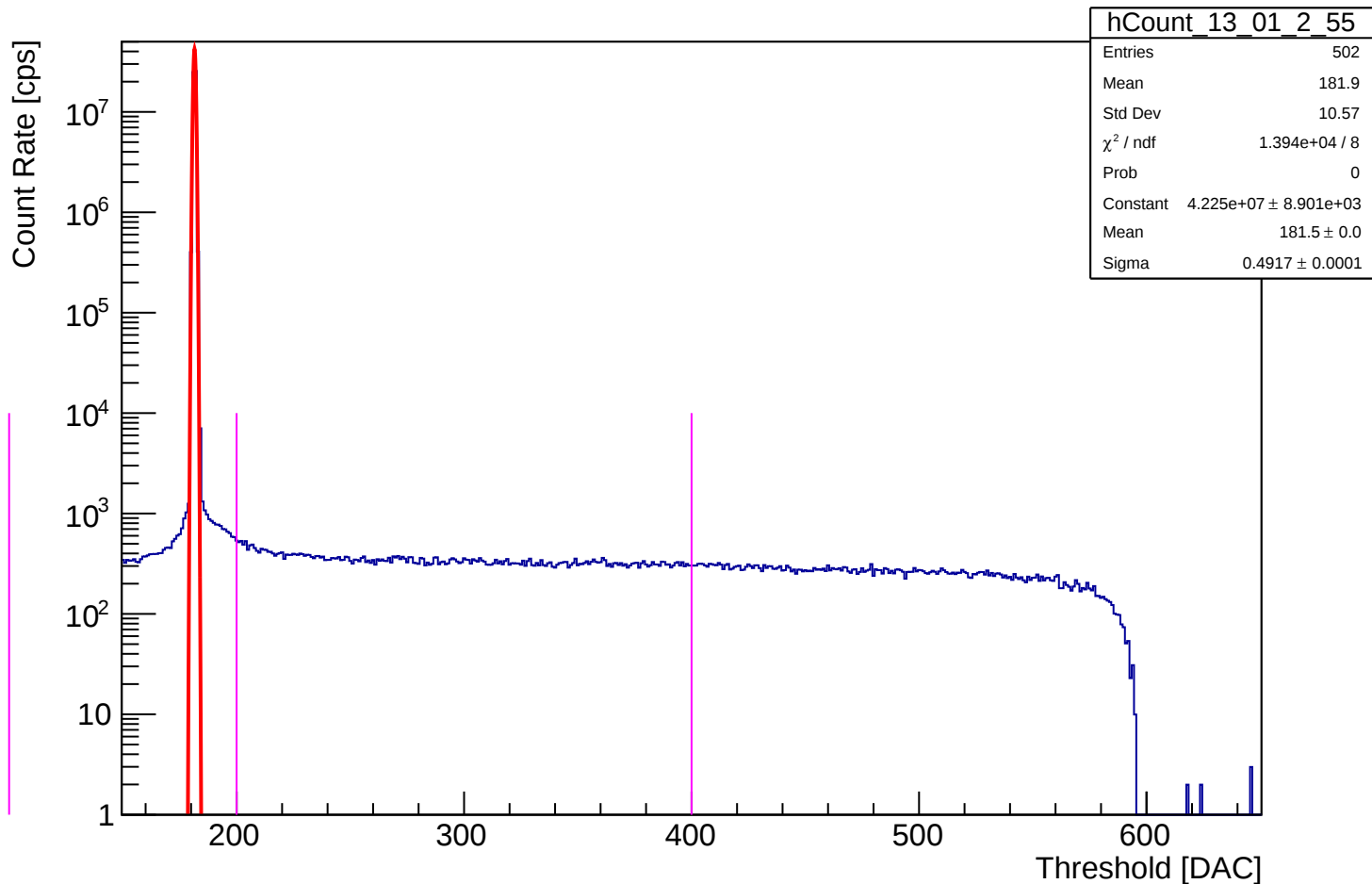
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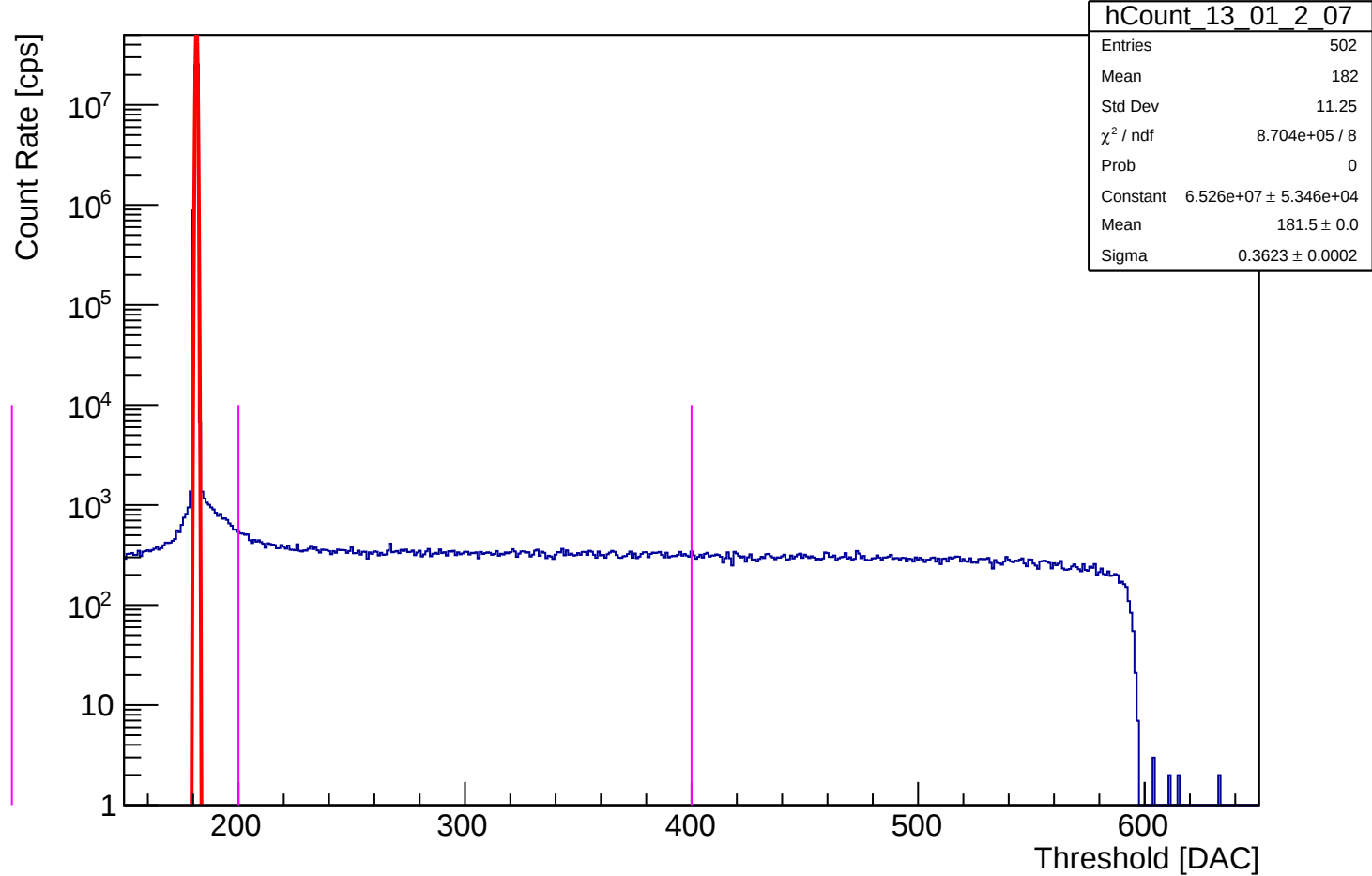


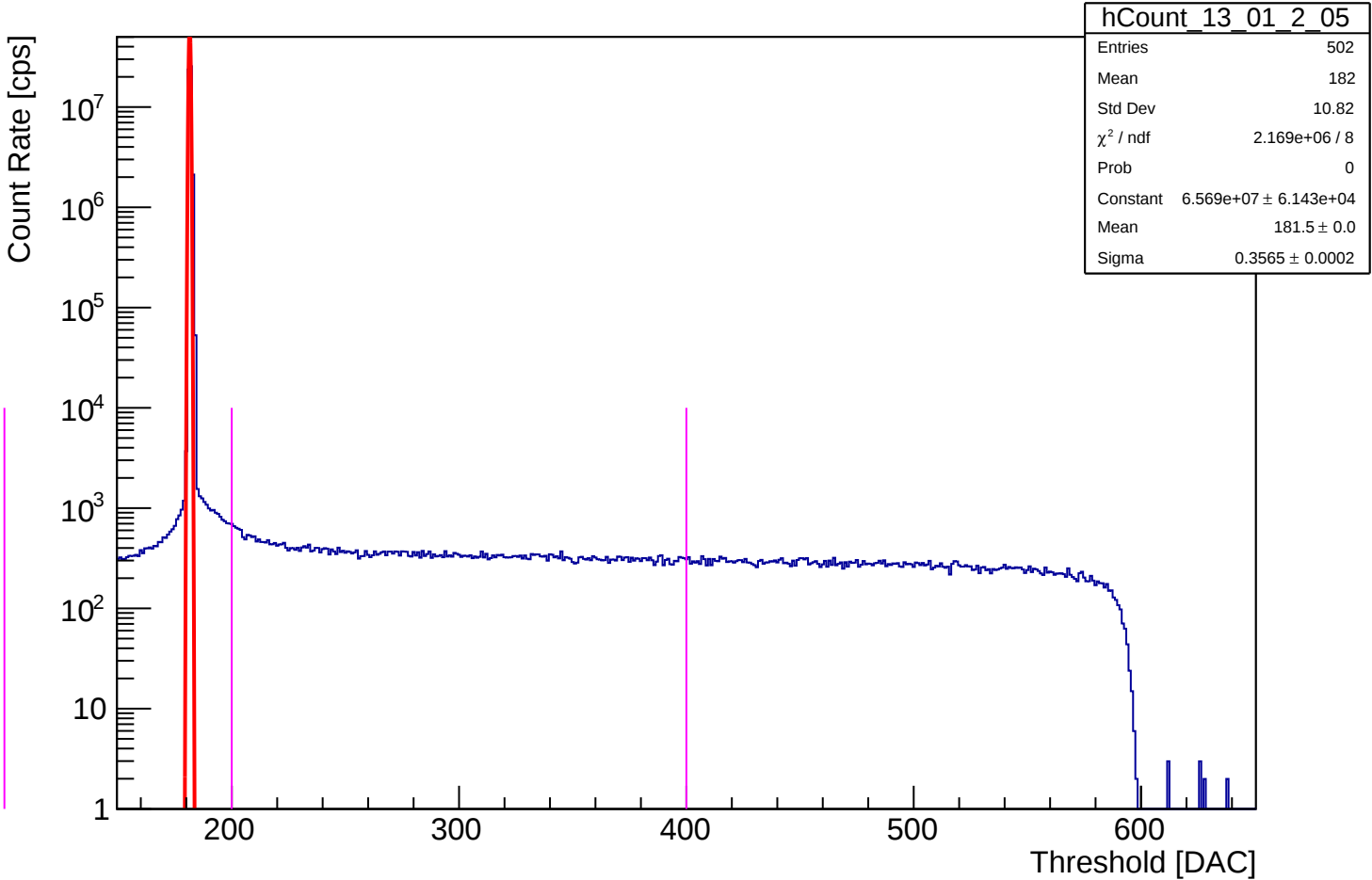
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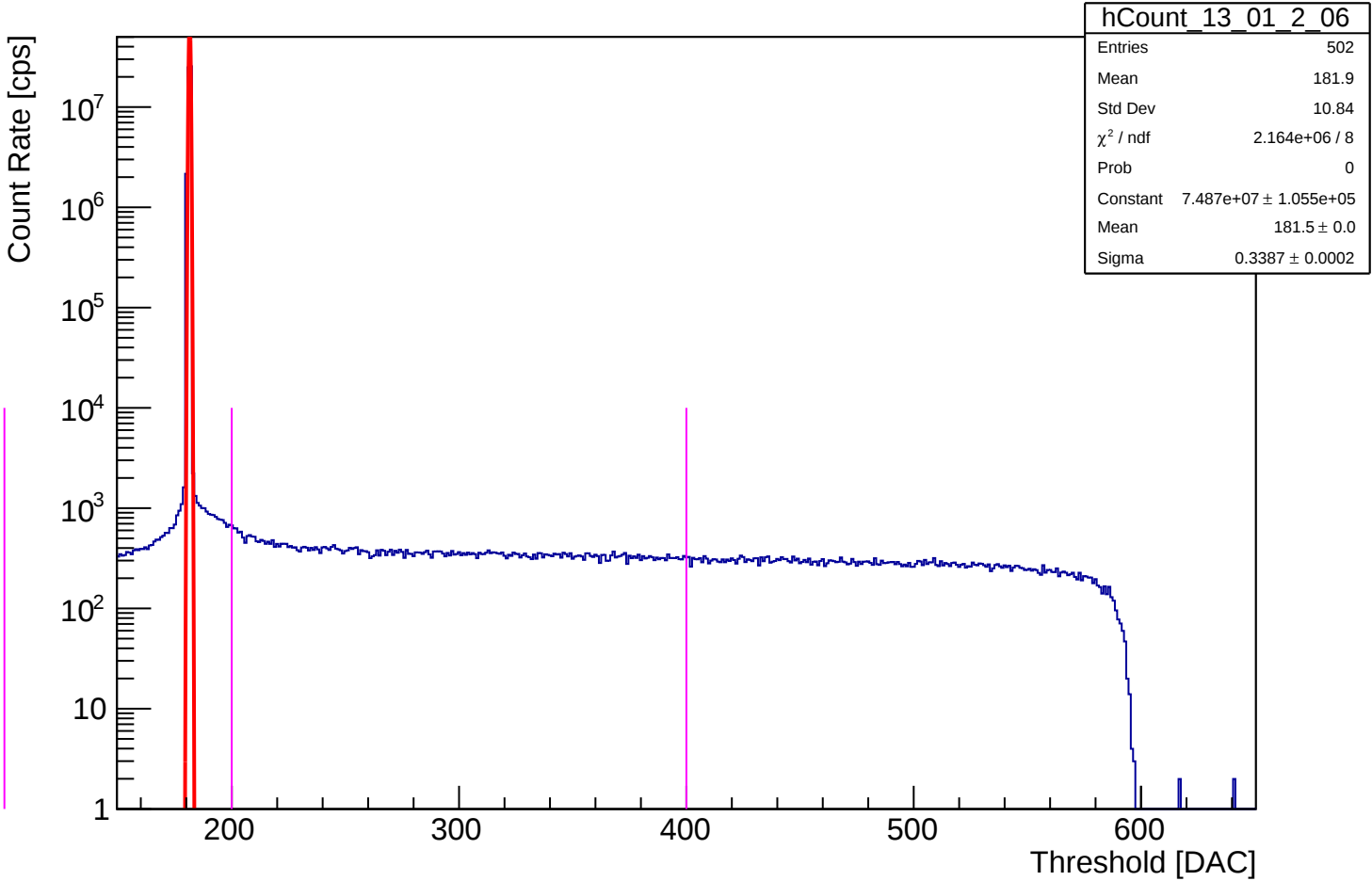


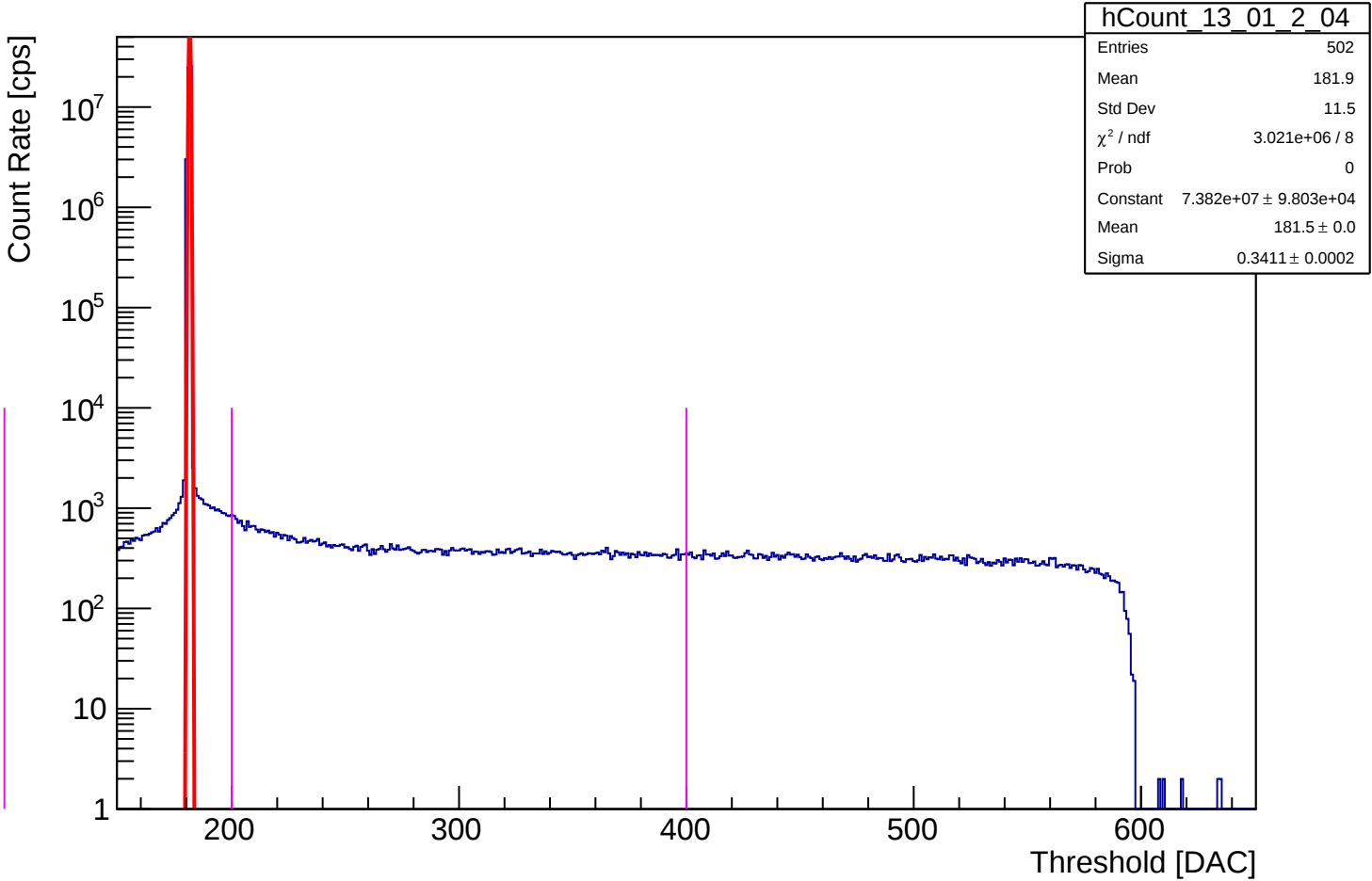
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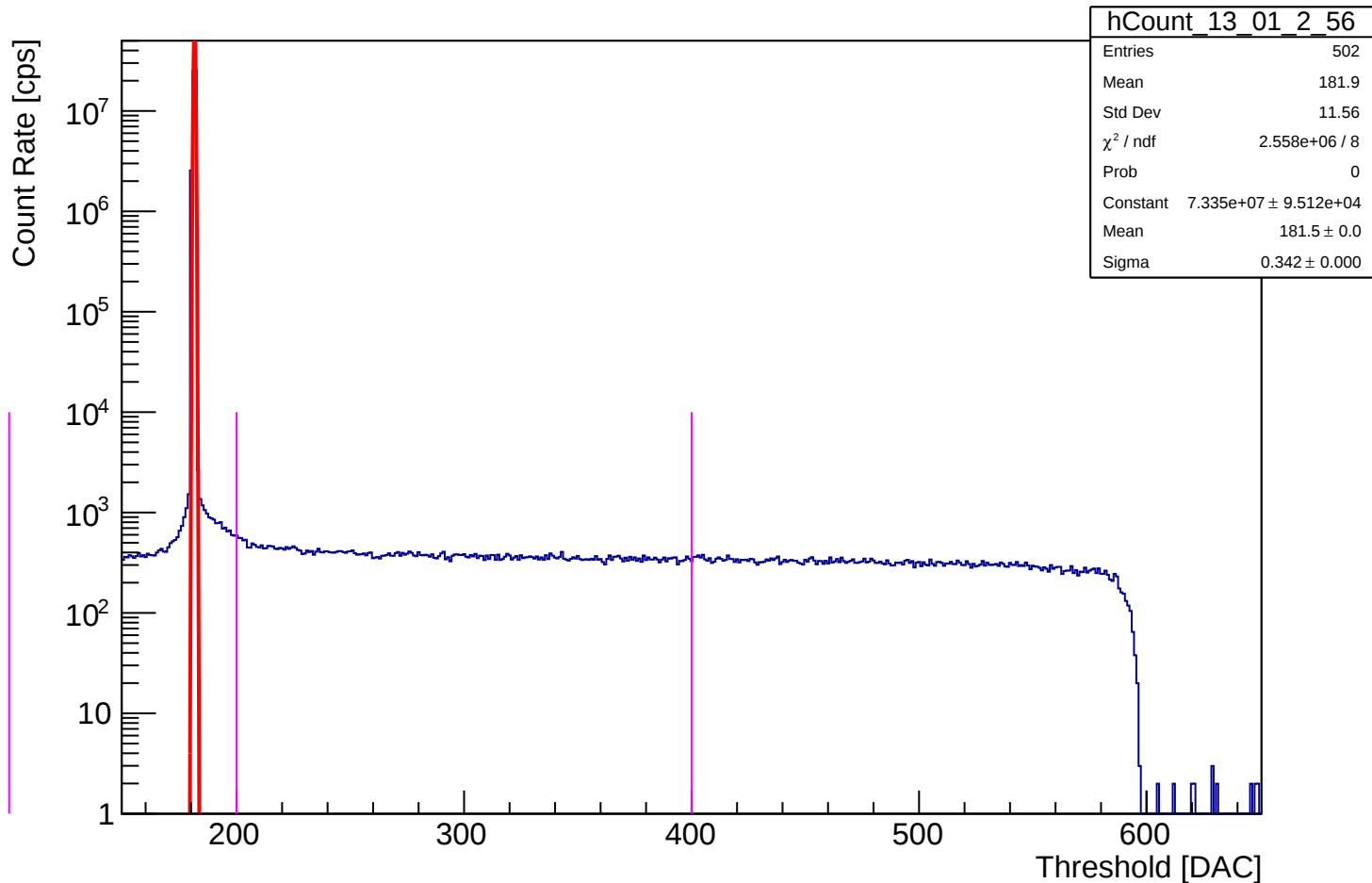




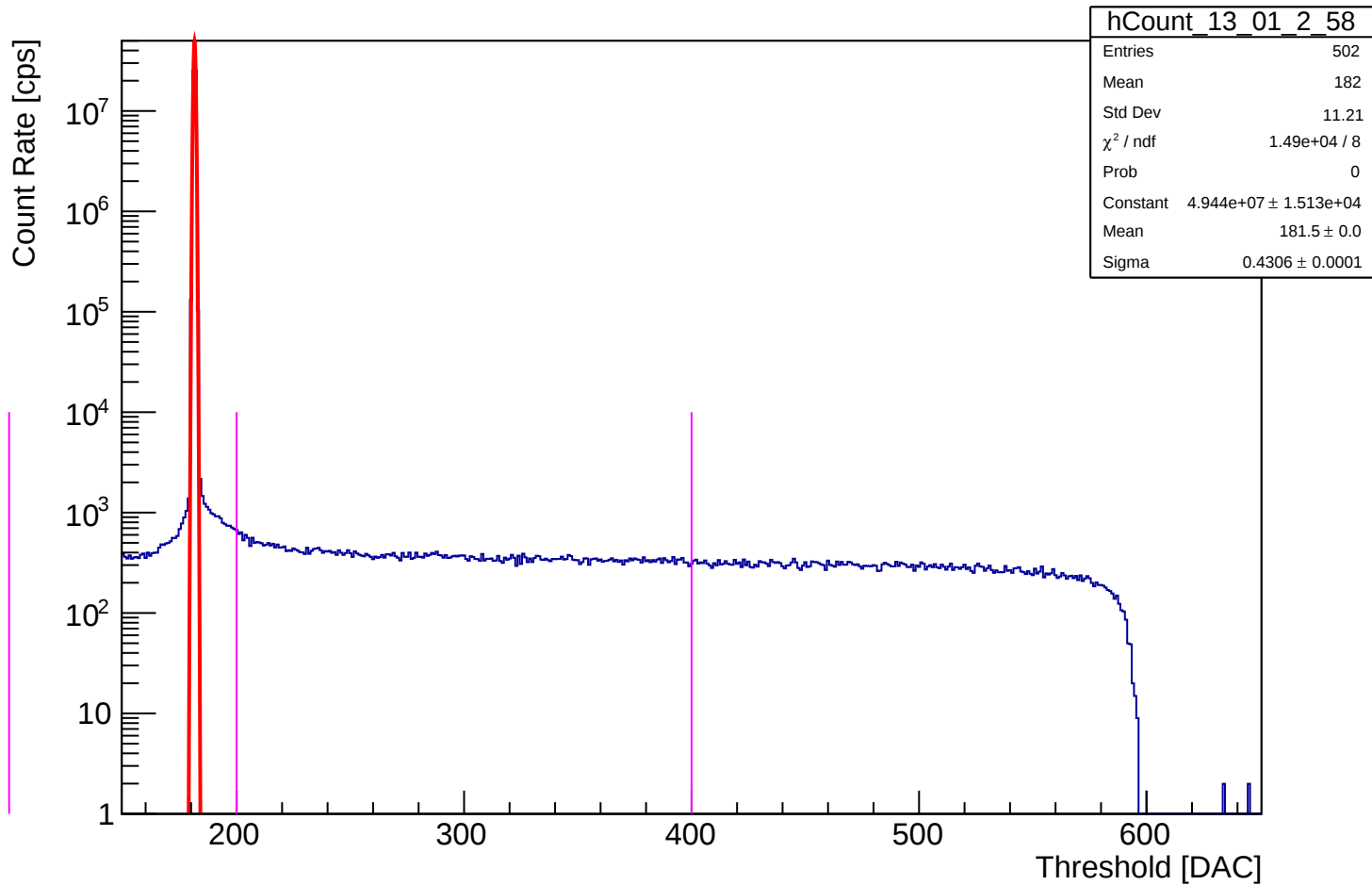




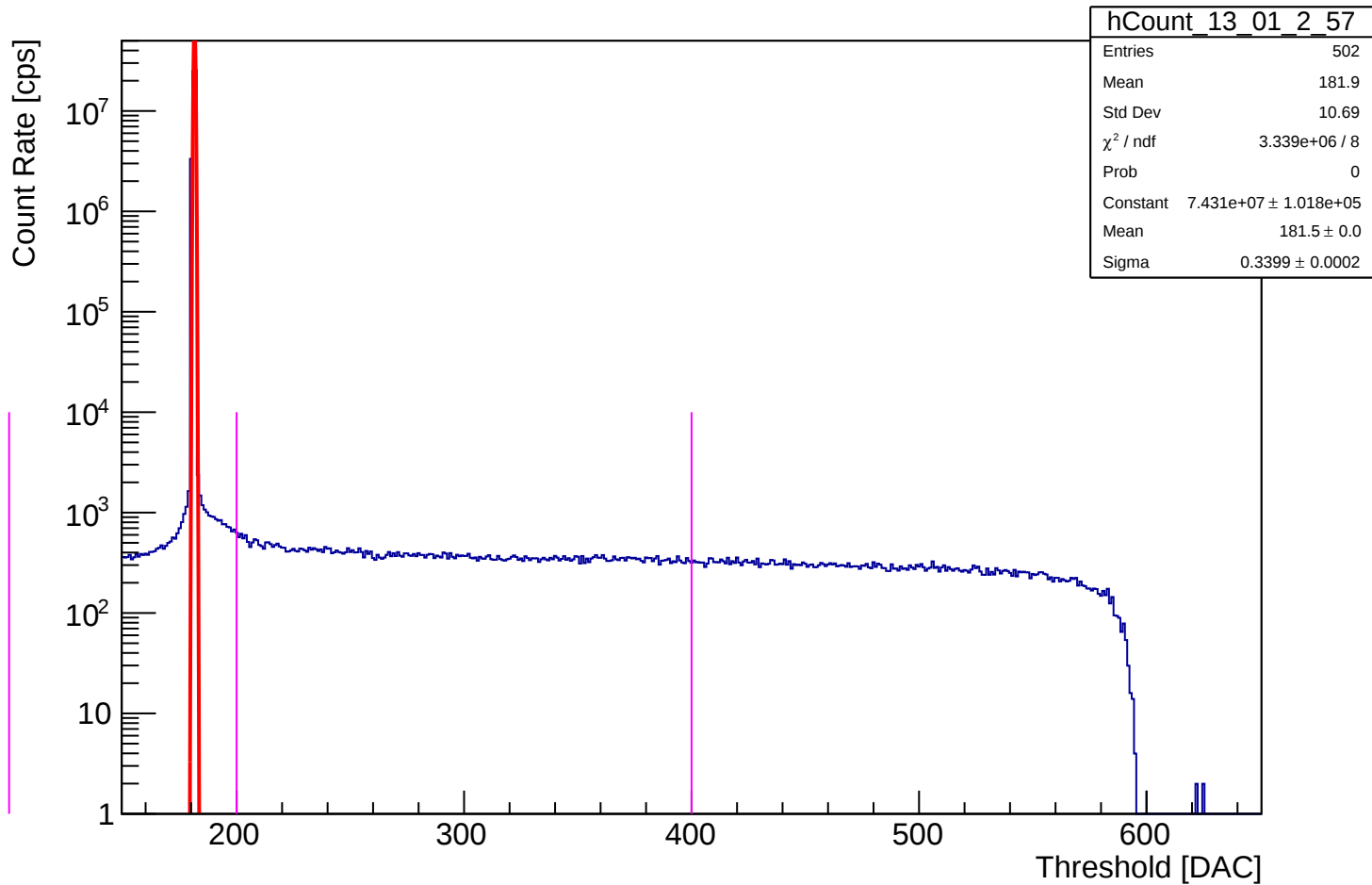
Row 1 Tile 1 Pmt 6 Pixel 53 Slot 13 Fiber 1 Asic 2 Channel 56



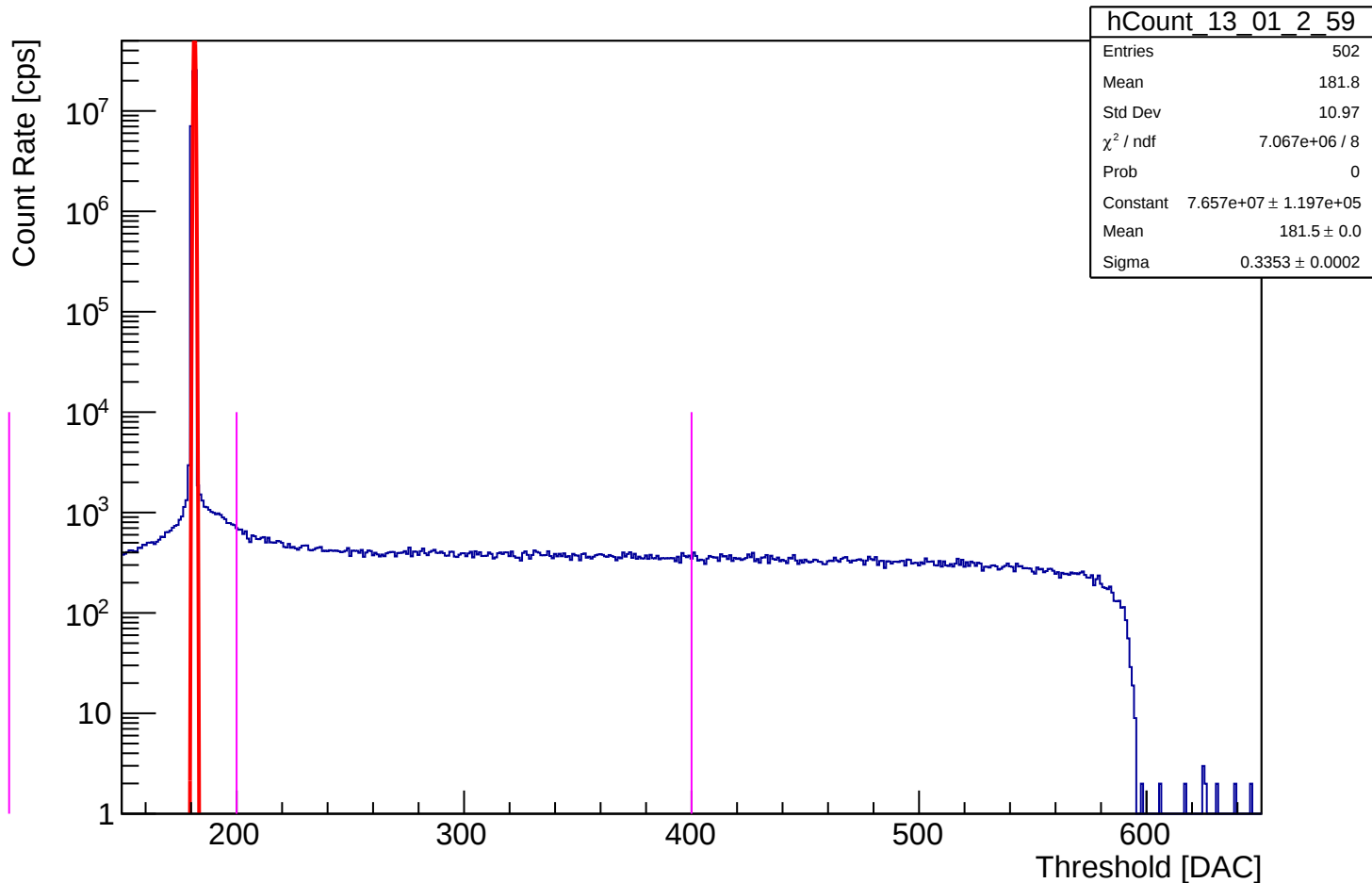
Row 1 Tile 1 Pmt 6 Pixel 54 Slot 13 Fiber 1 Asic 2 Channel 58

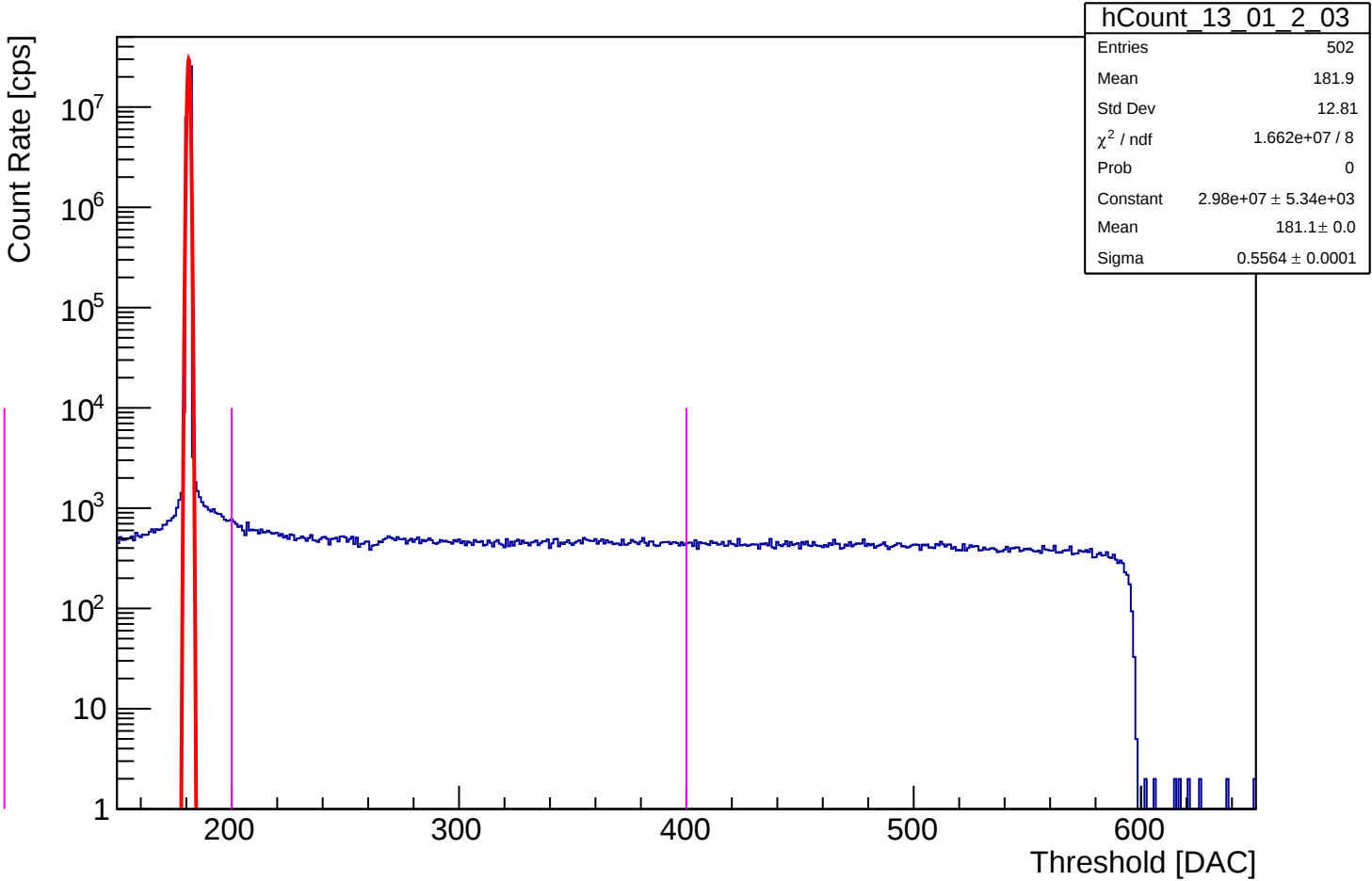


Row 1 Tile 1 Pmt 6 Pixel 55 Slot 13 Fiber 1 Asic 2 Channel 57

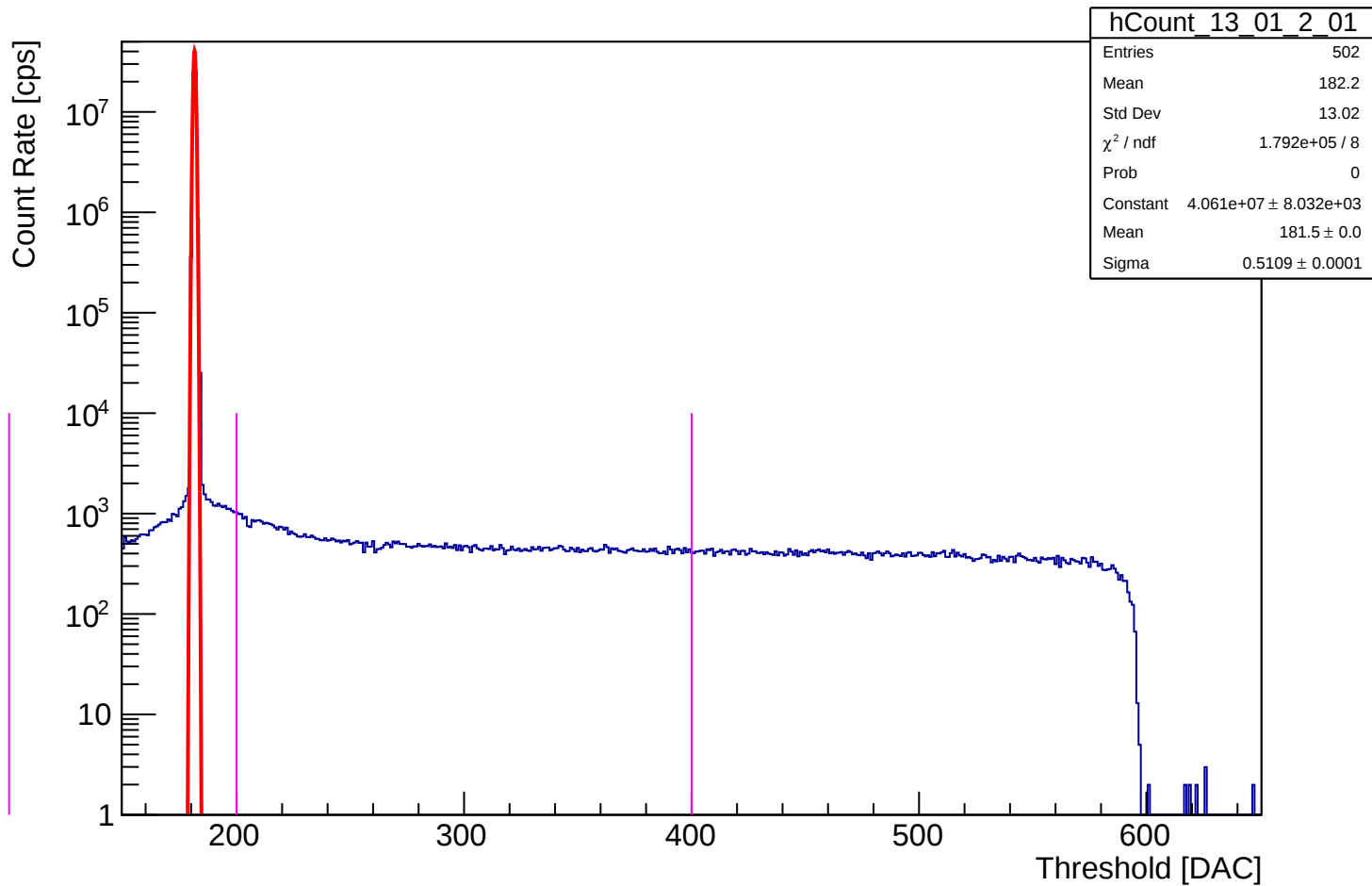


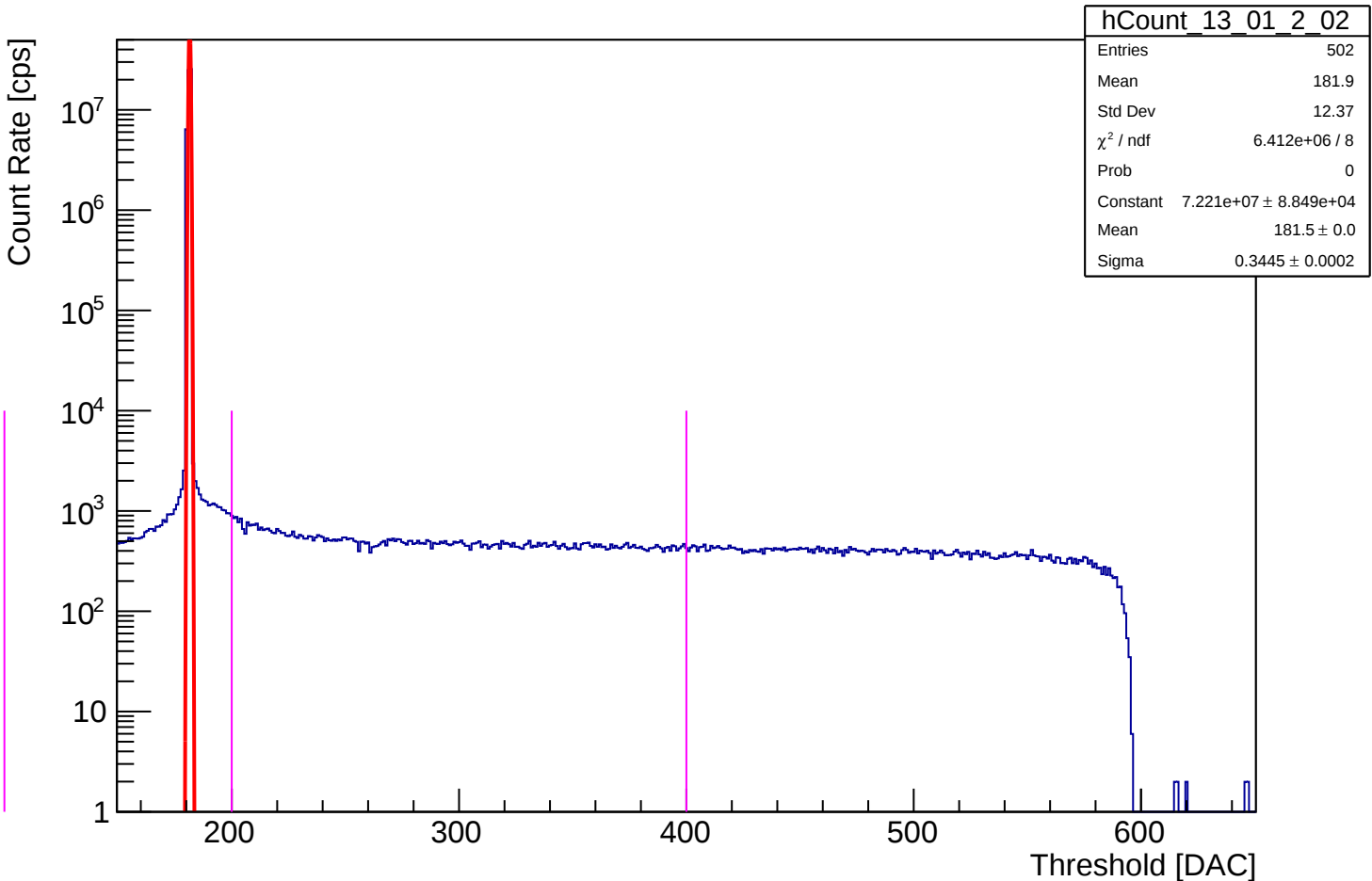
Row 1 Tile 1 Pmt 6 Pixel 56 Slot 13 Fiber 1 Asic 2 Channel 59

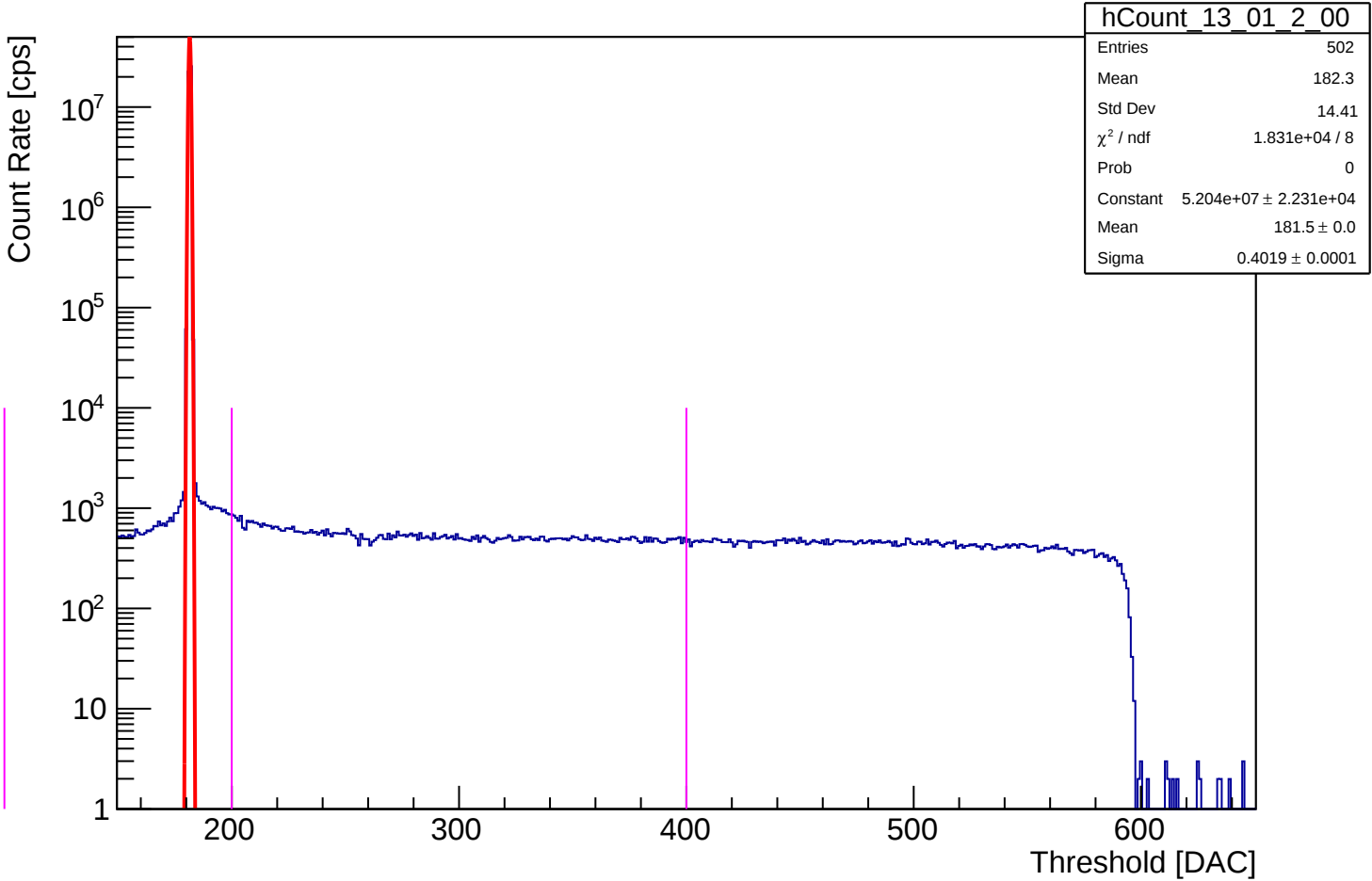




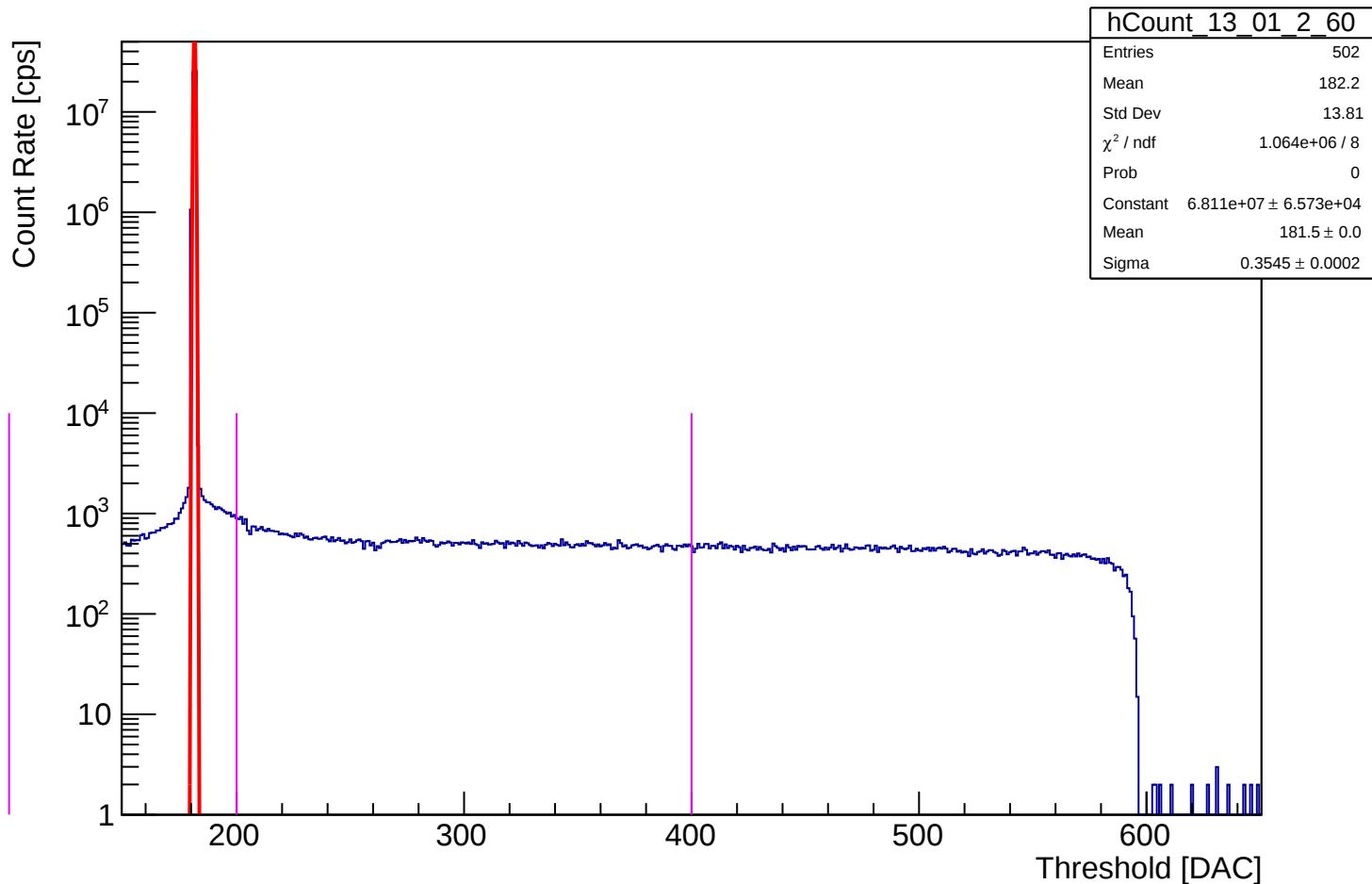
Row 1 Tile 1 Pmt 6 Pixel 58 Slot 13 Fiber 1 Asic 2 Channel 1



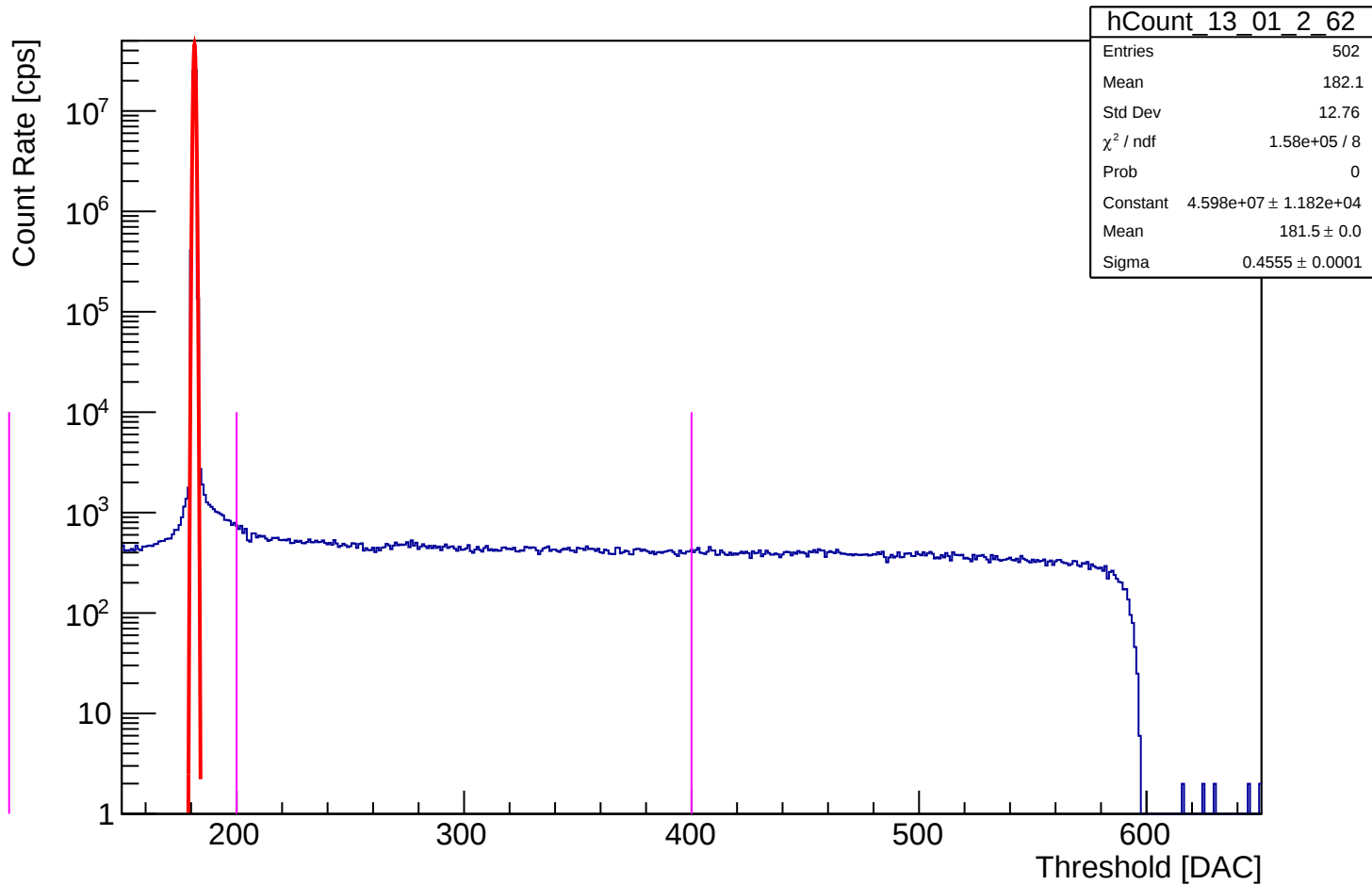




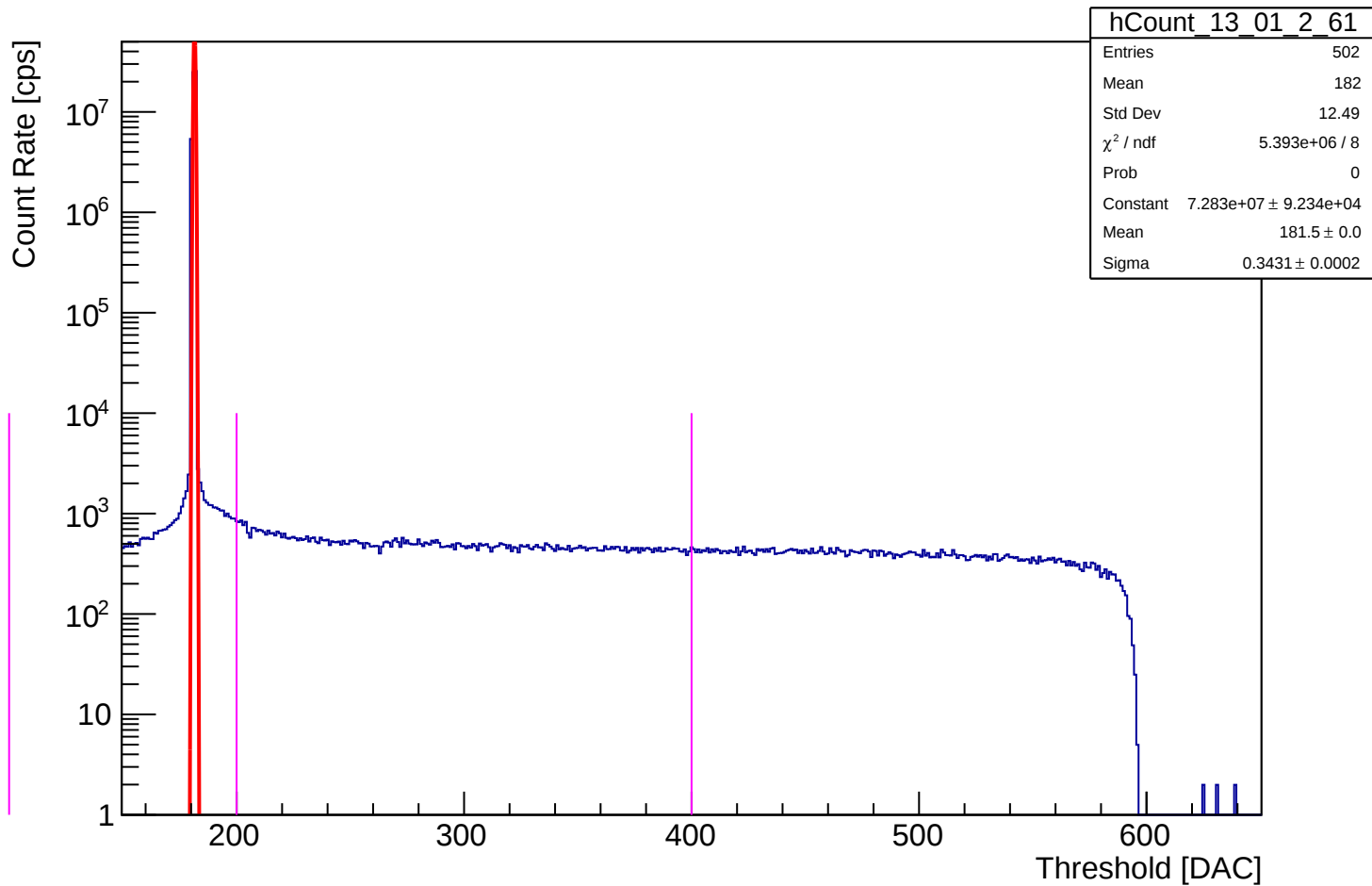
Row 1 Tile 1 Pmt 6 Pixel 61 Slot 13 Fiber 1 Asic 2 Channel 60



Row 1 Tile 1 Pmt 6 Pixel 62 Slot 13 Fiber 1 Asic 2 Channel 62



Row 1 Tile 1 Pmt 6 Pixel 63 Slot 13 Fiber 1 Asic 2 Channel 61



Row 1 Tile 1 Pmt 6 Pixel 64 Slot 13 Fiber 1 Asic 2 Channel 63

