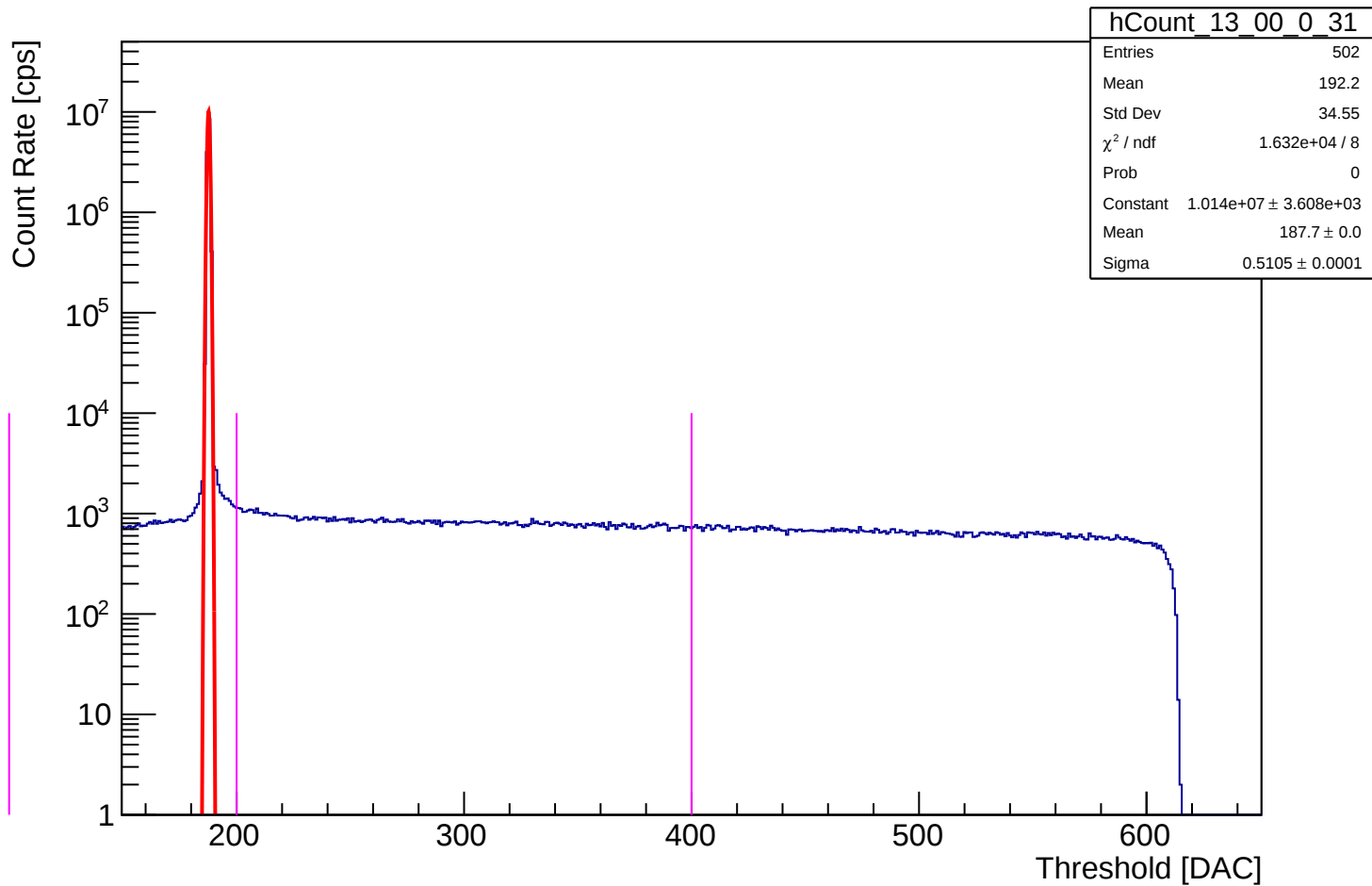
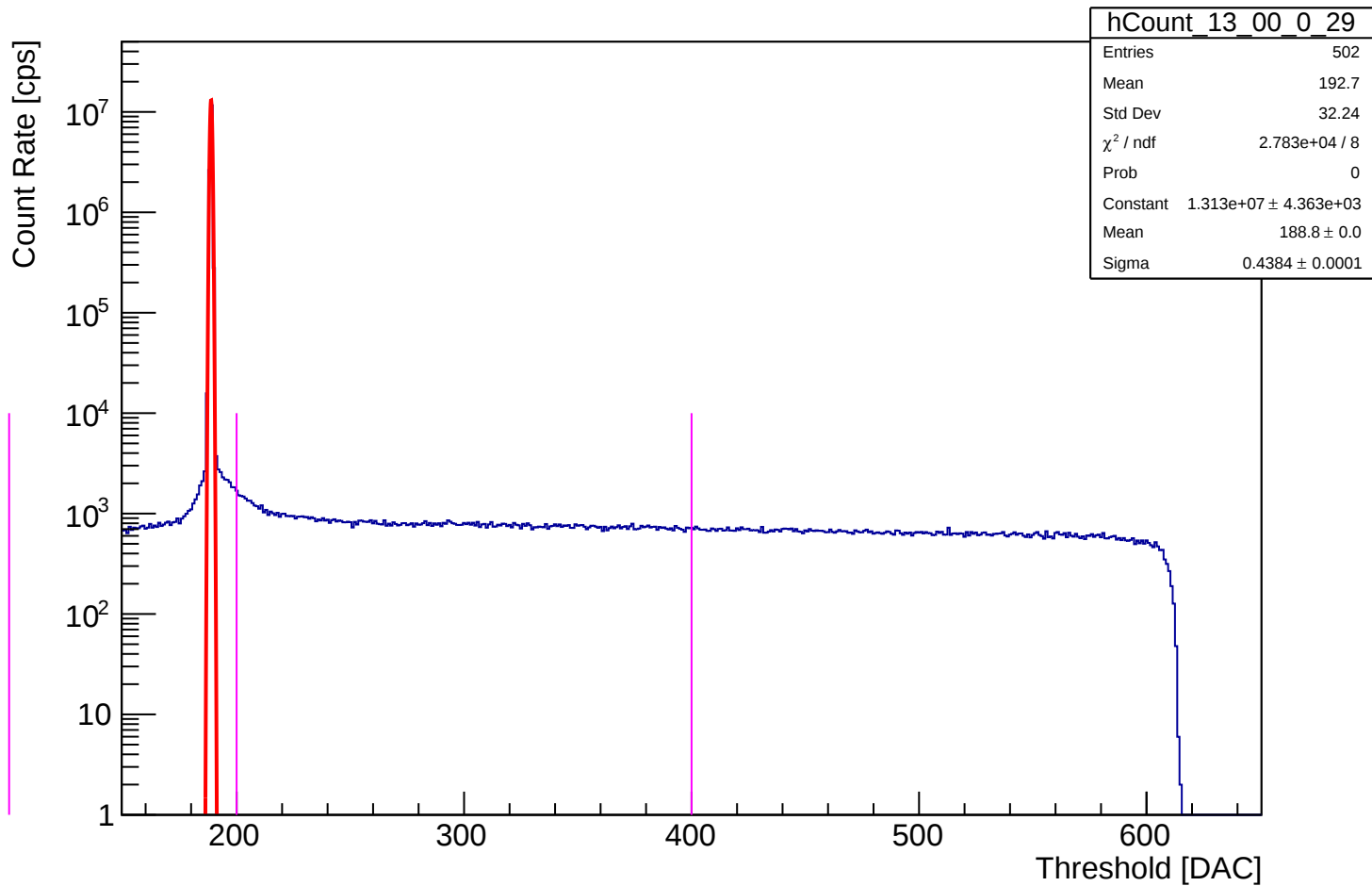


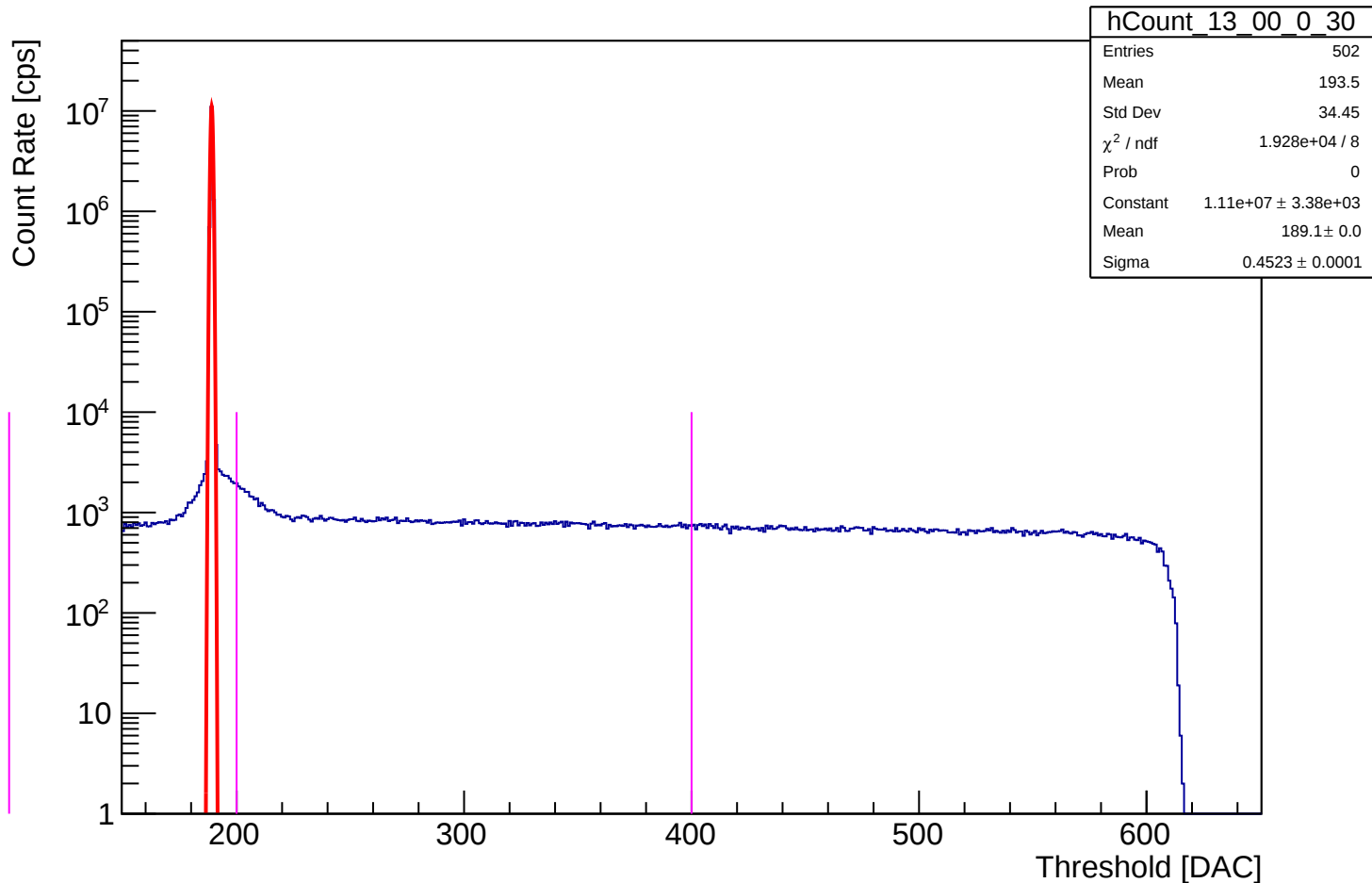
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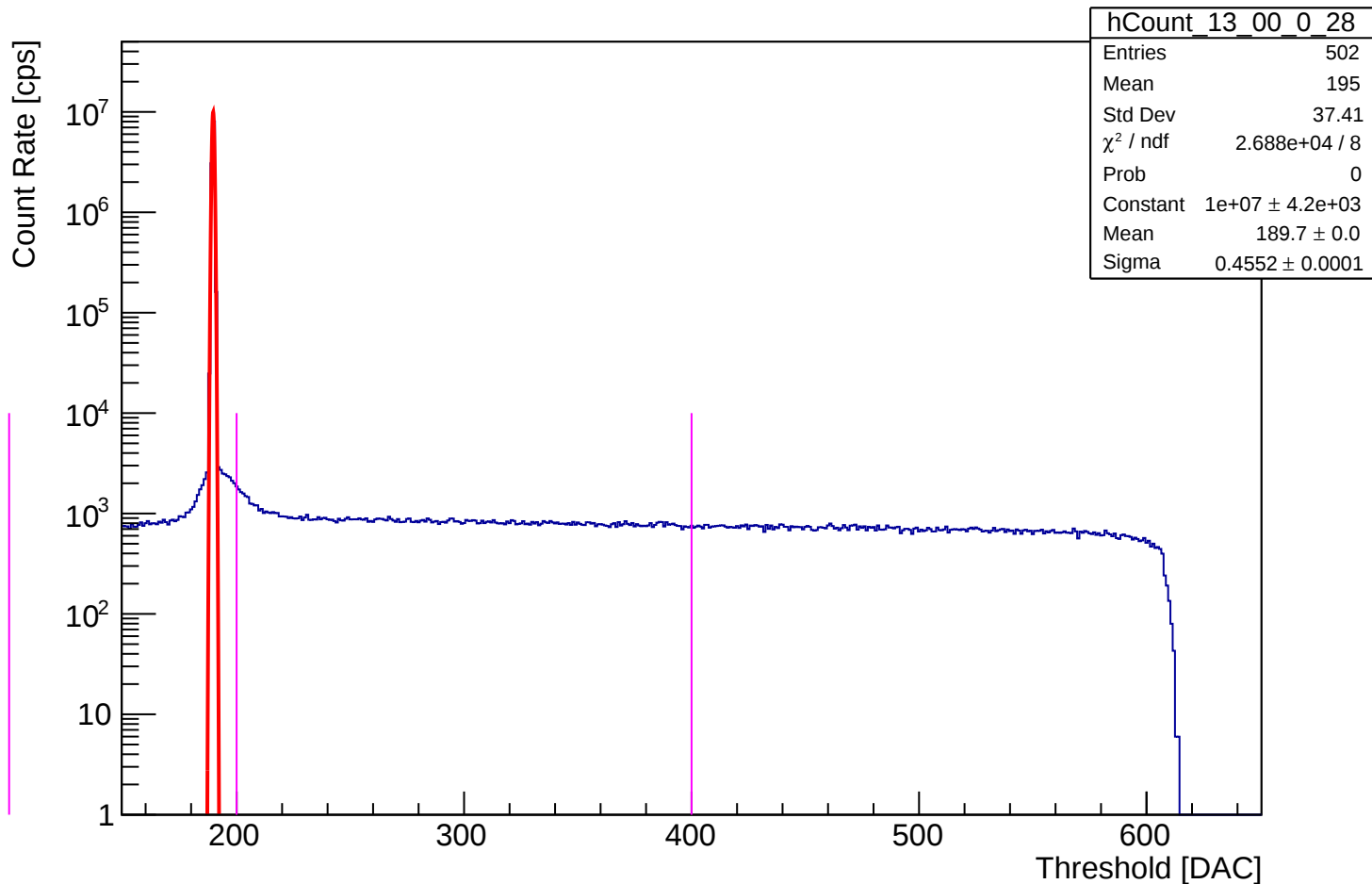
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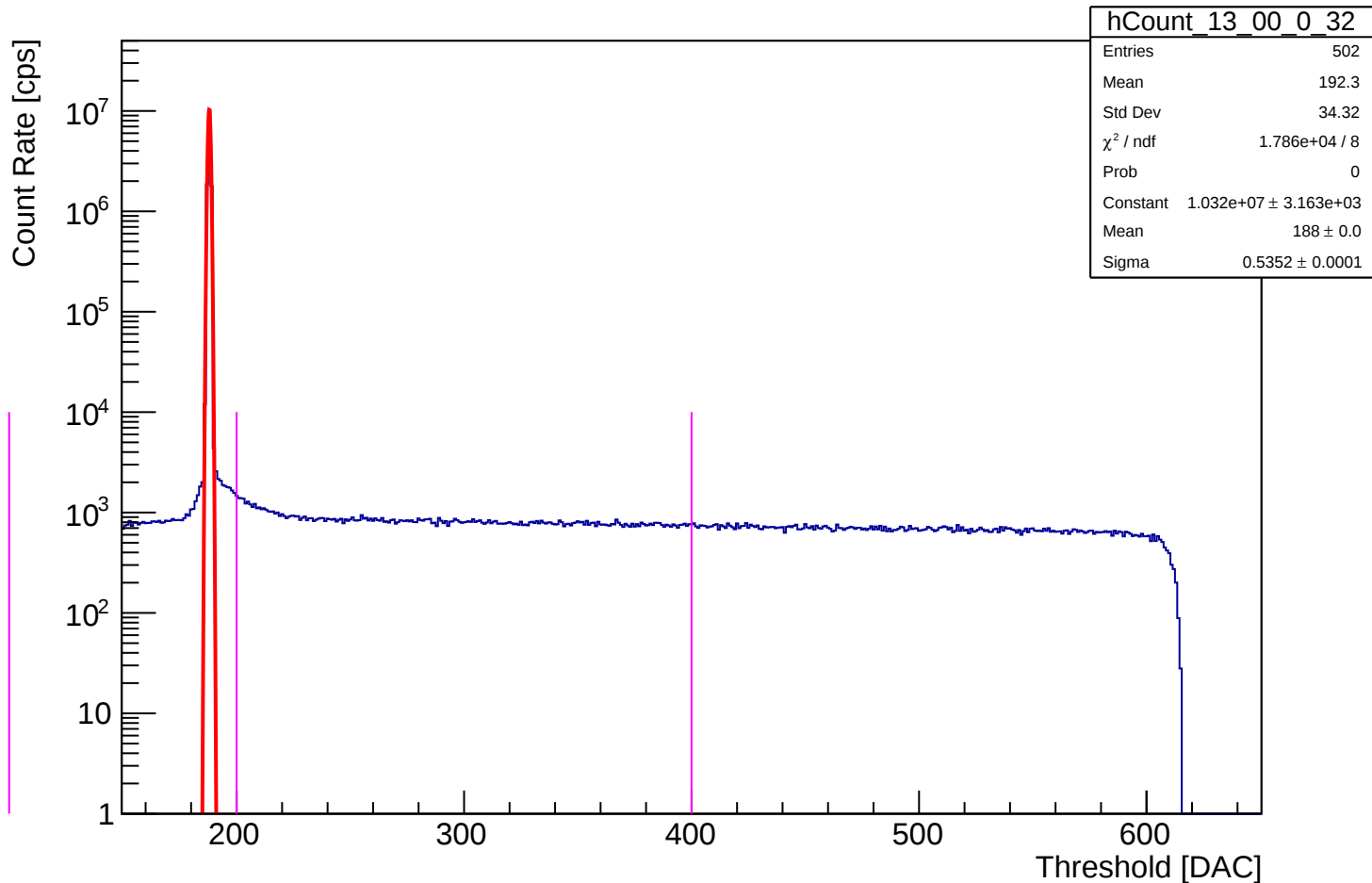
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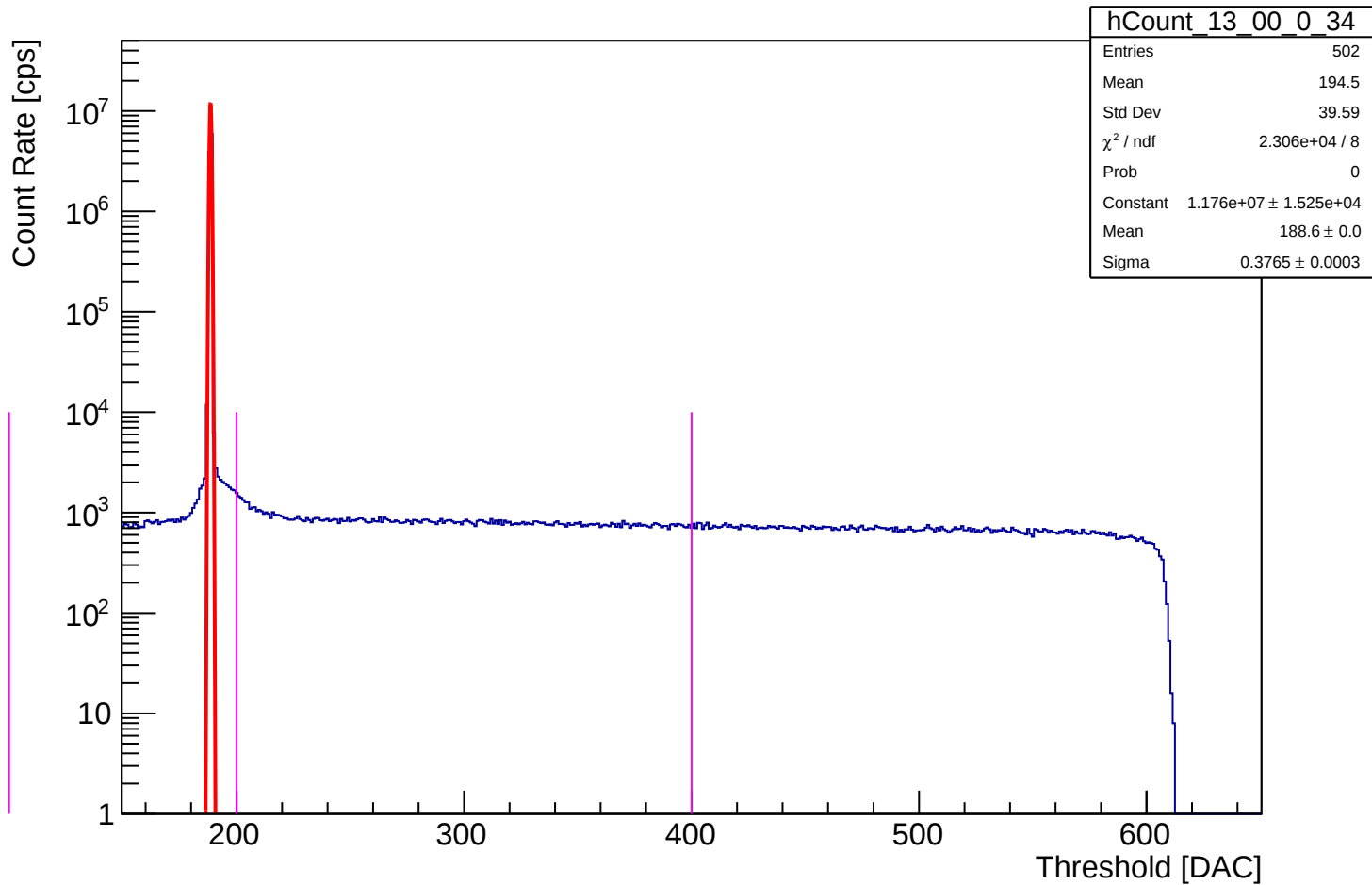
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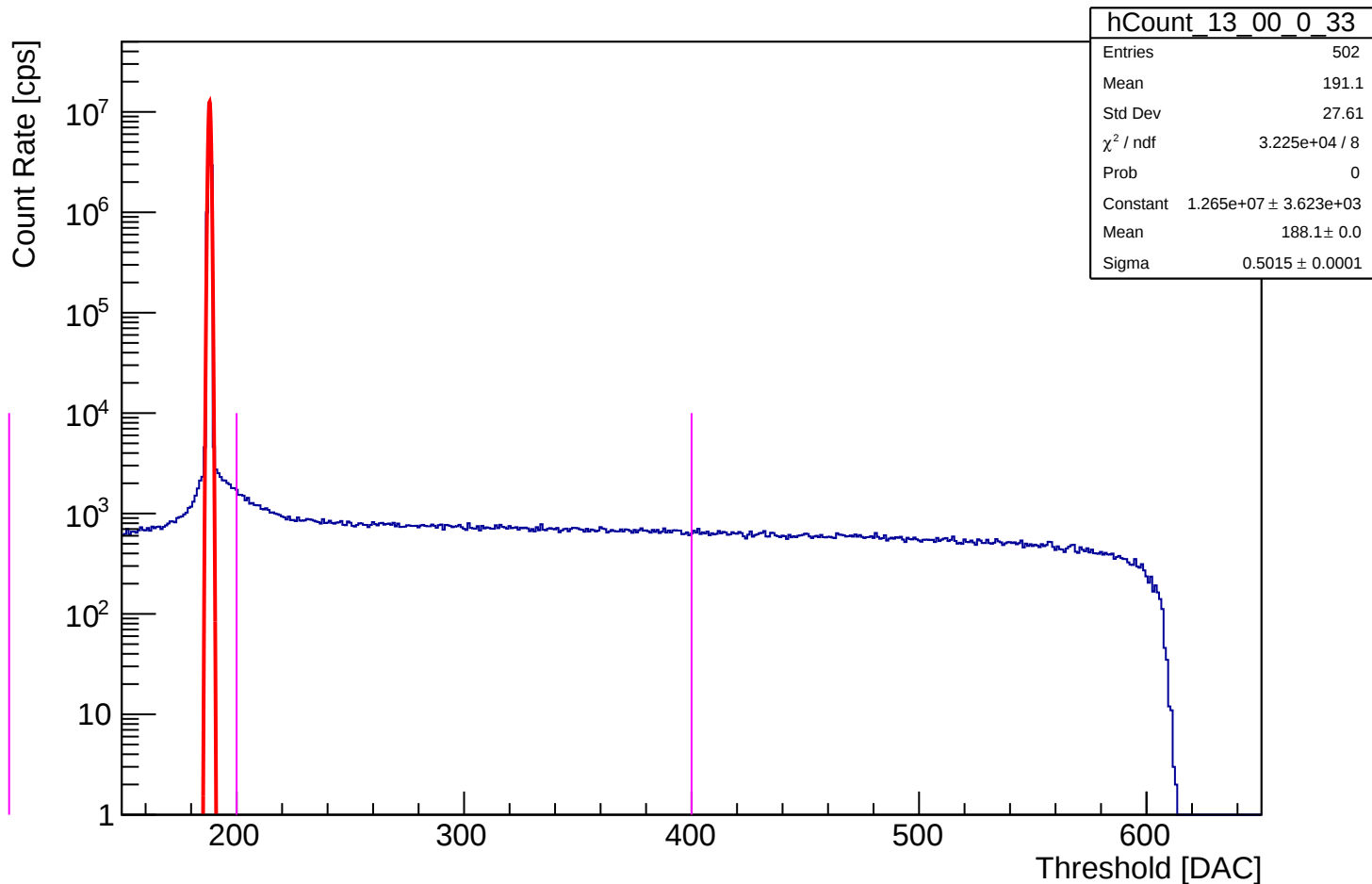
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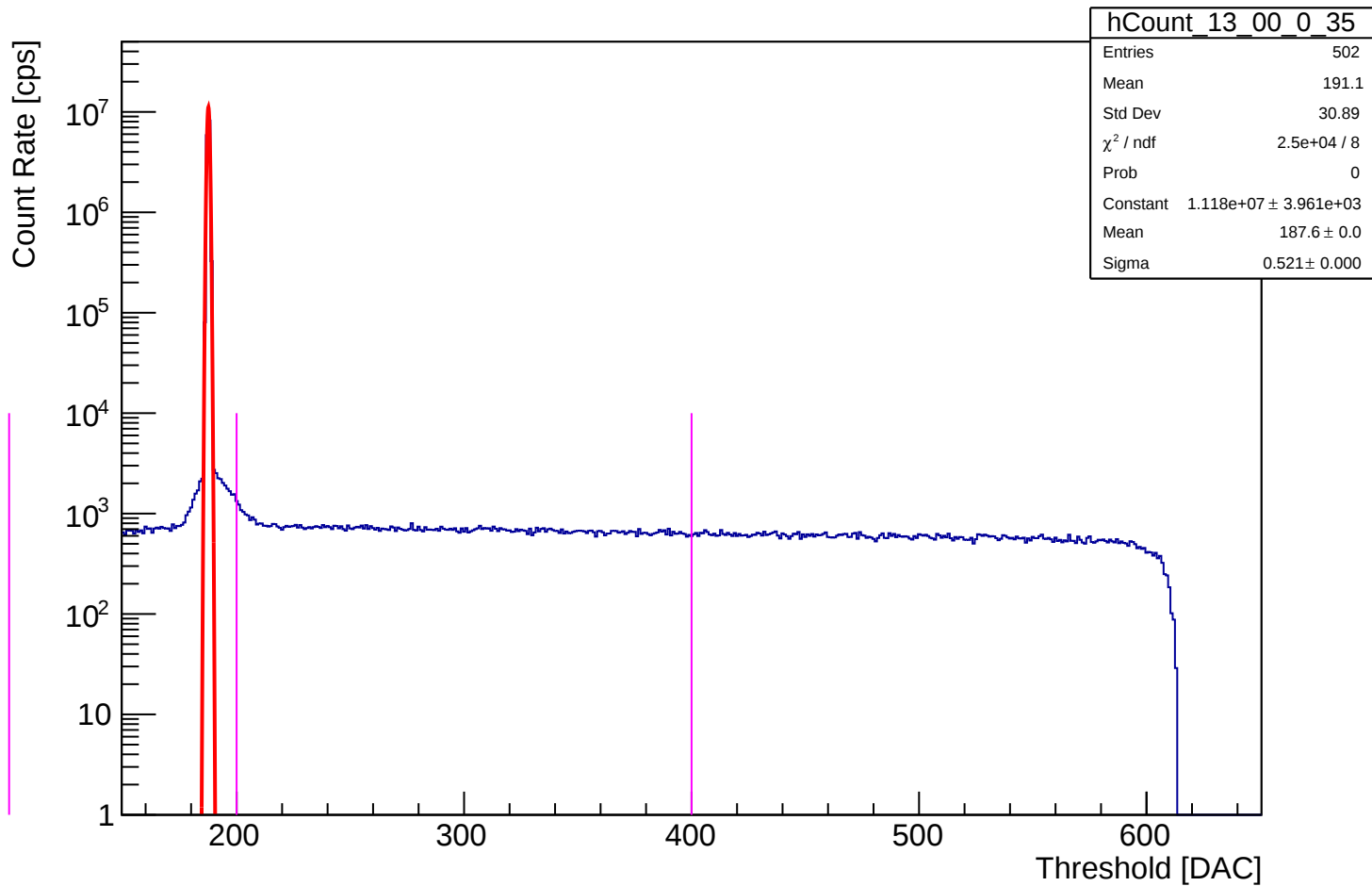
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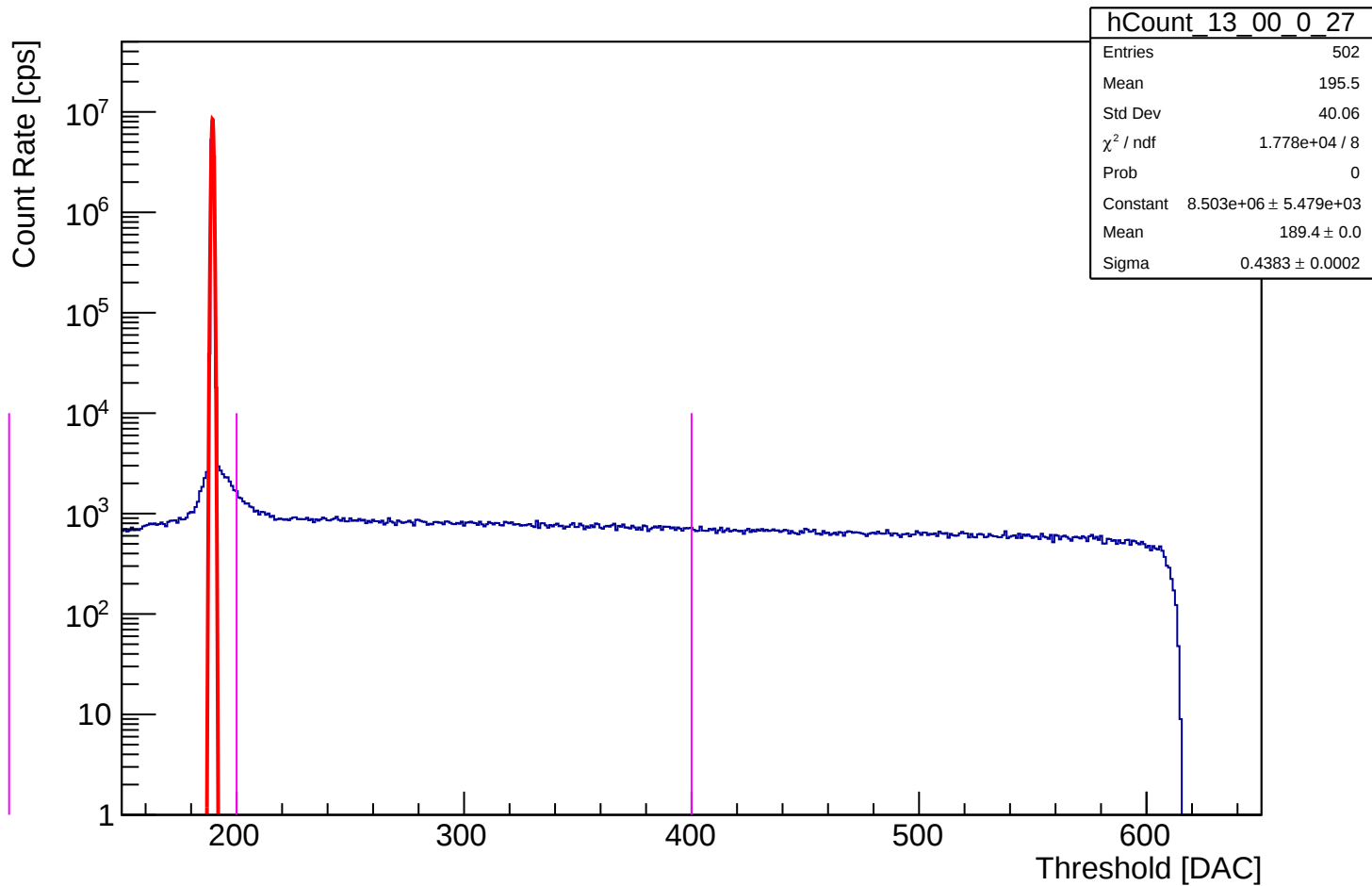
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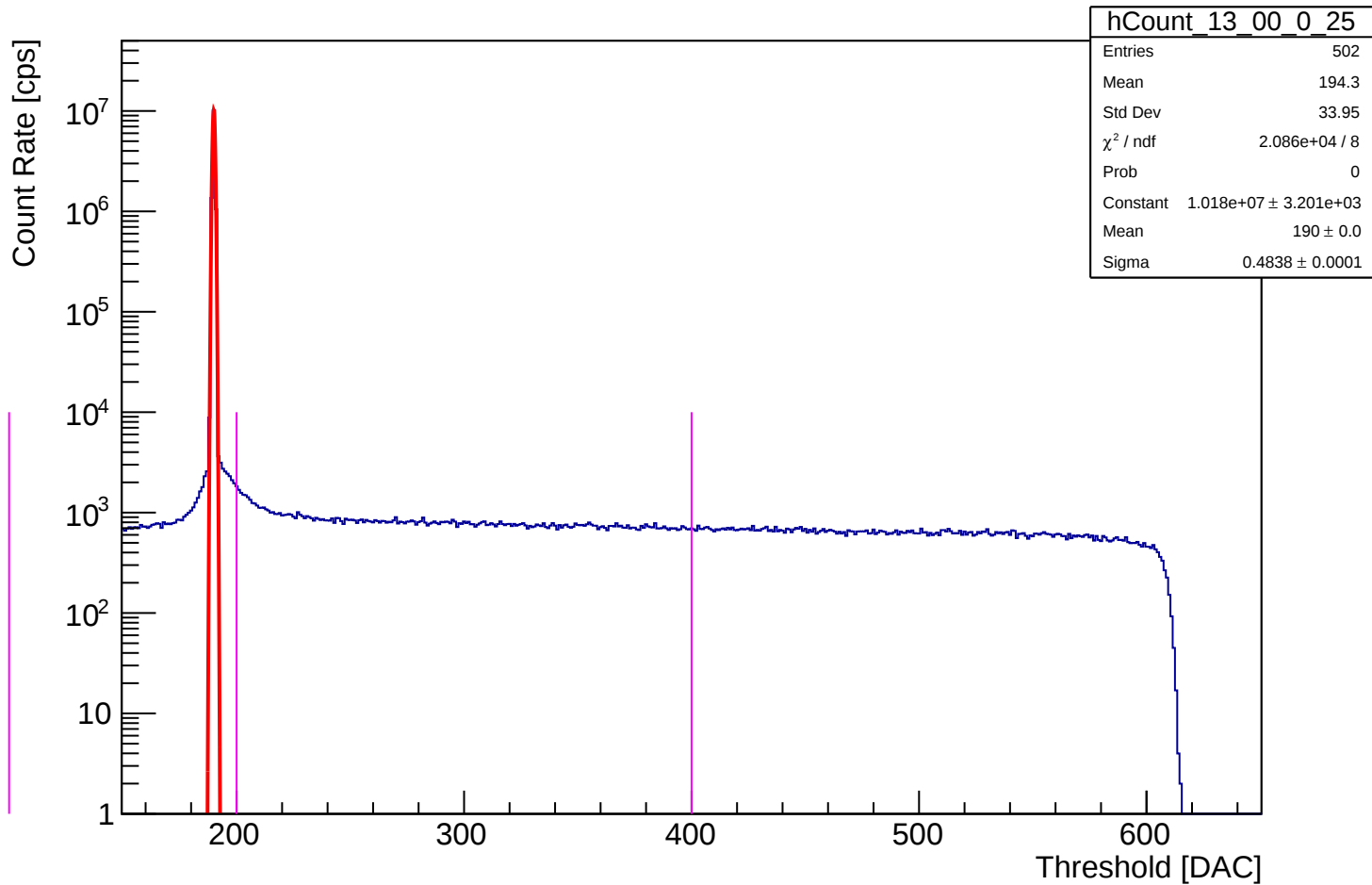
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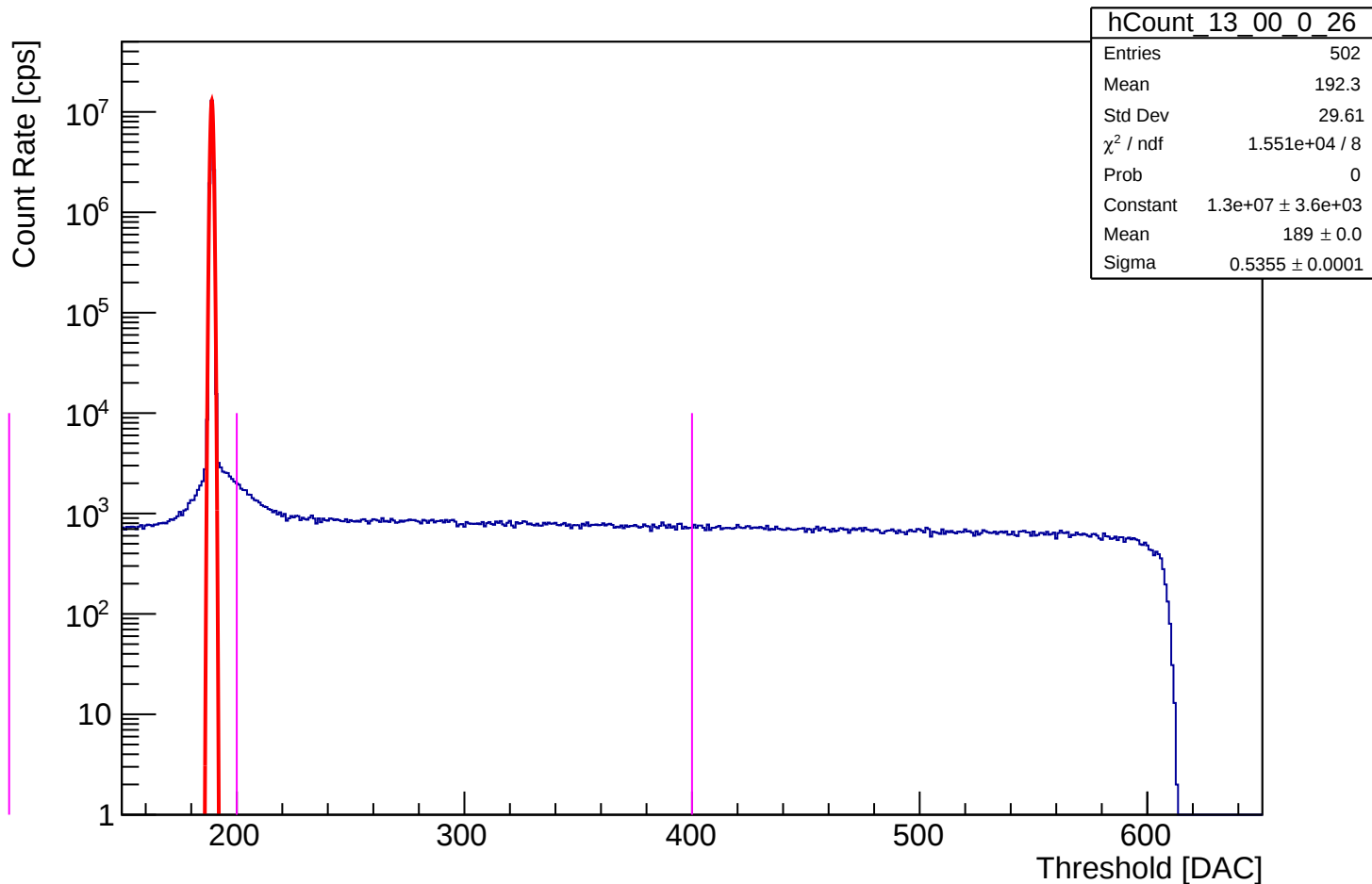
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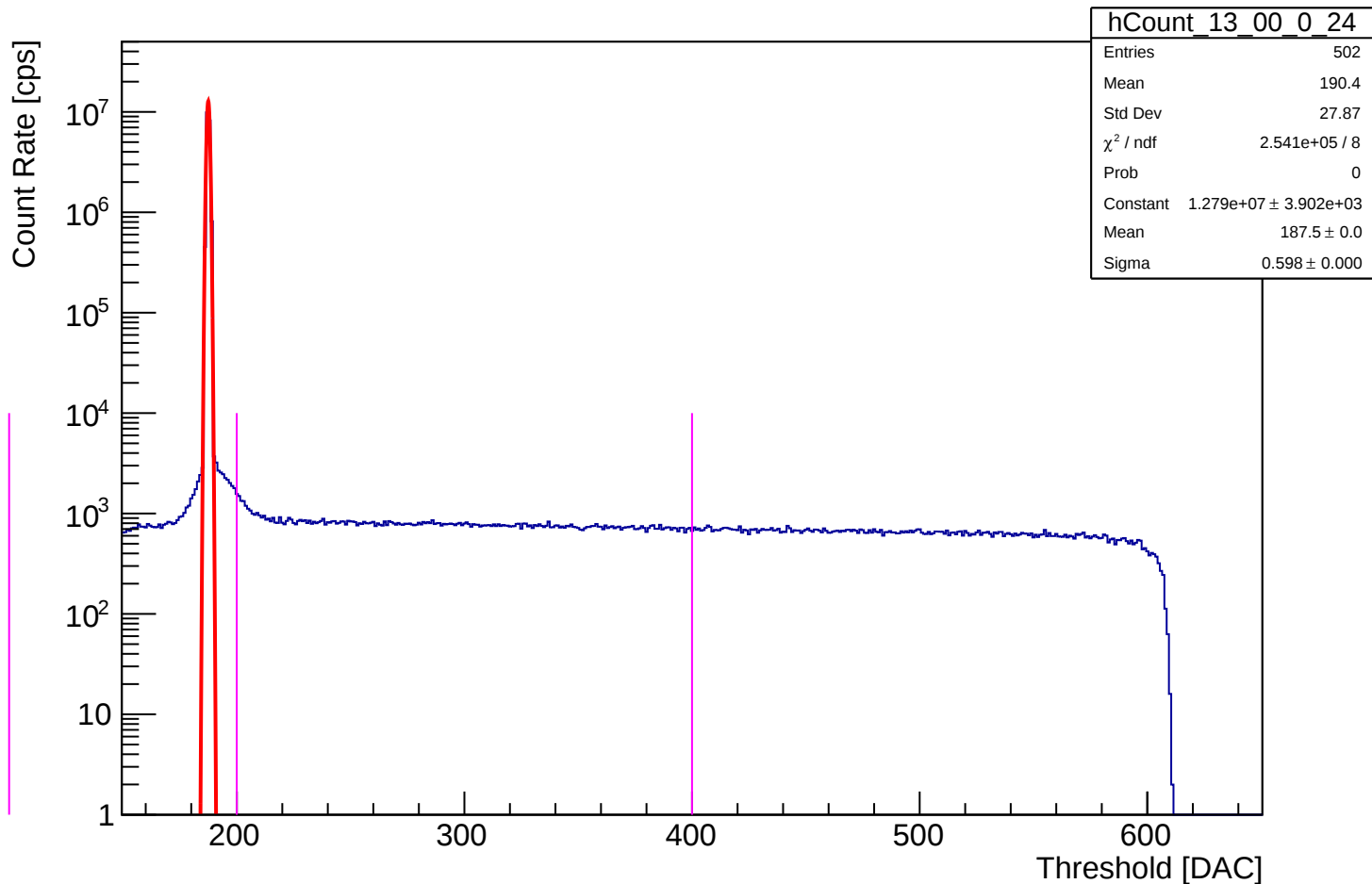
Row 1 Tile 1 Pmt 1 Pixel 10 Slot 13 Fiber 0 Asic 0 Channel 25



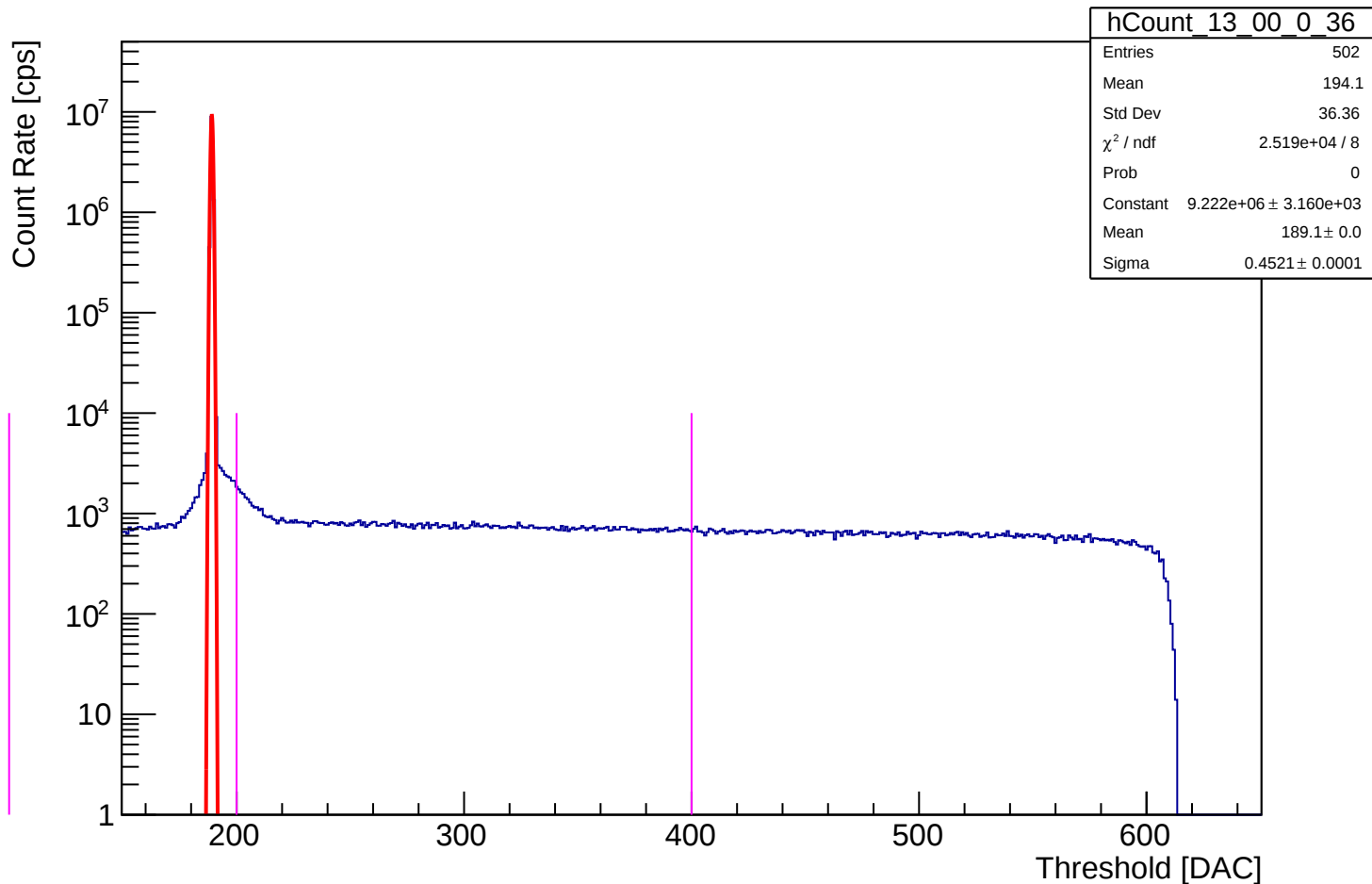
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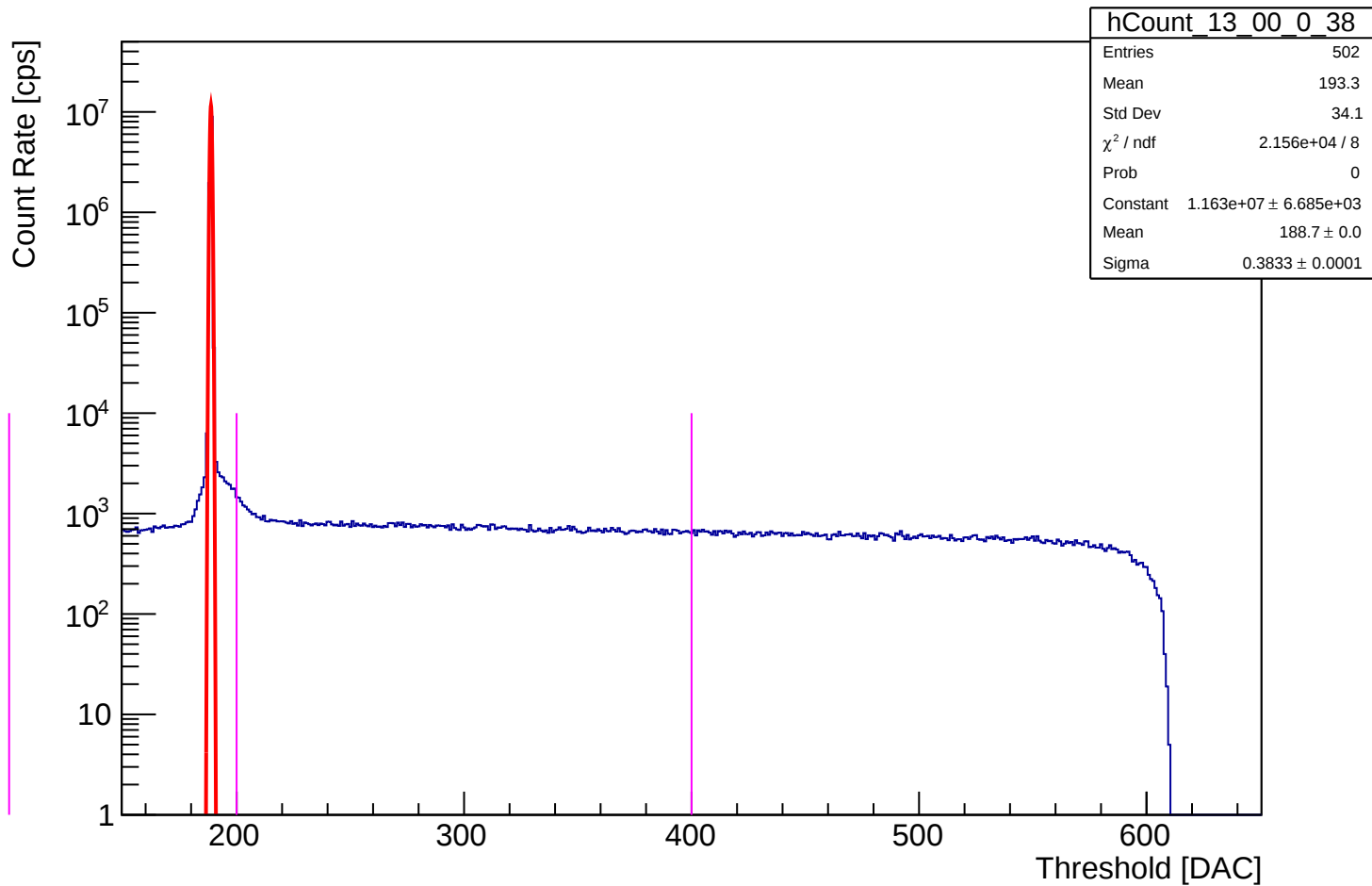
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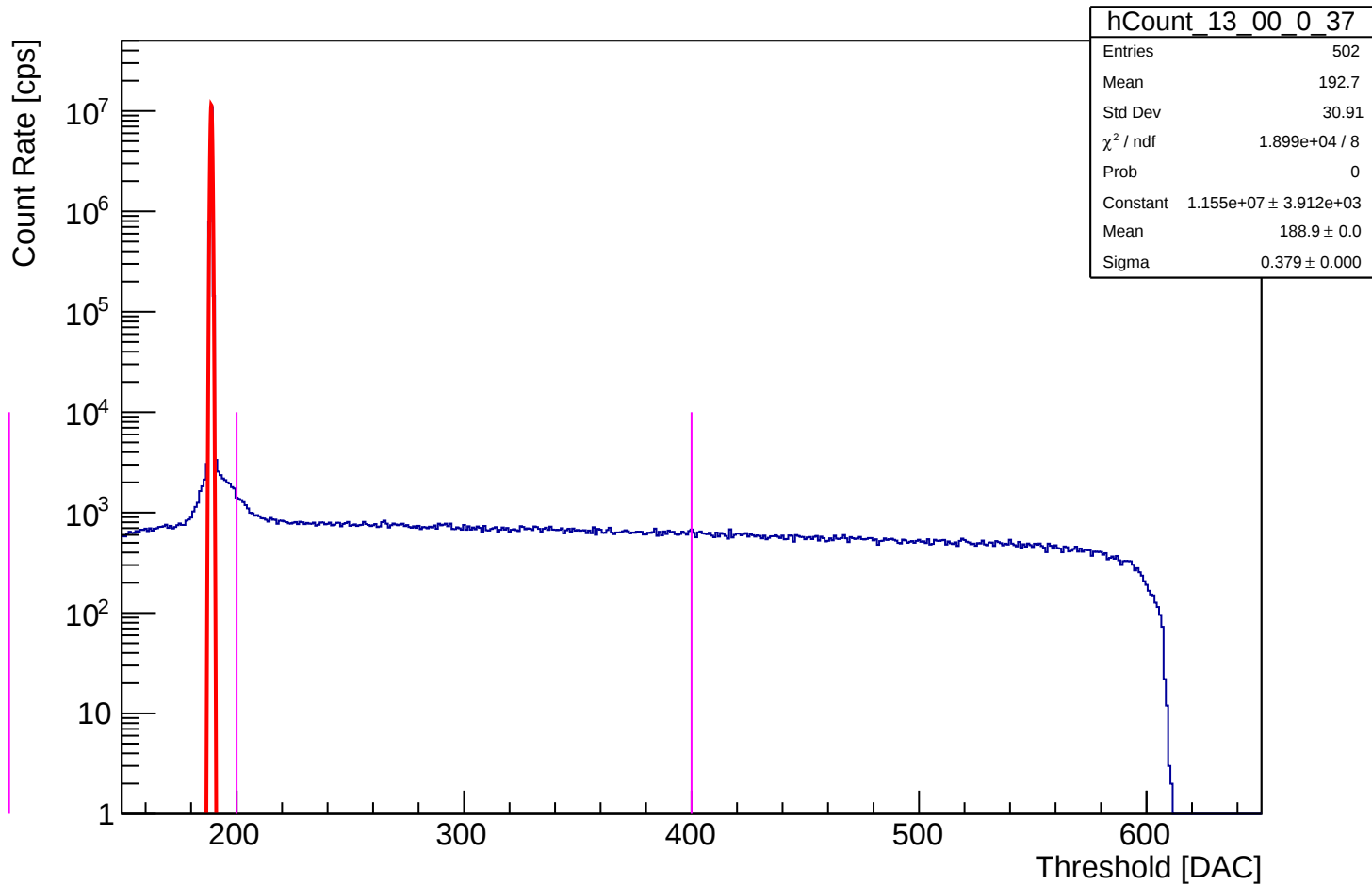
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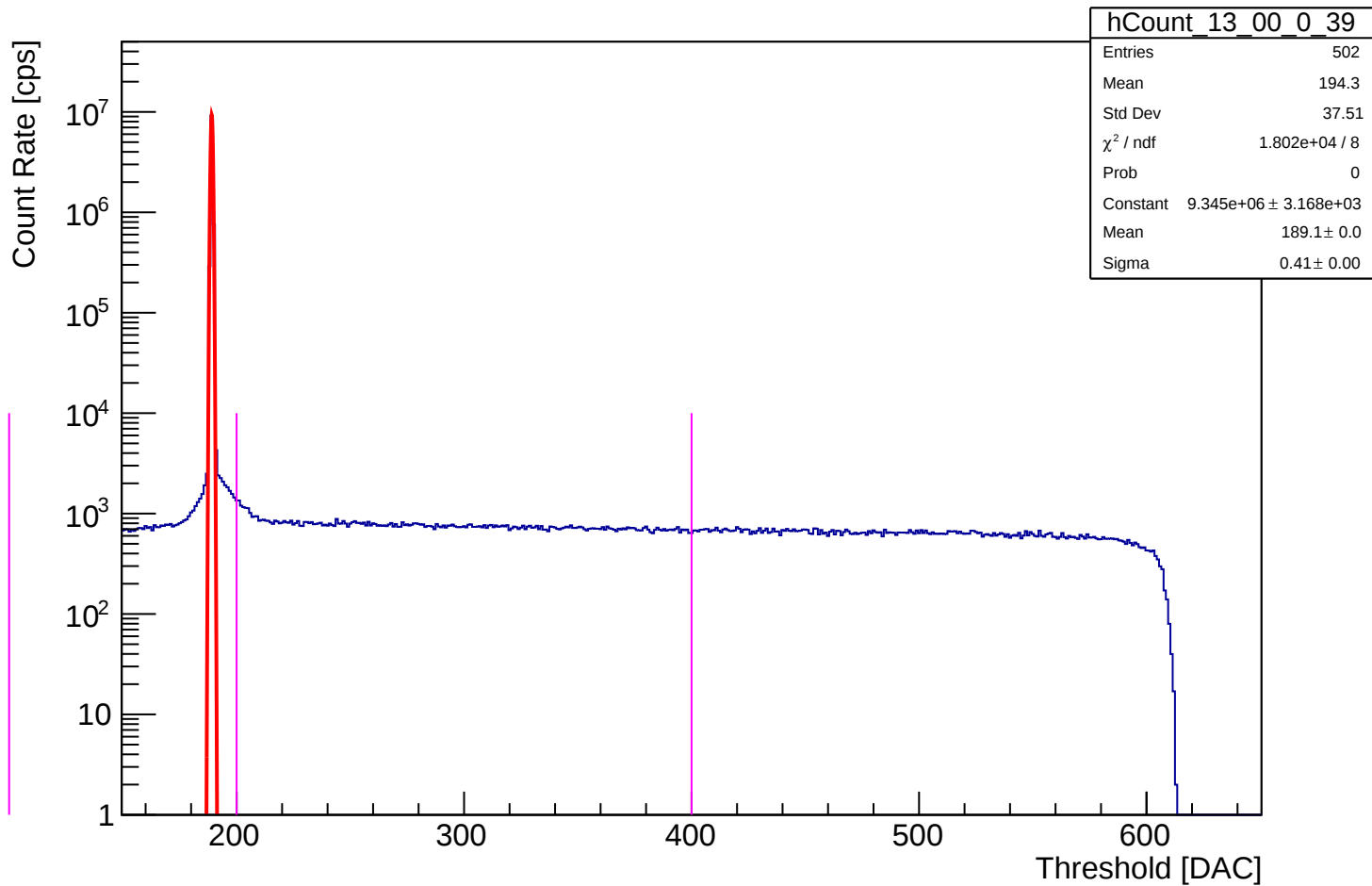
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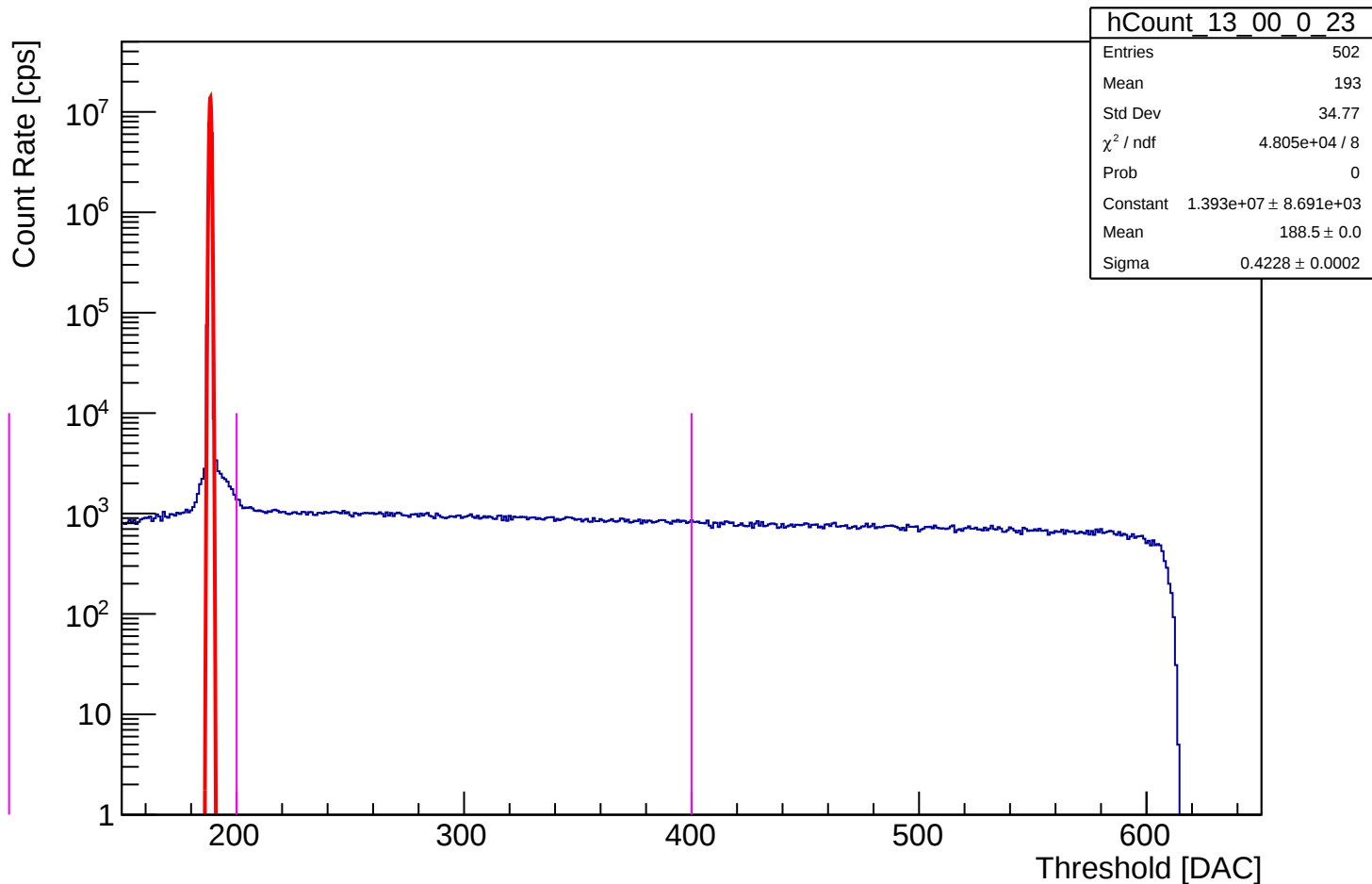
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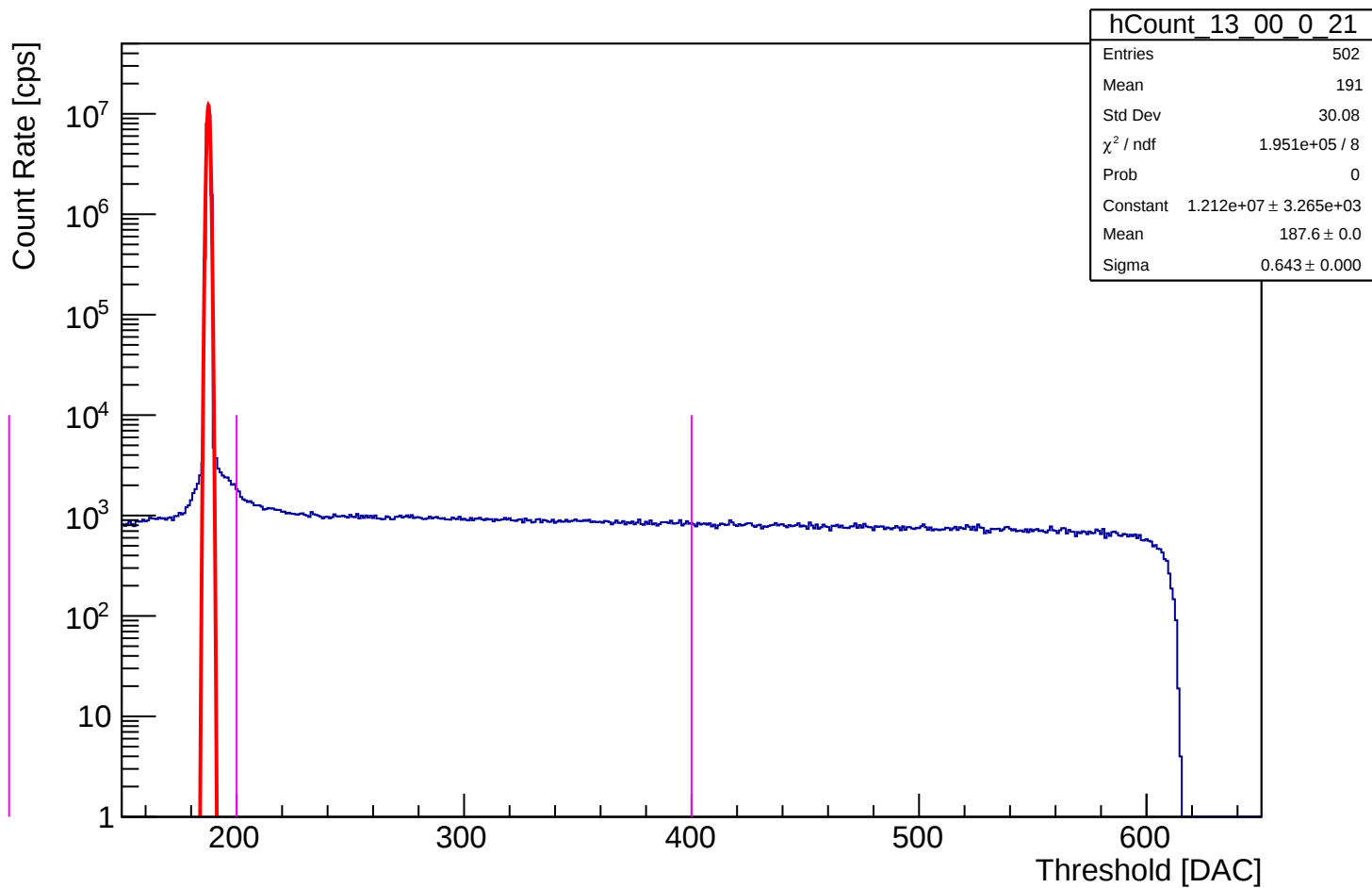
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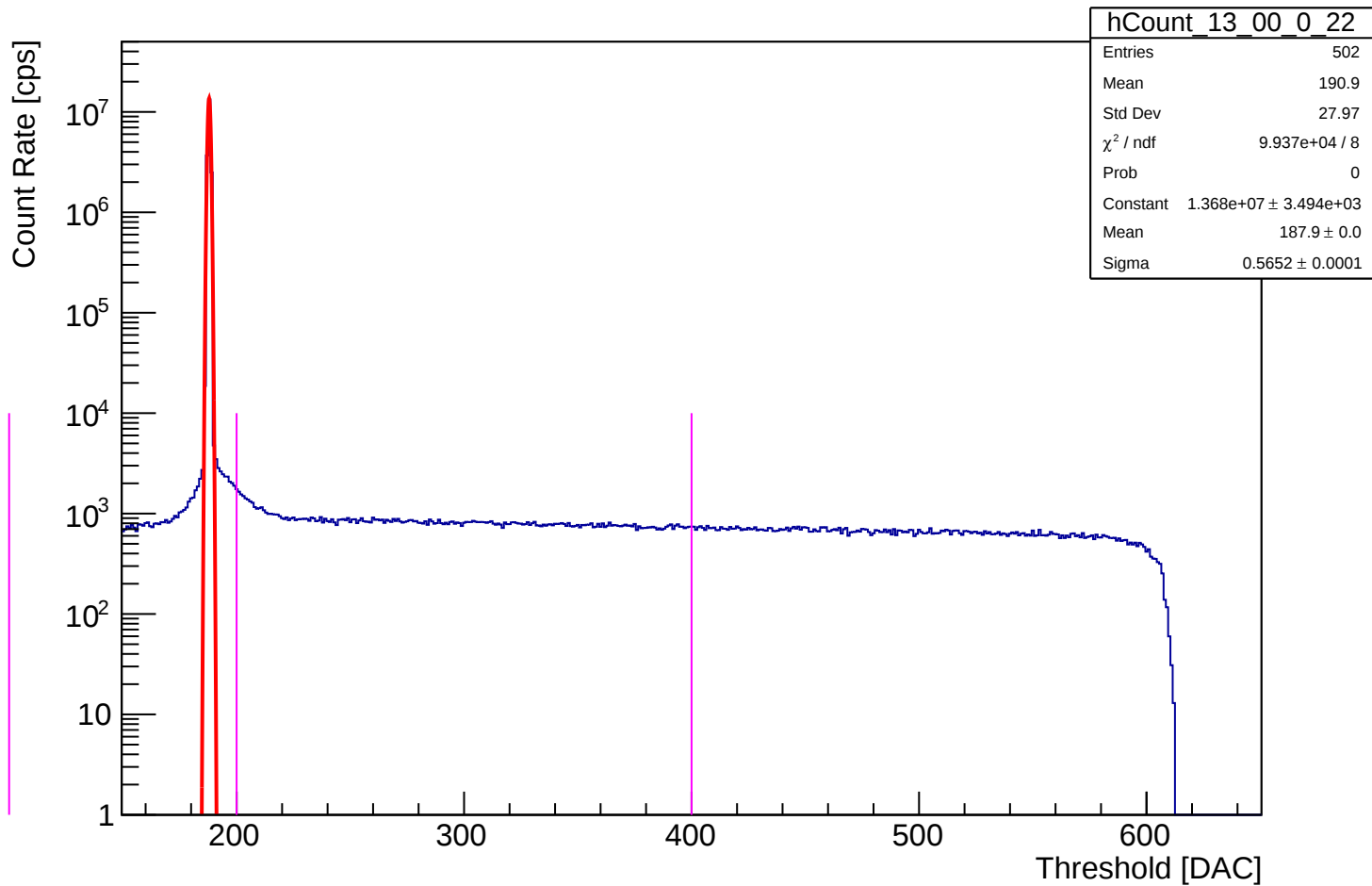
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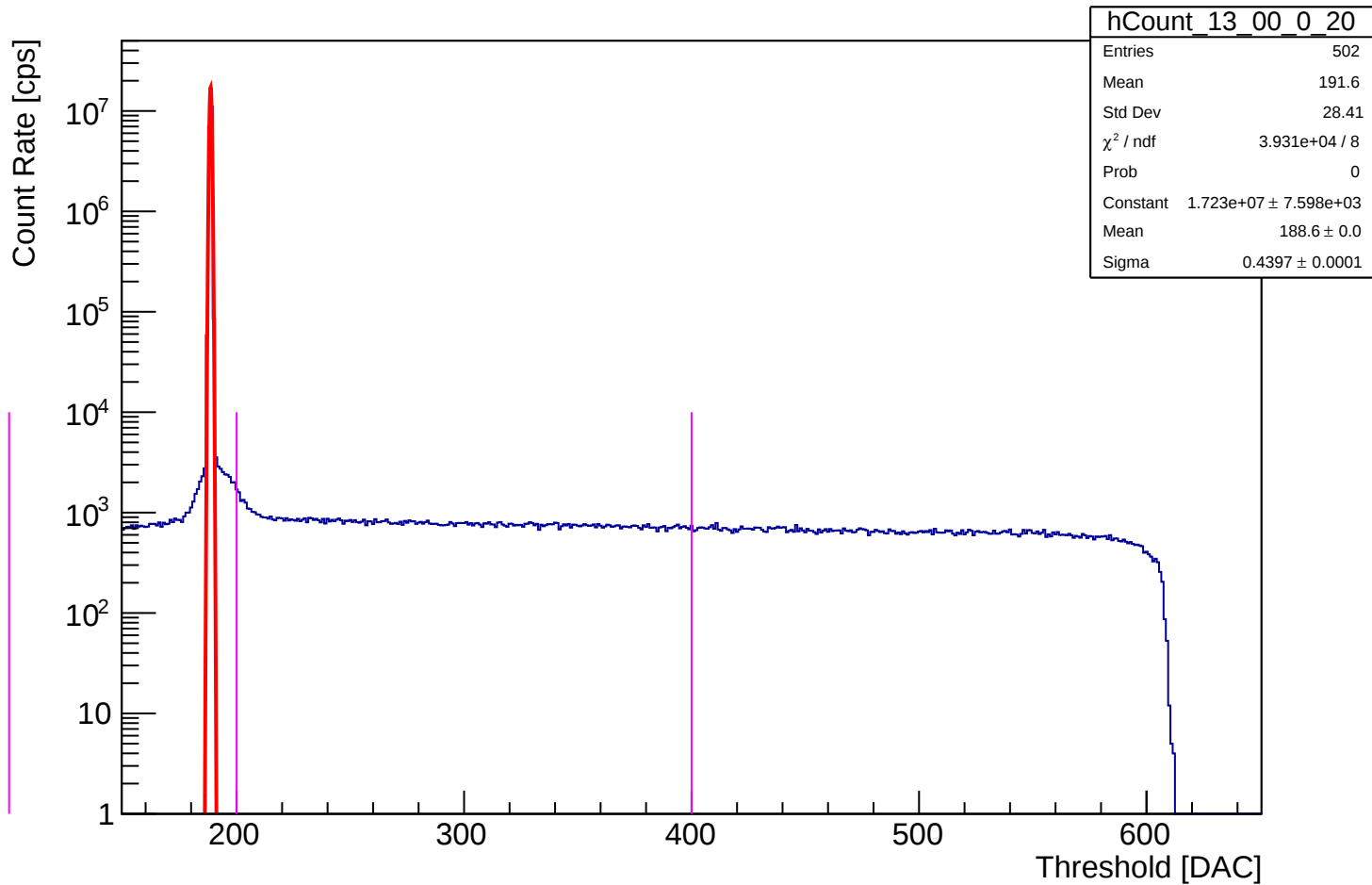
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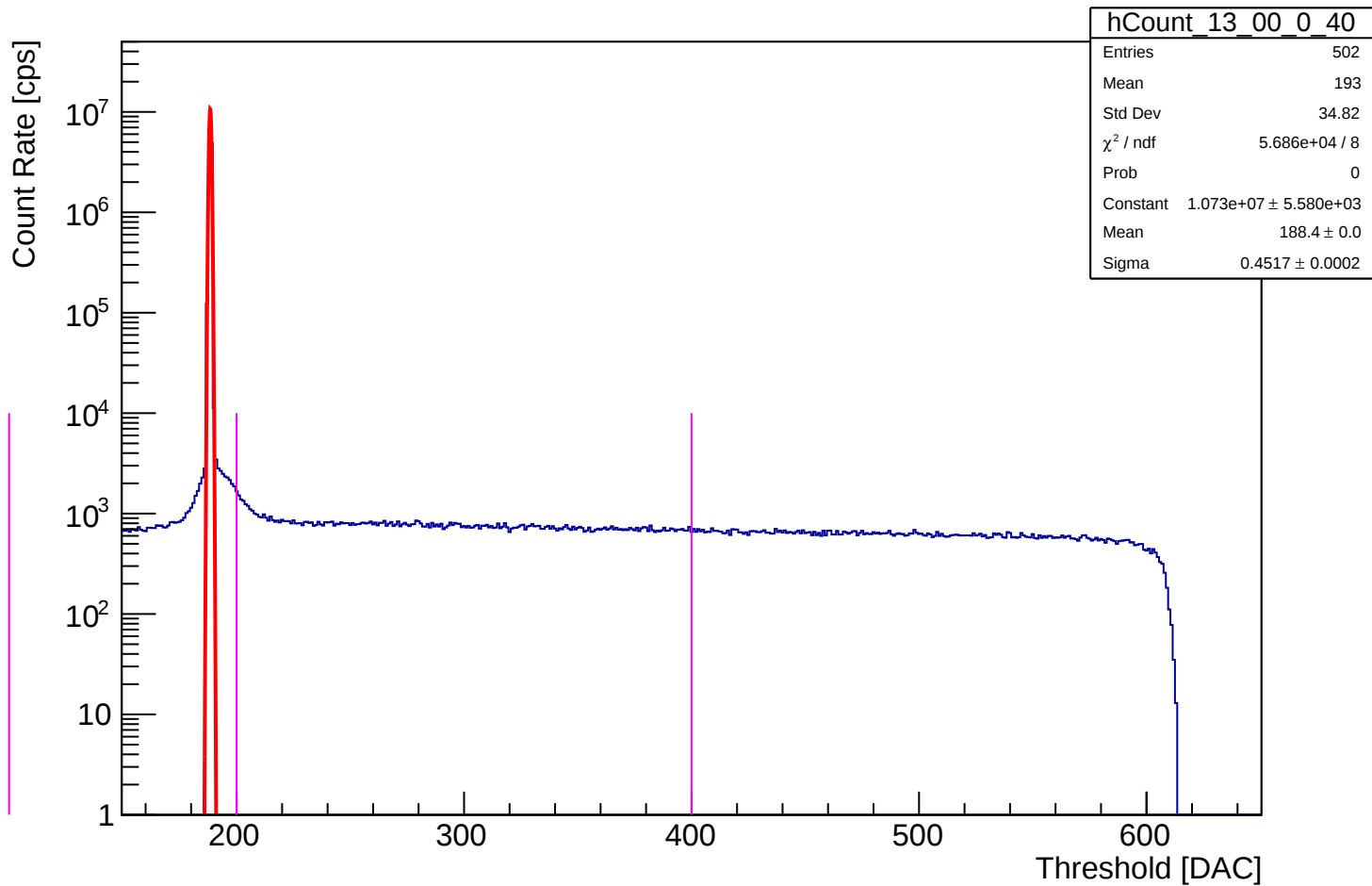
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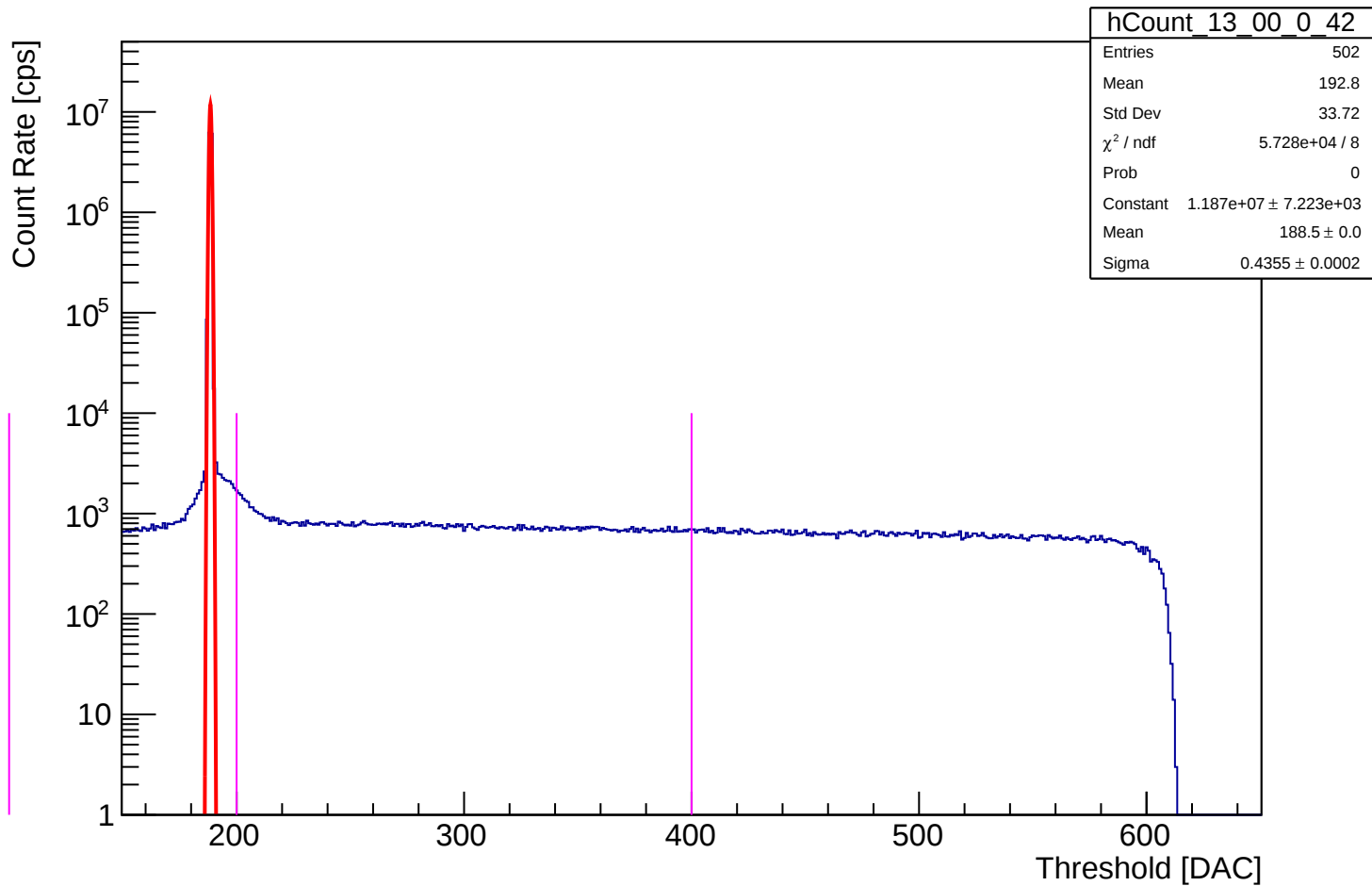
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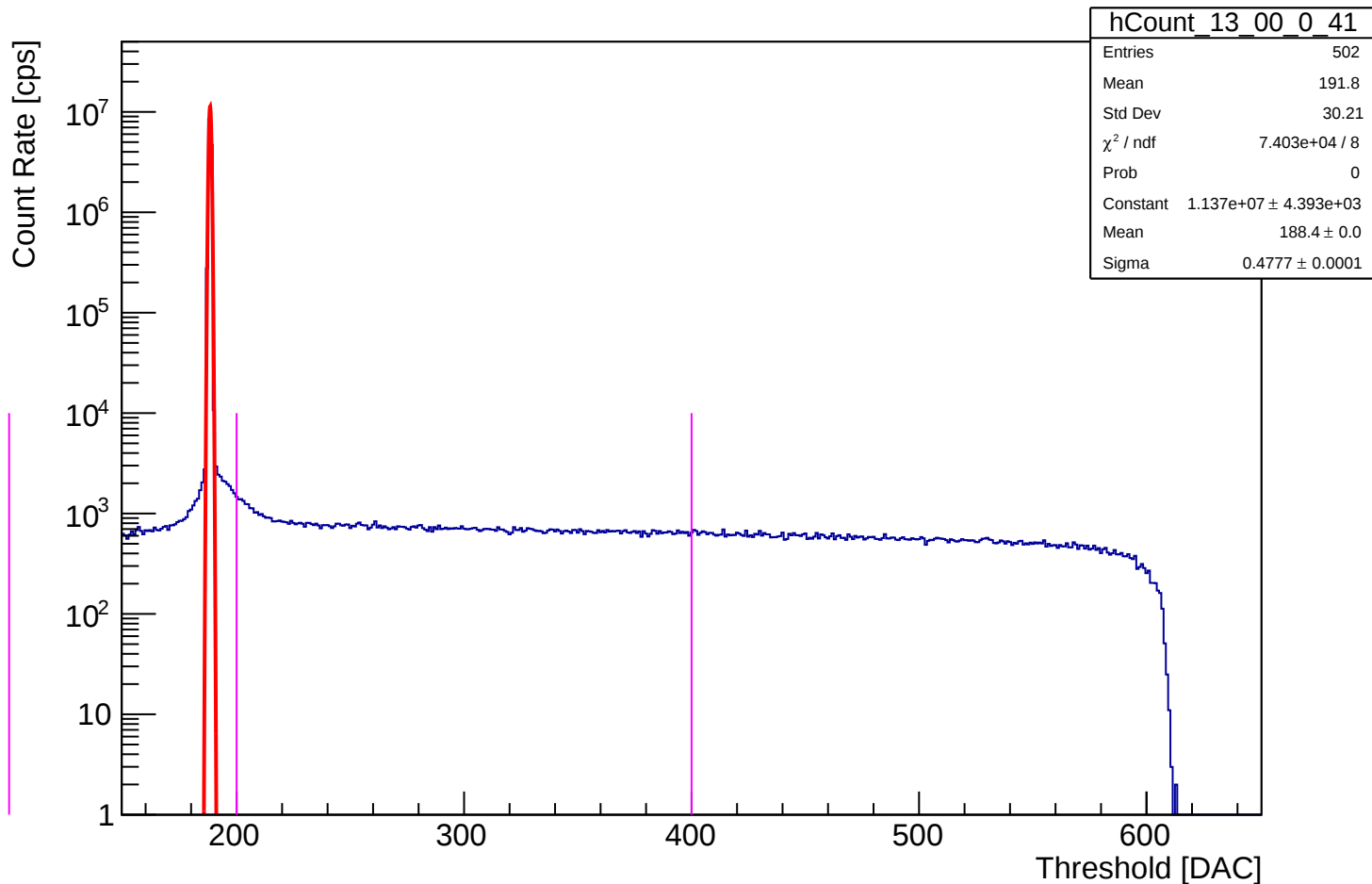
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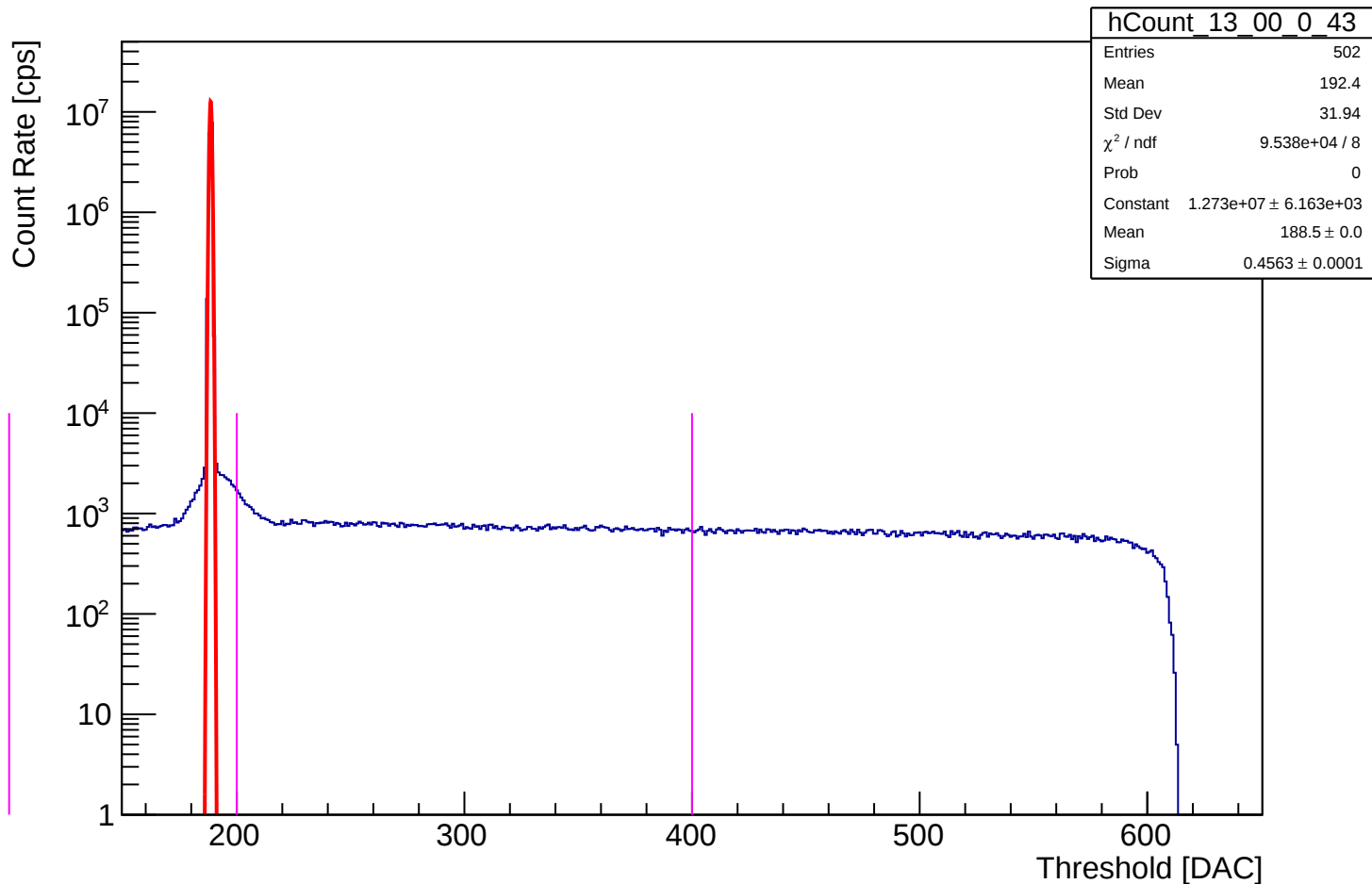
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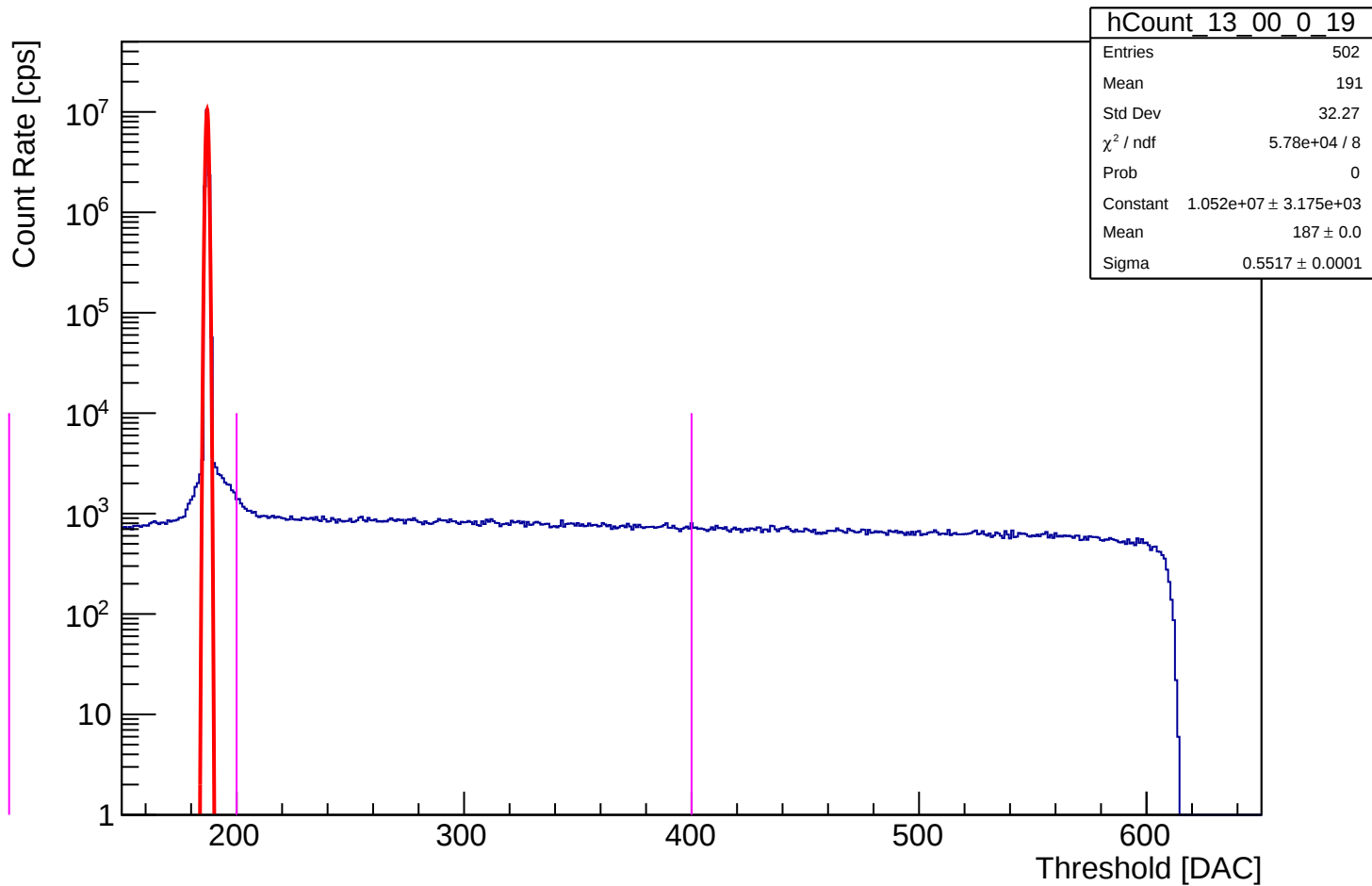
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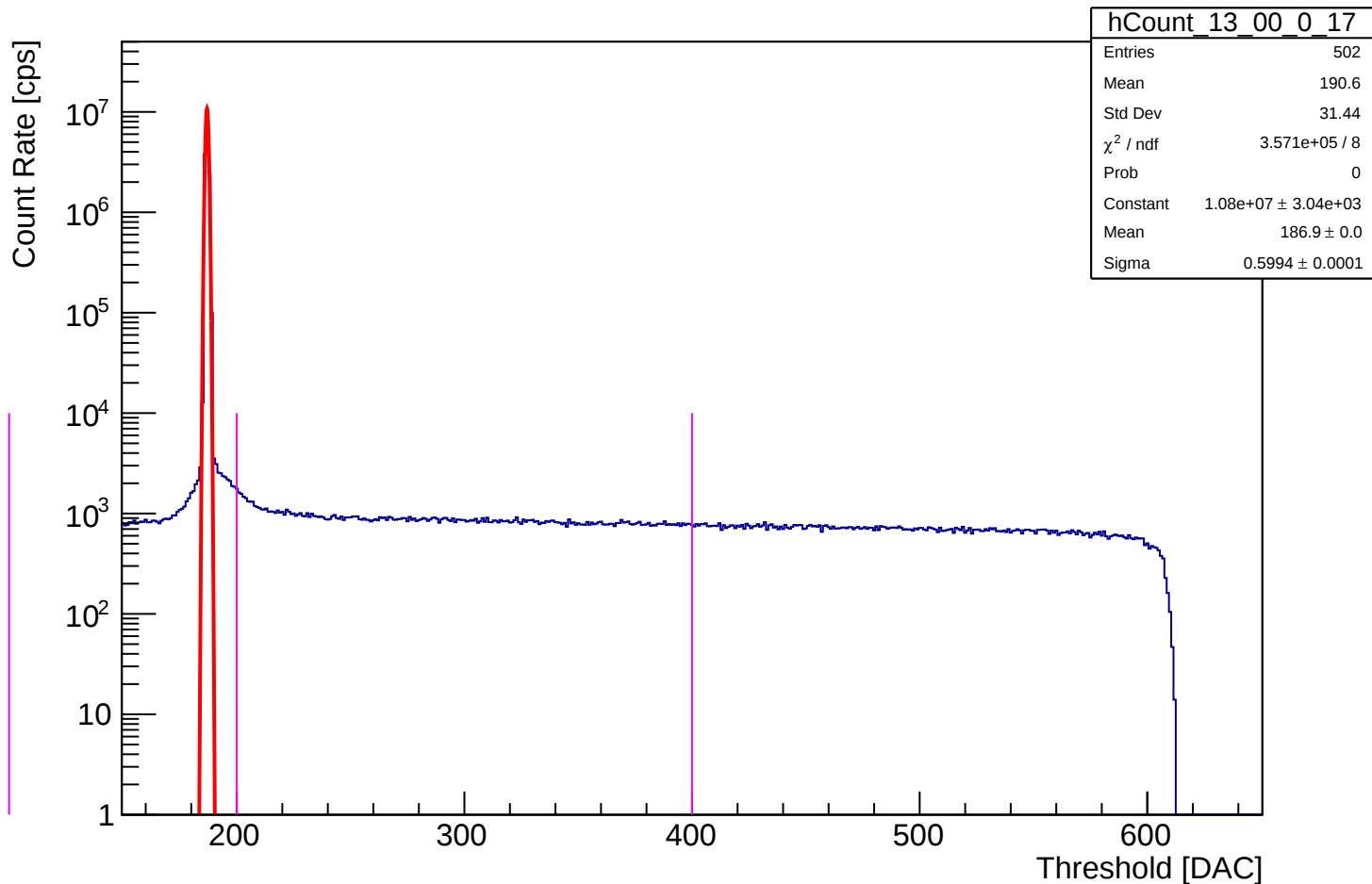
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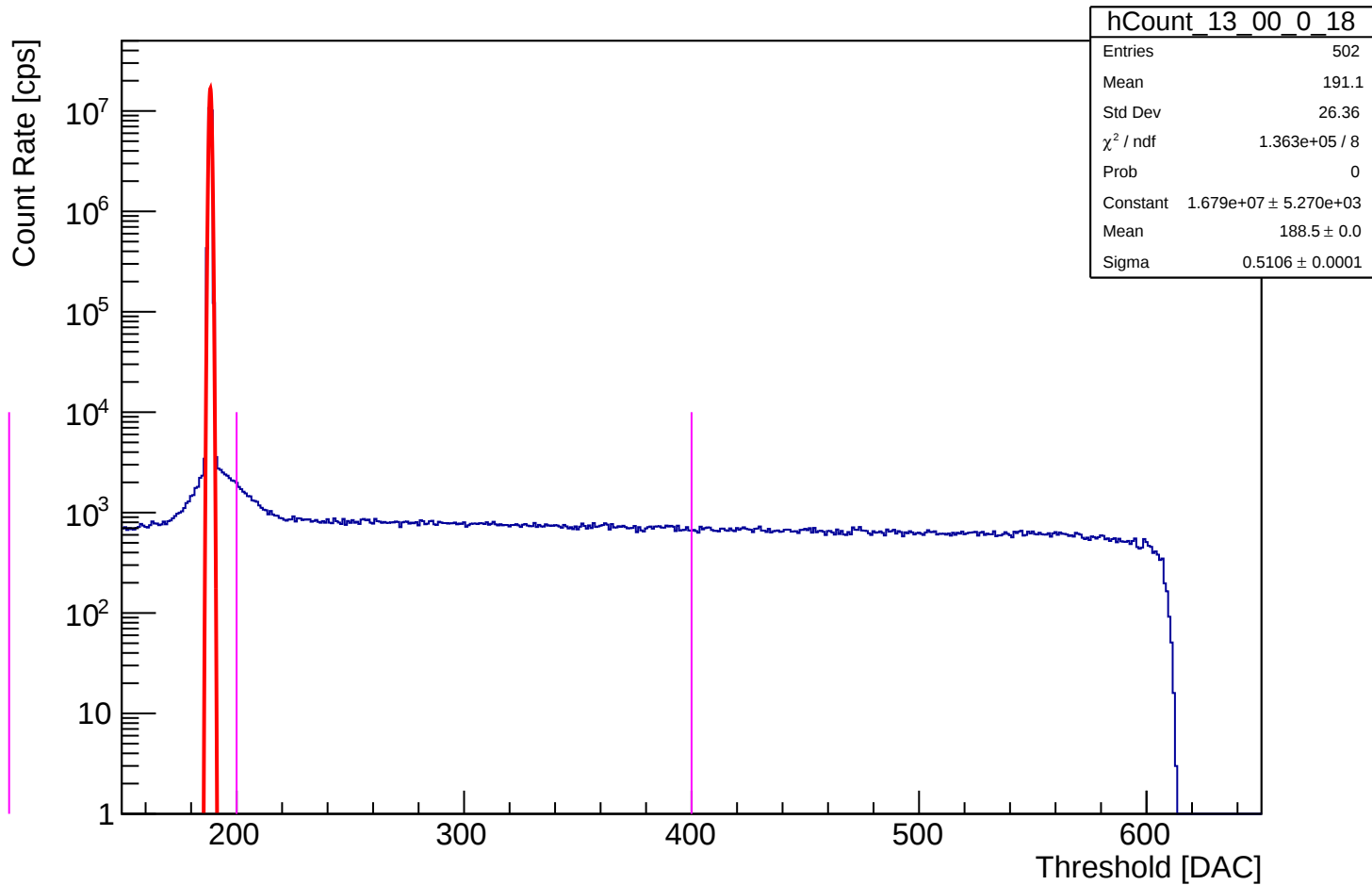
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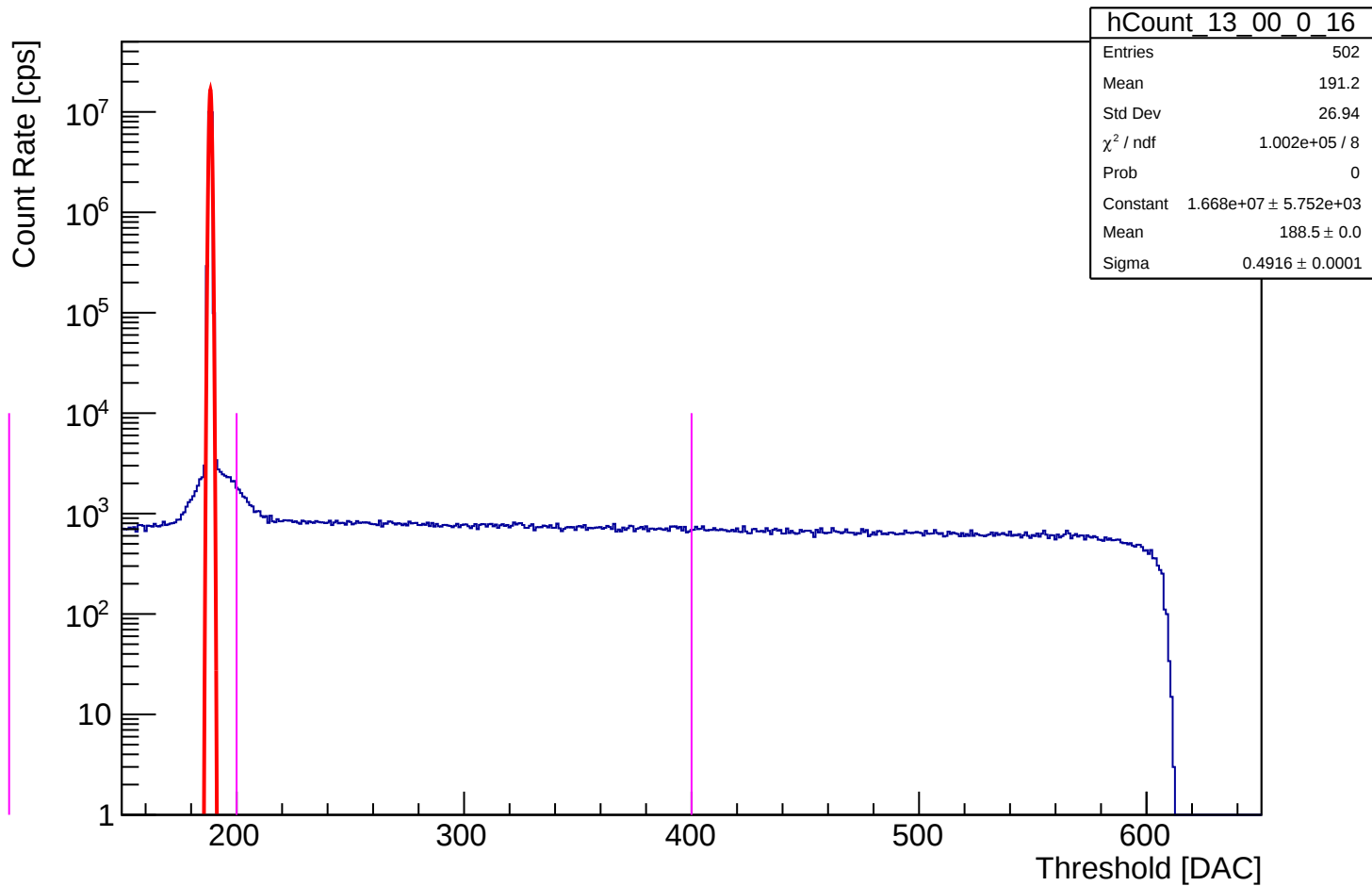
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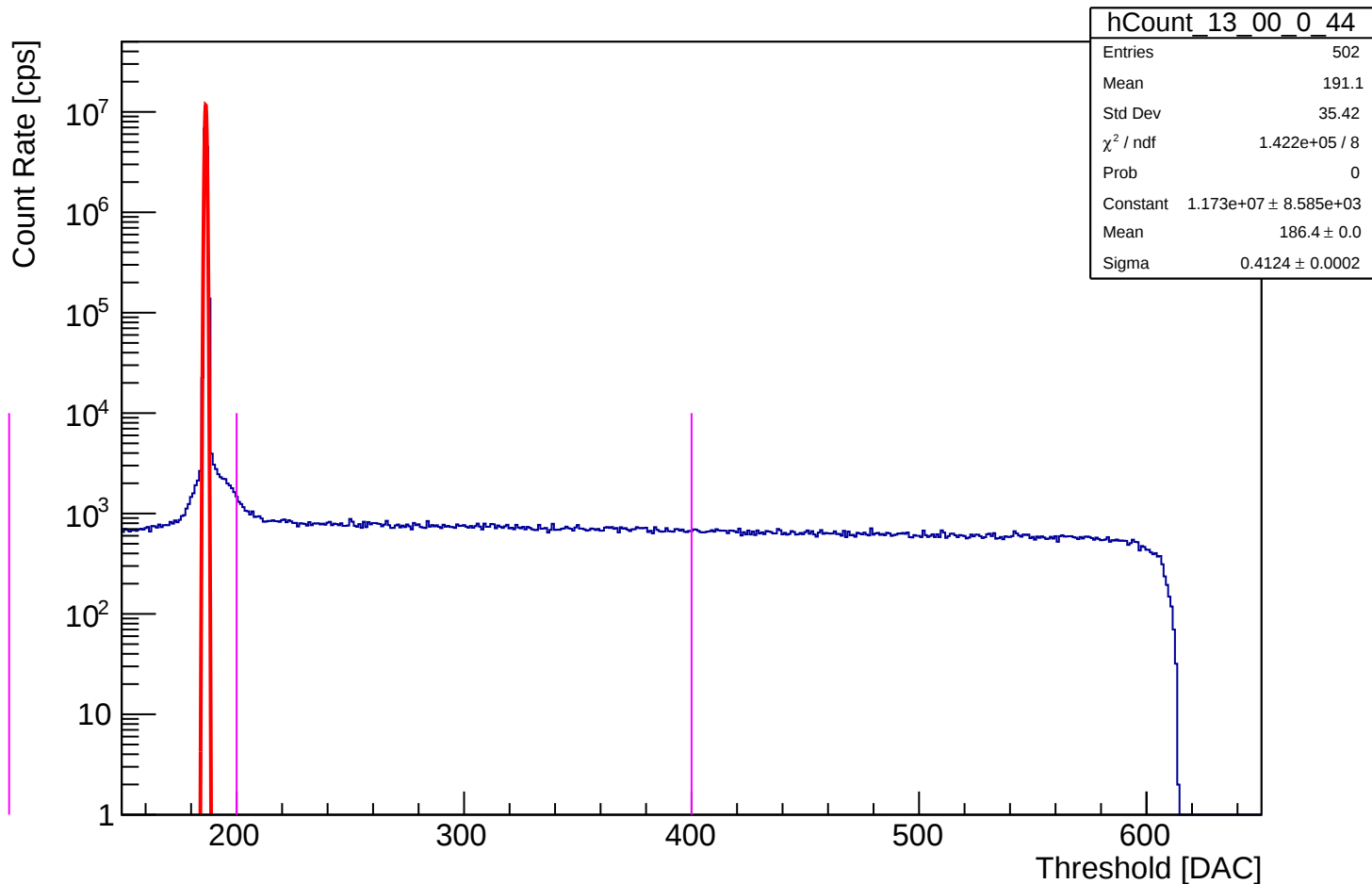
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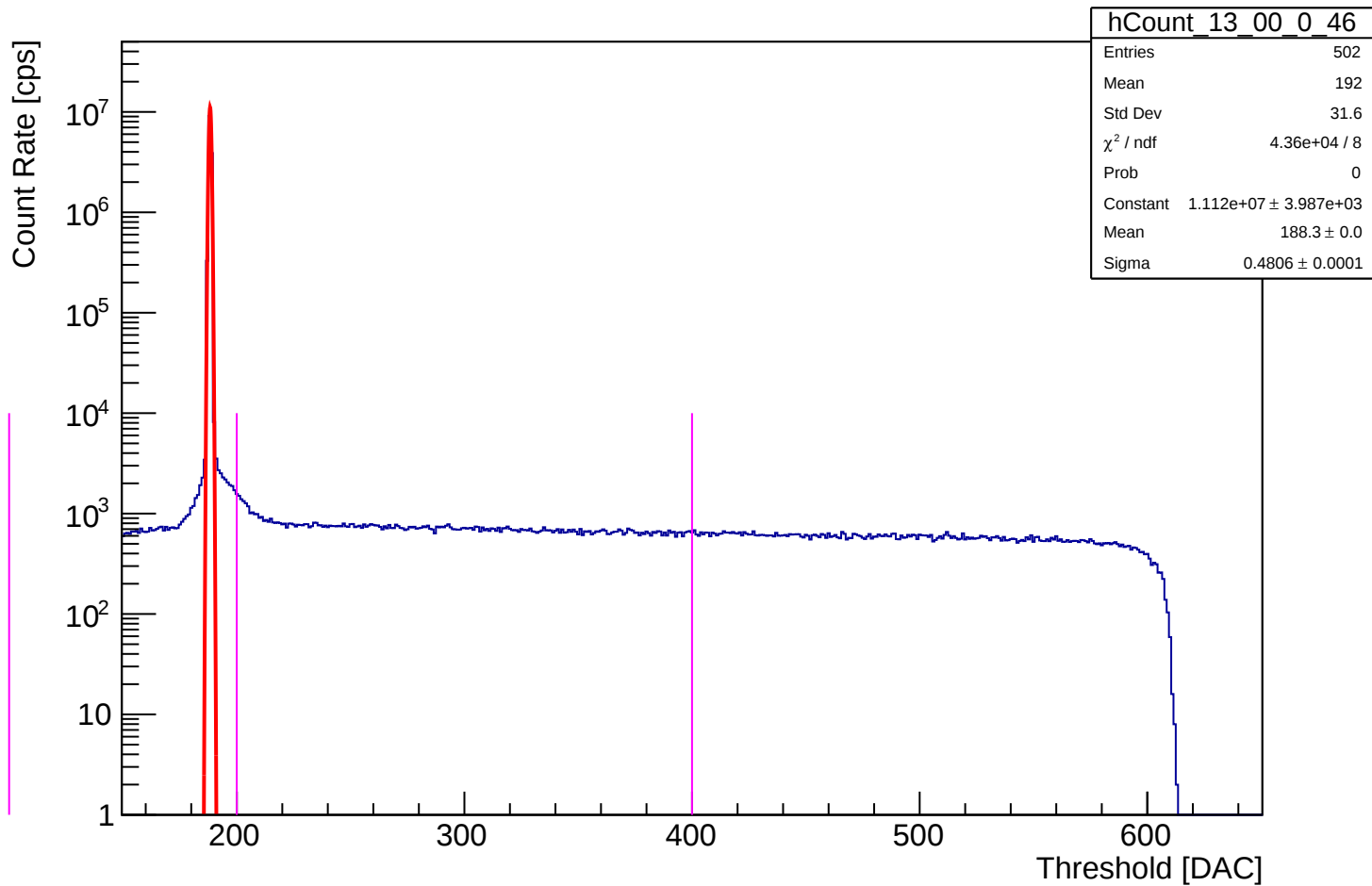
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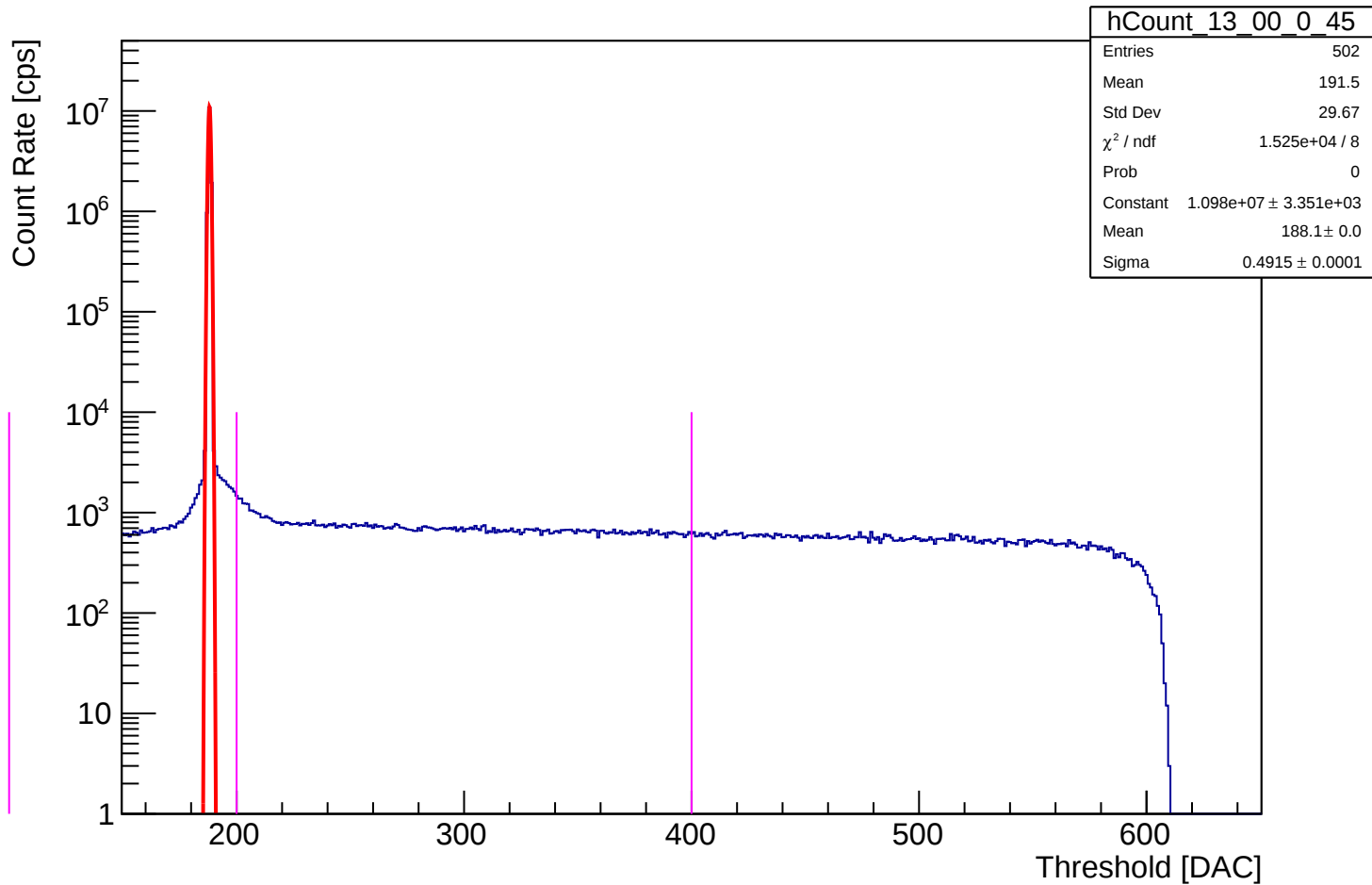
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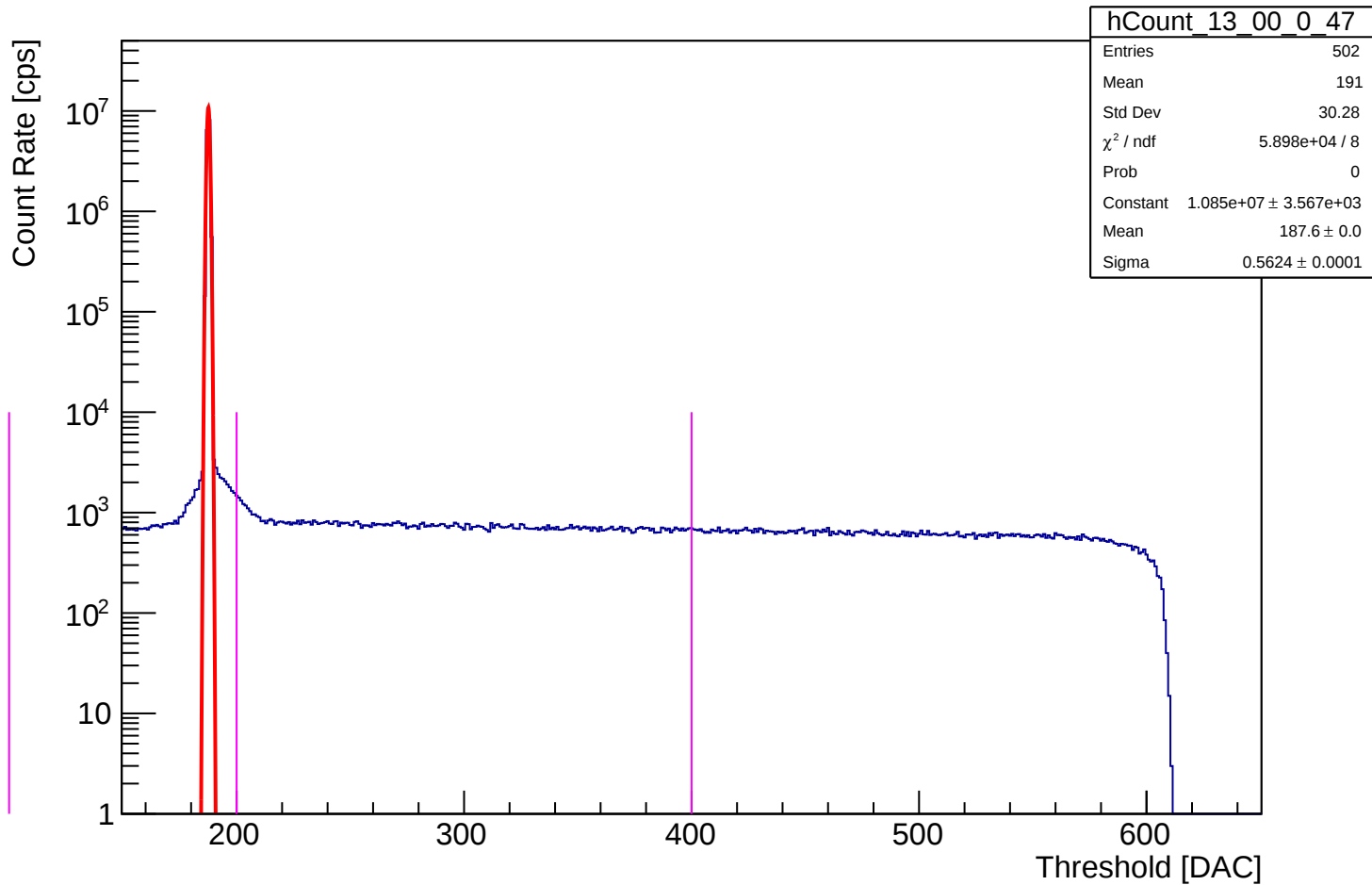
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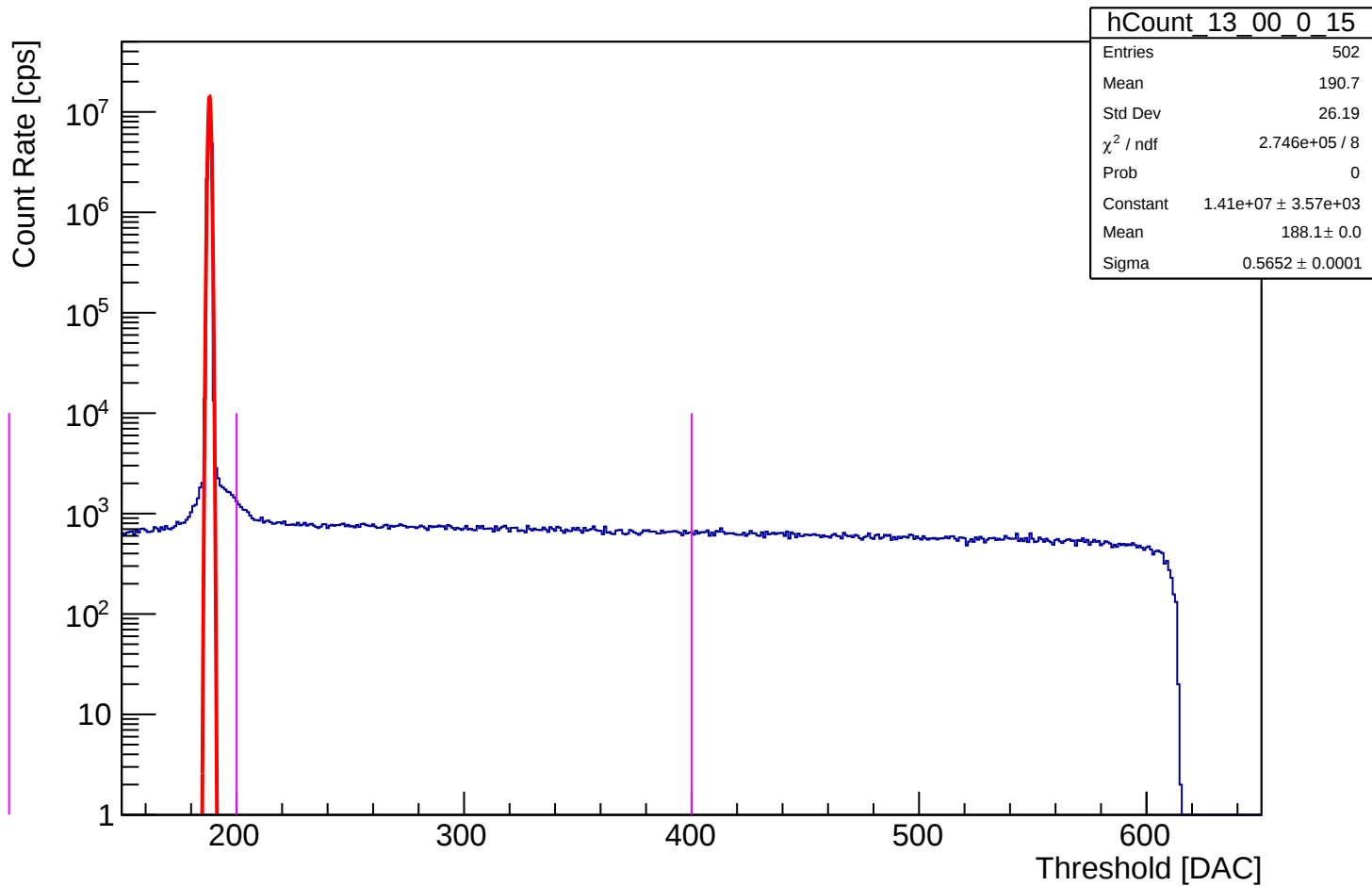
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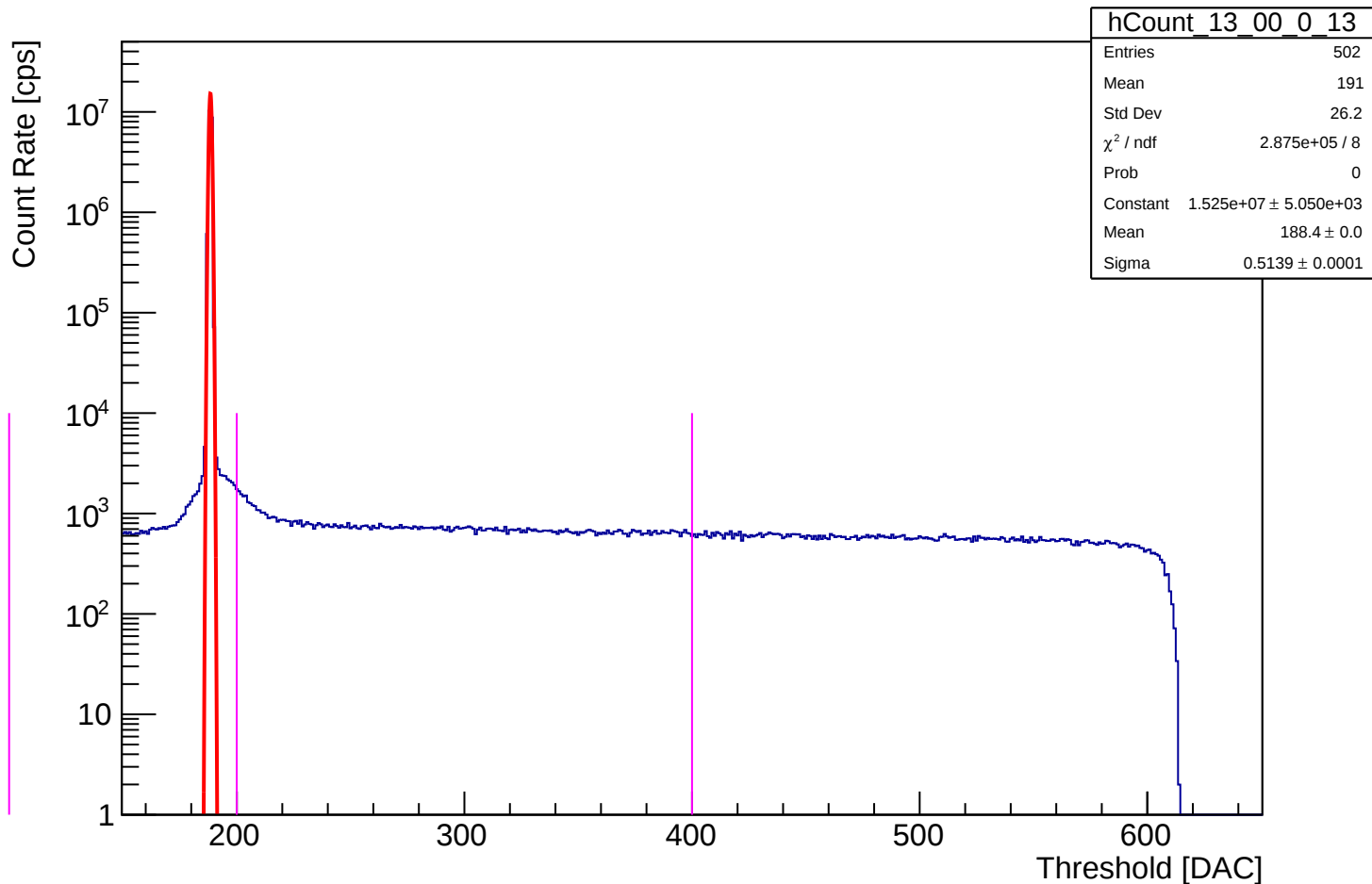
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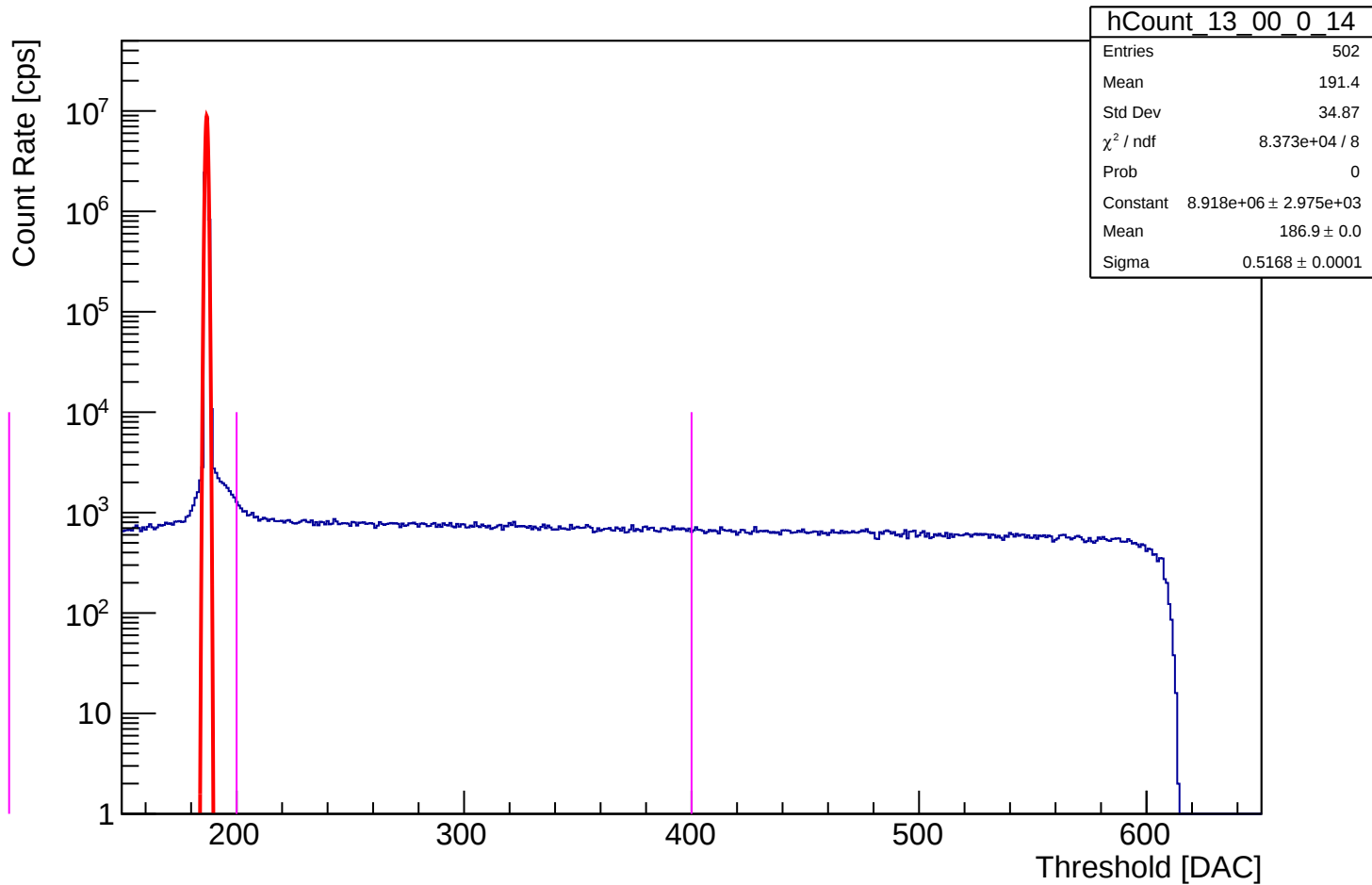
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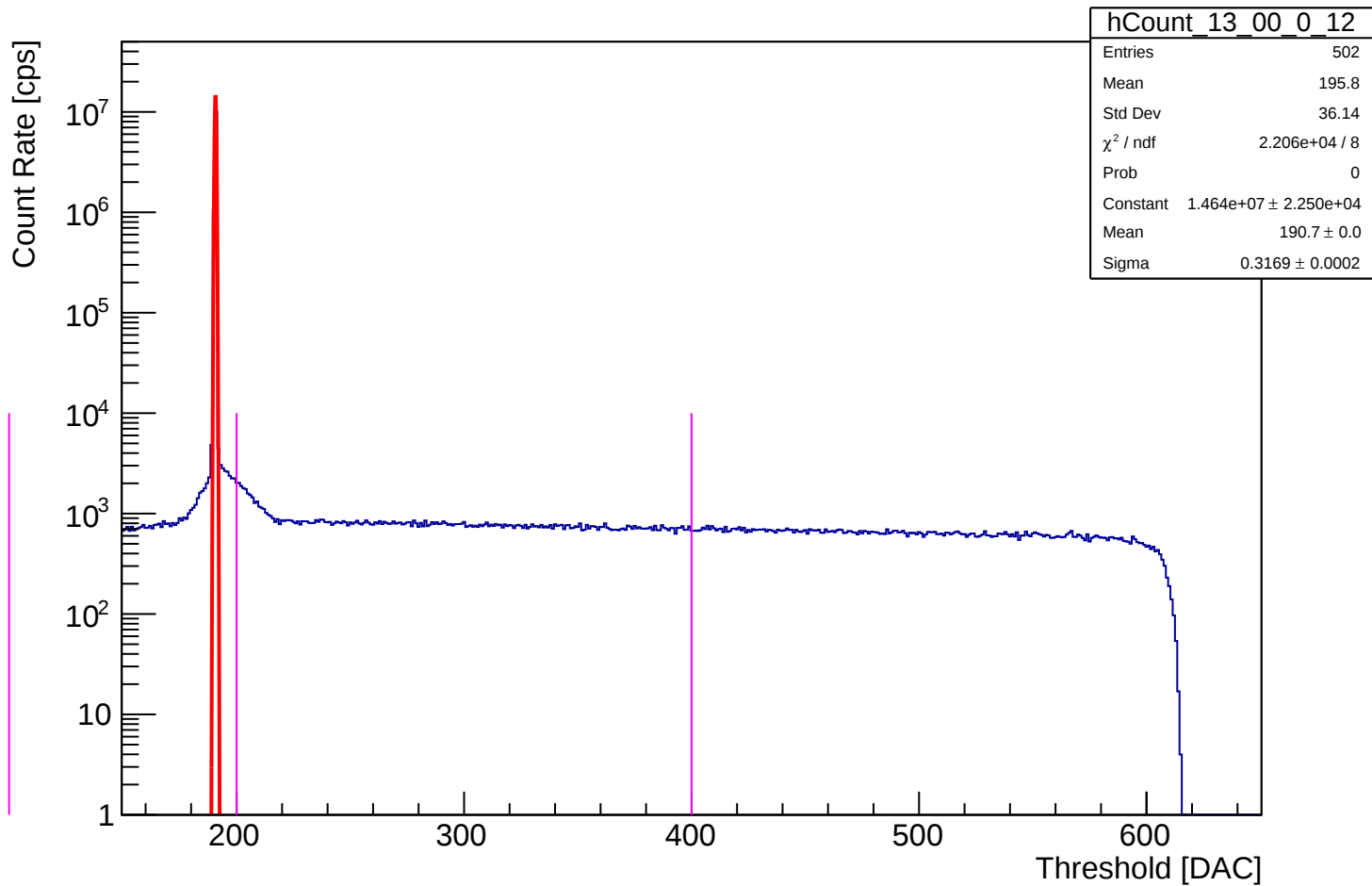
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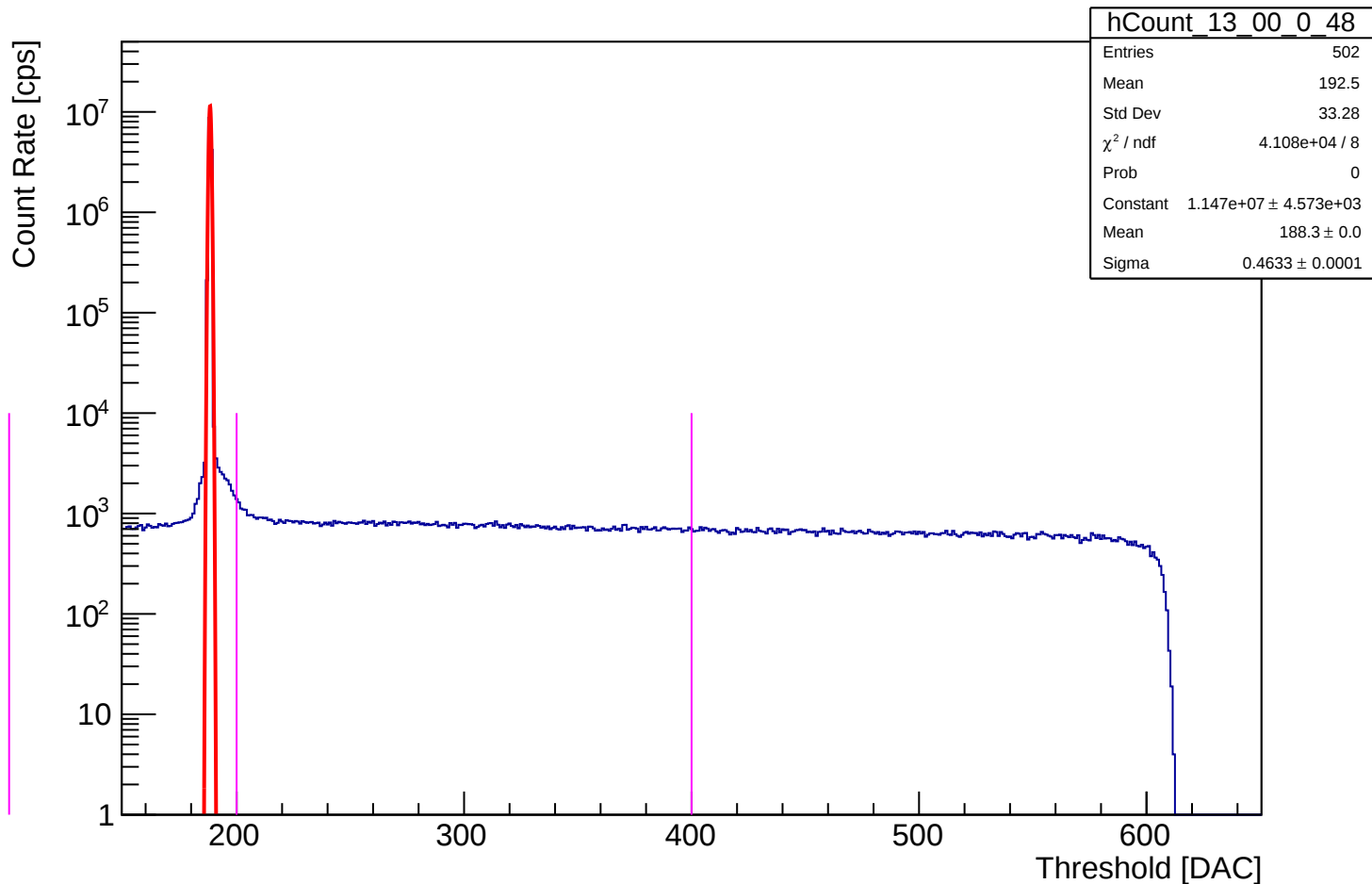
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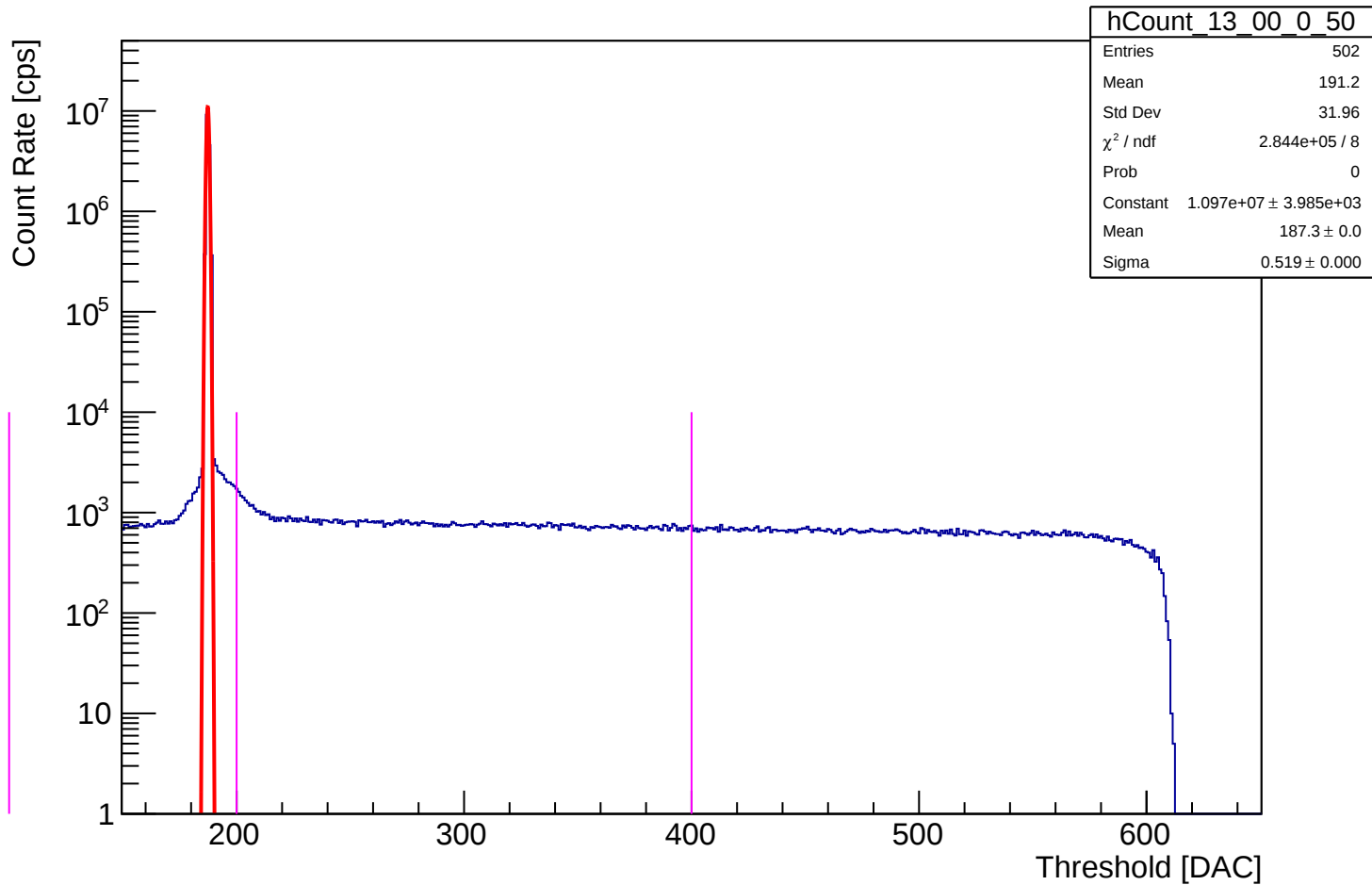
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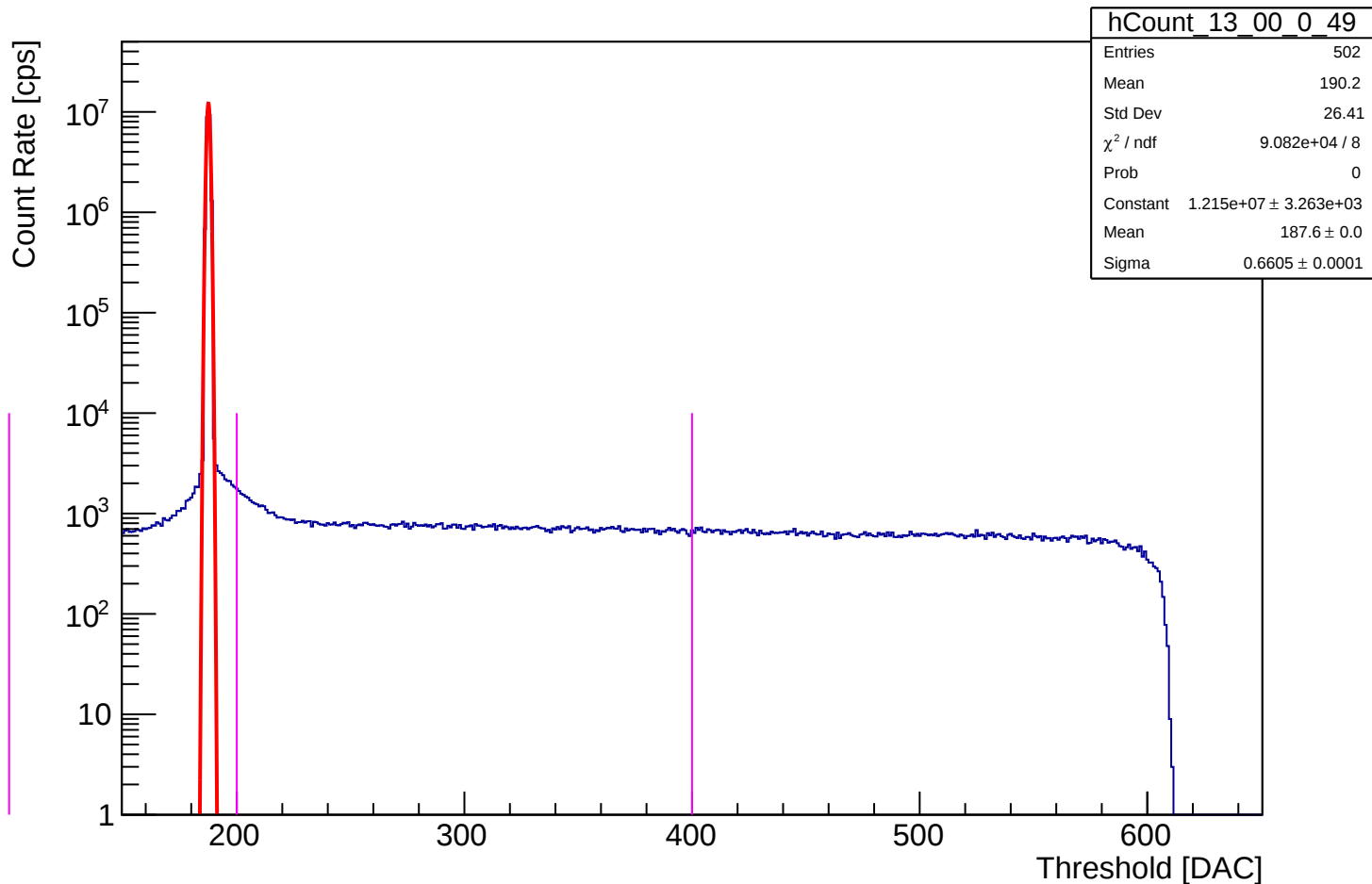
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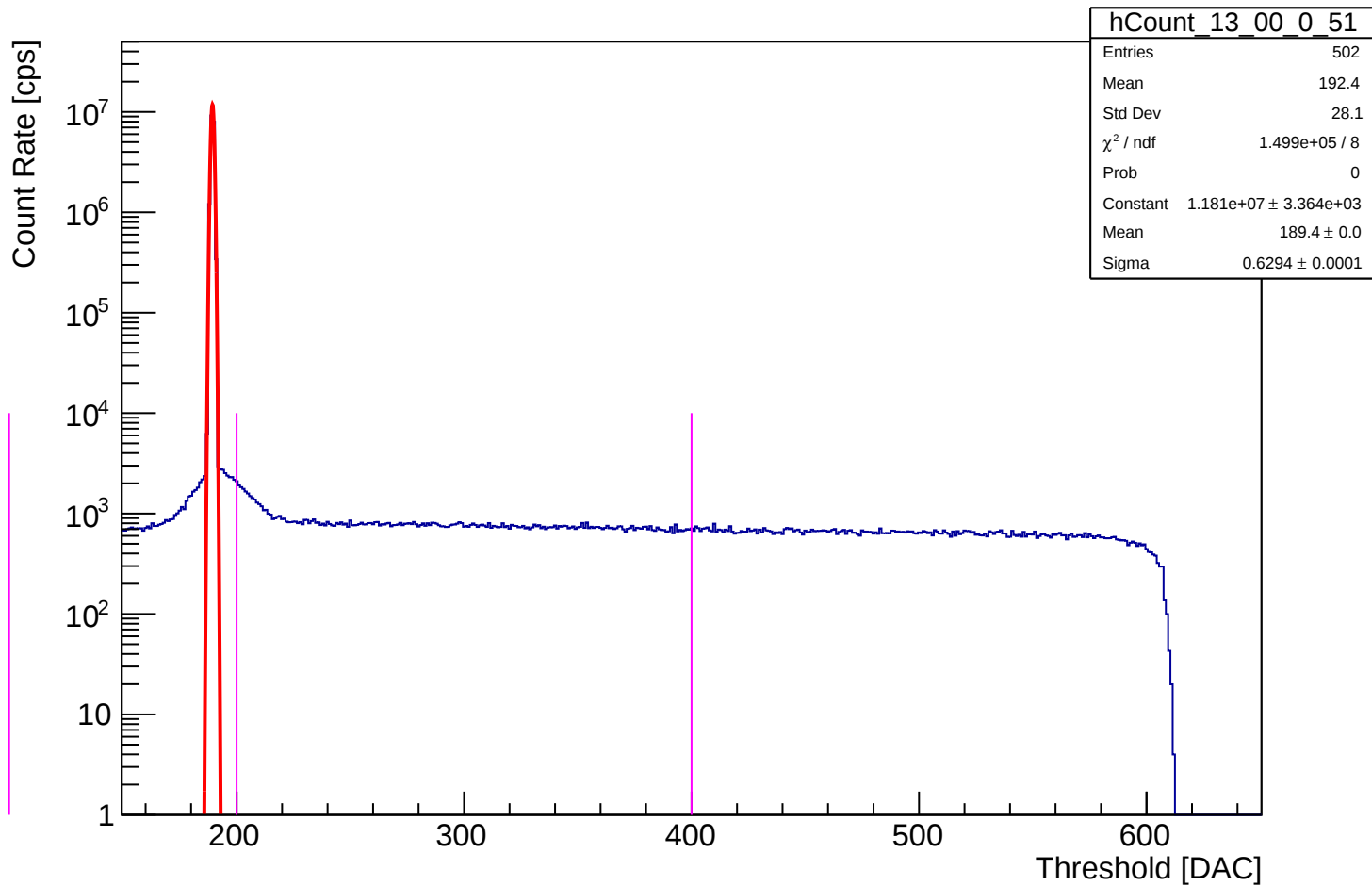
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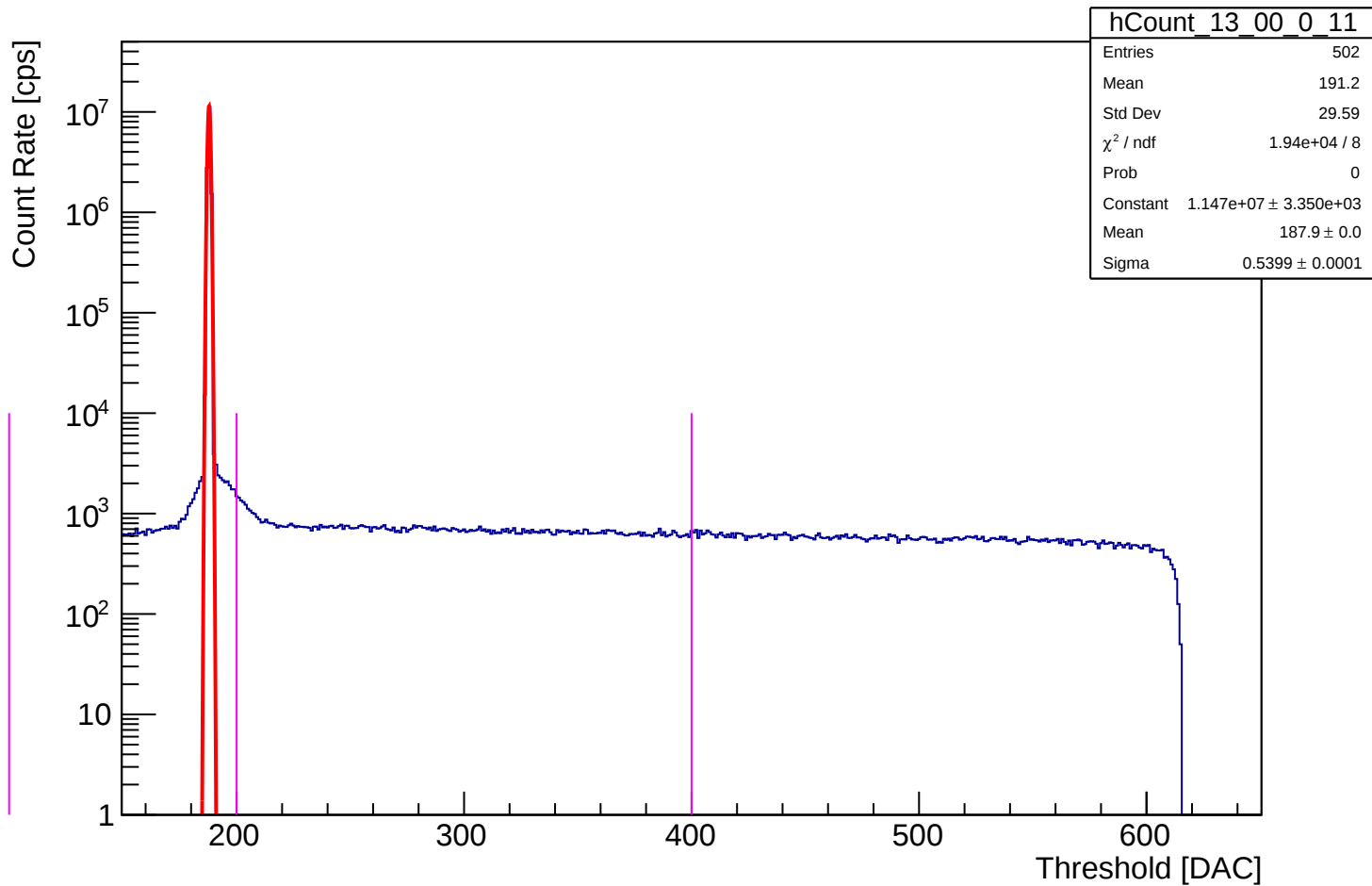
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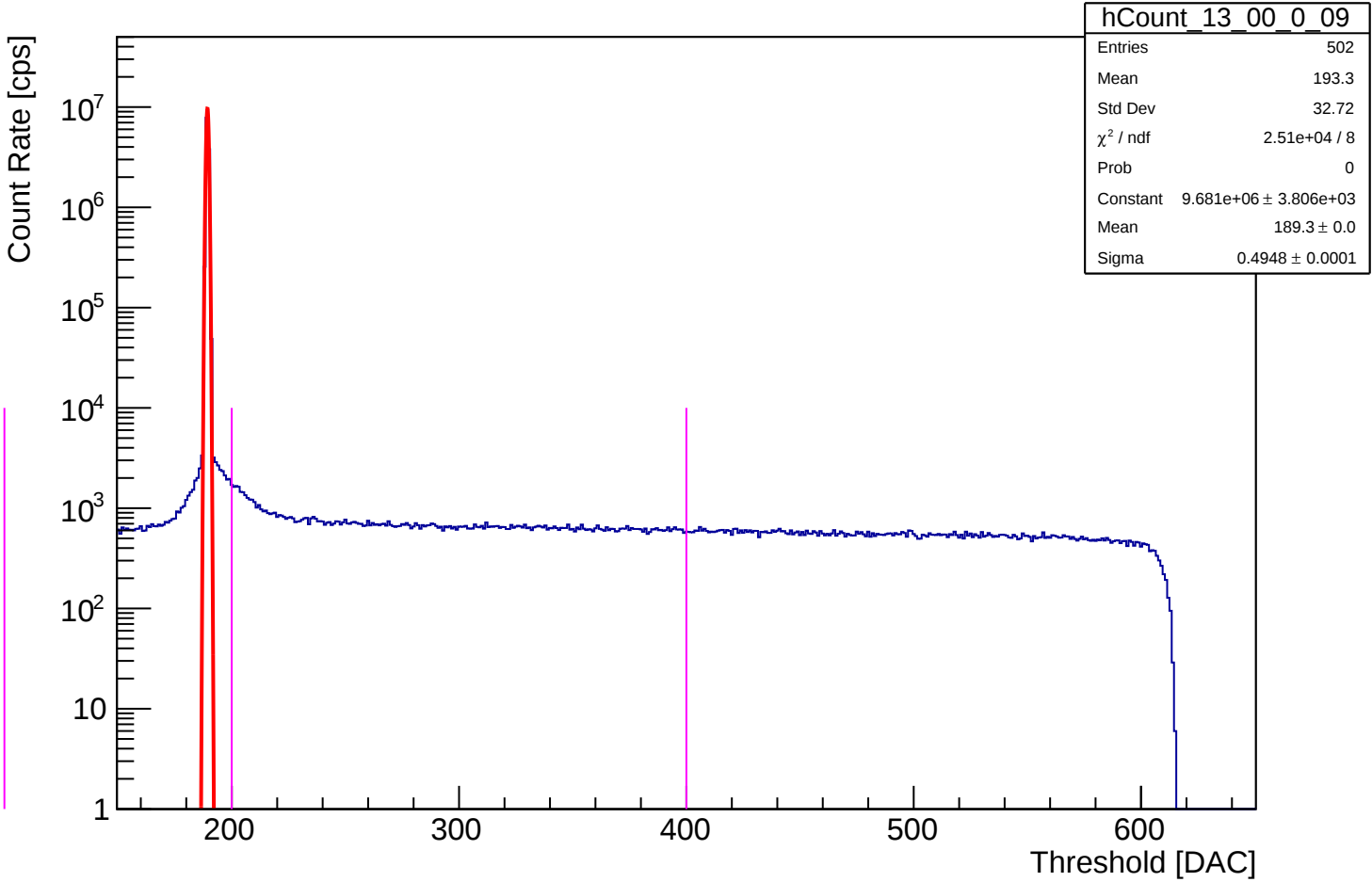


Row 1 Tile 1 Pmt 1 Pixel 40 Slot 13 Fiber 0 Asic 0 Channel 51

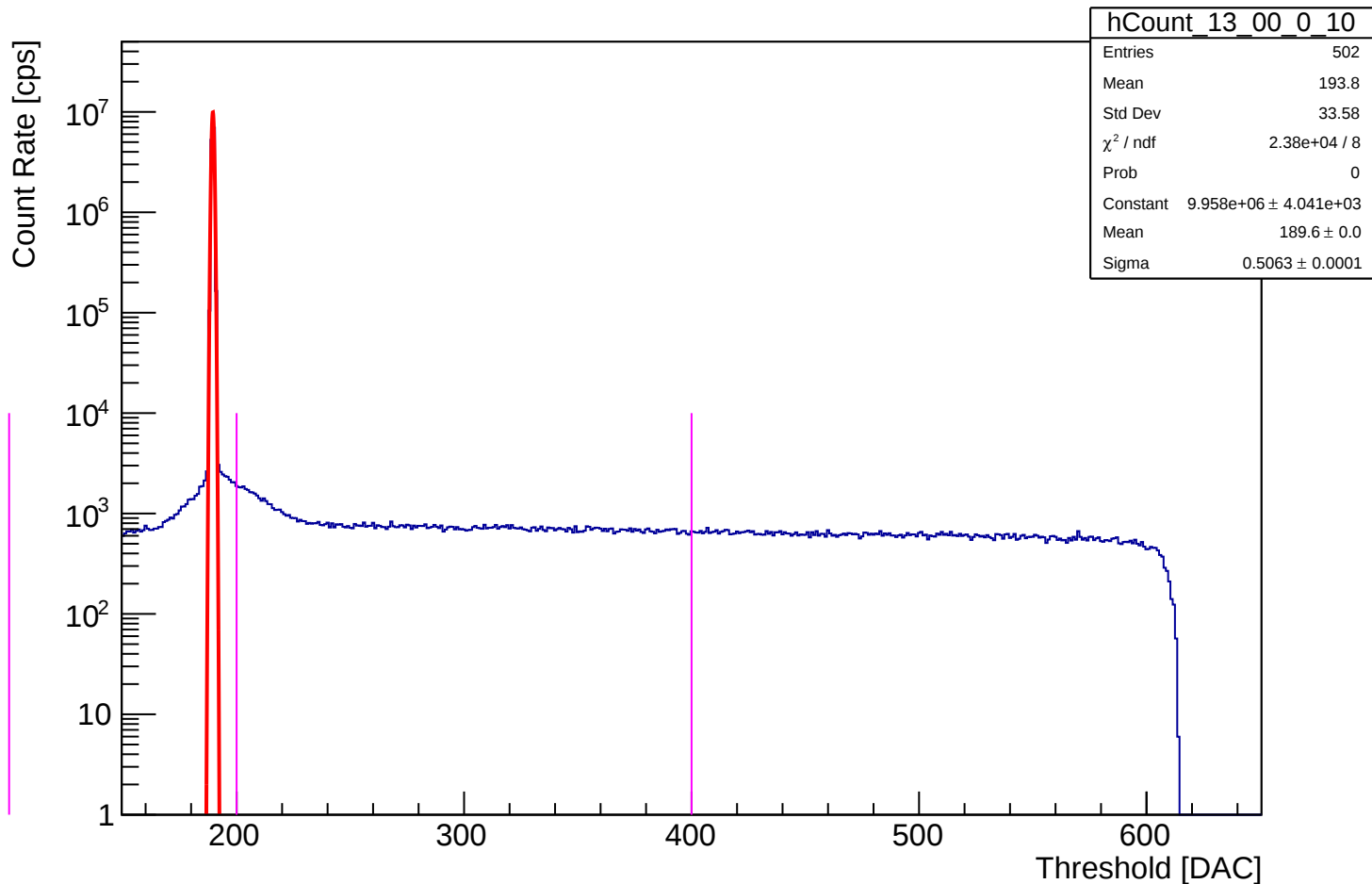


Row 1 Tile 1 Pmt 1 Pixel 41 Slot 13 Fiber 0 Asic 0 Channel 11



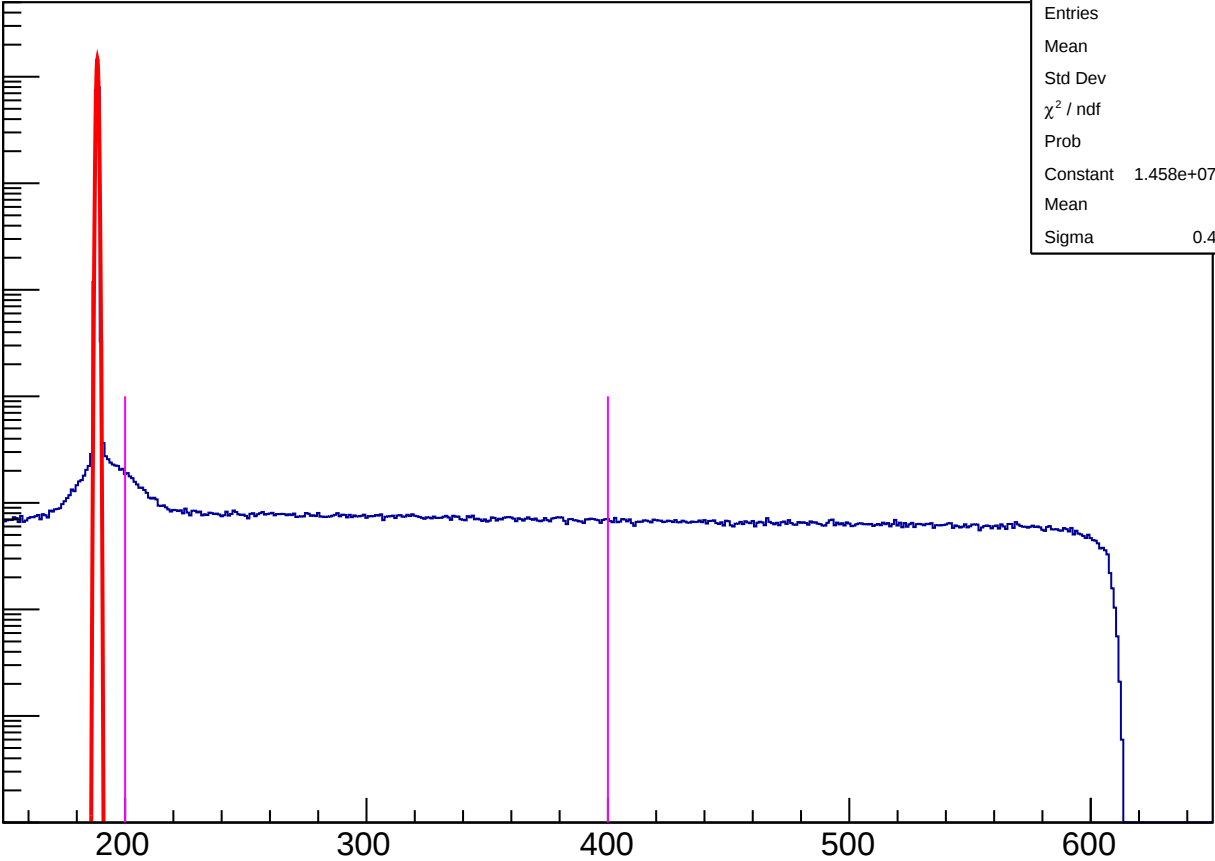


Row 1 Tile 1 Pmt 1 Pixel 43 Slot 13 Fiber 0 Asic 0 Channel 10



Count Rate [cps]

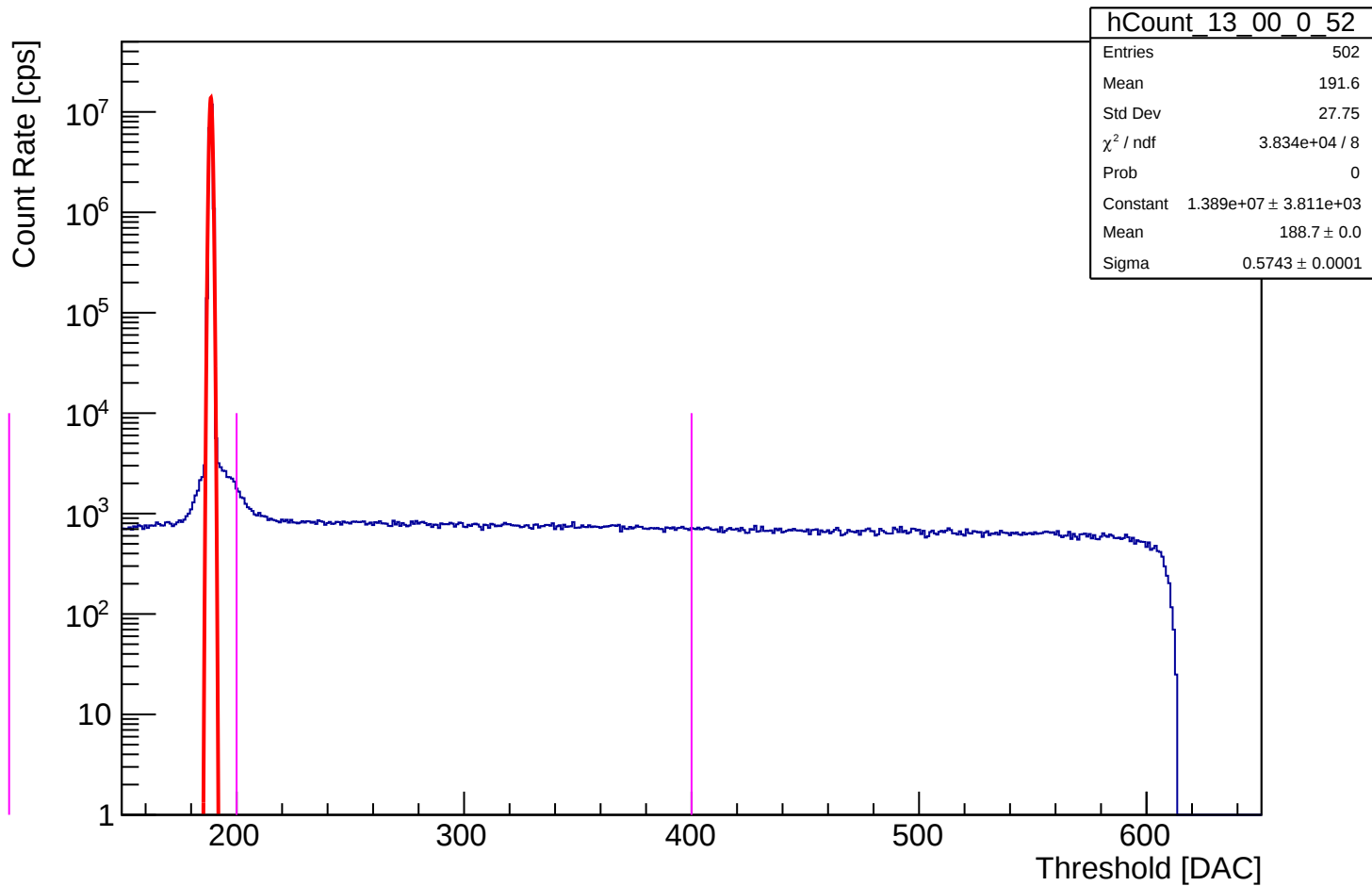
10⁷
10⁶
10⁵
10⁴
10³
10²
10
1



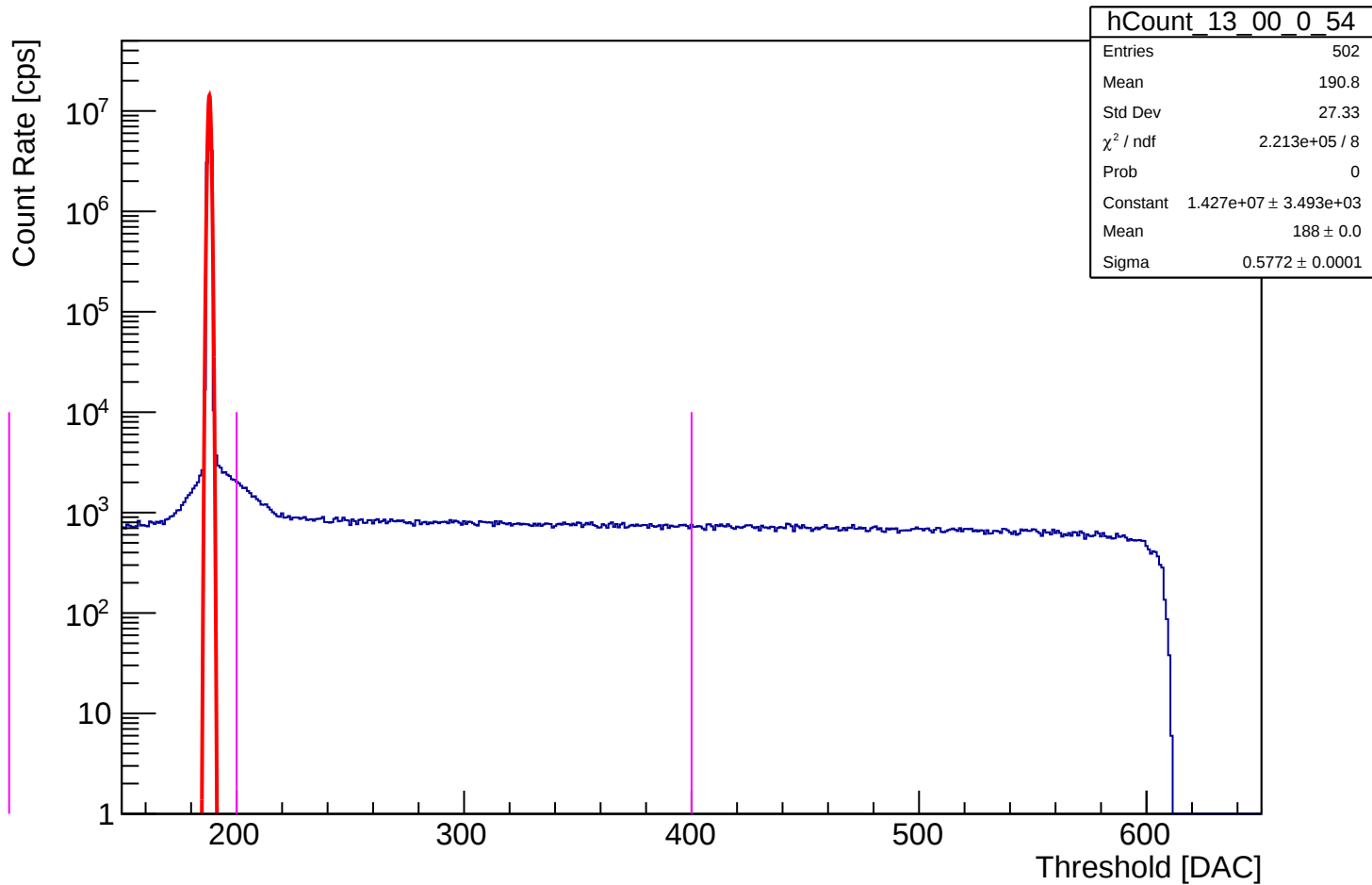
Threshold [DAC]

hCount_13_00_0_08	
Entries	502
Mean	192
Std Dev	30.76
χ^2 / ndf	7.69e+04 / 8
Prob	0
Constant	1.458e+07 $\pm$ 7.390e+03
Mean	188.5 $\pm$ 0.0
Sigma	0.4432 $\pm$ 0.0001

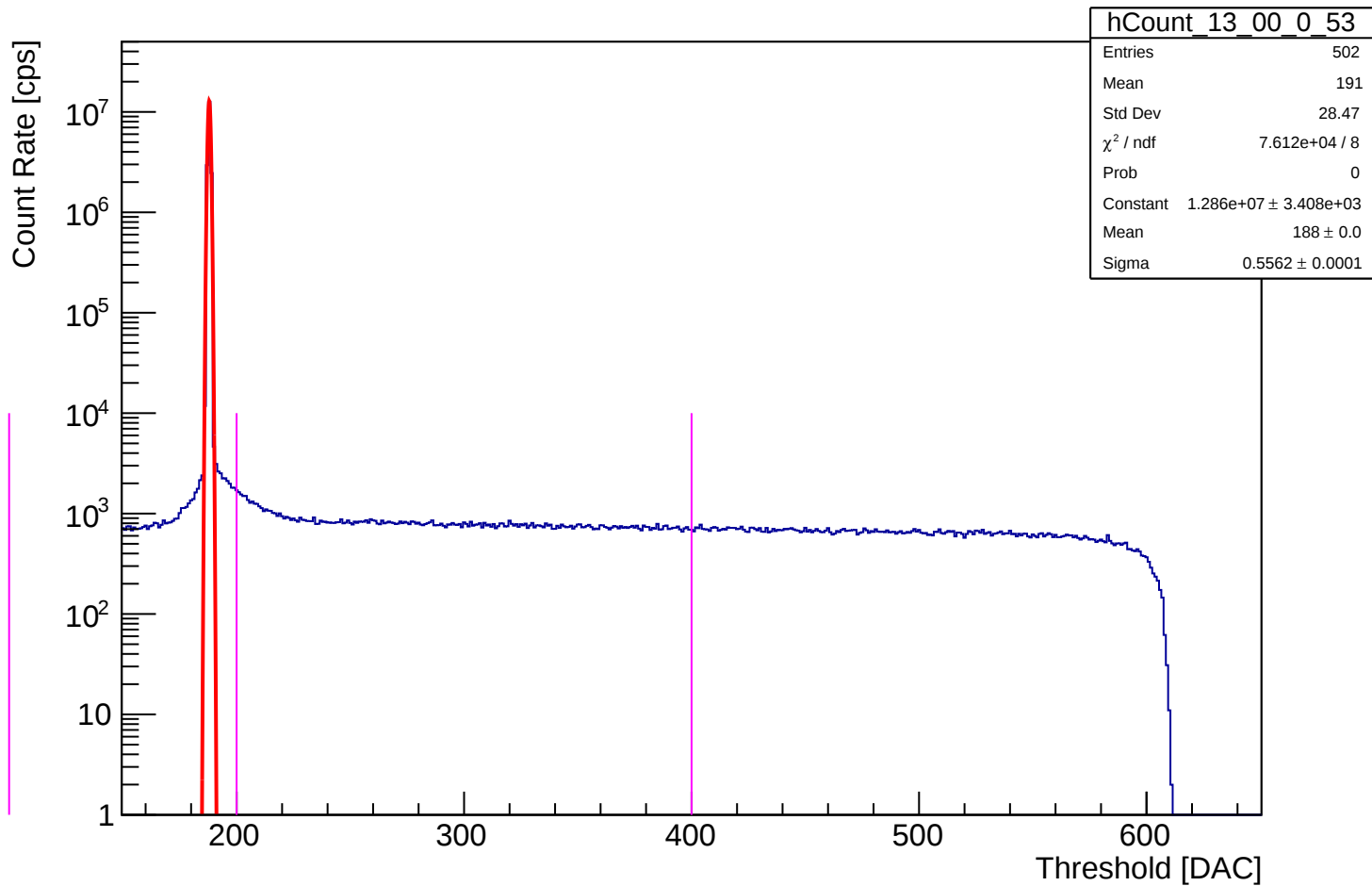
Row 1 Tile 1 Pmt 1 Pixel 45 Slot 13 Fiber 0 Asic 0 Channel 52



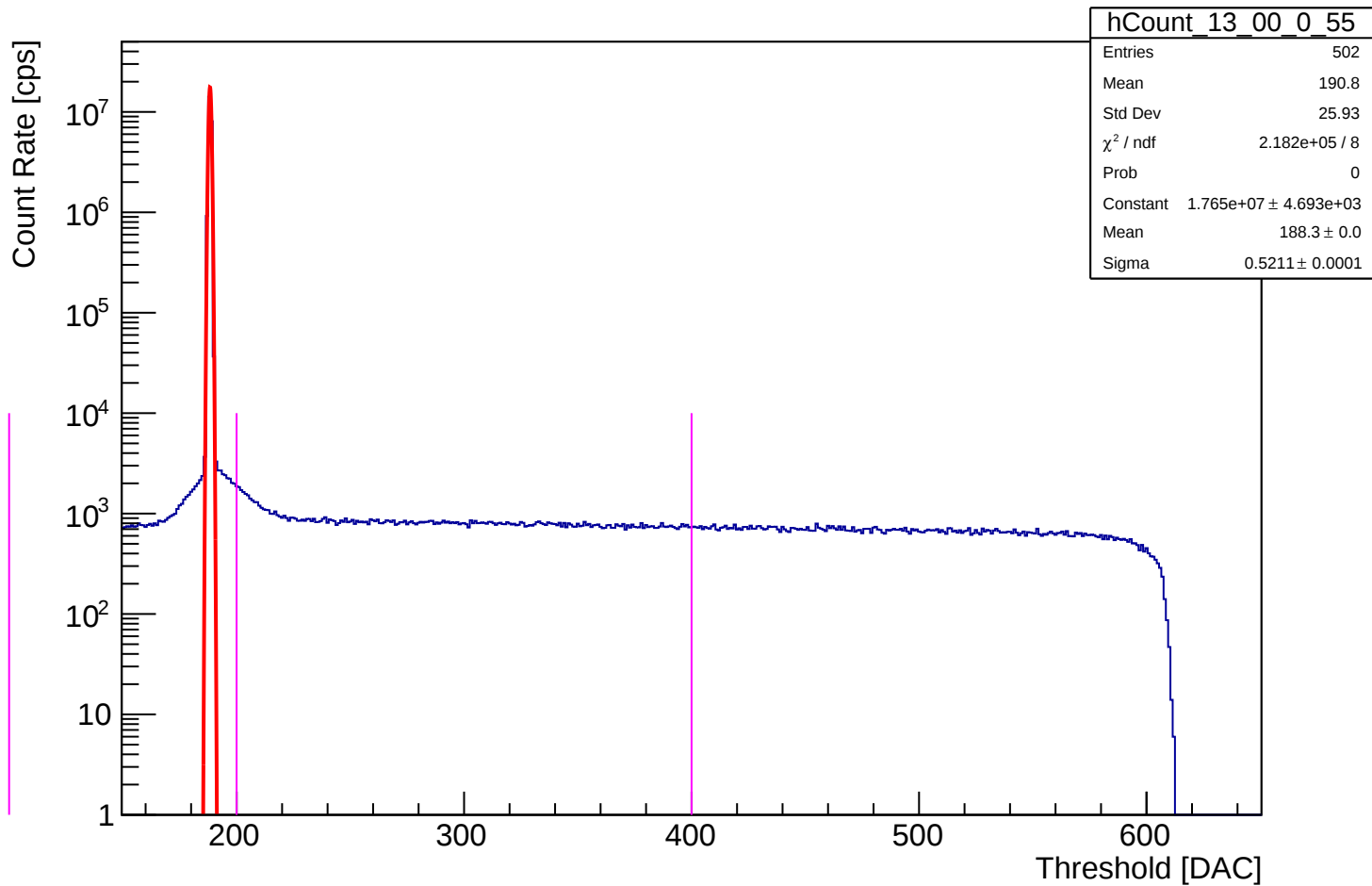
Row 1 Tile 1 Pmt 1 Pixel 46 Slot 13 Fiber 0 Asic 0 Channel 54

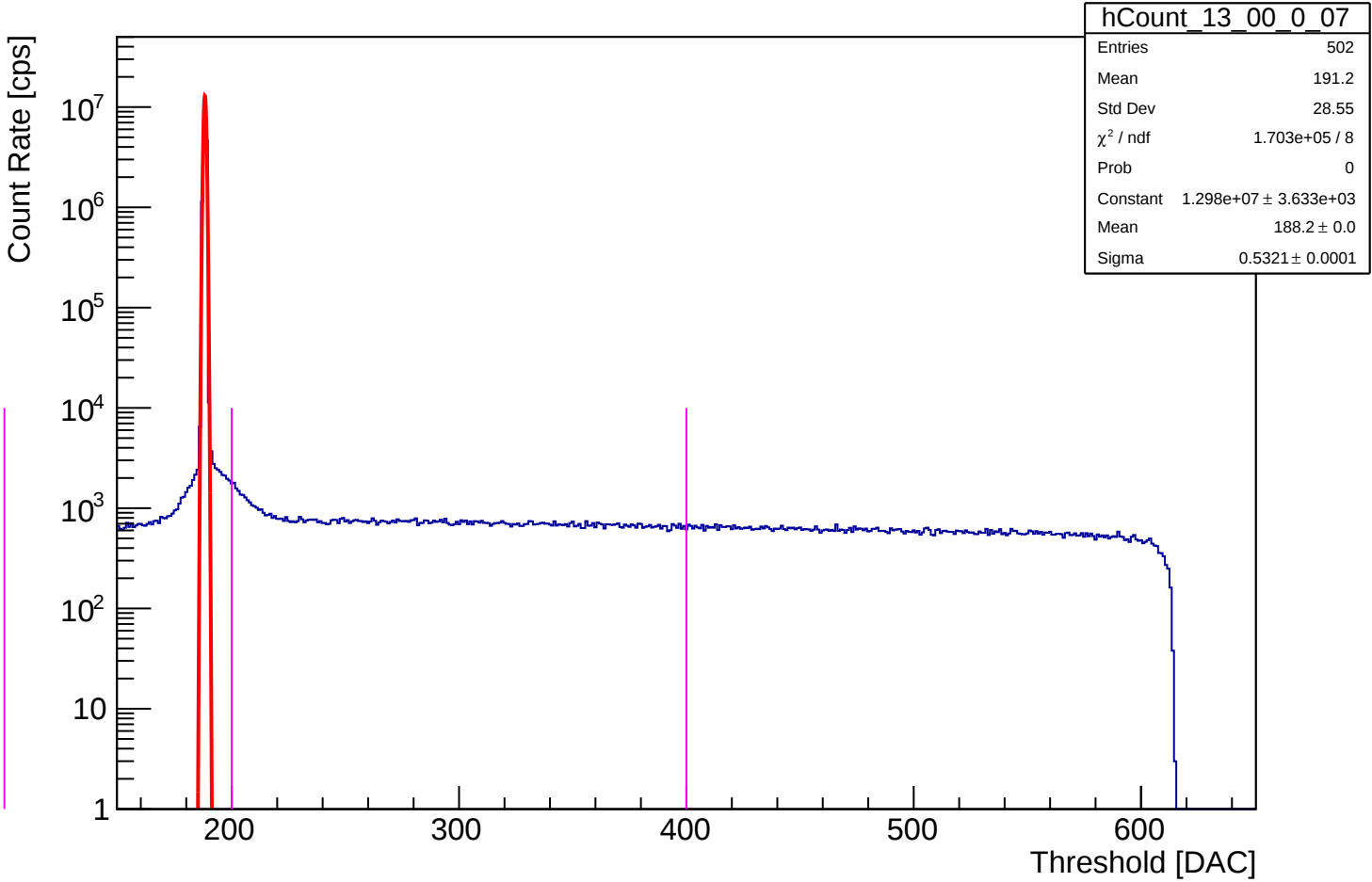


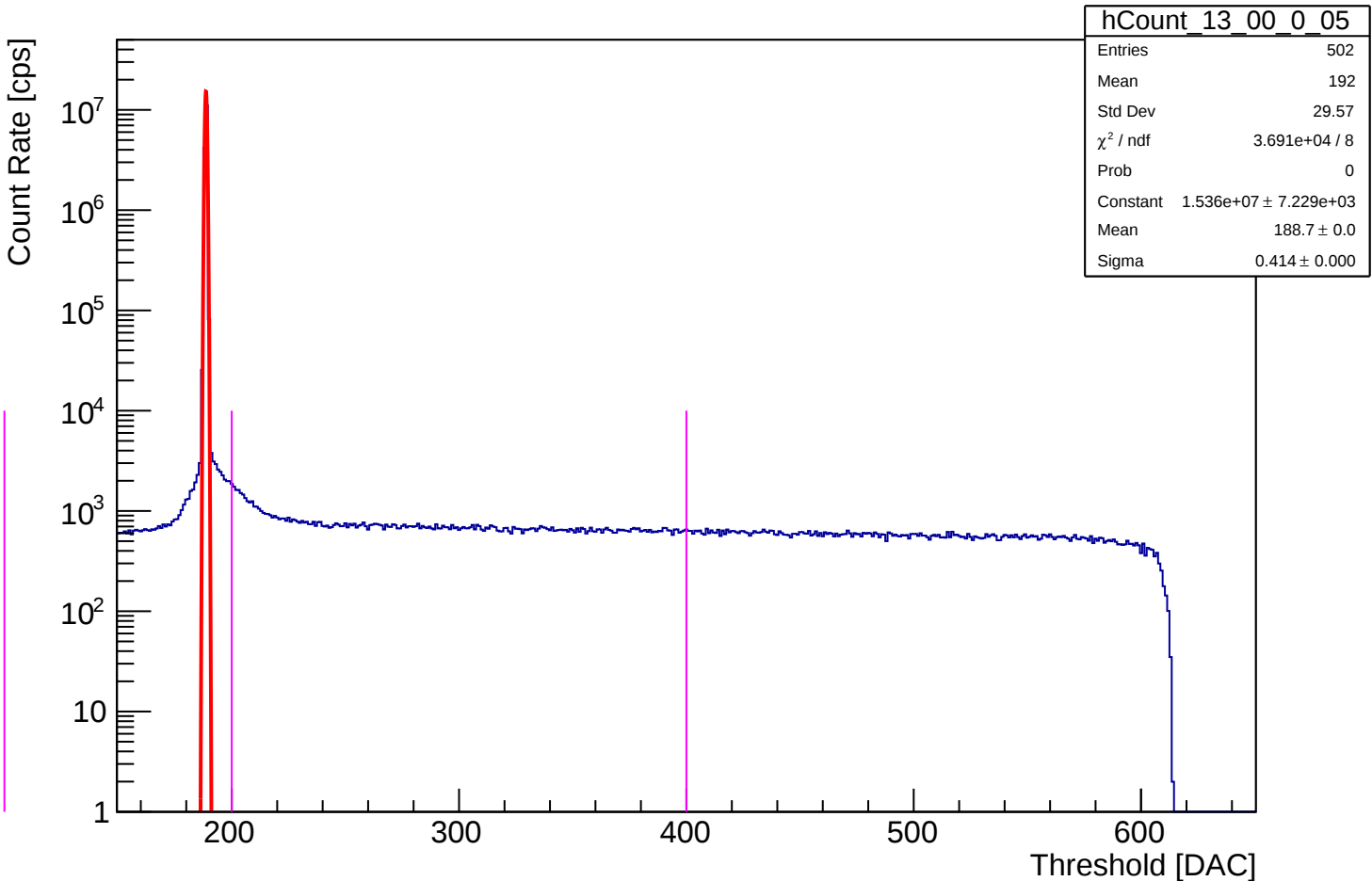
Row 1 Tile 1 Pmt 1 Pixel 47 Slot 13 Fiber 0 Asic 0 Channel 53

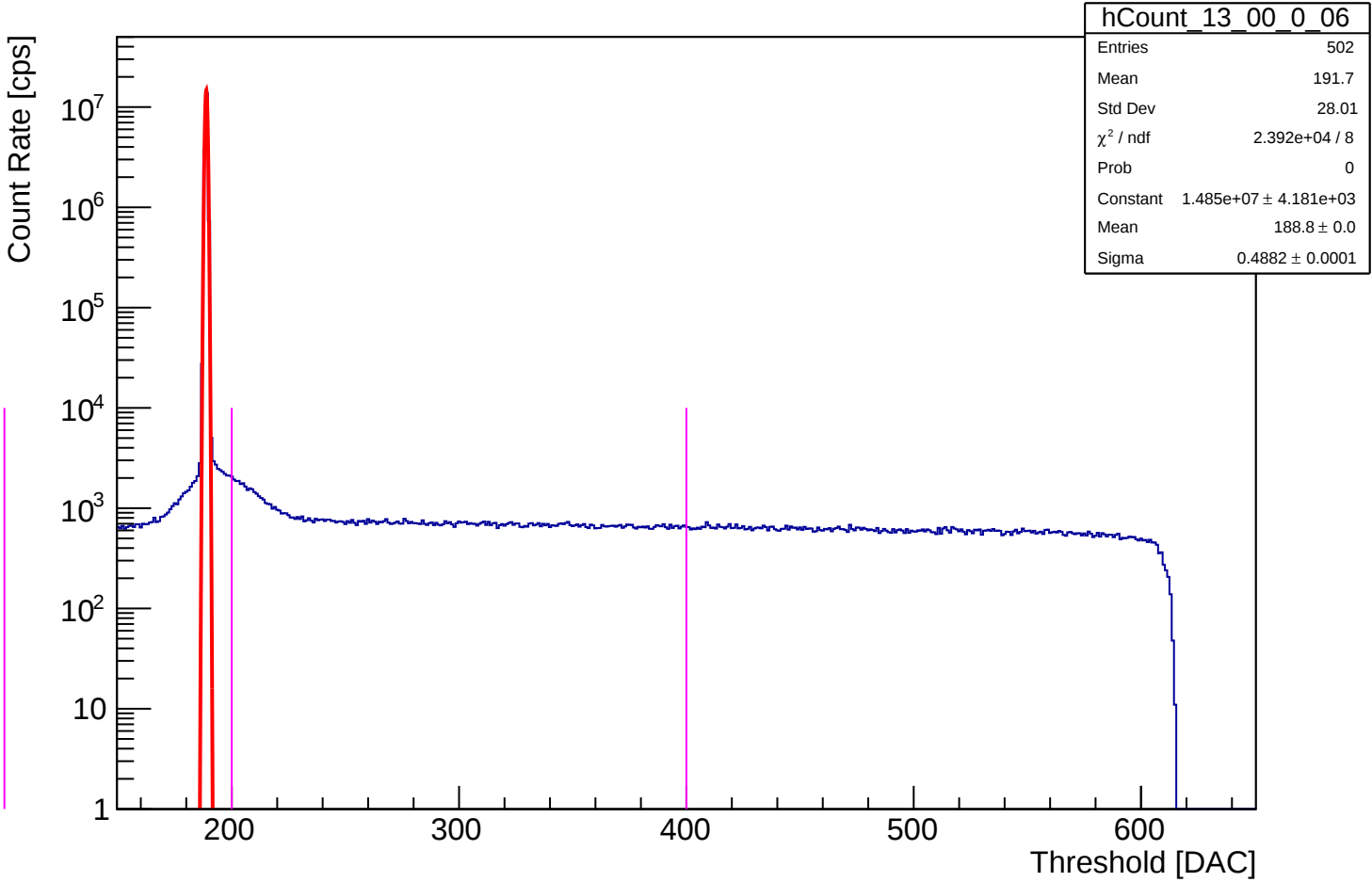


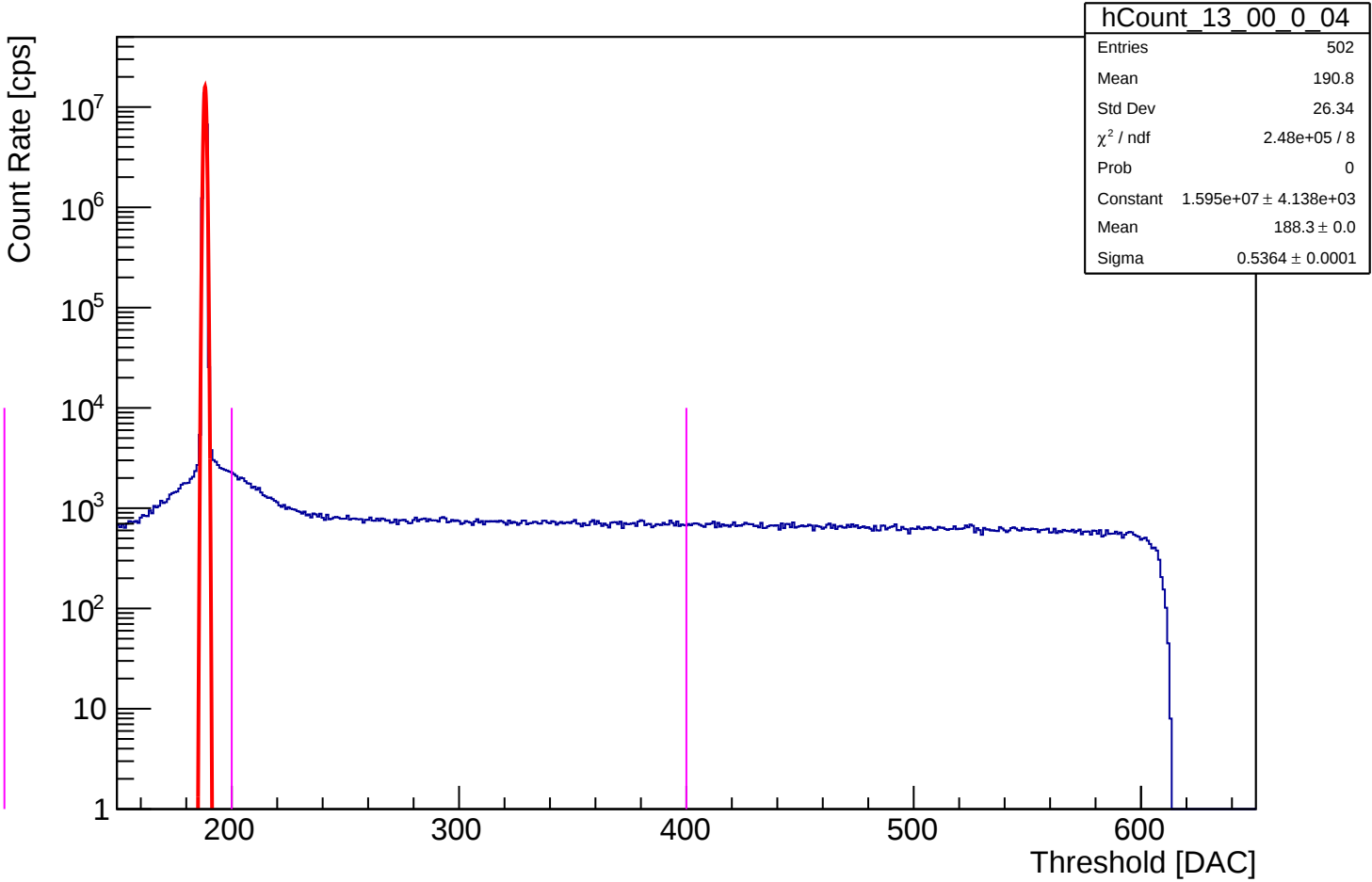
Row 1 Tile 1 Pmt 1 Pixel 48 Slot 13 Fiber 0 Asic 0 Channel 55



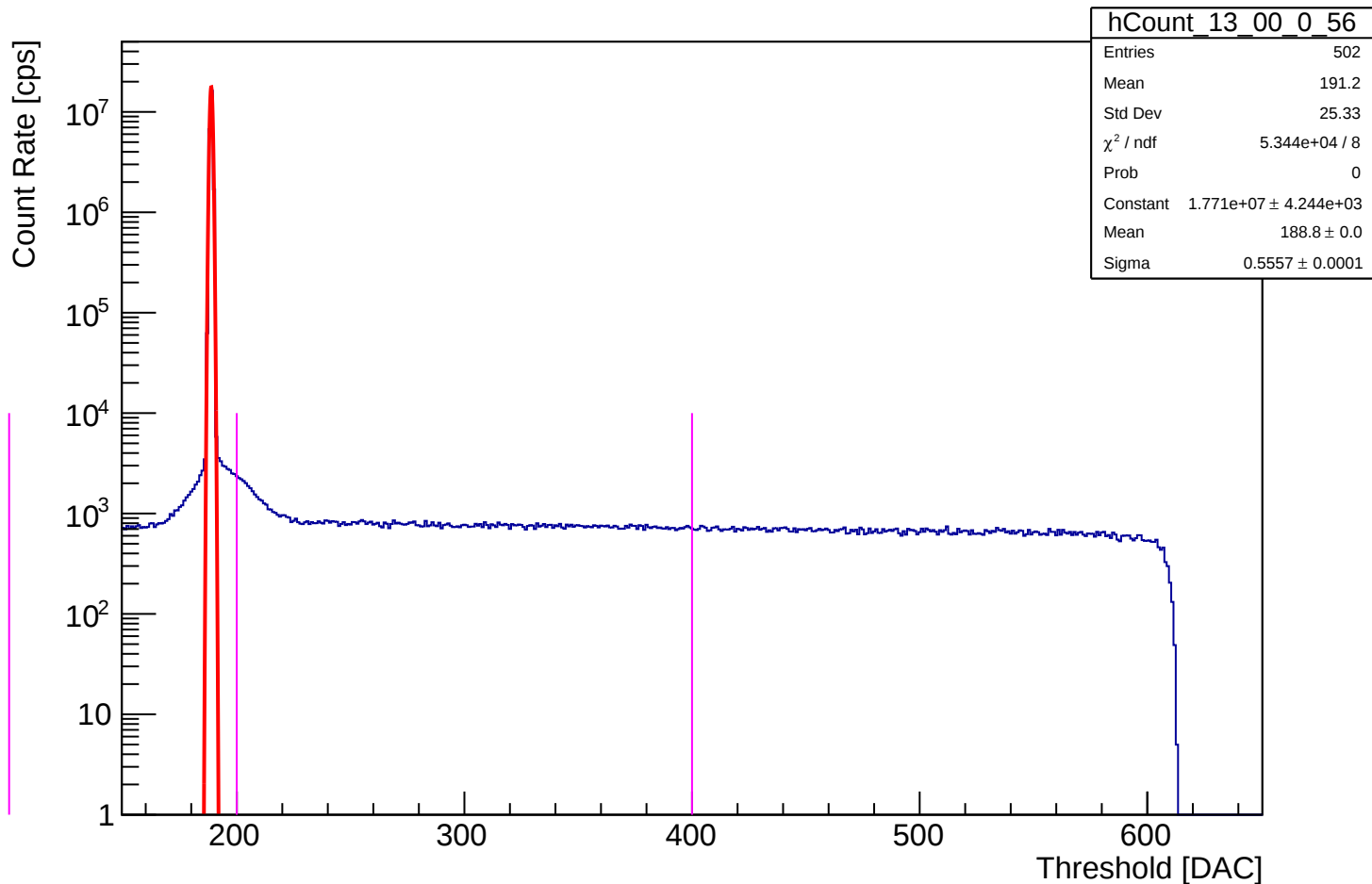




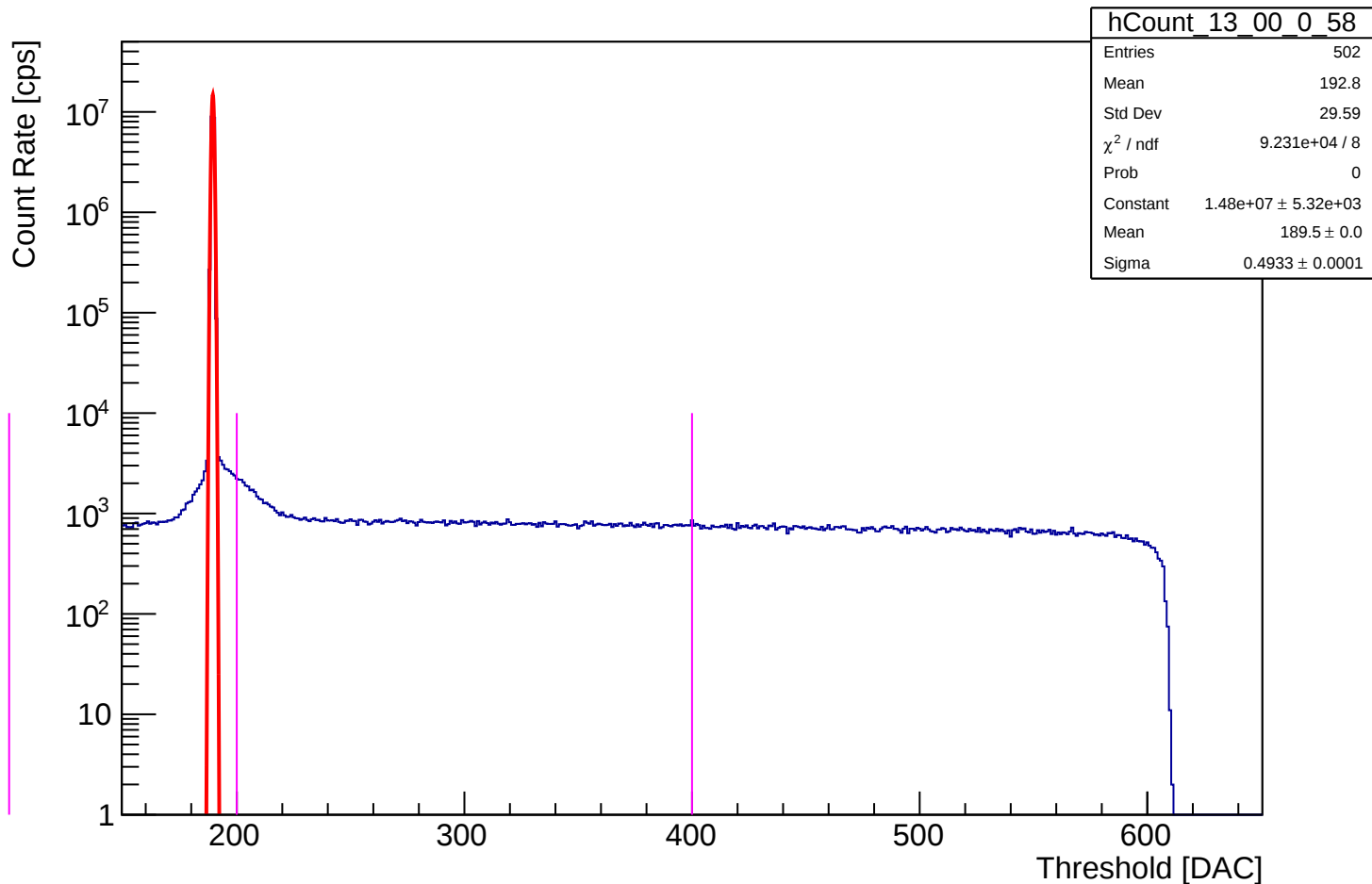




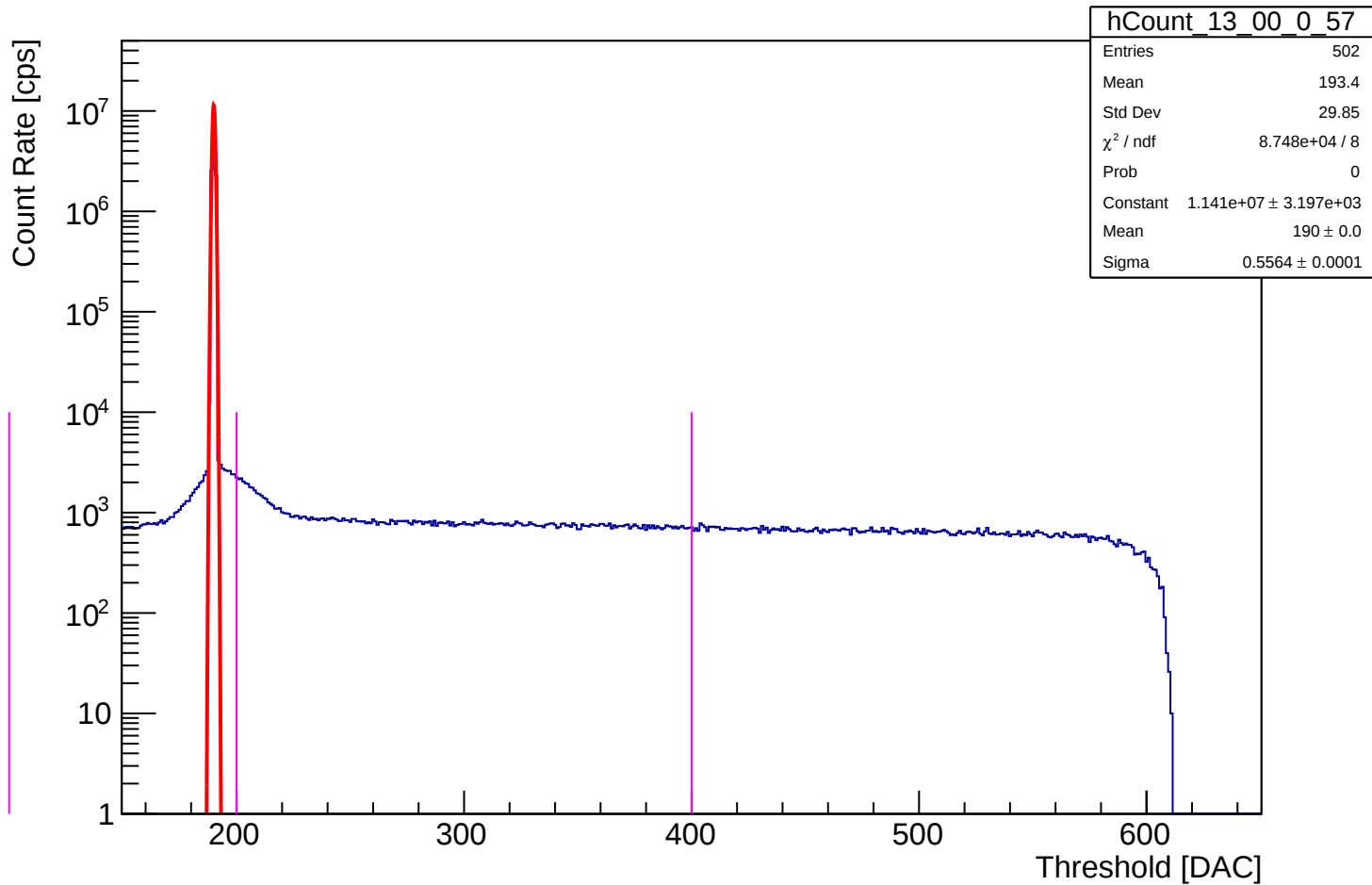
Row 1 Tile 1 Pmt 1 Pixel 53 Slot 13 Fiber 0 Asic 0 Channel 56



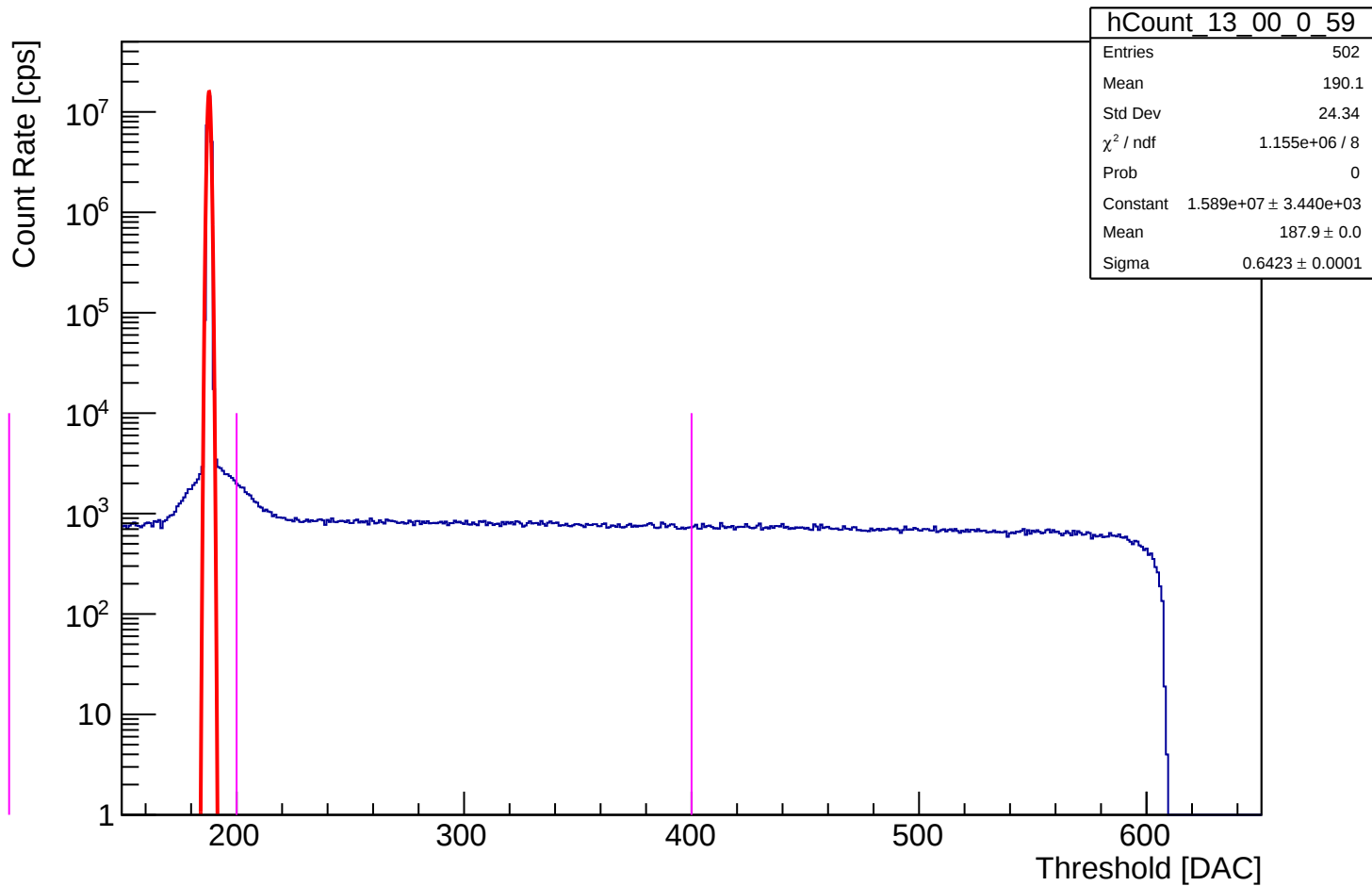
Row 1 Tile 1 Pmt 1 Pixel 54 Slot 13 Fiber 0 Asic 0 Channel 58

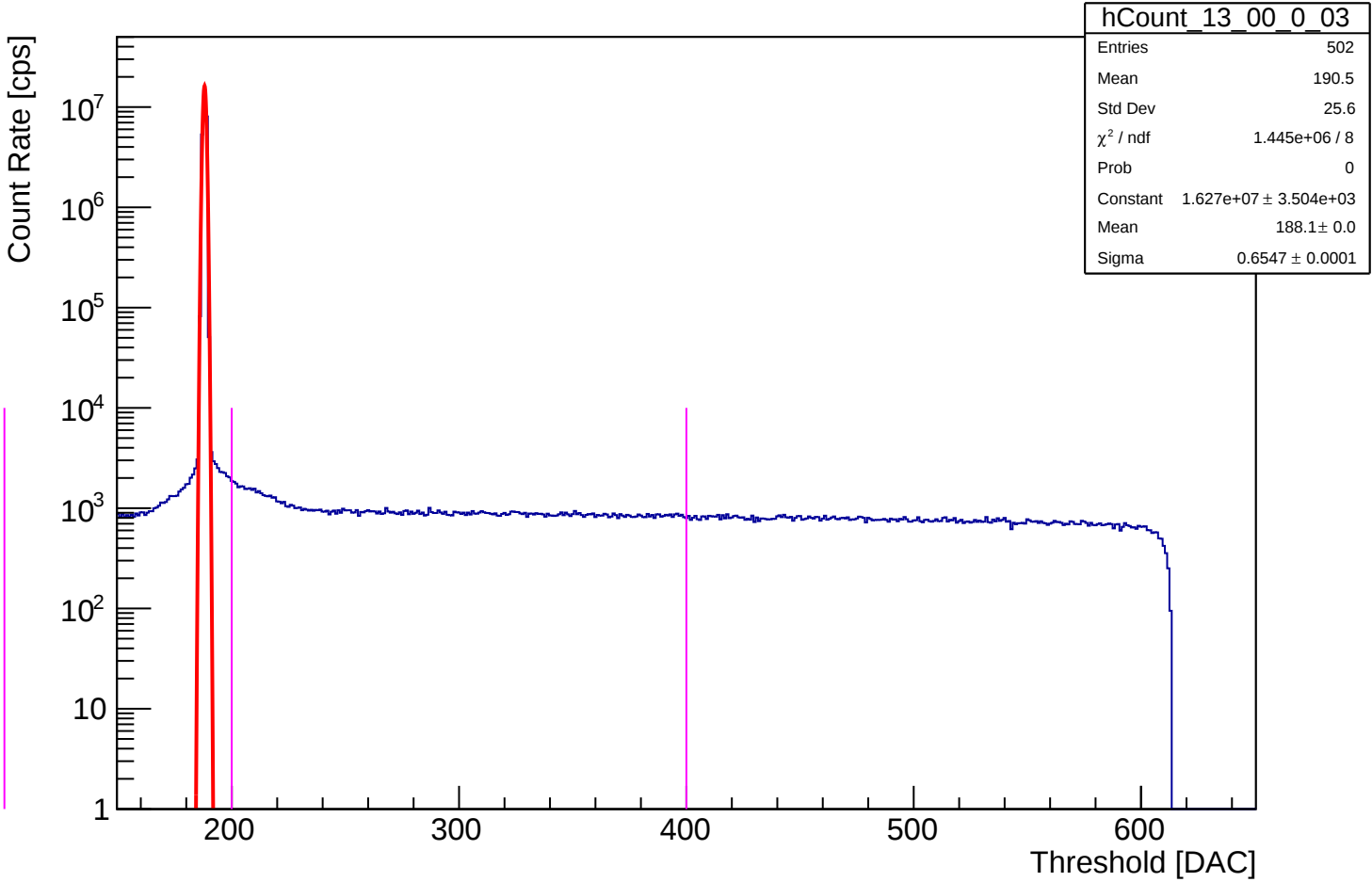


Row 1 Tile 1 Pmt 1 Pixel 55 Slot 13 Fiber 0 Asic 0 Channel 57

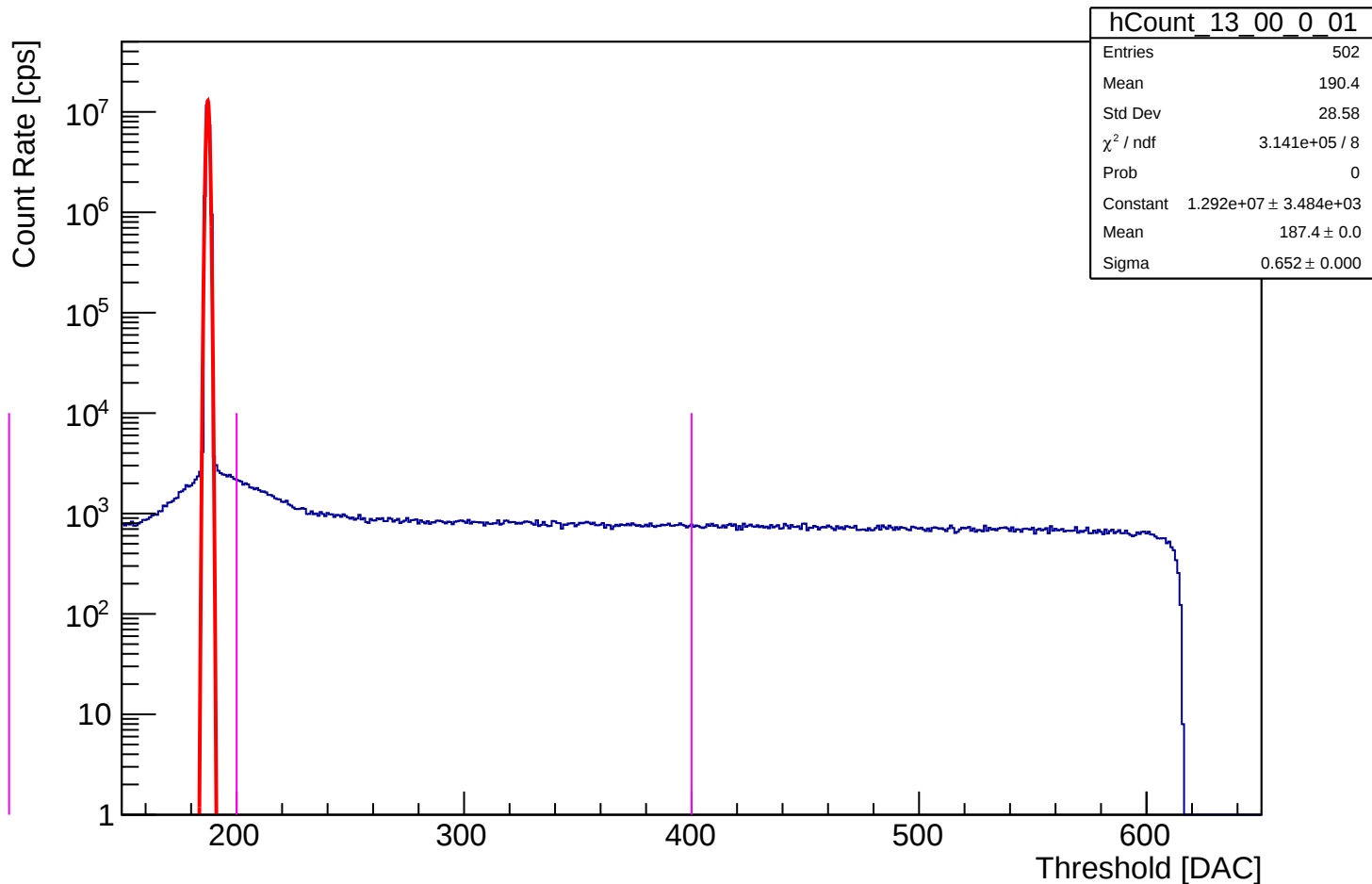


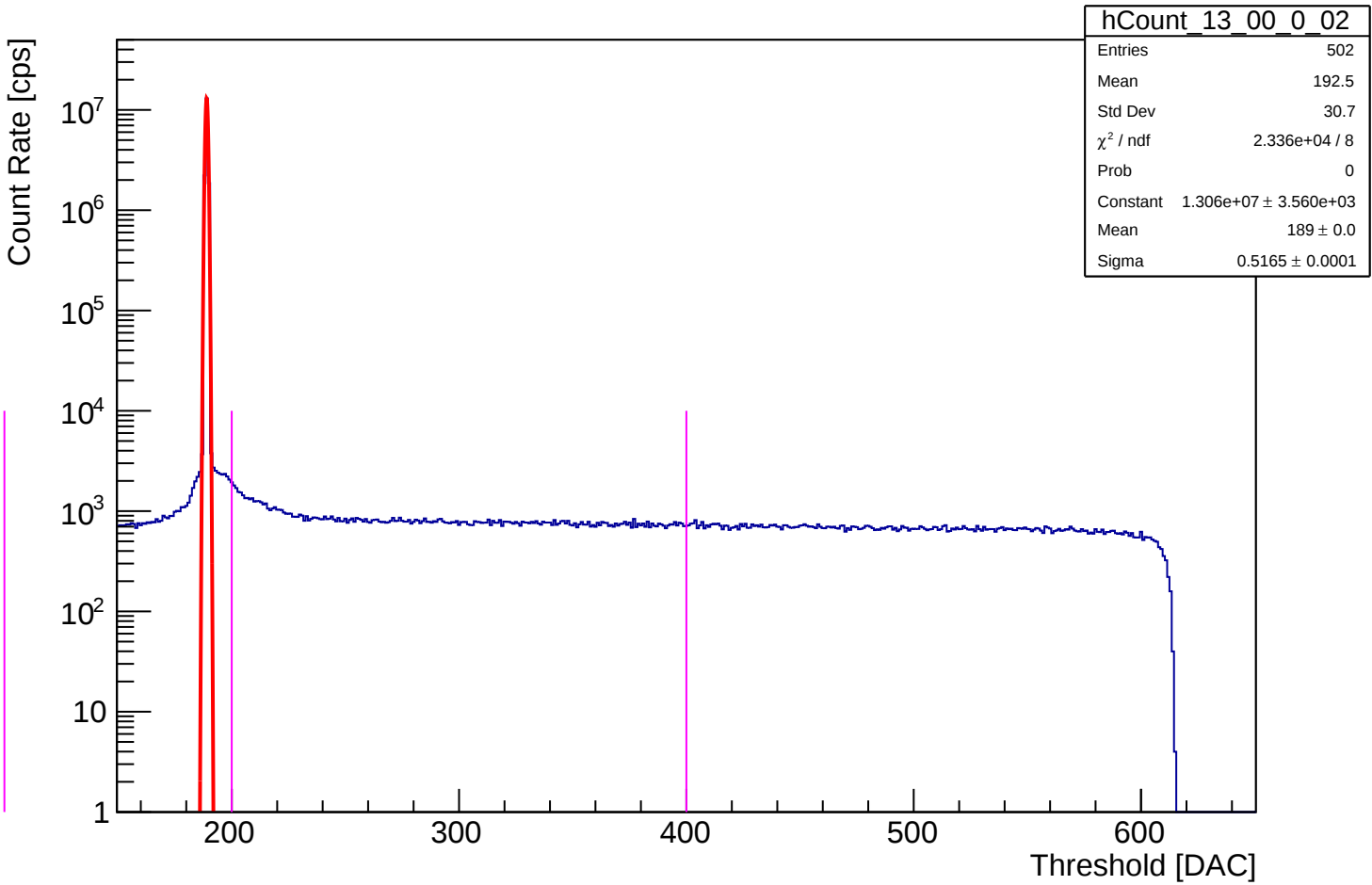
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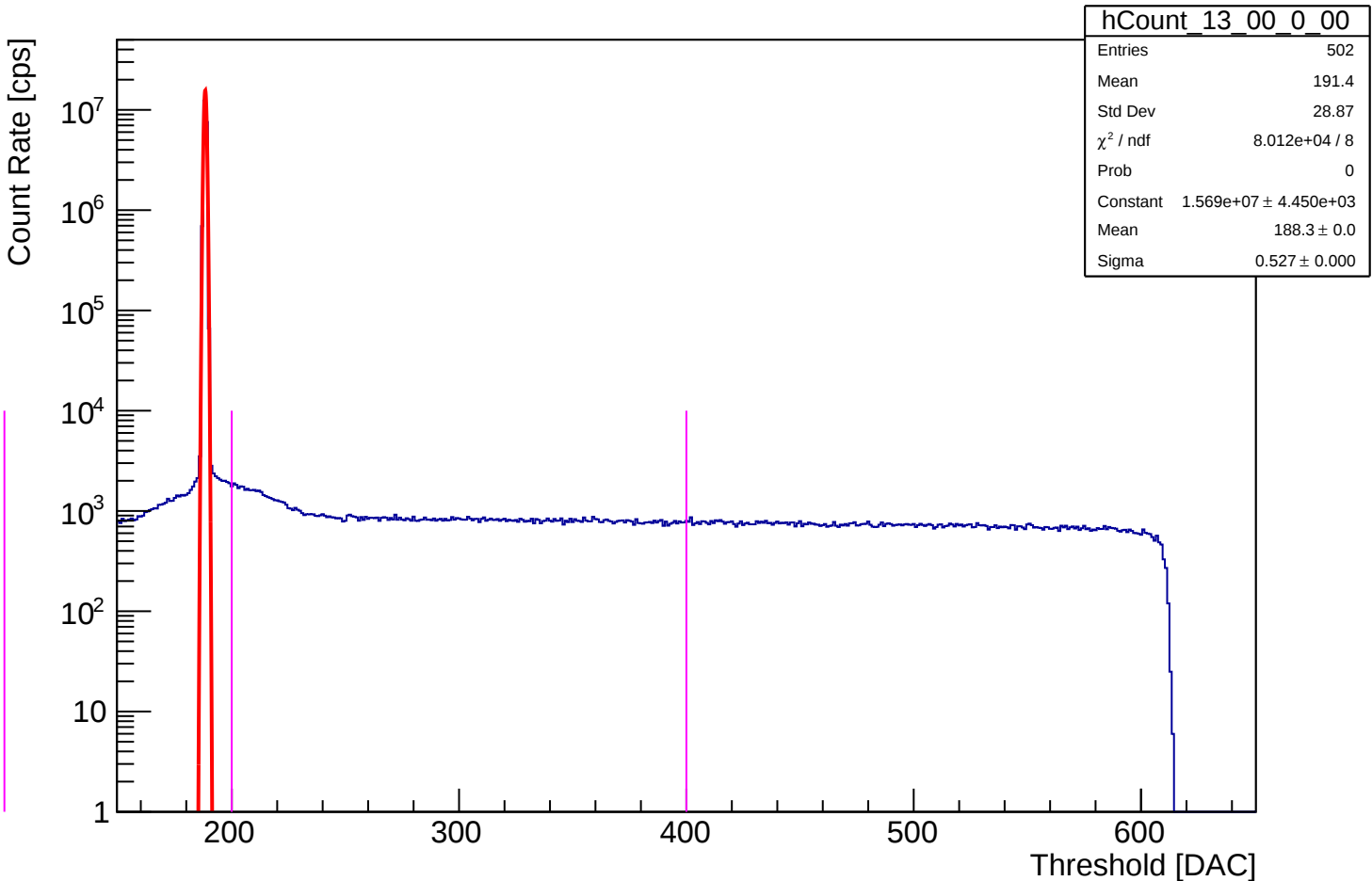




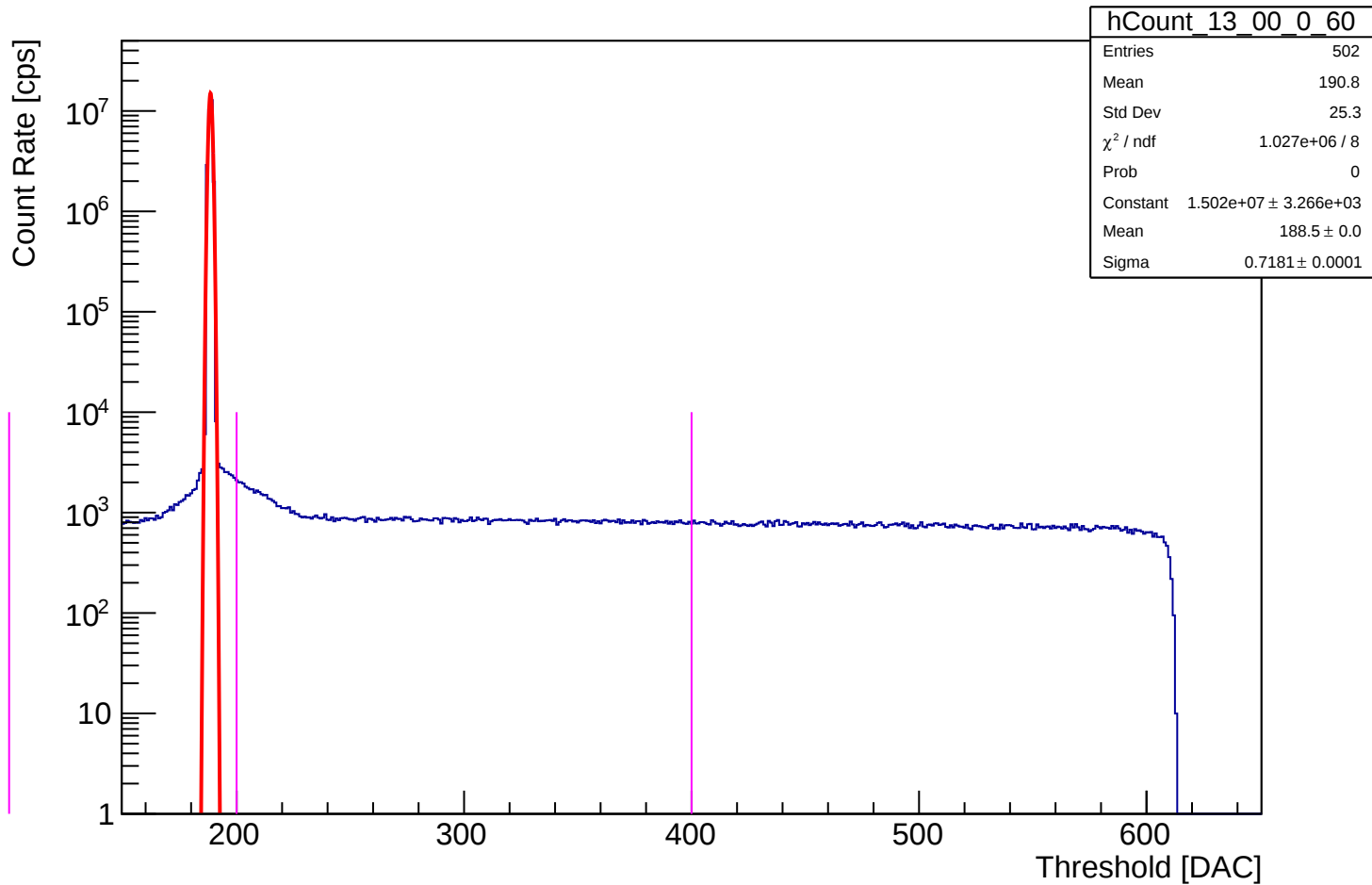
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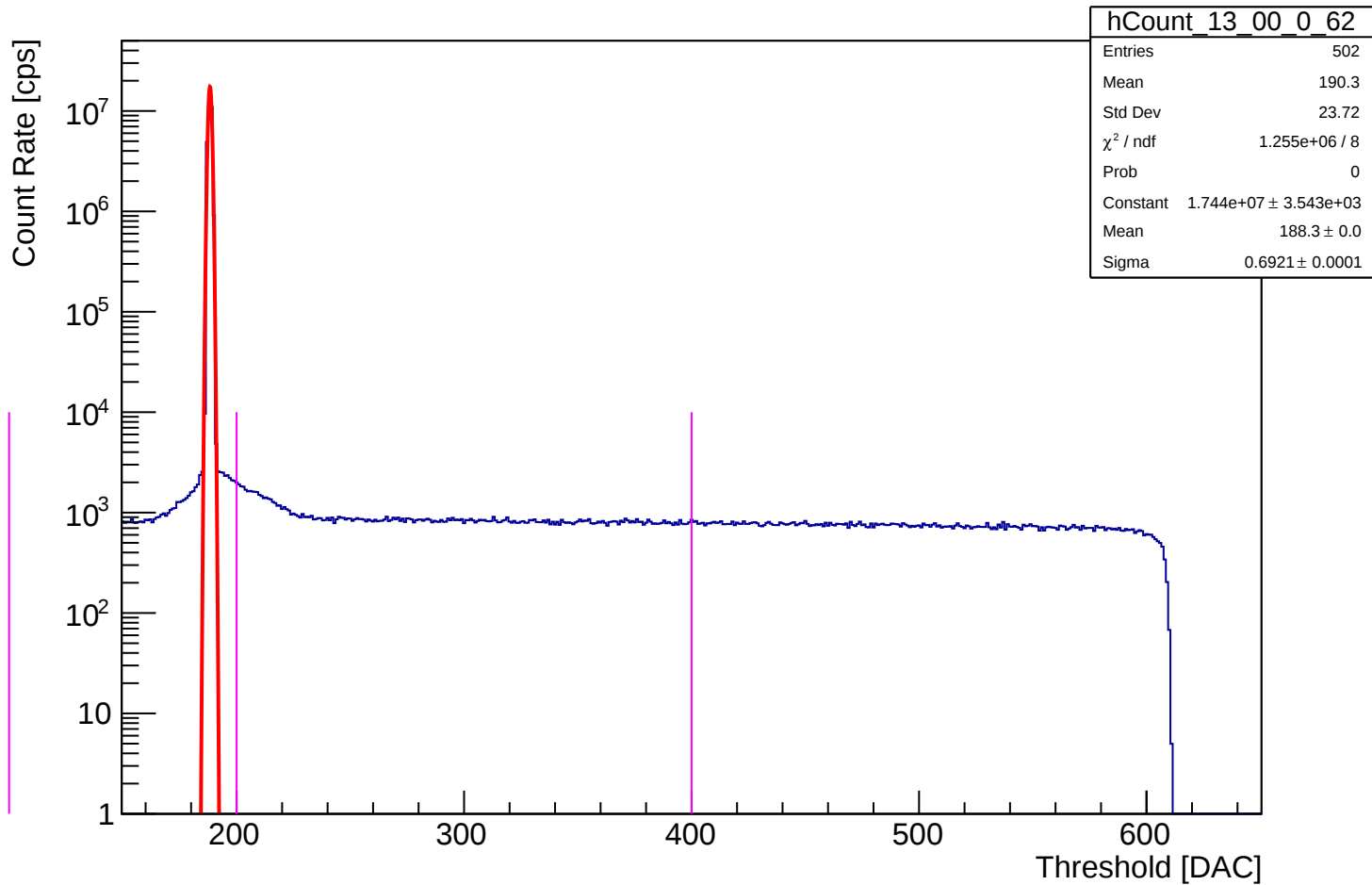




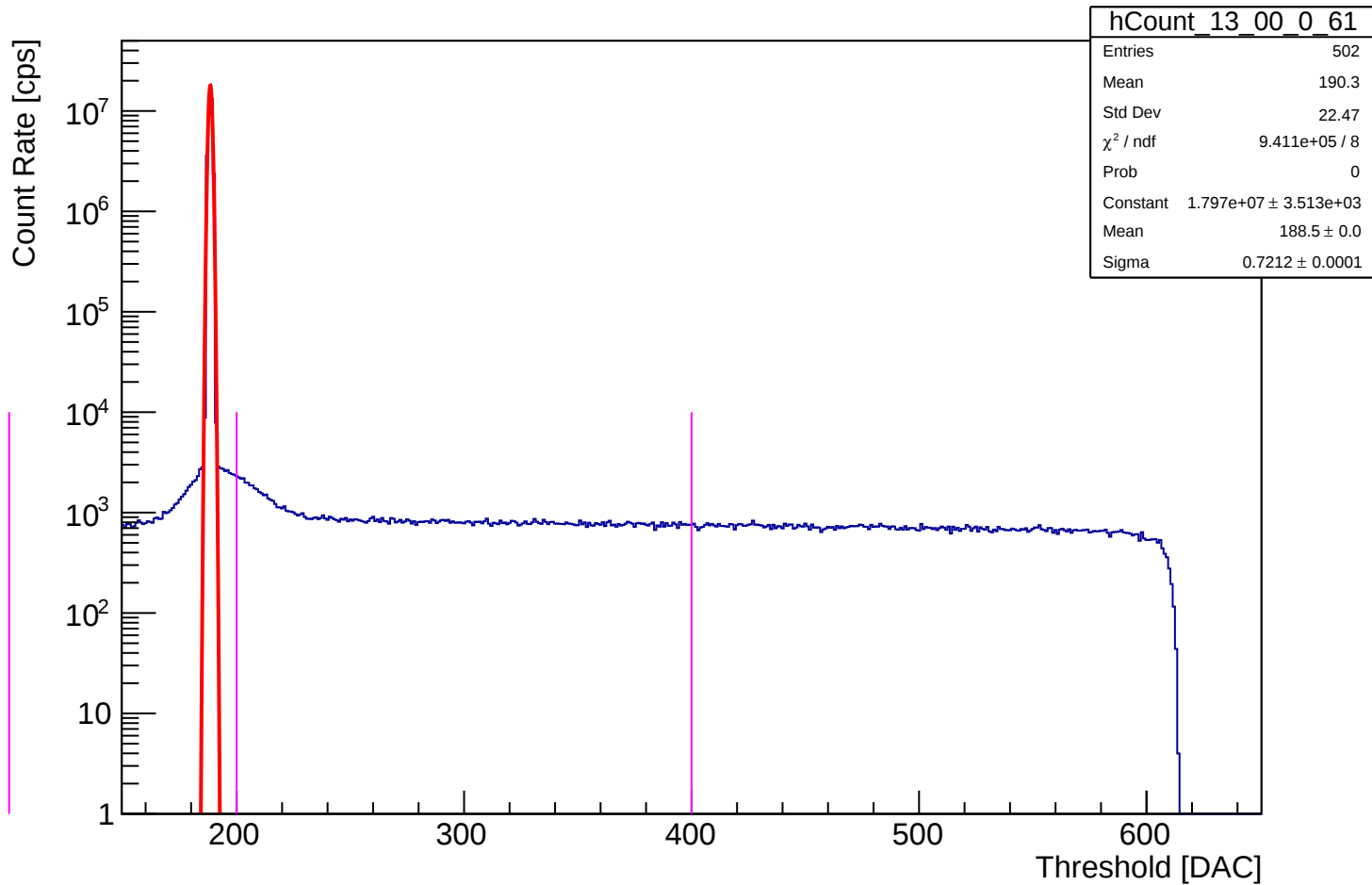
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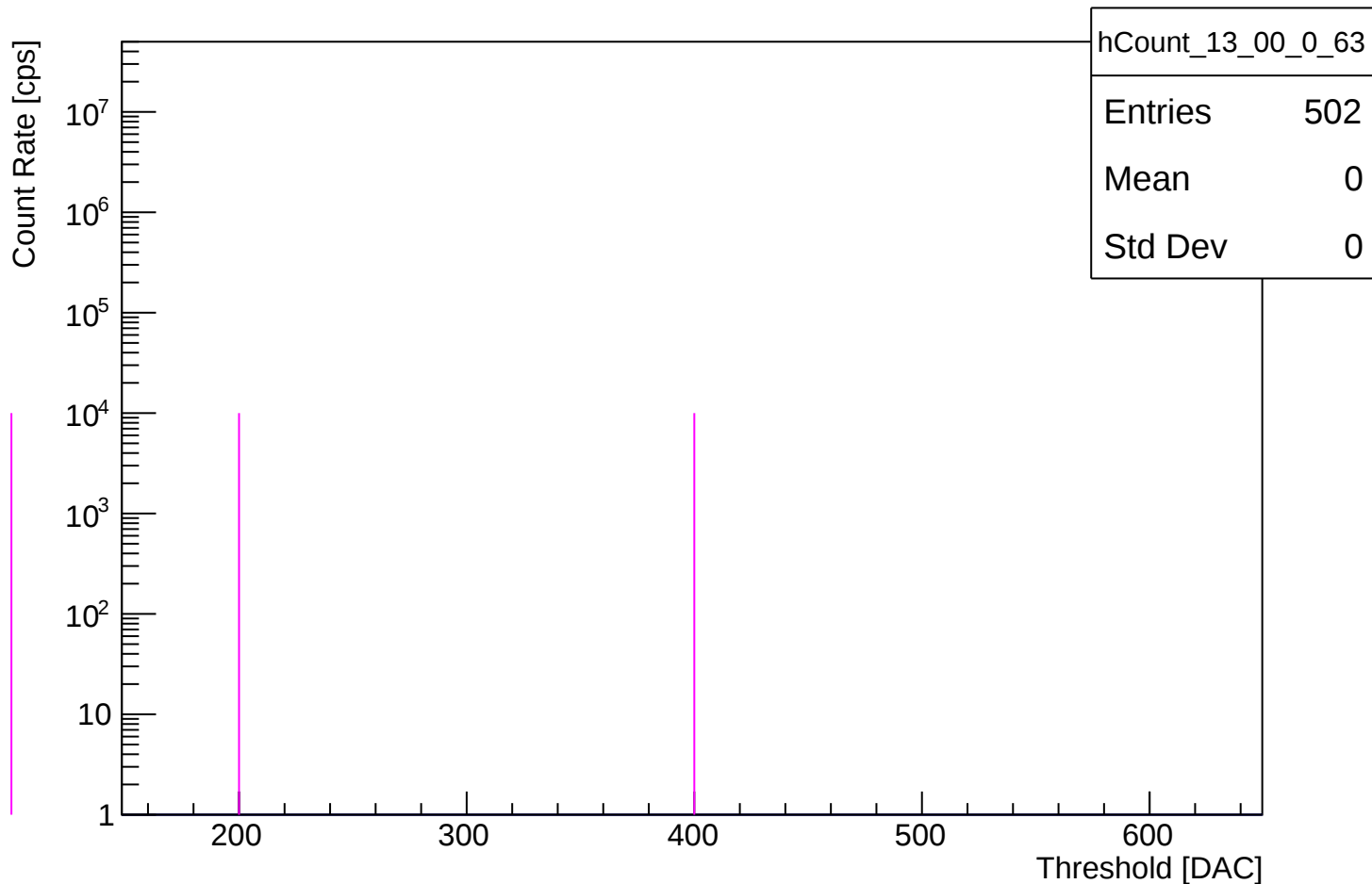
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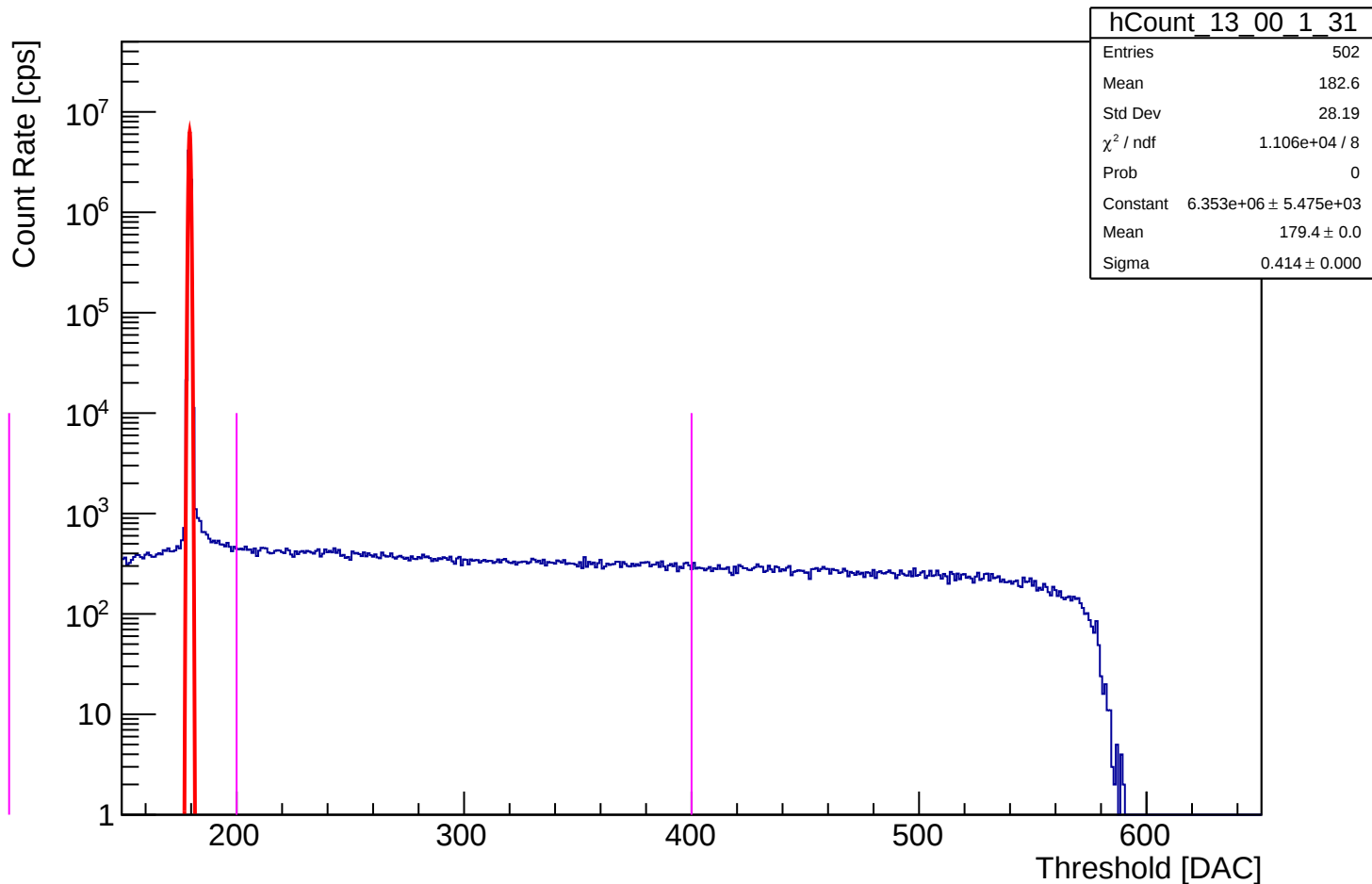
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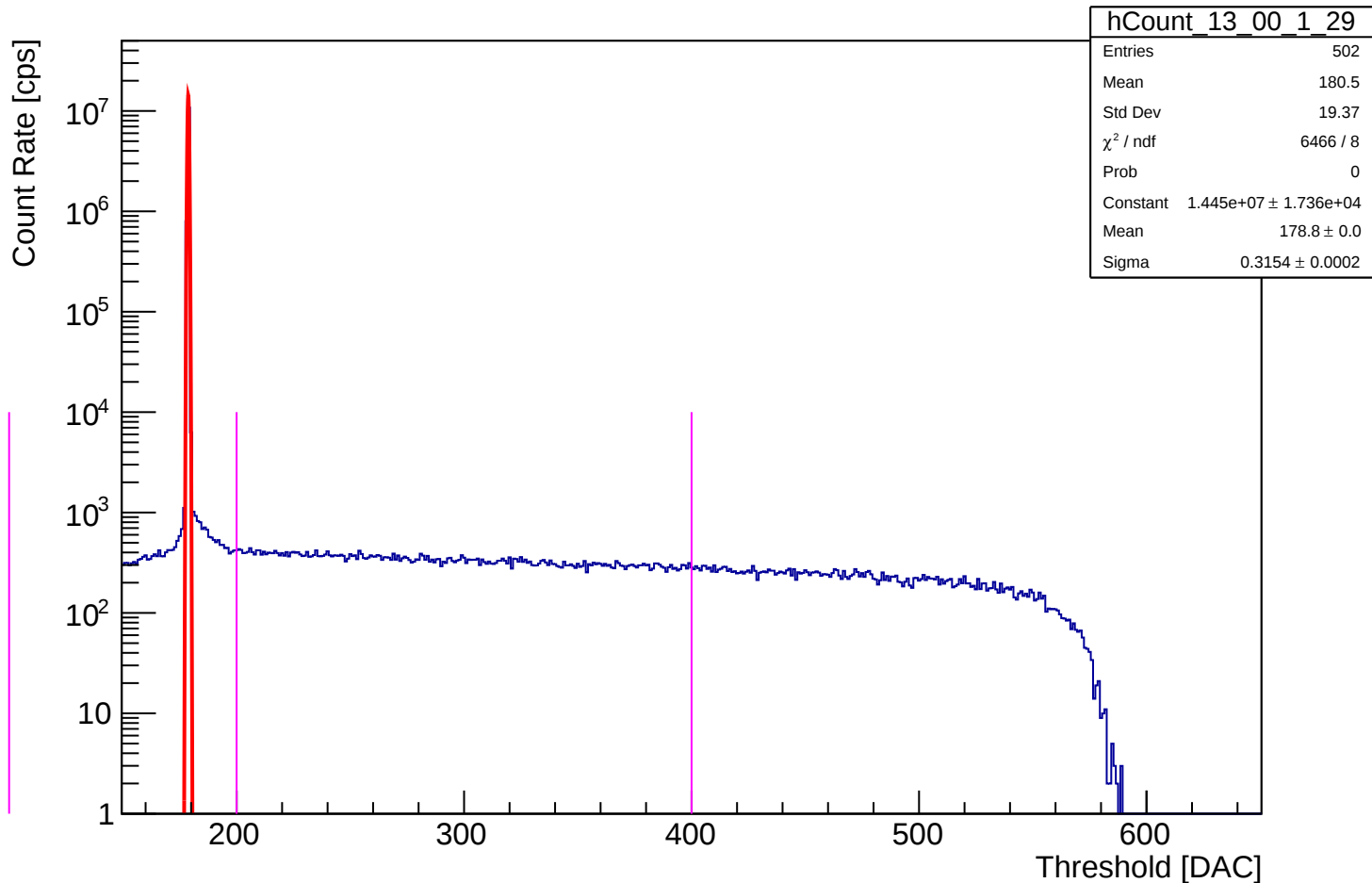
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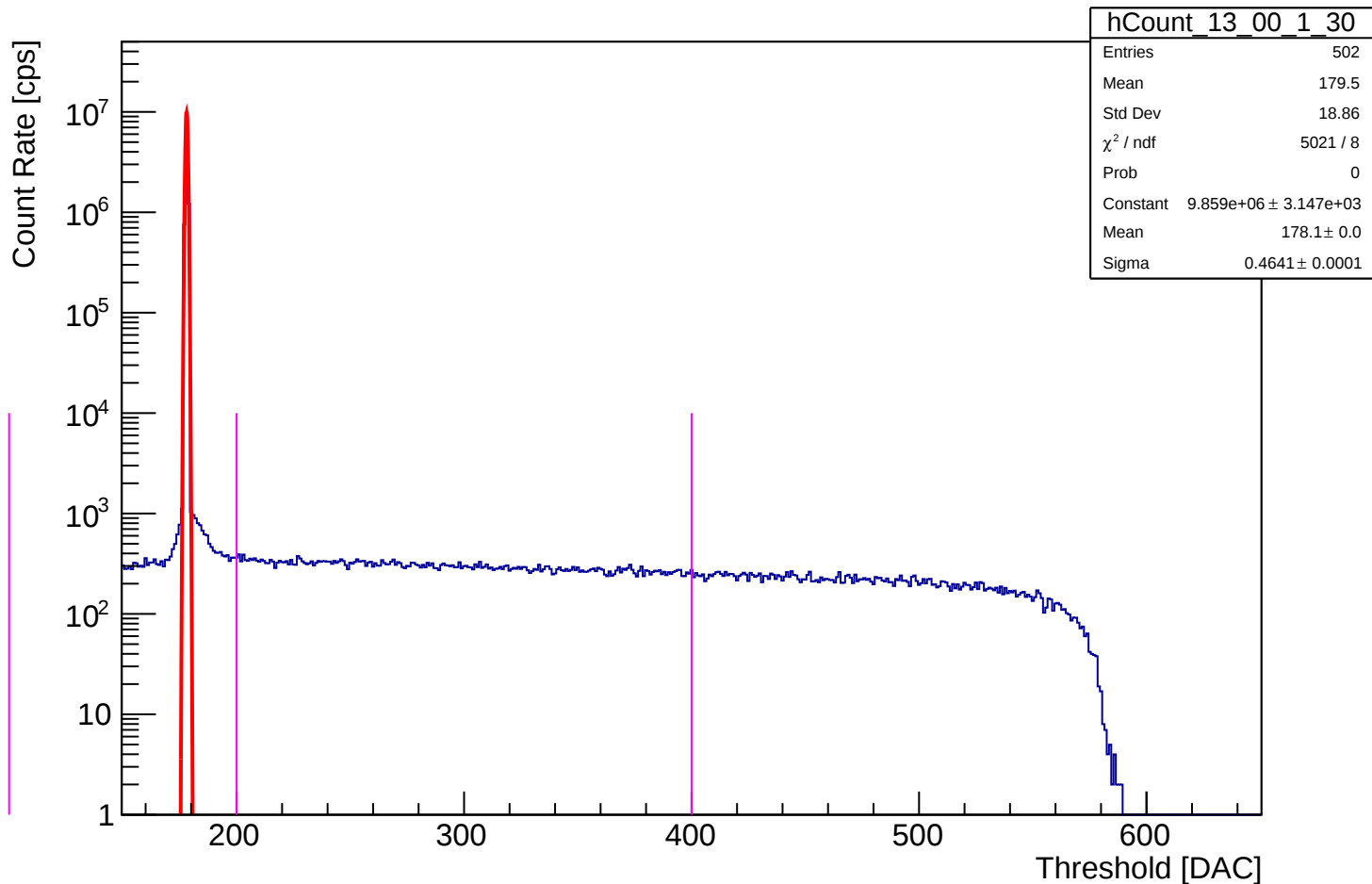
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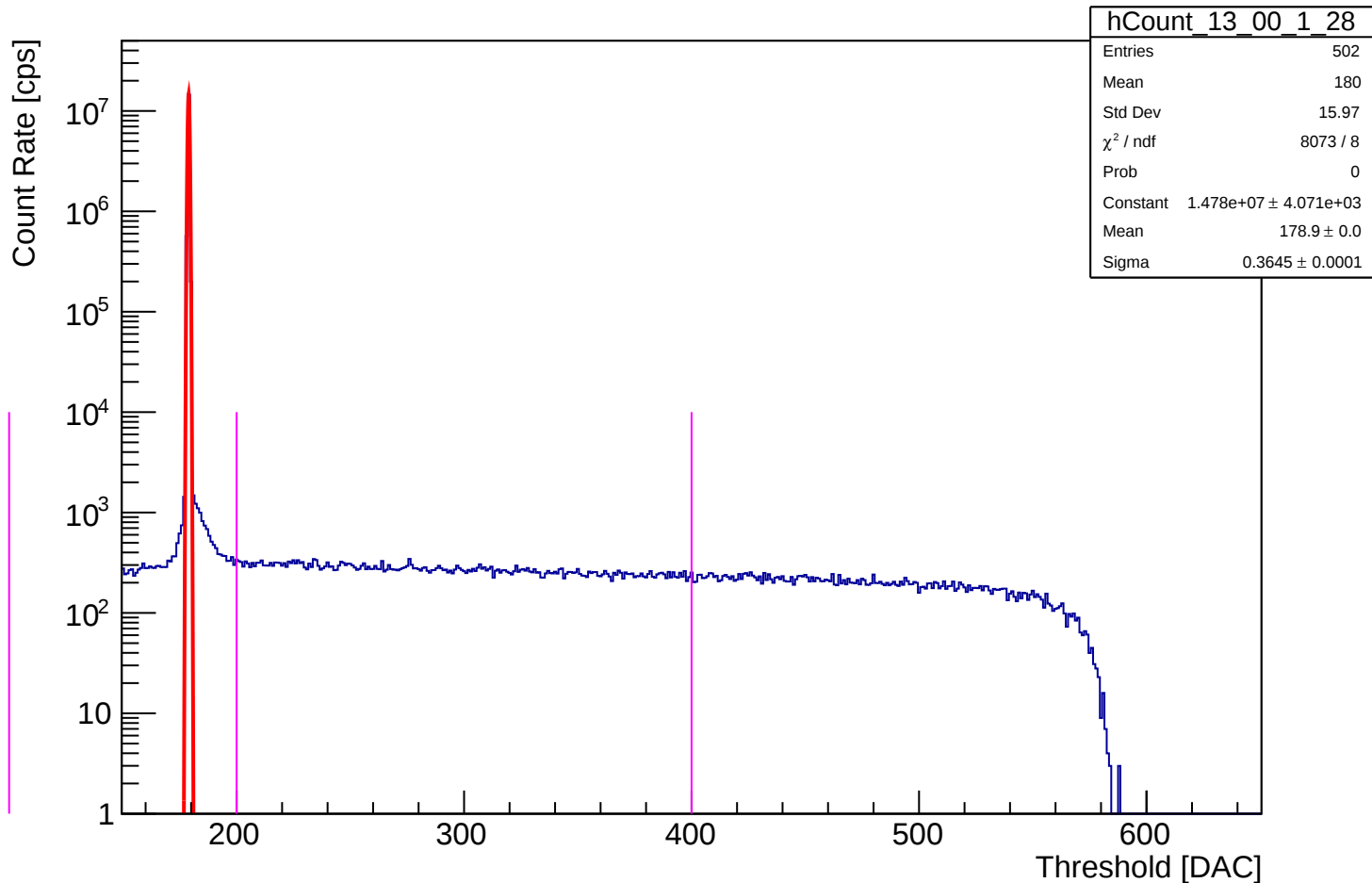
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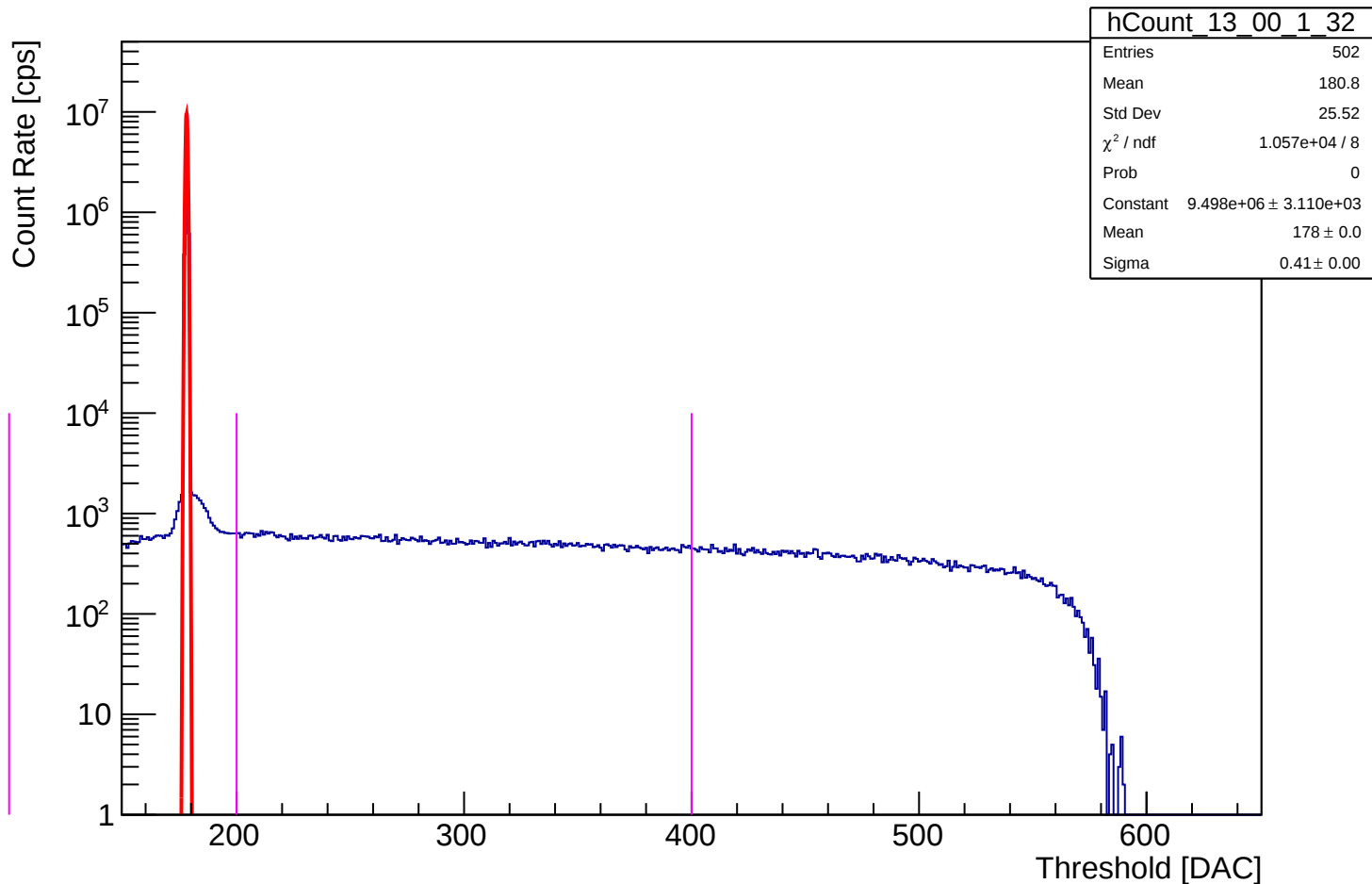
Row 1 Tile 1 Pmt 2 Pixel 3 Slot 13 Fiber 0 Asic 1 Channel 30



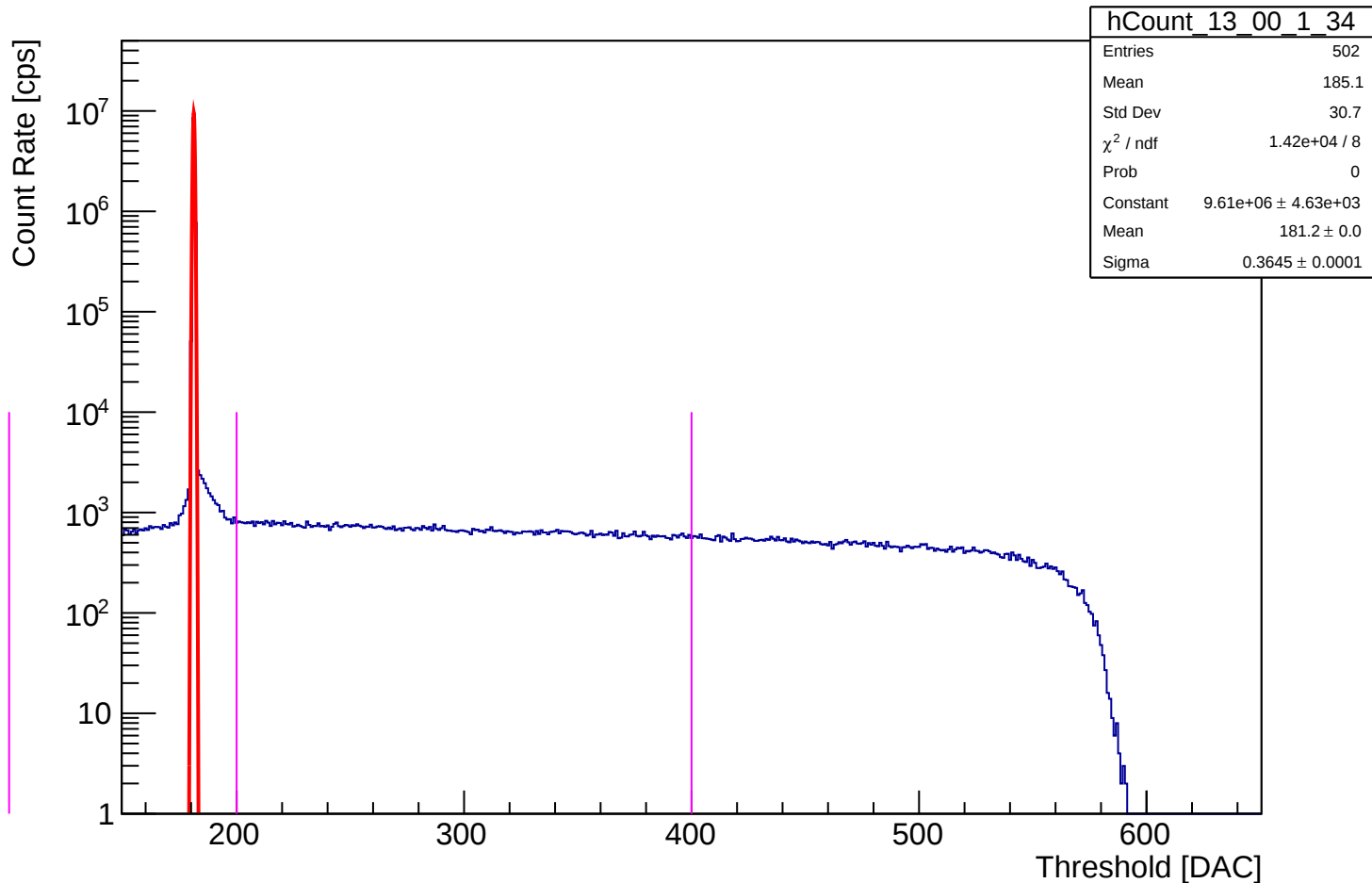
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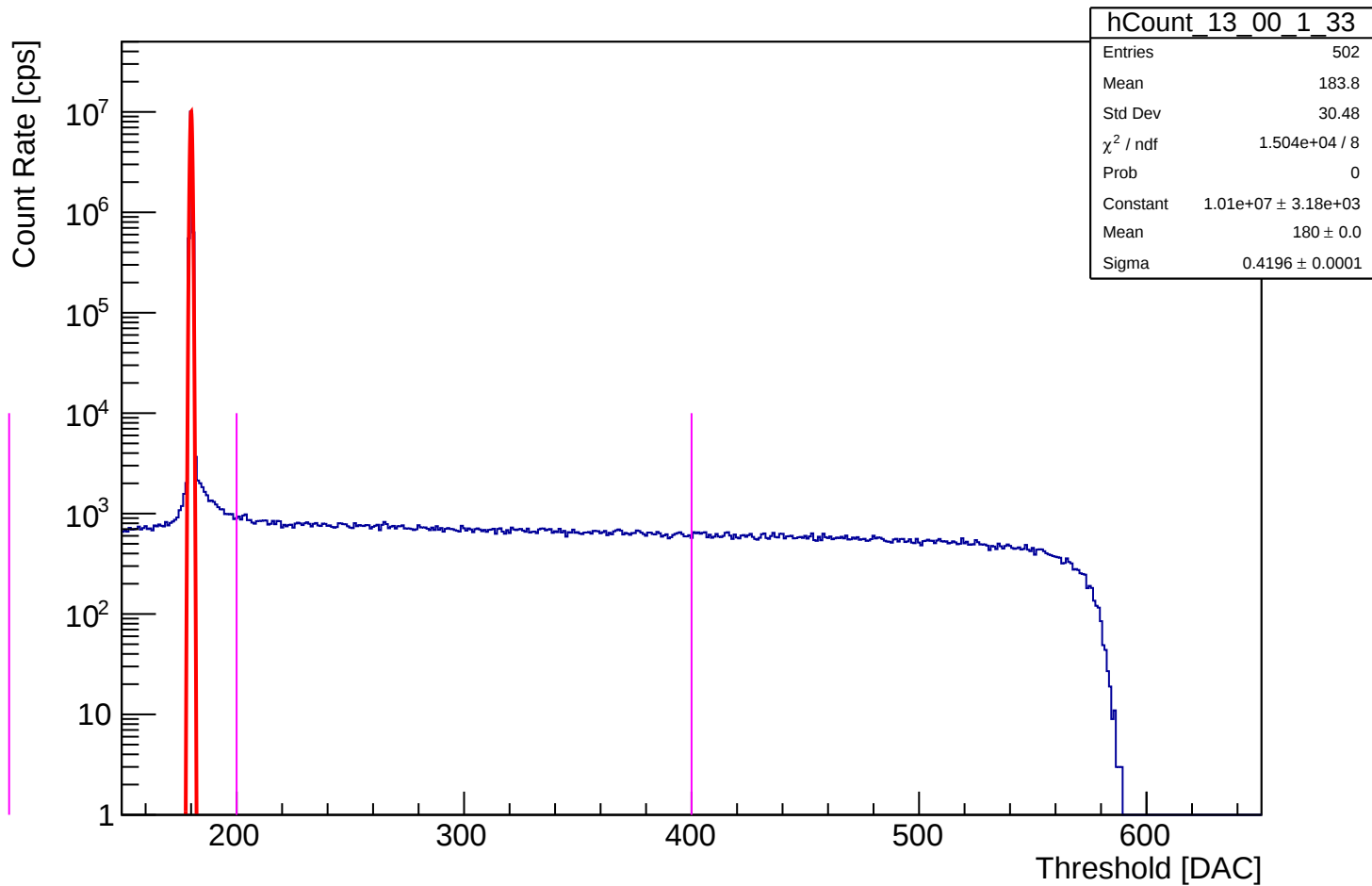
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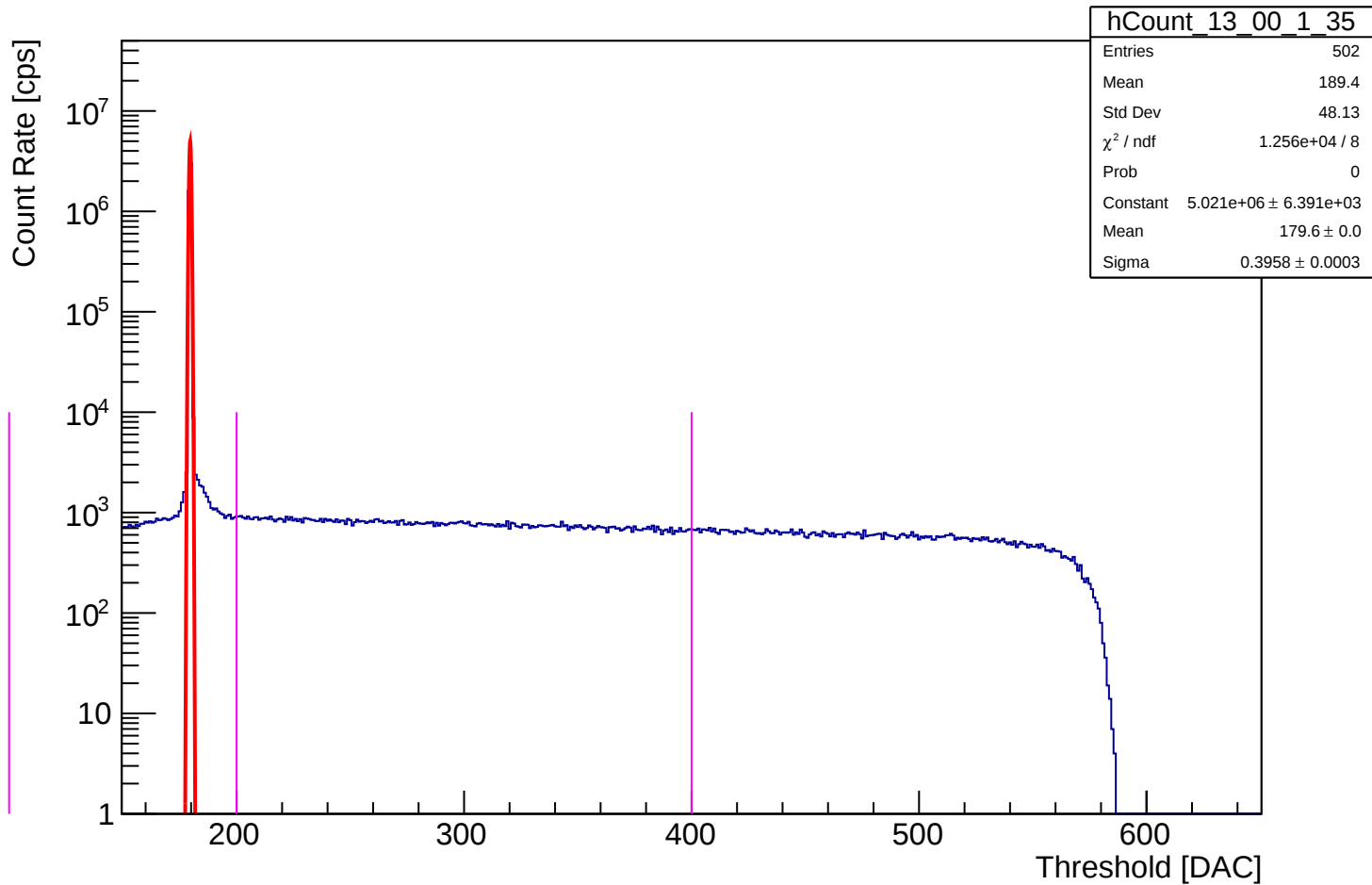
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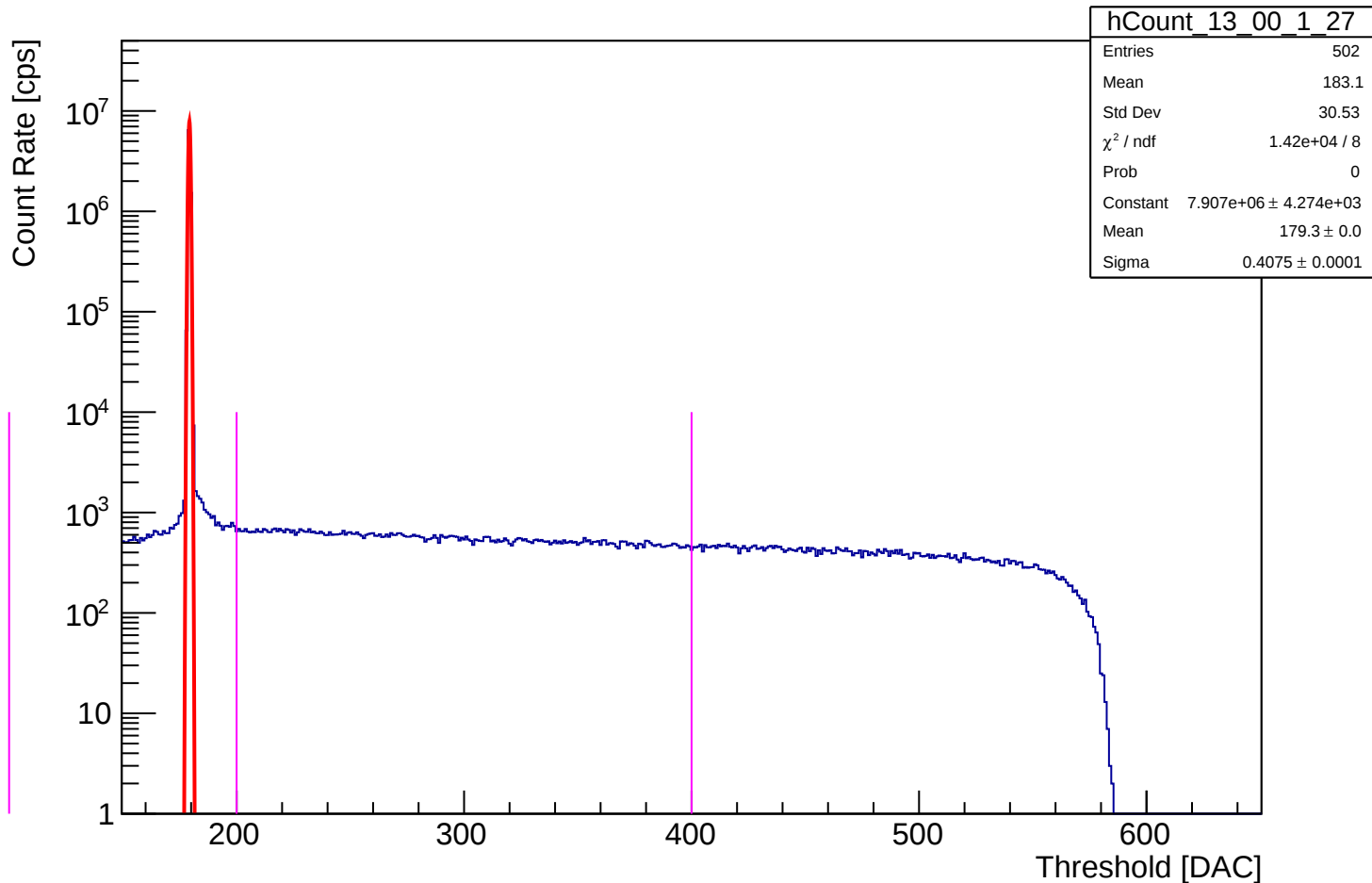
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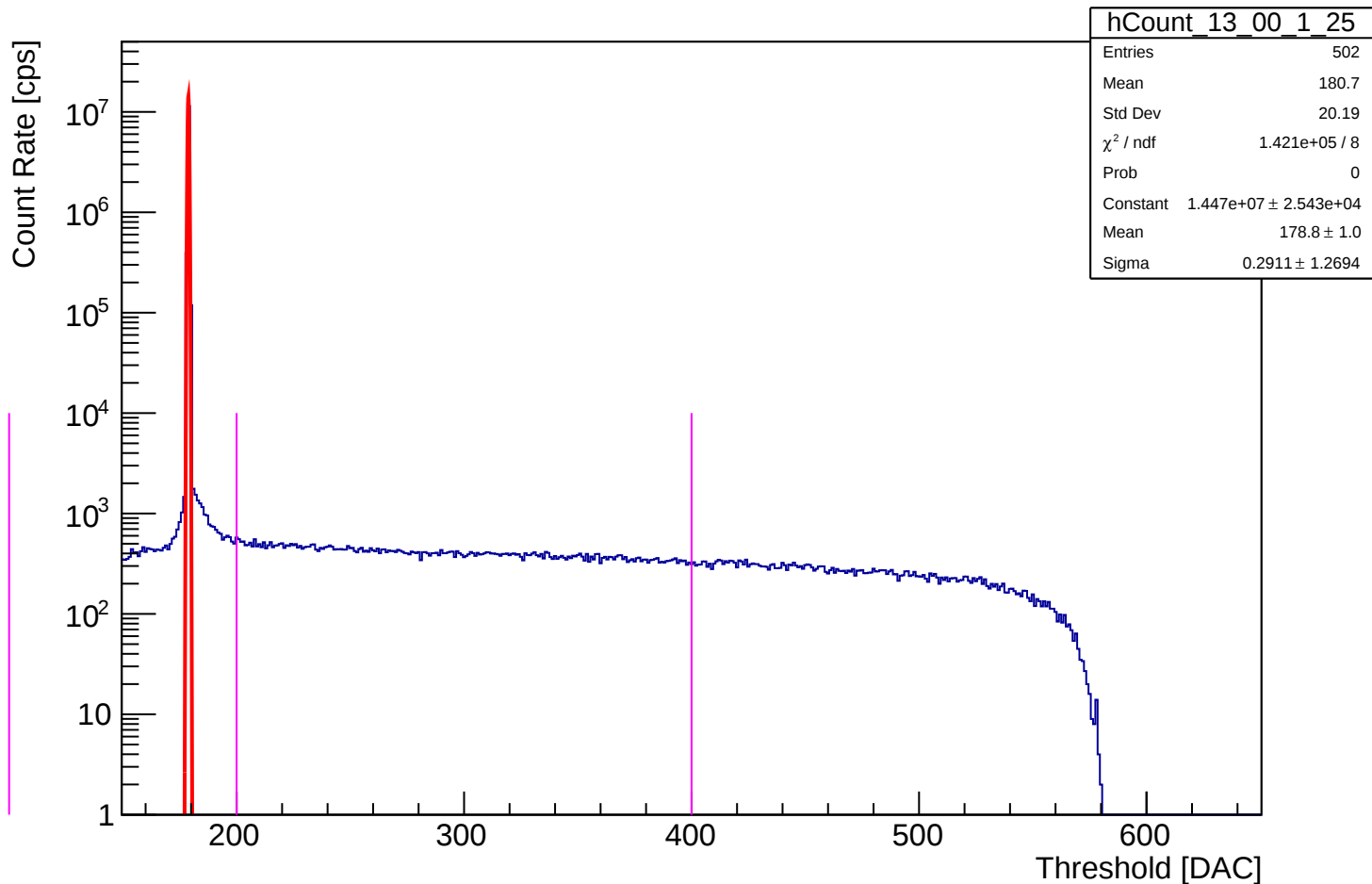
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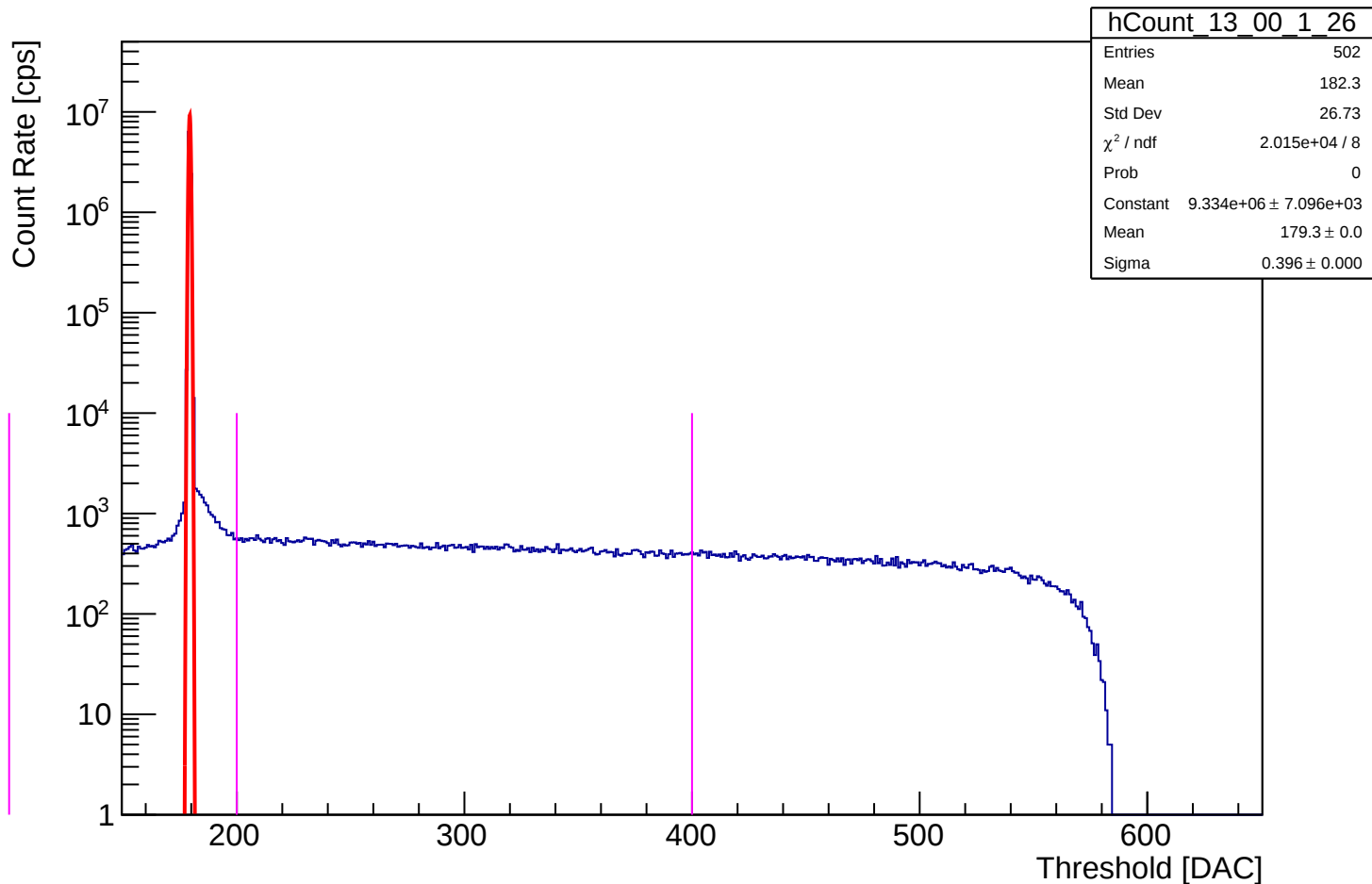
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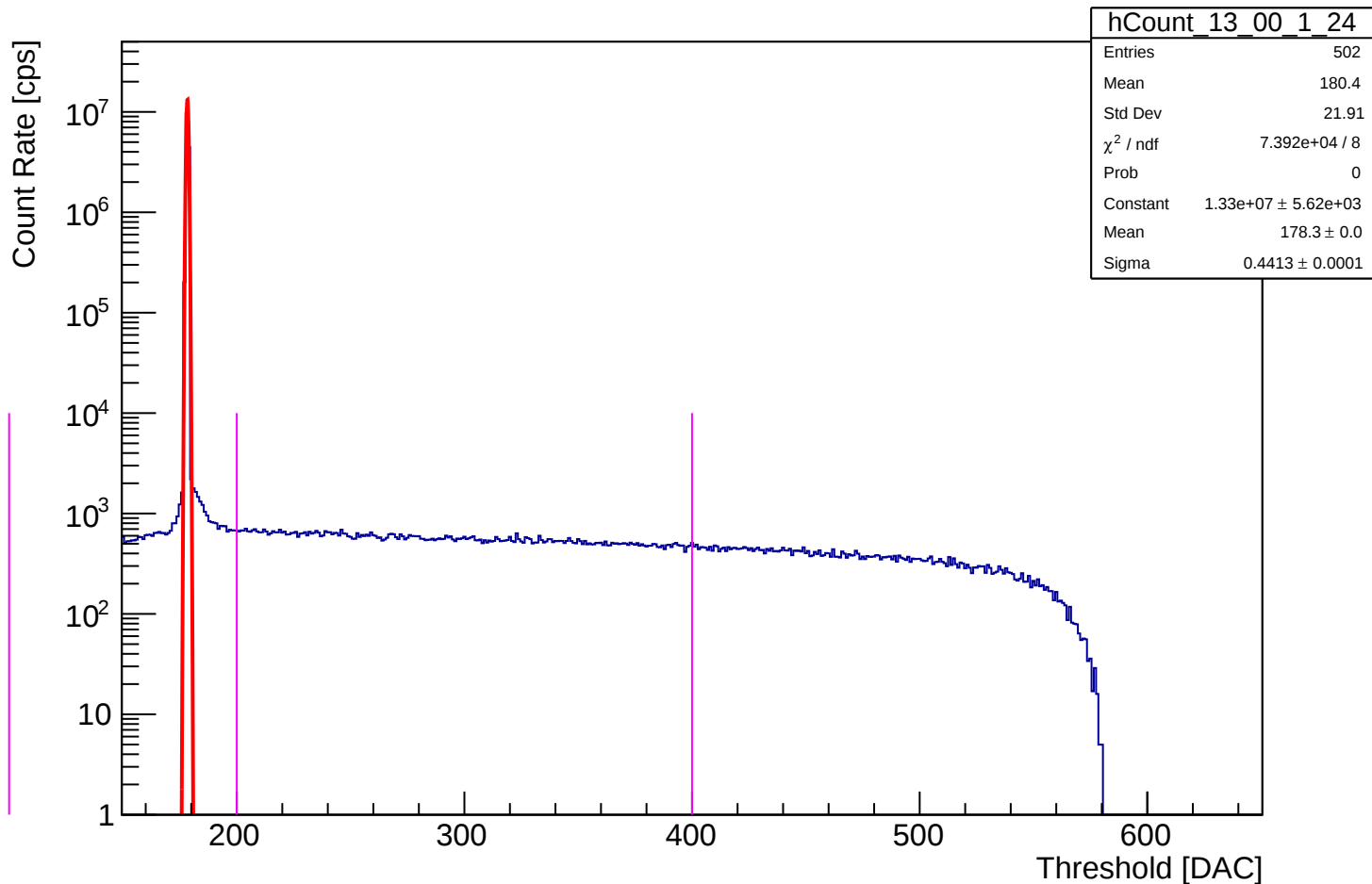
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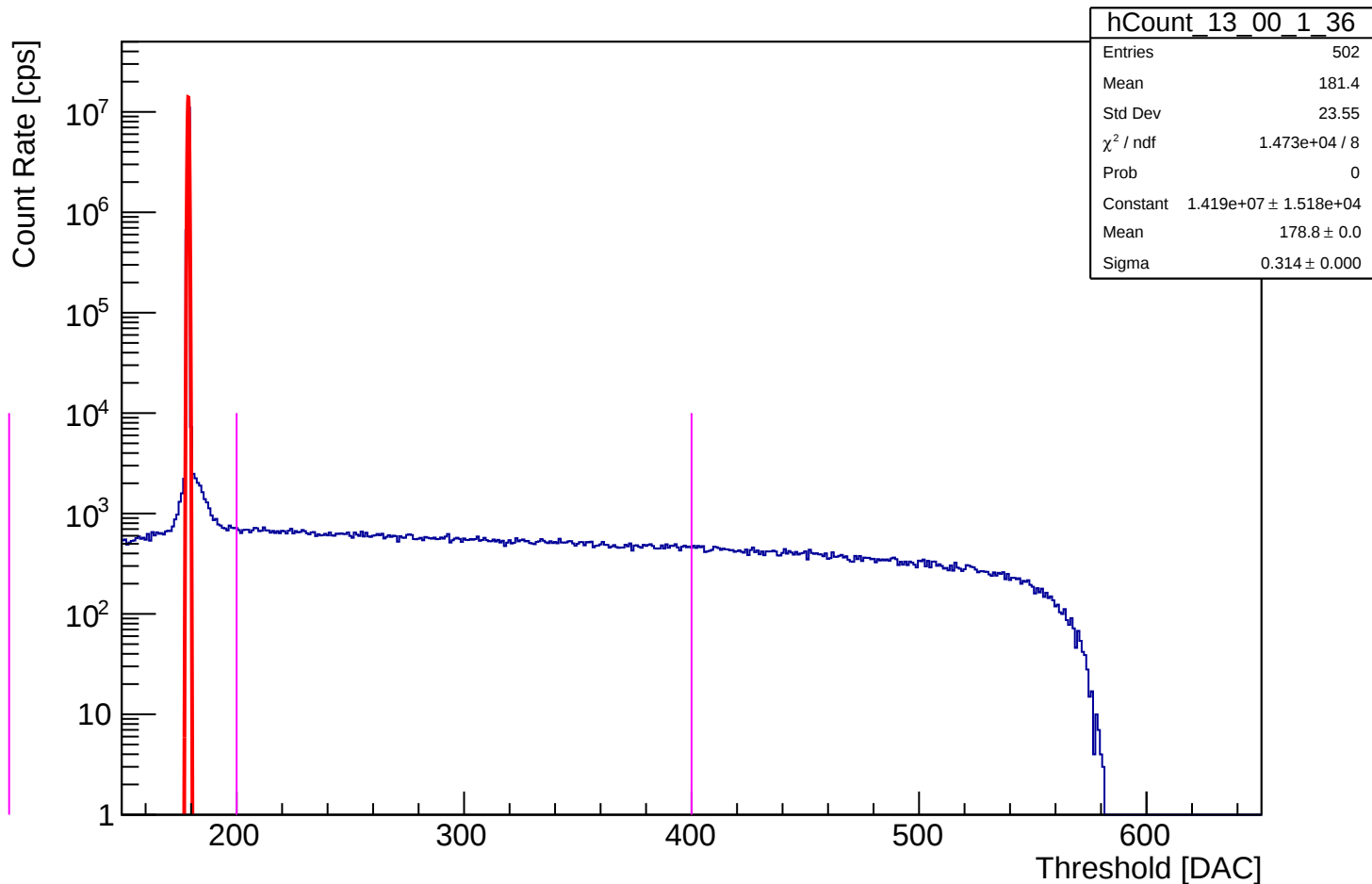
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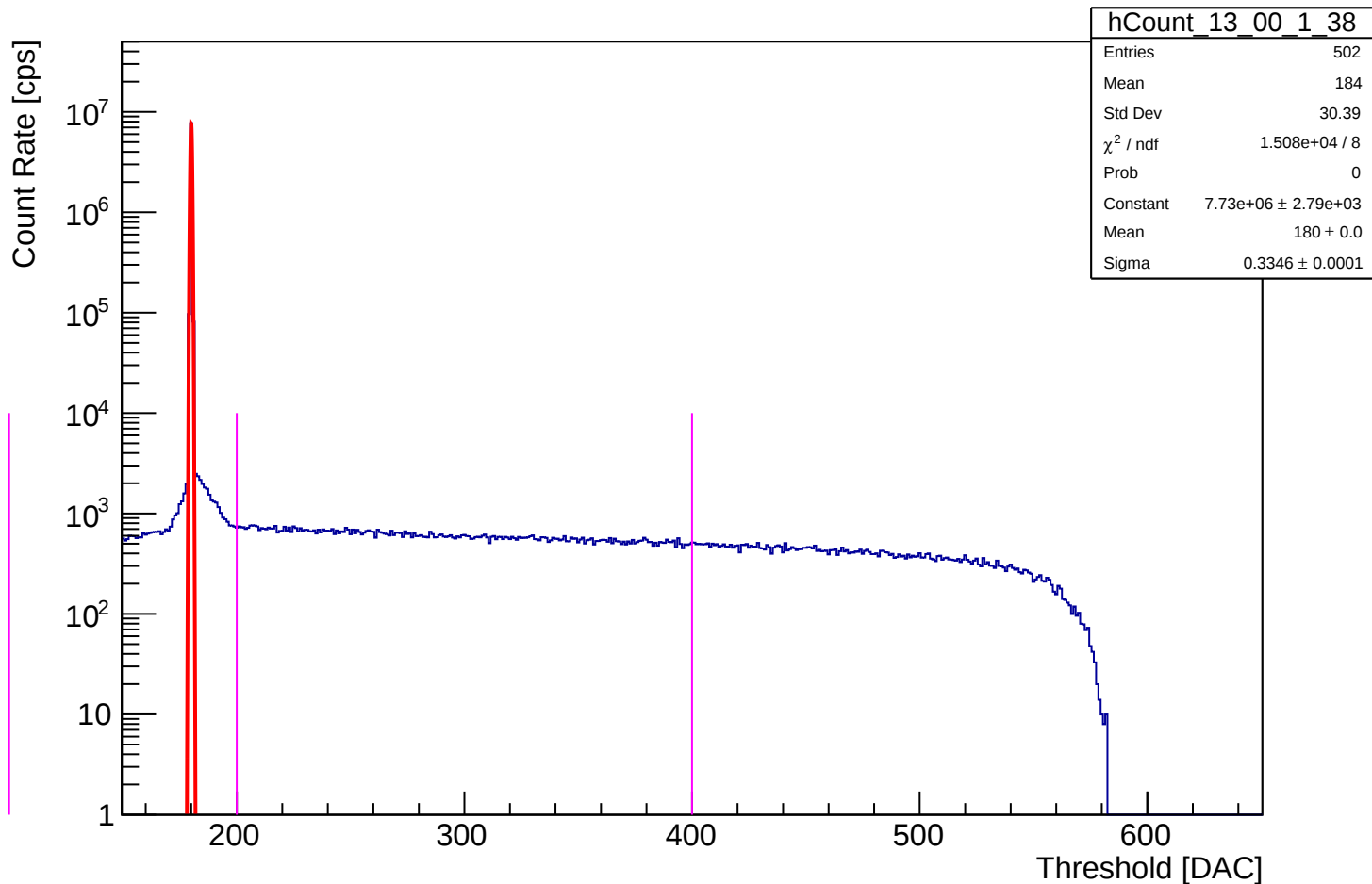
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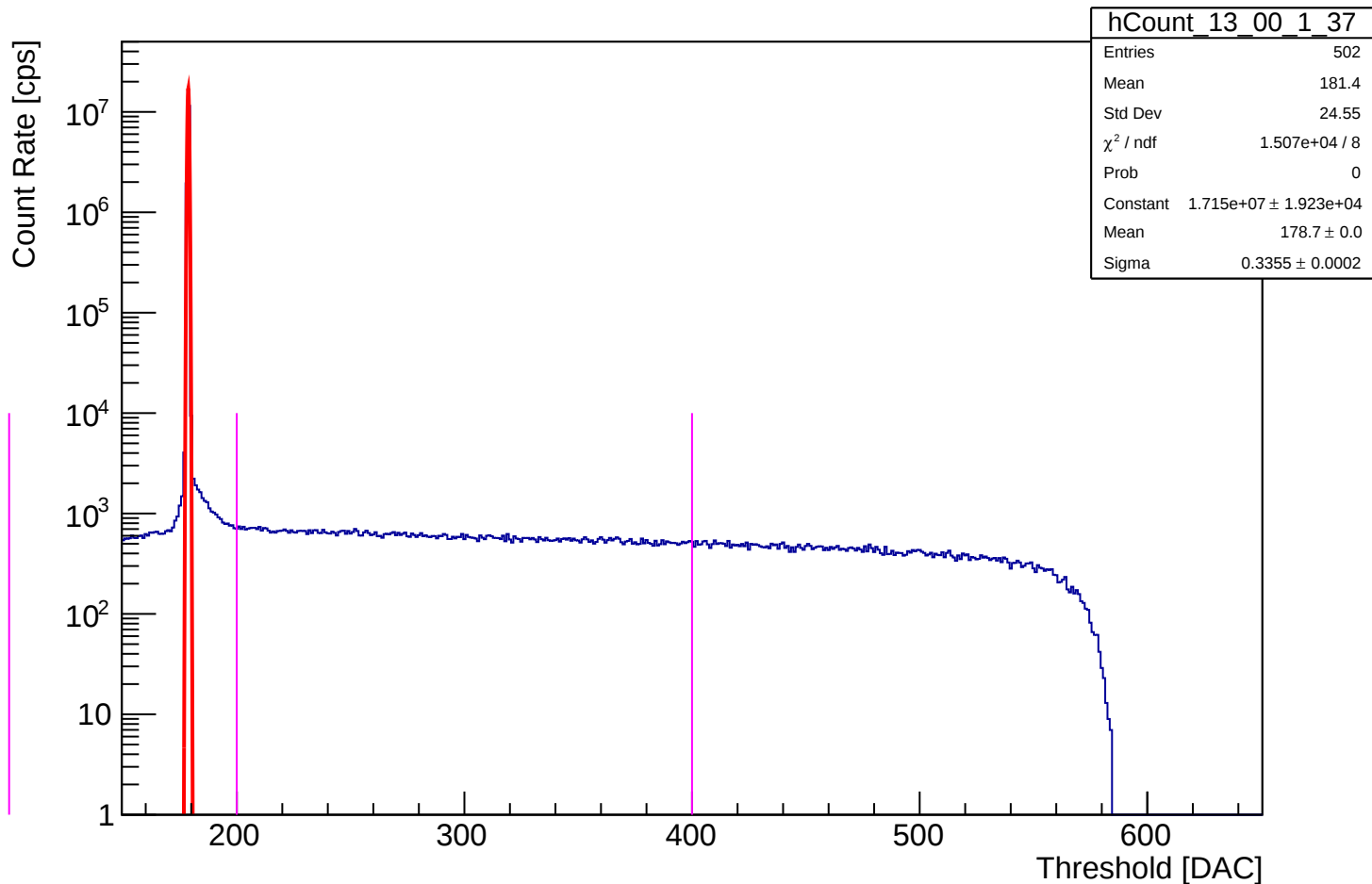
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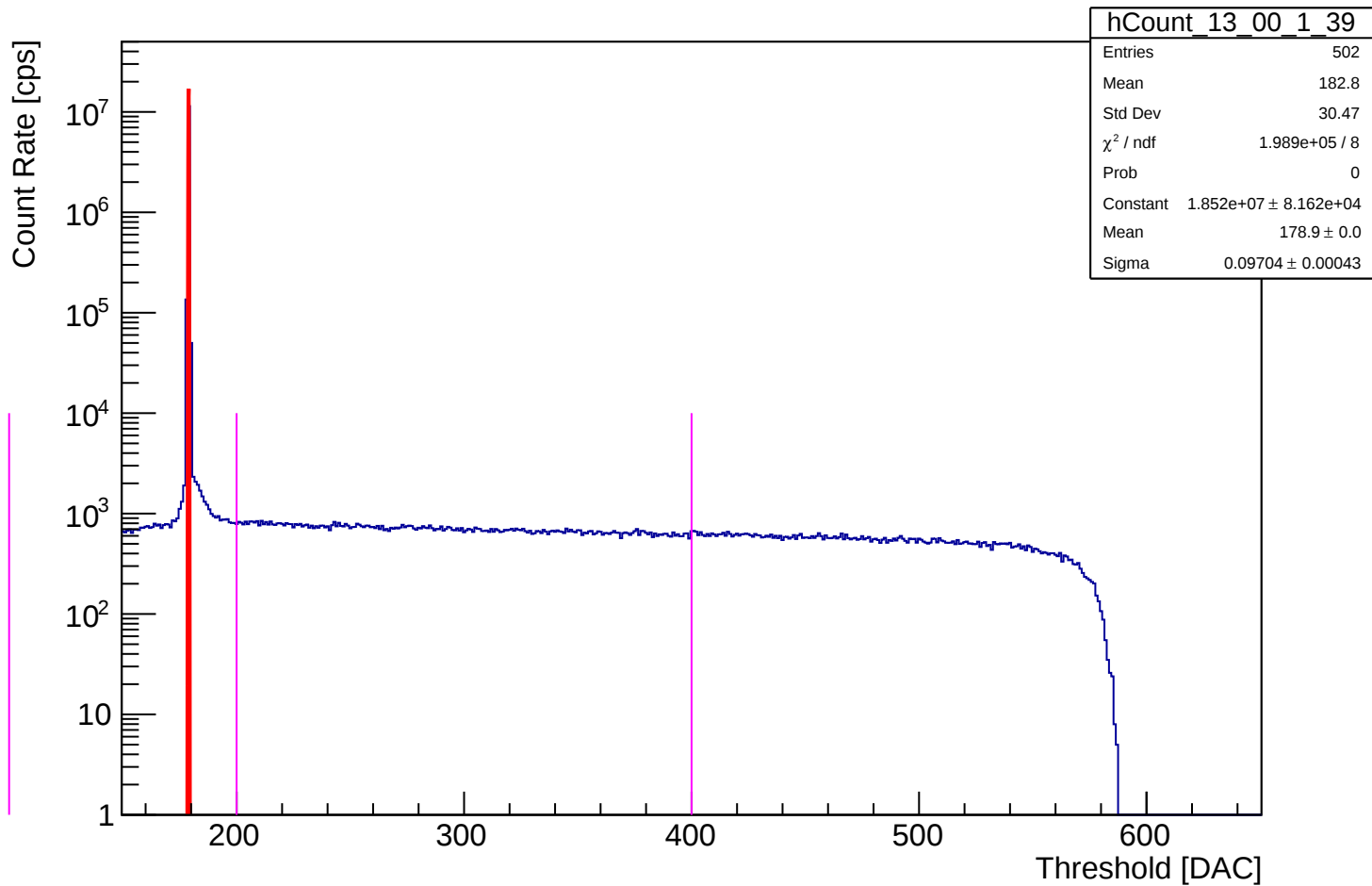
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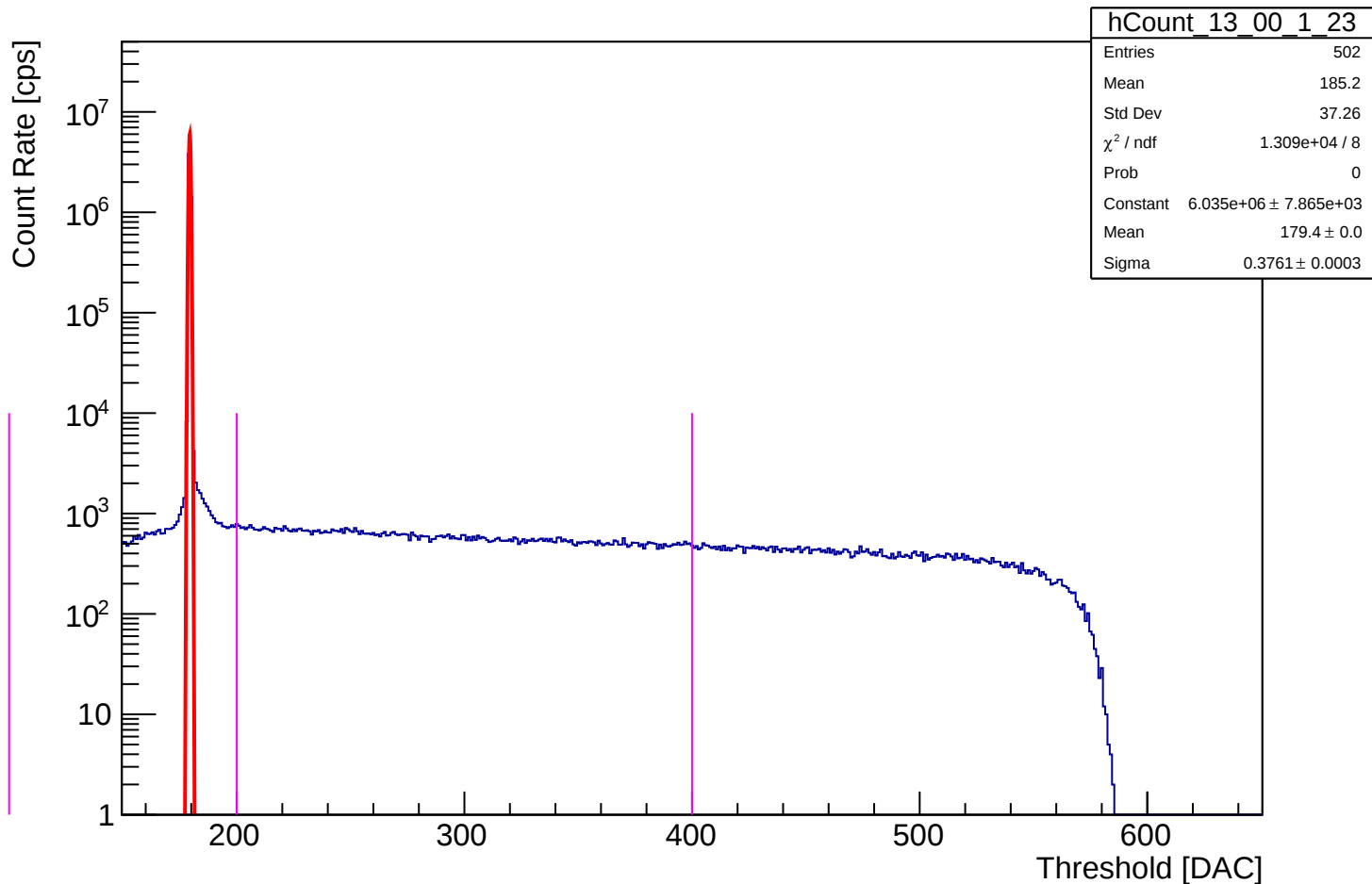
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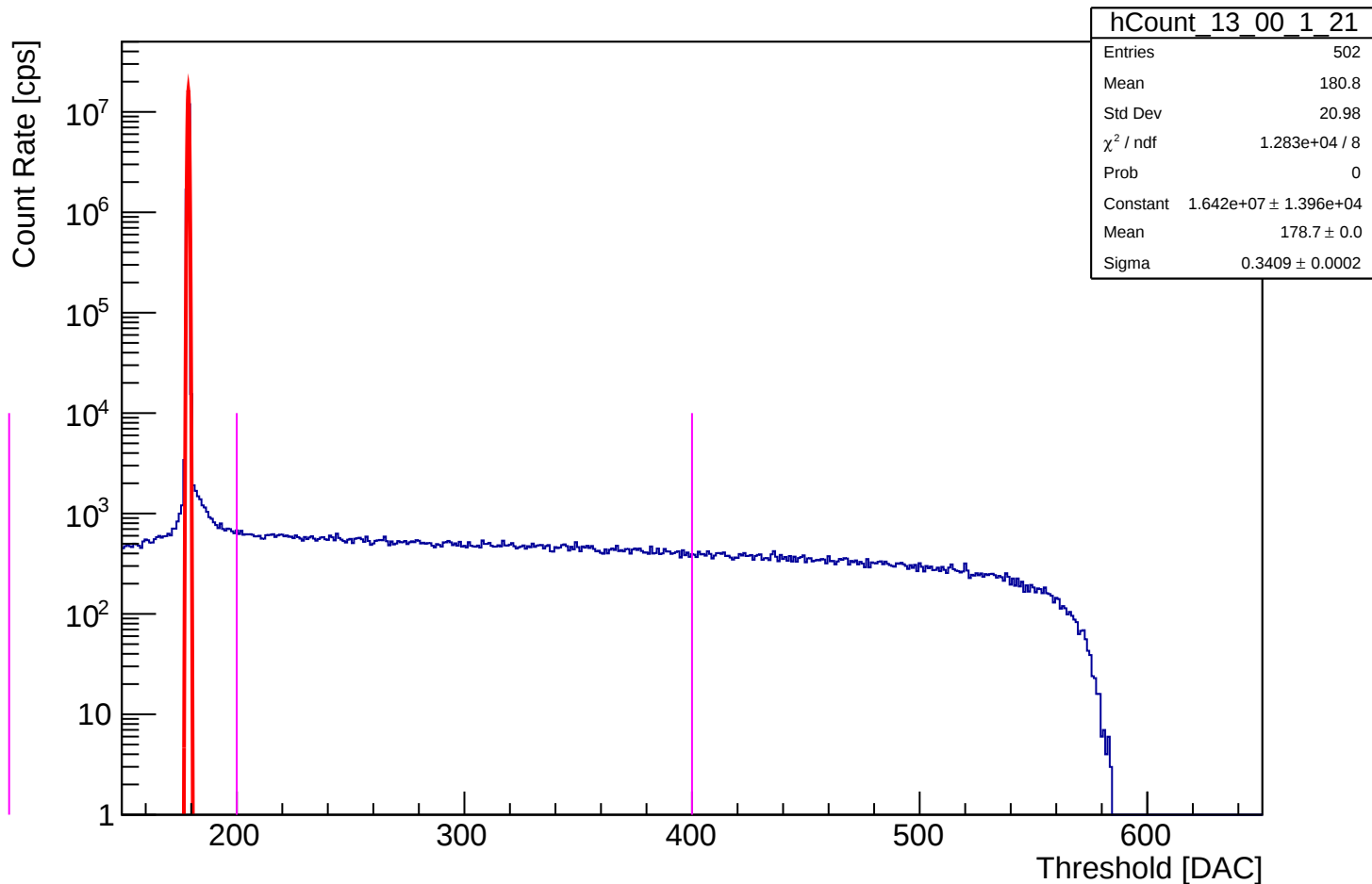
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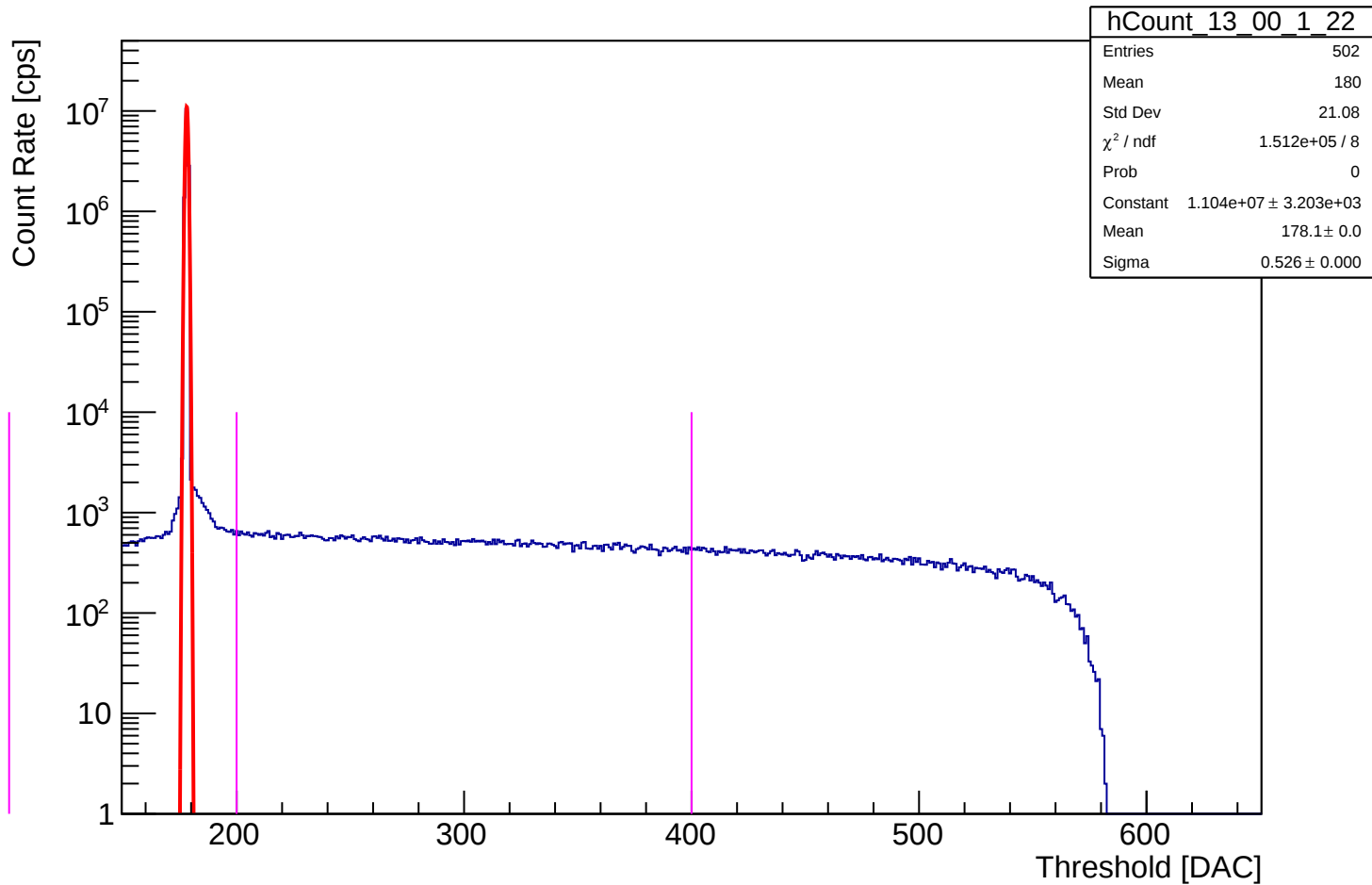
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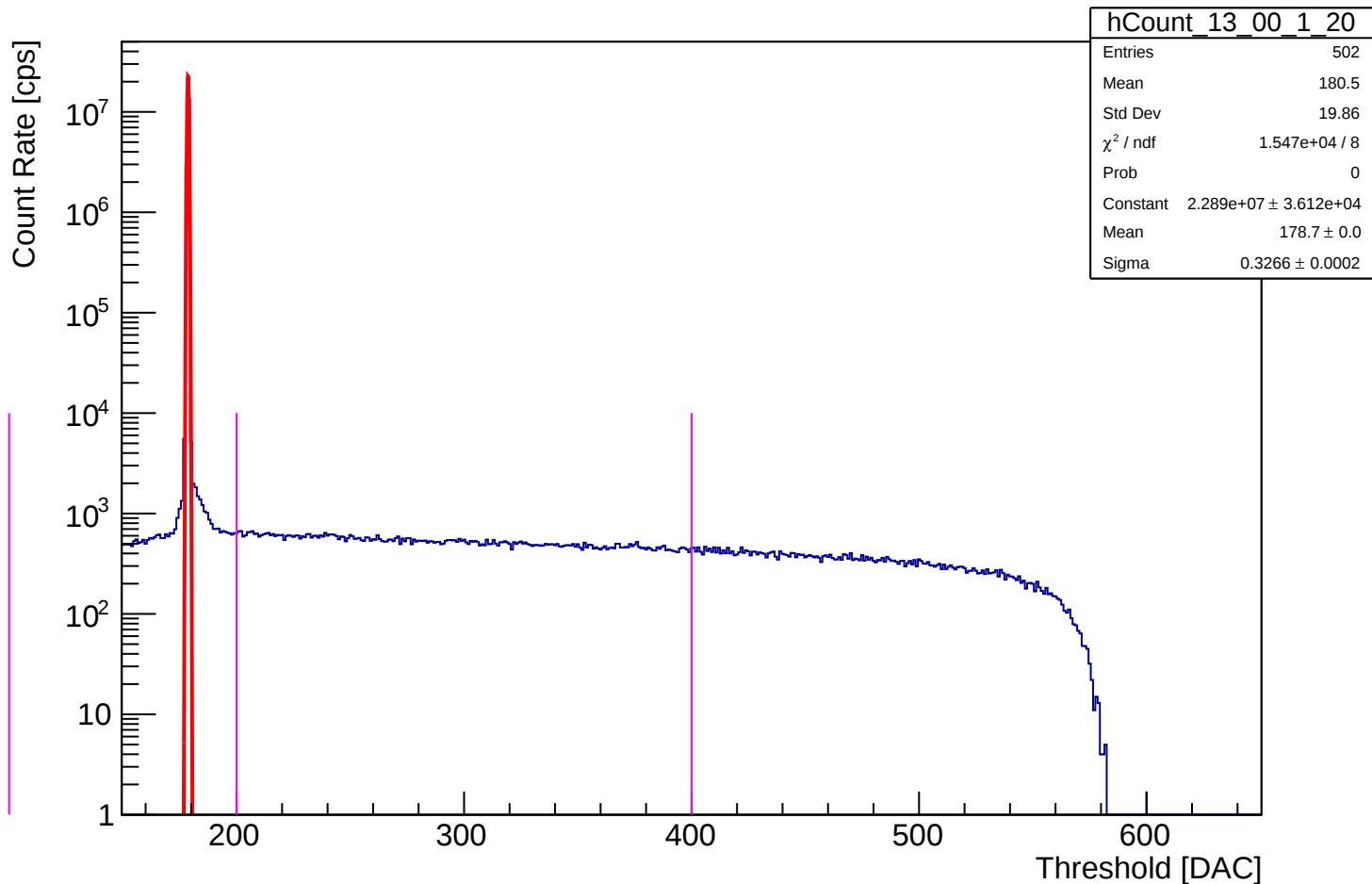
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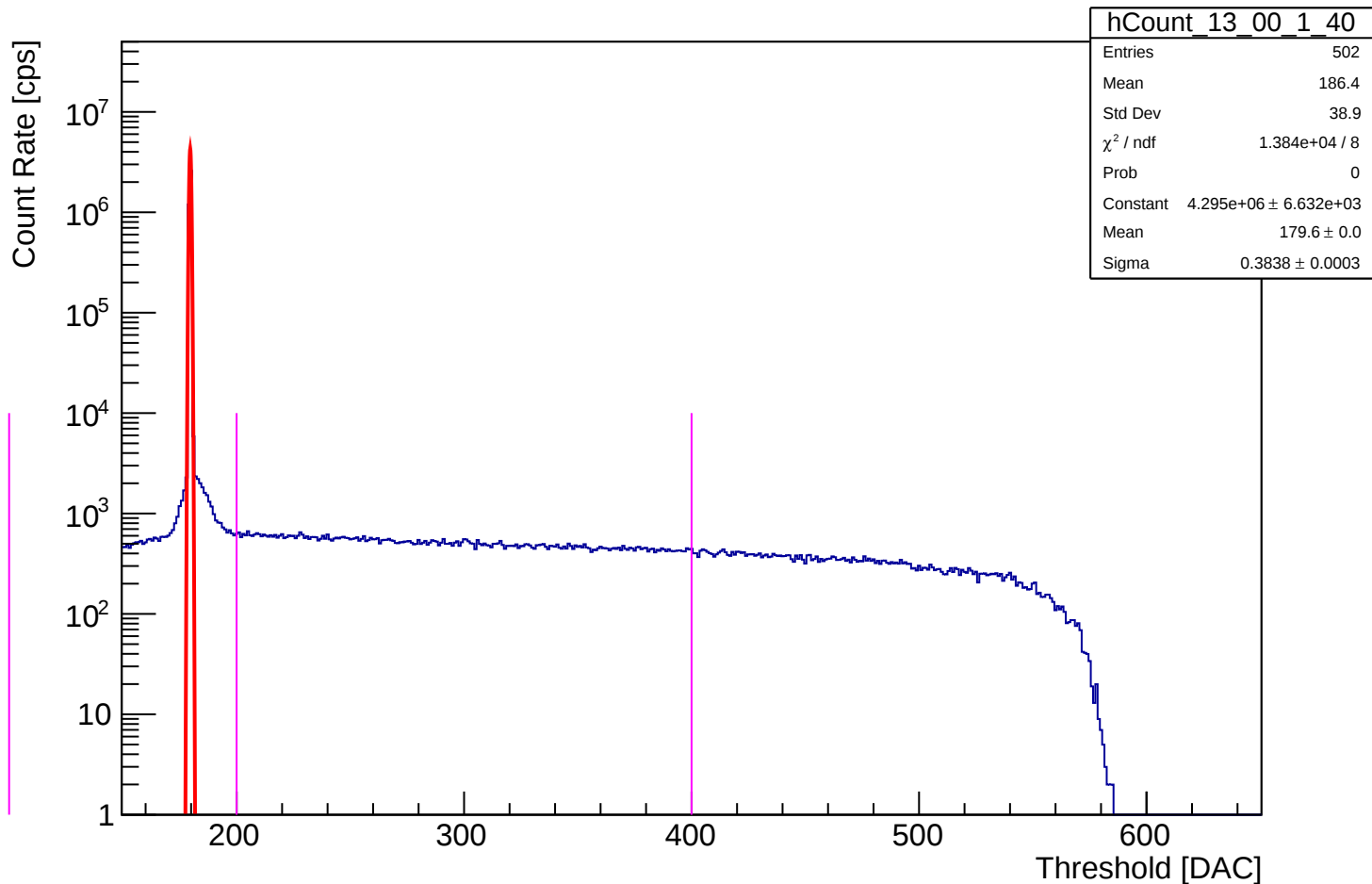
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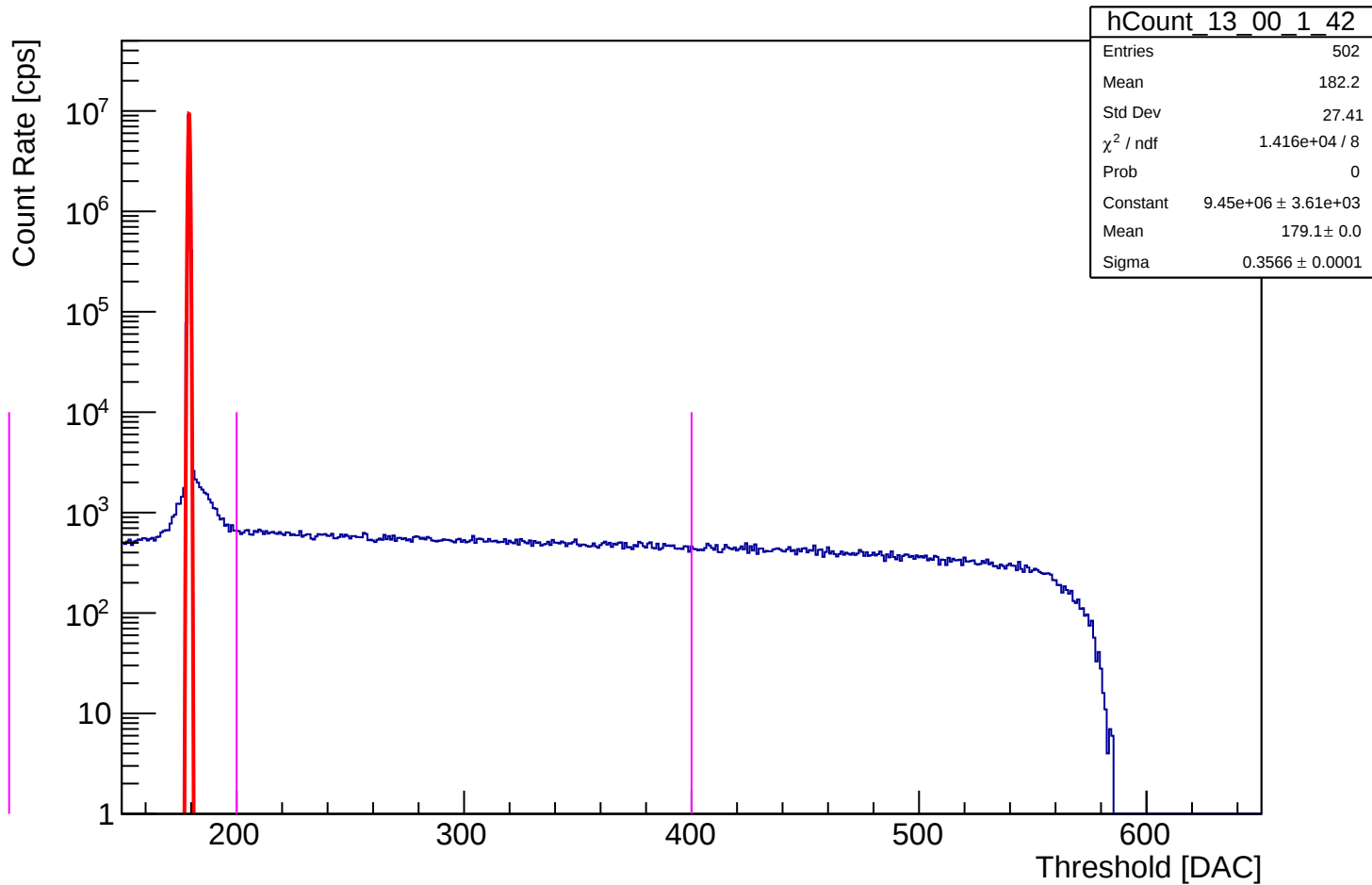
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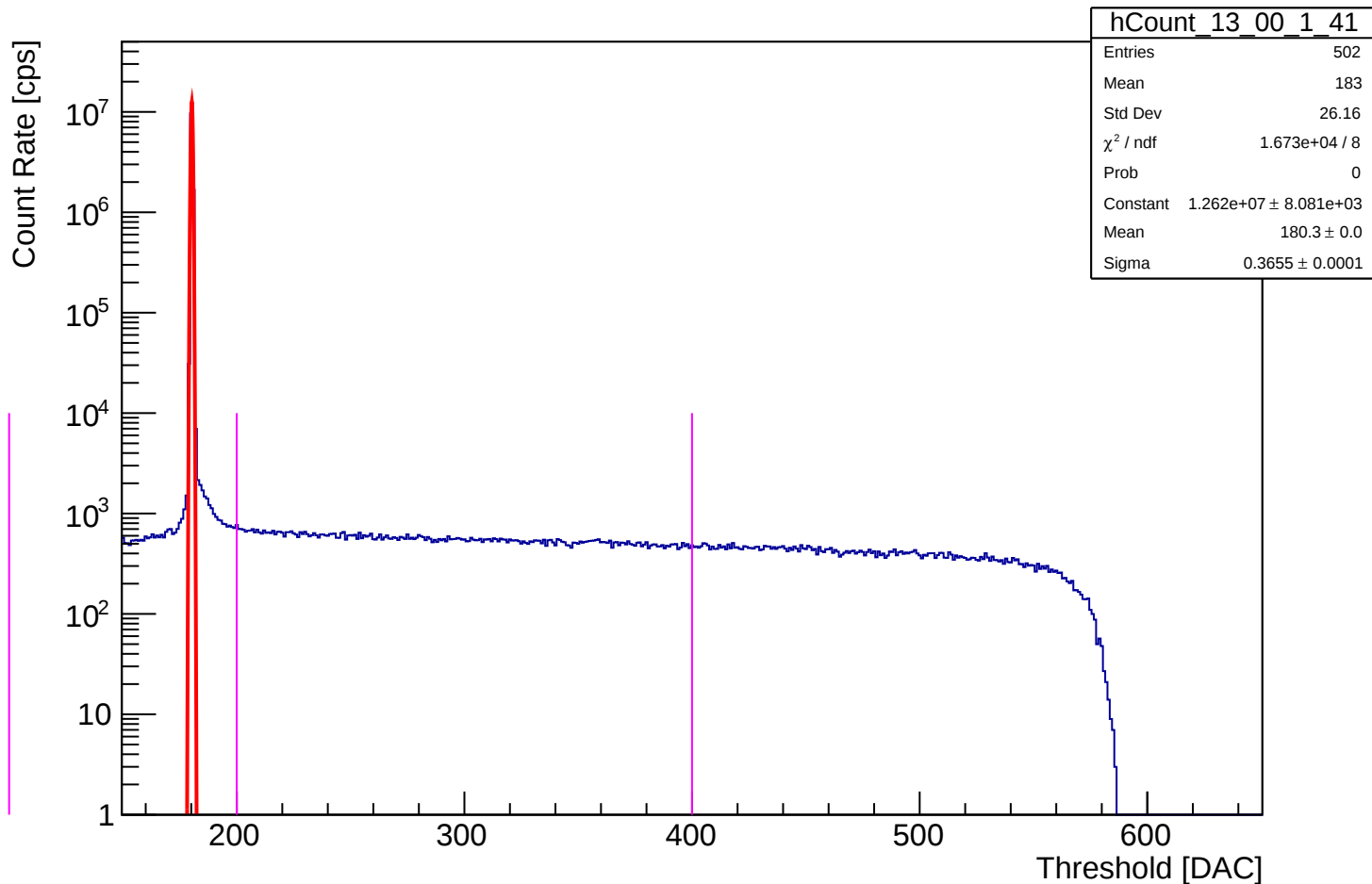
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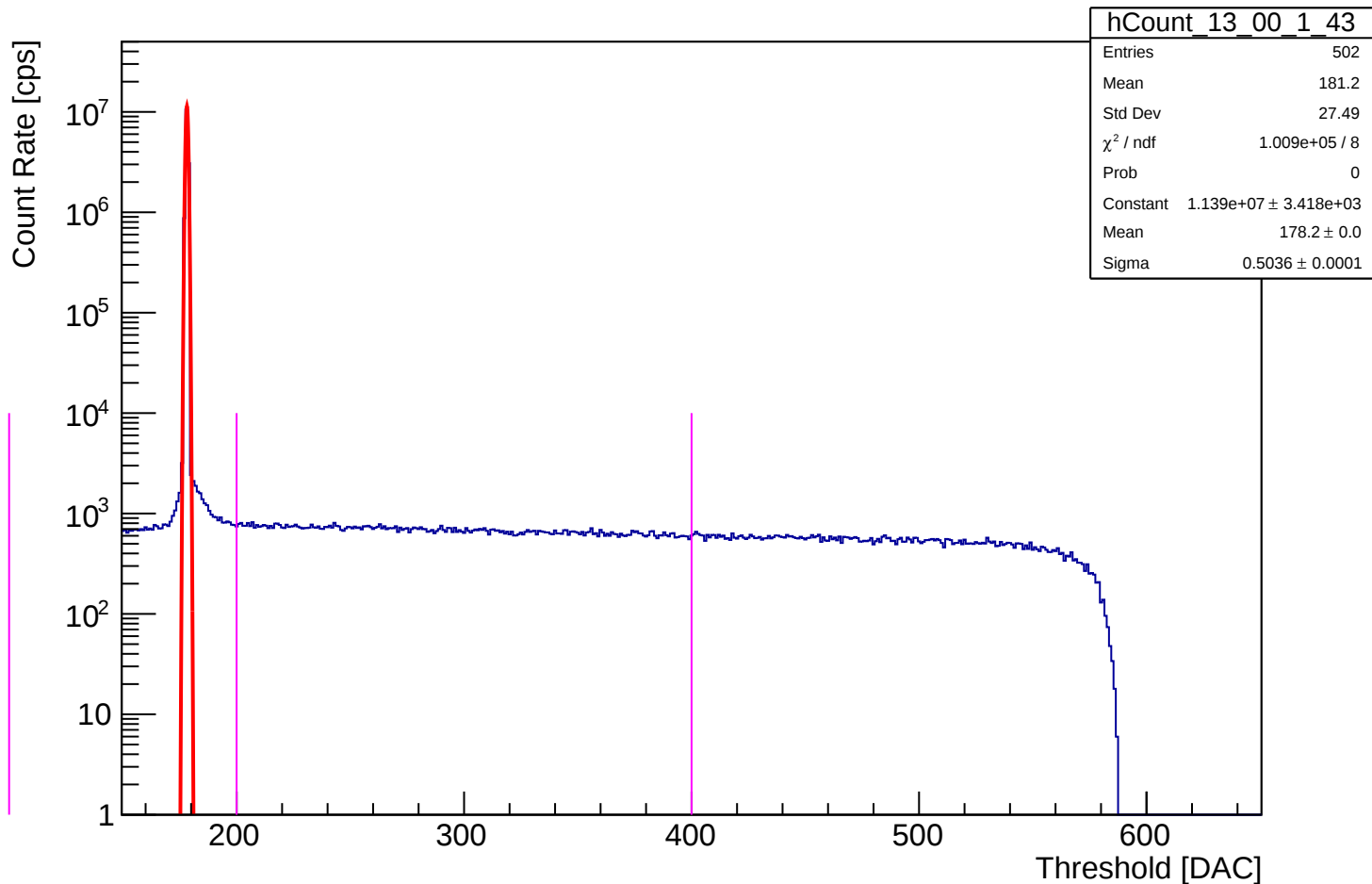
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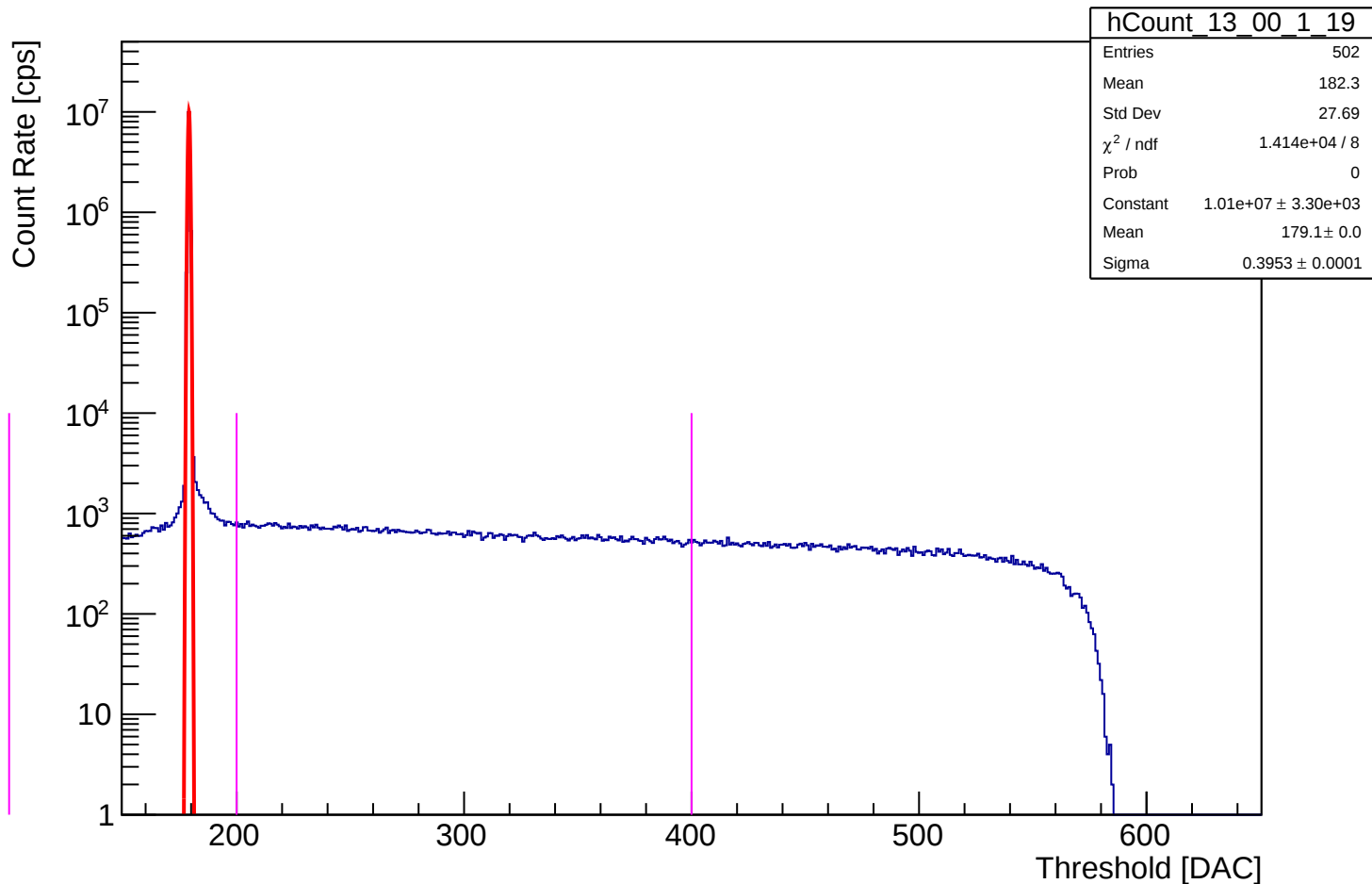
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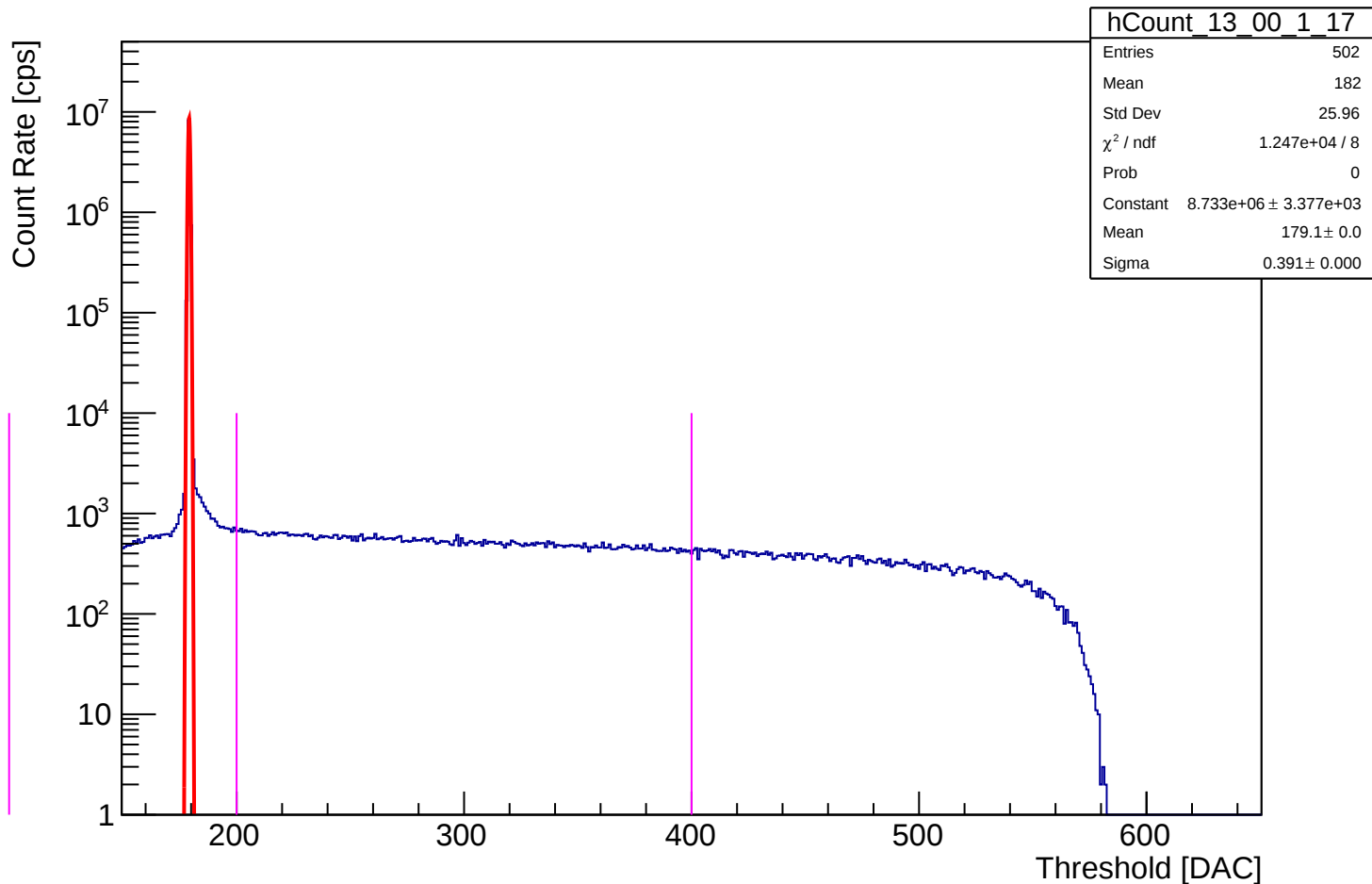
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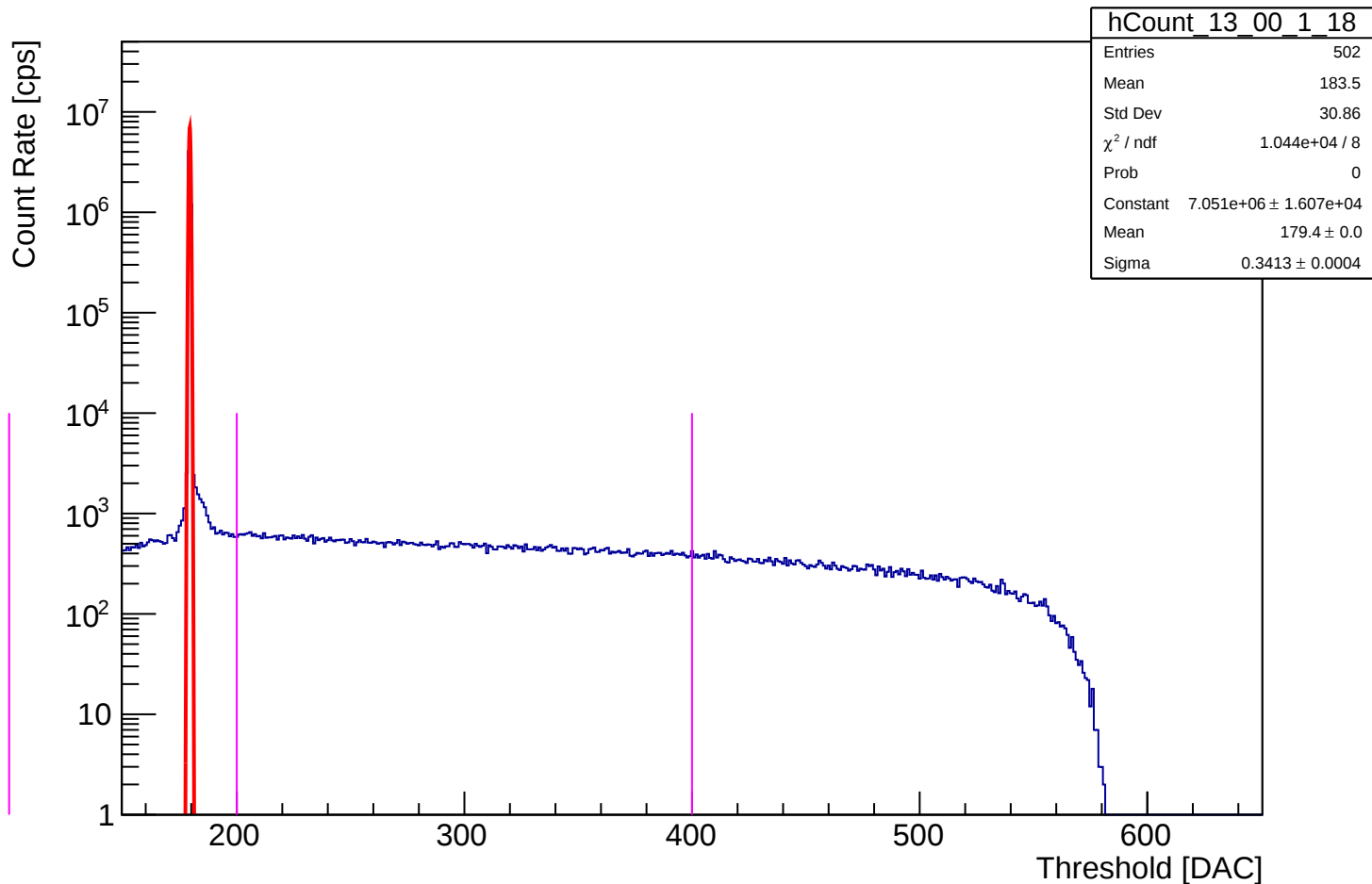
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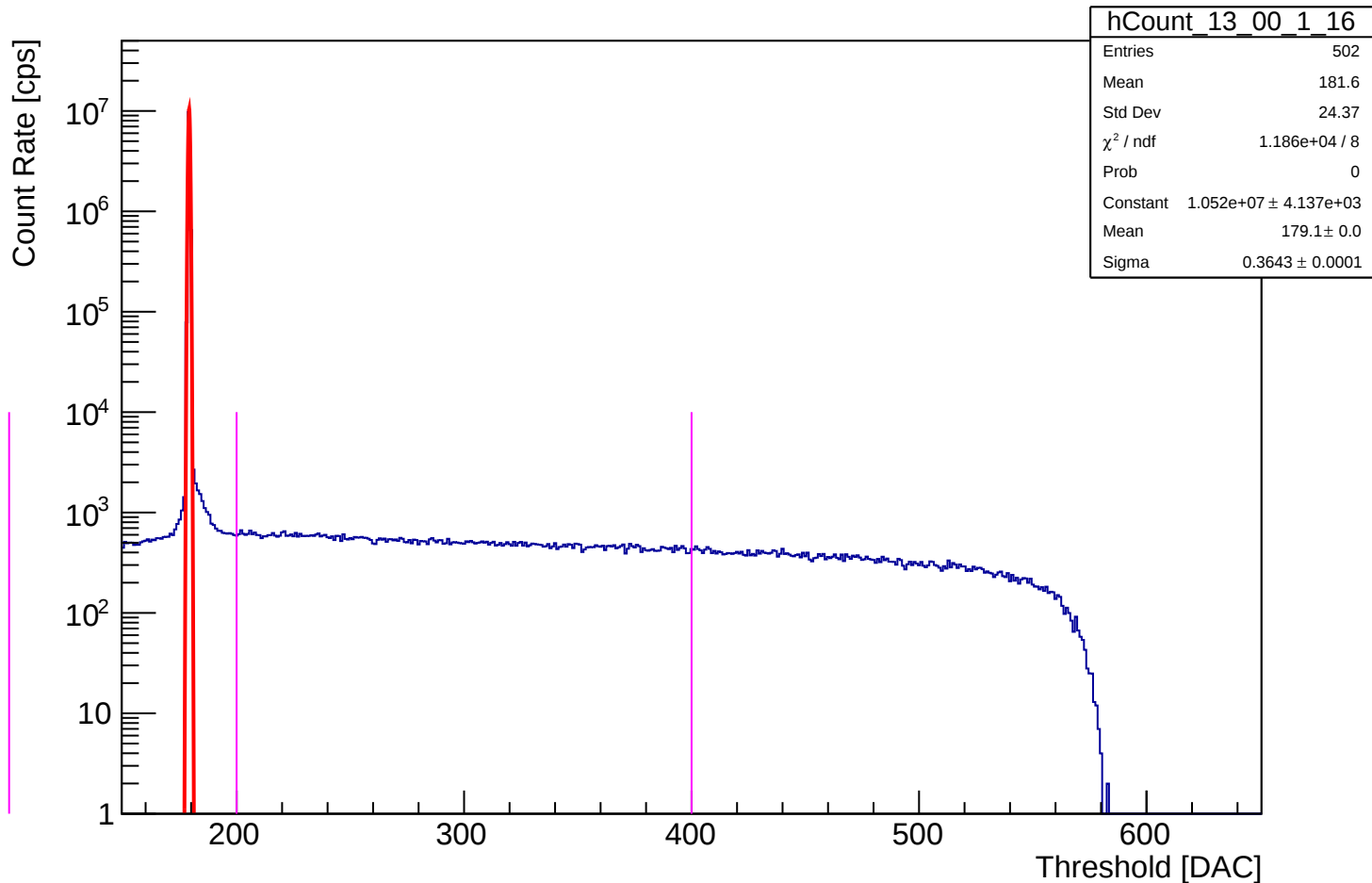
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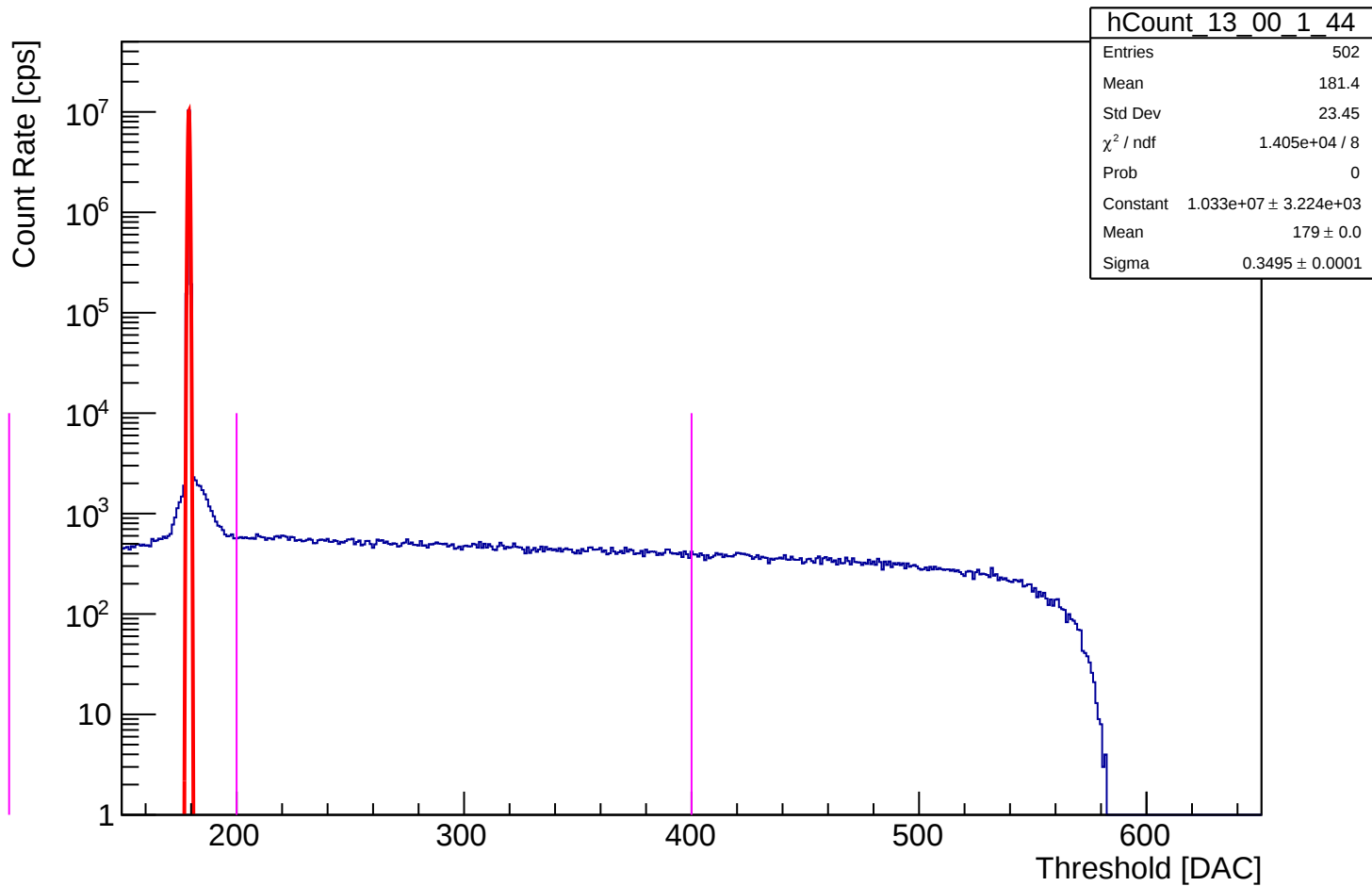
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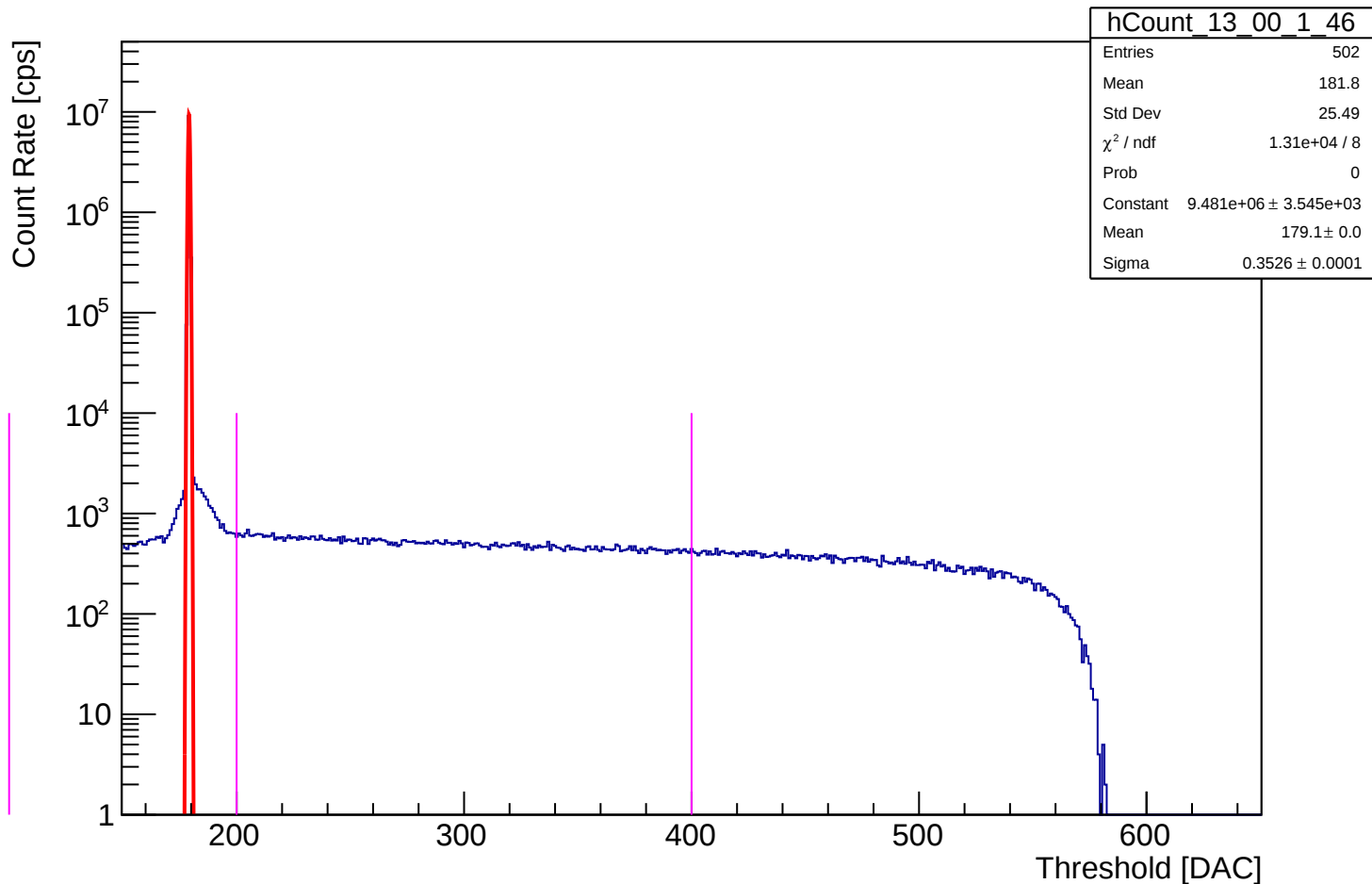
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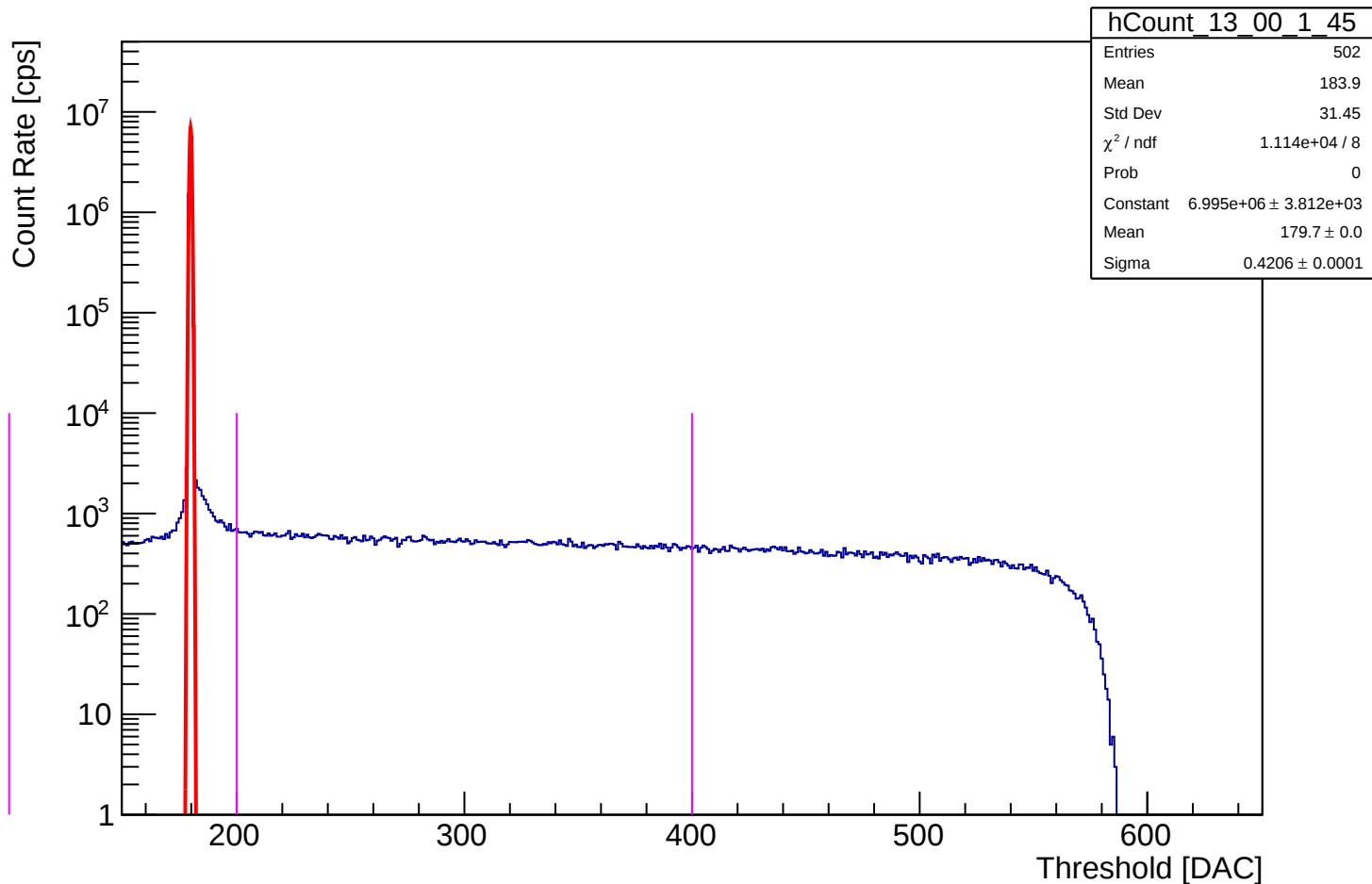
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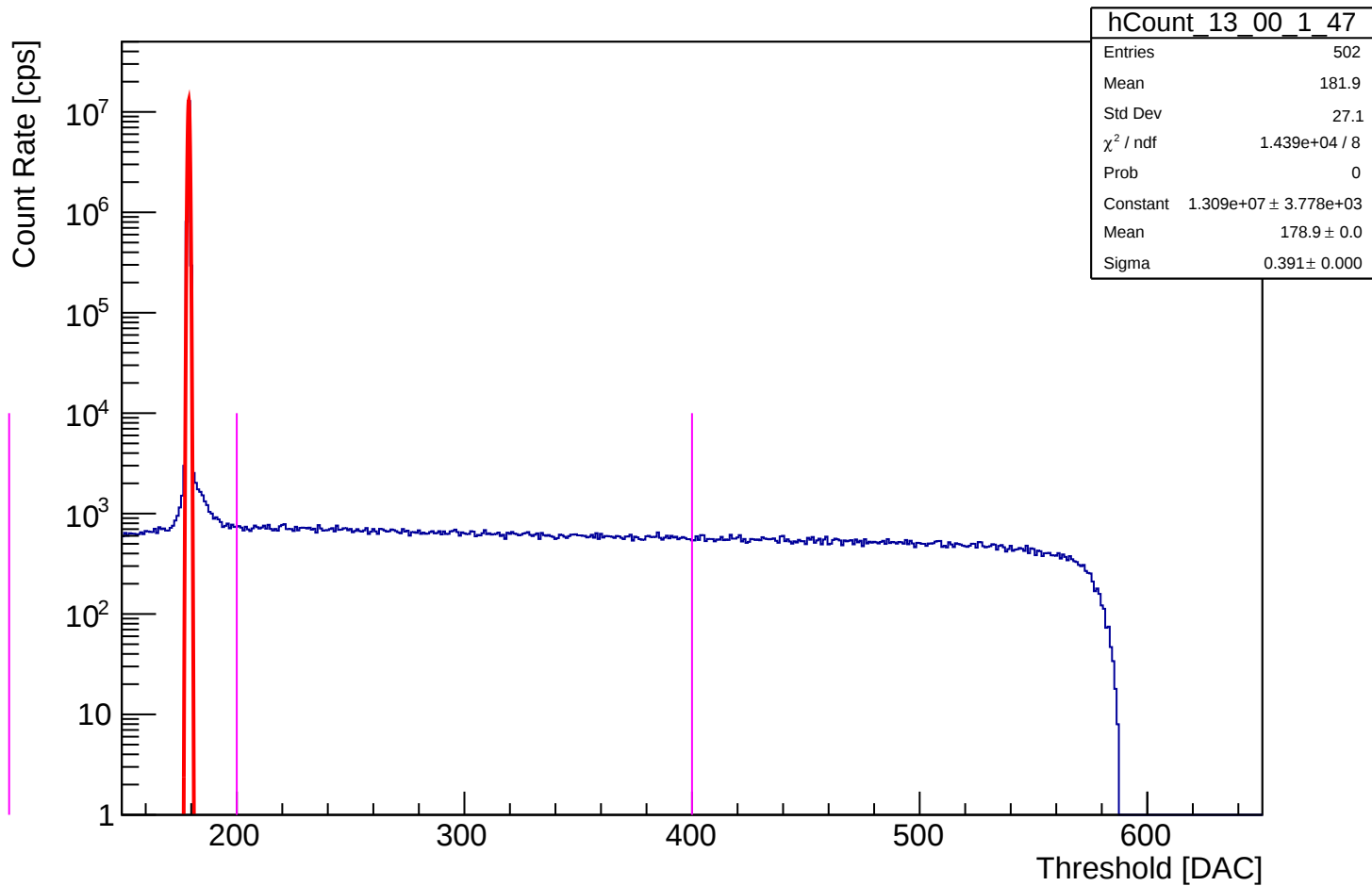
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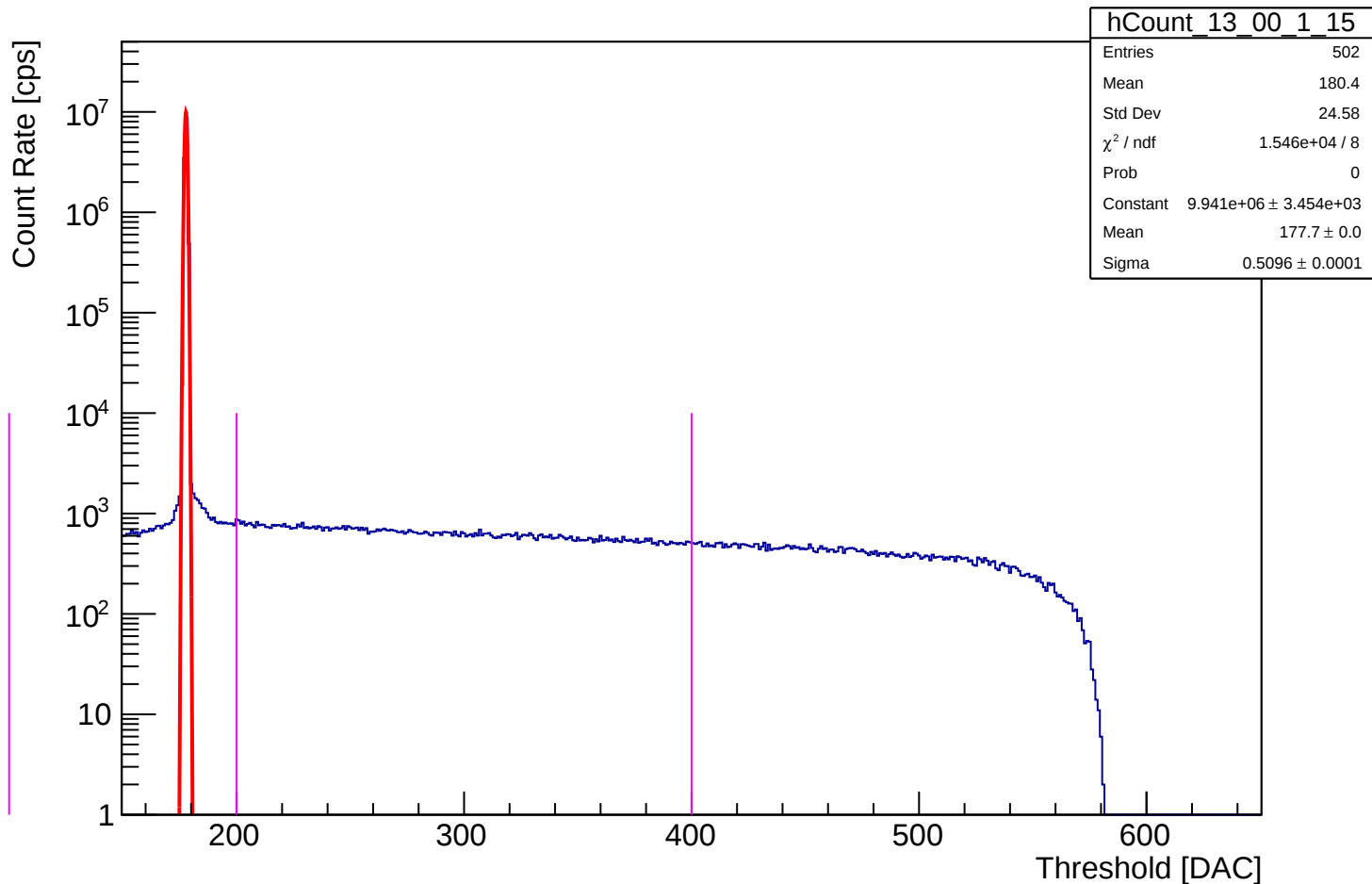
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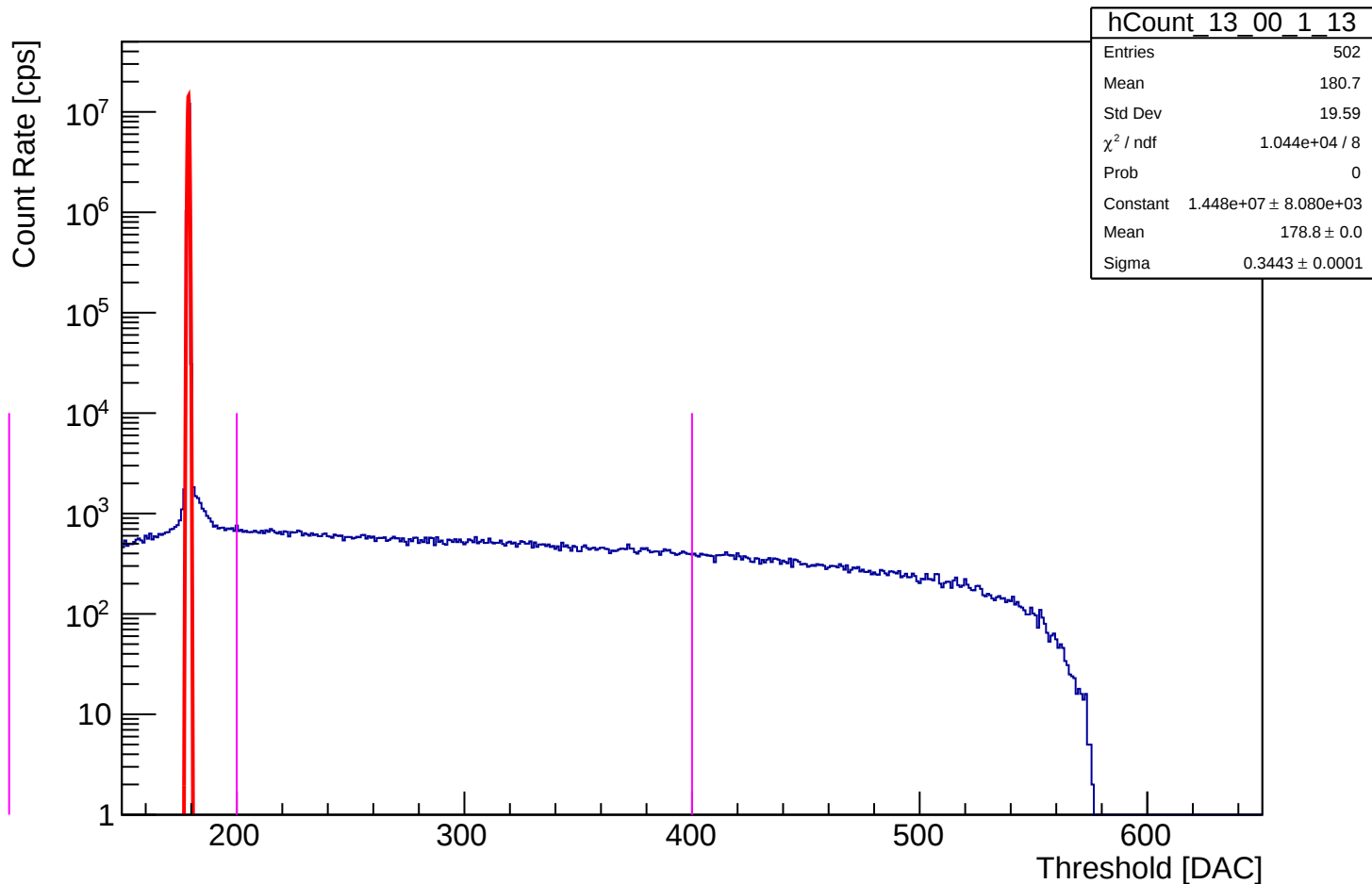
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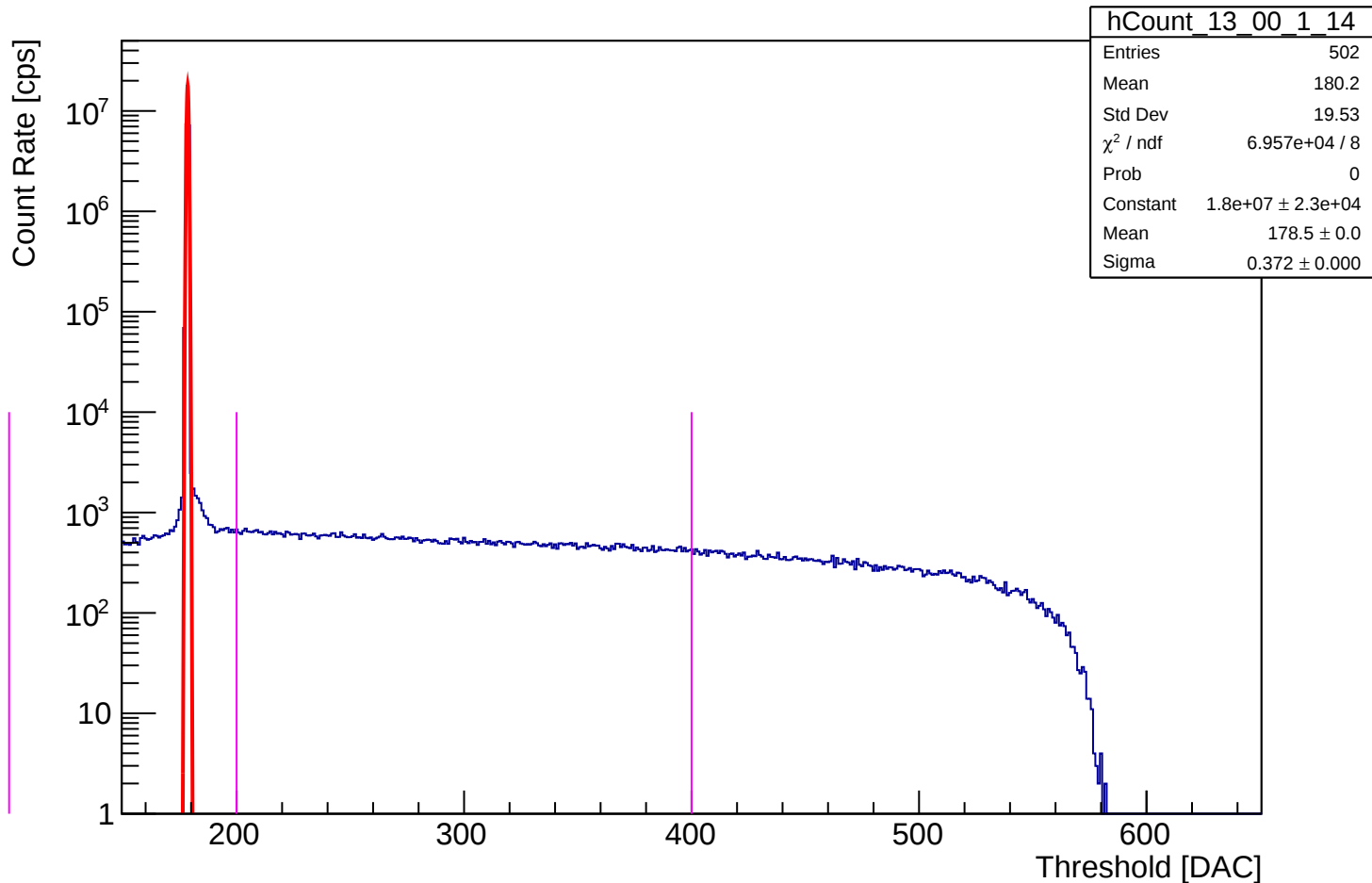
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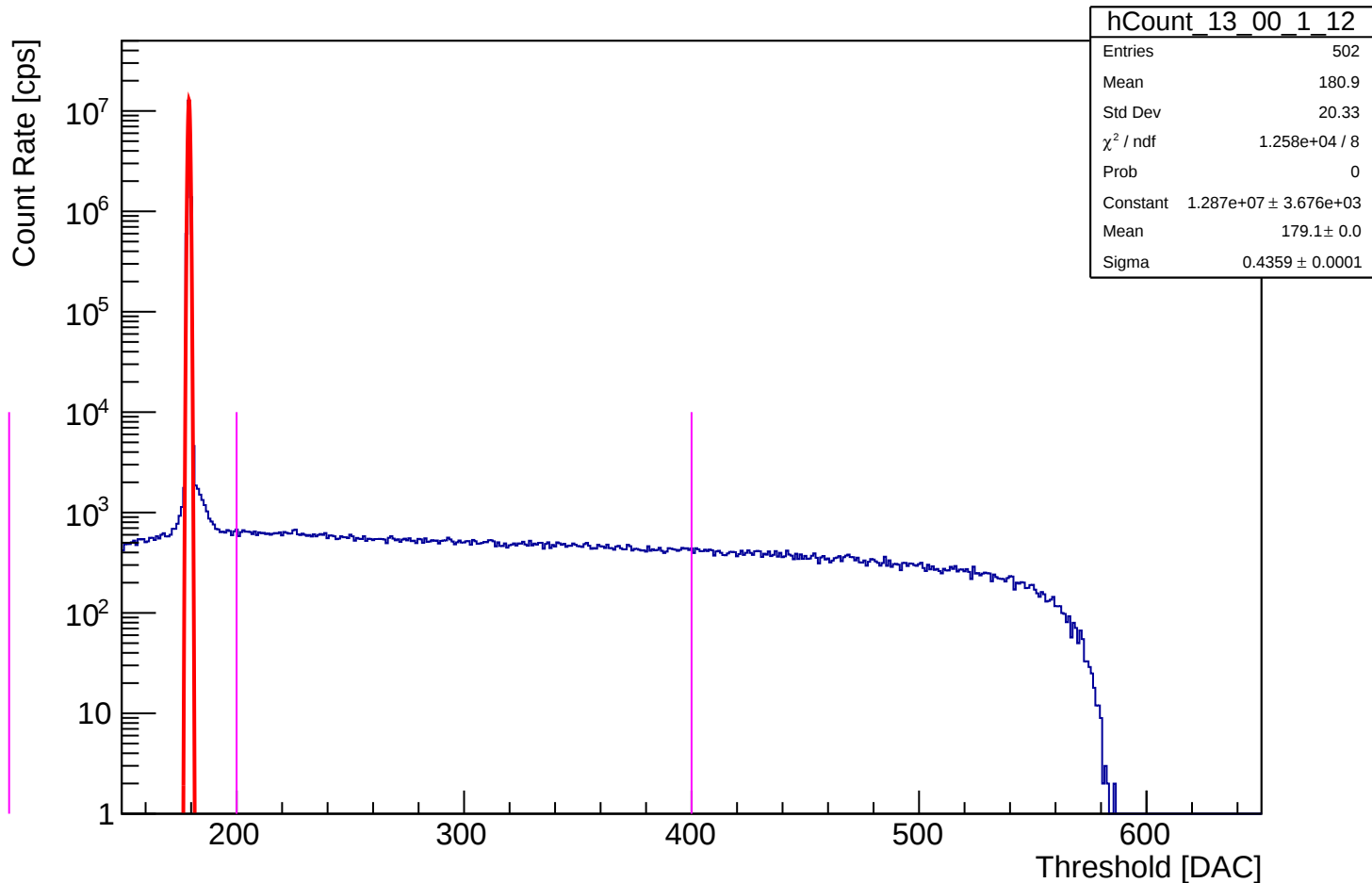
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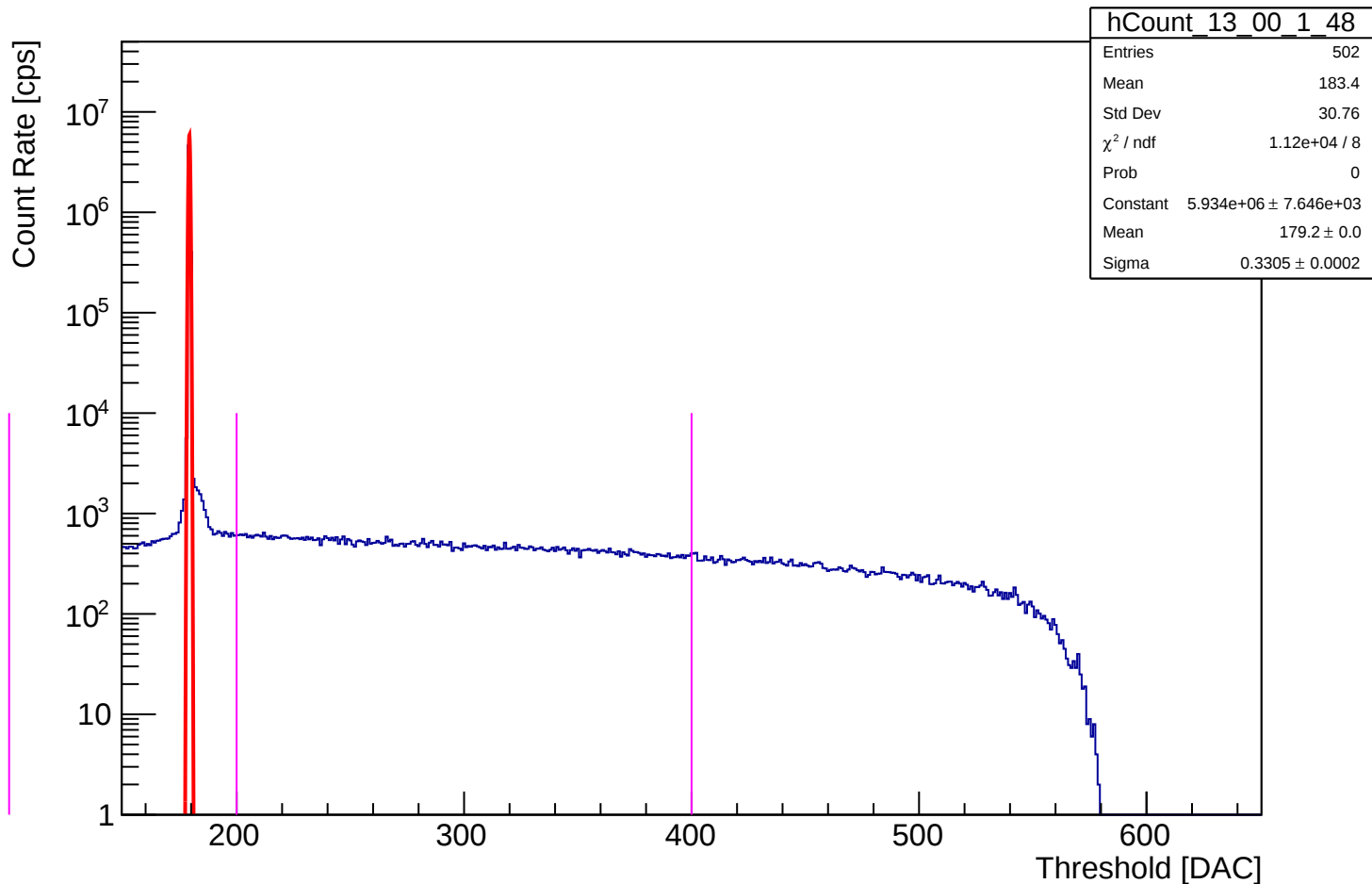
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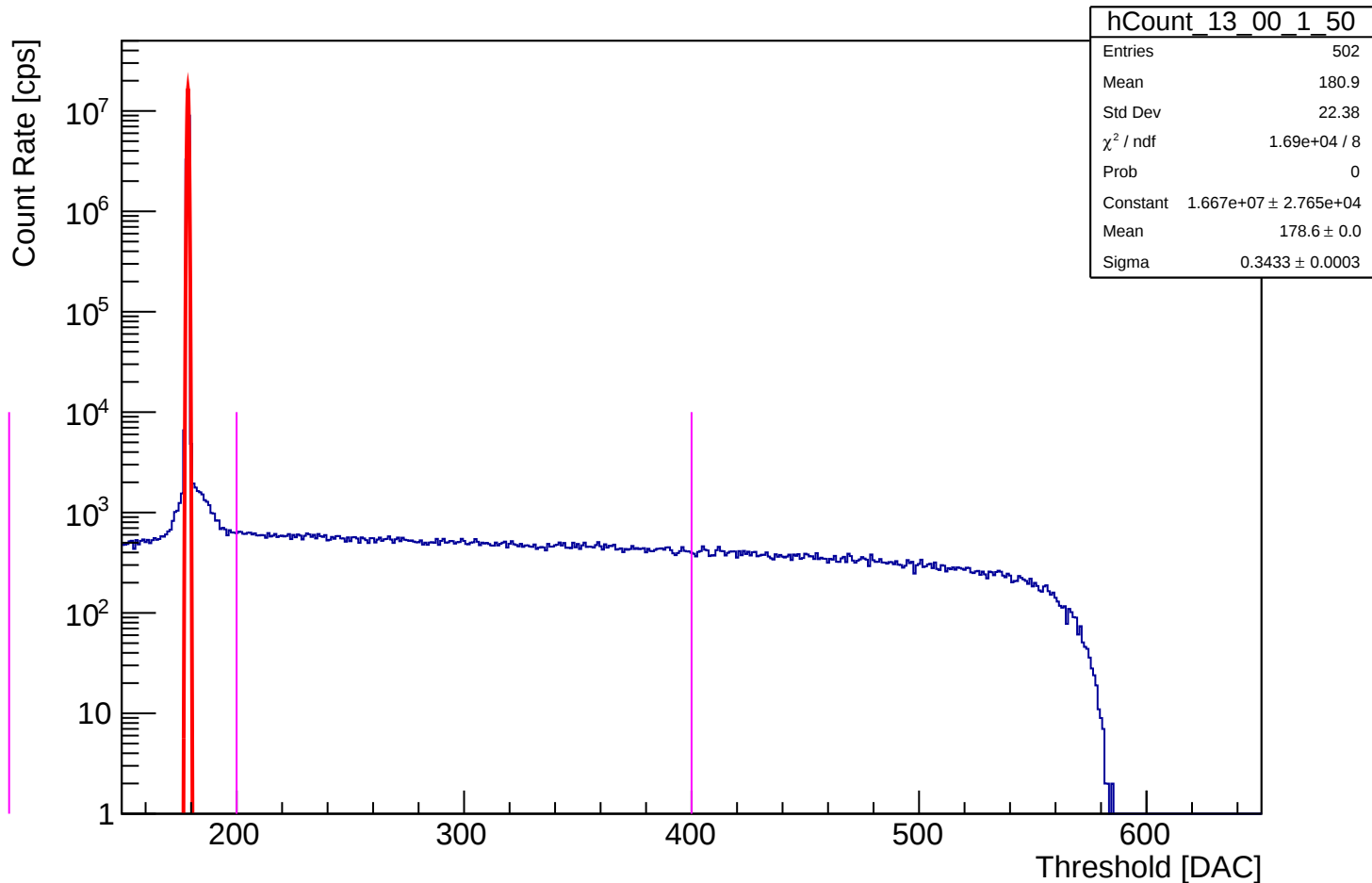
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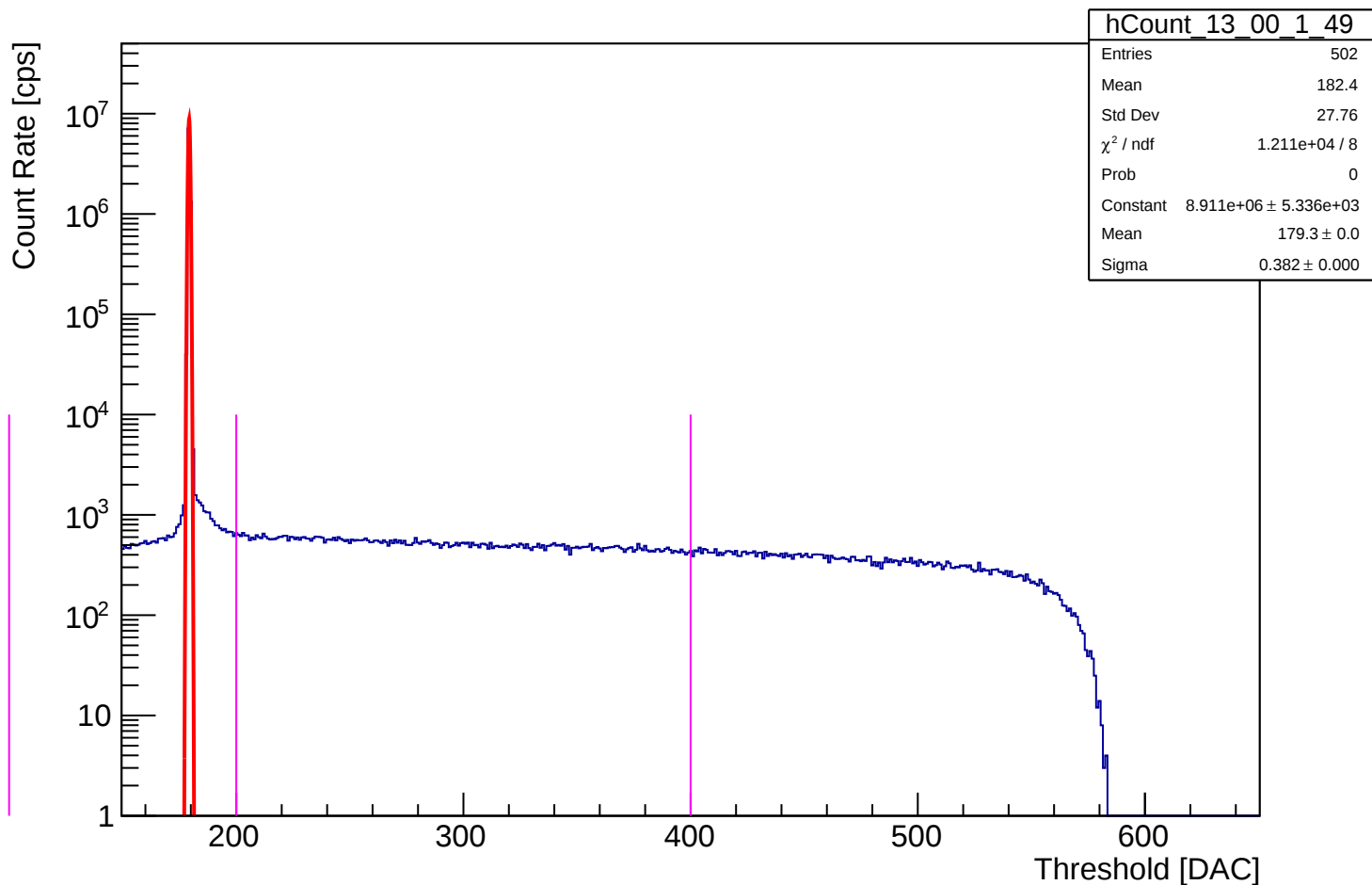
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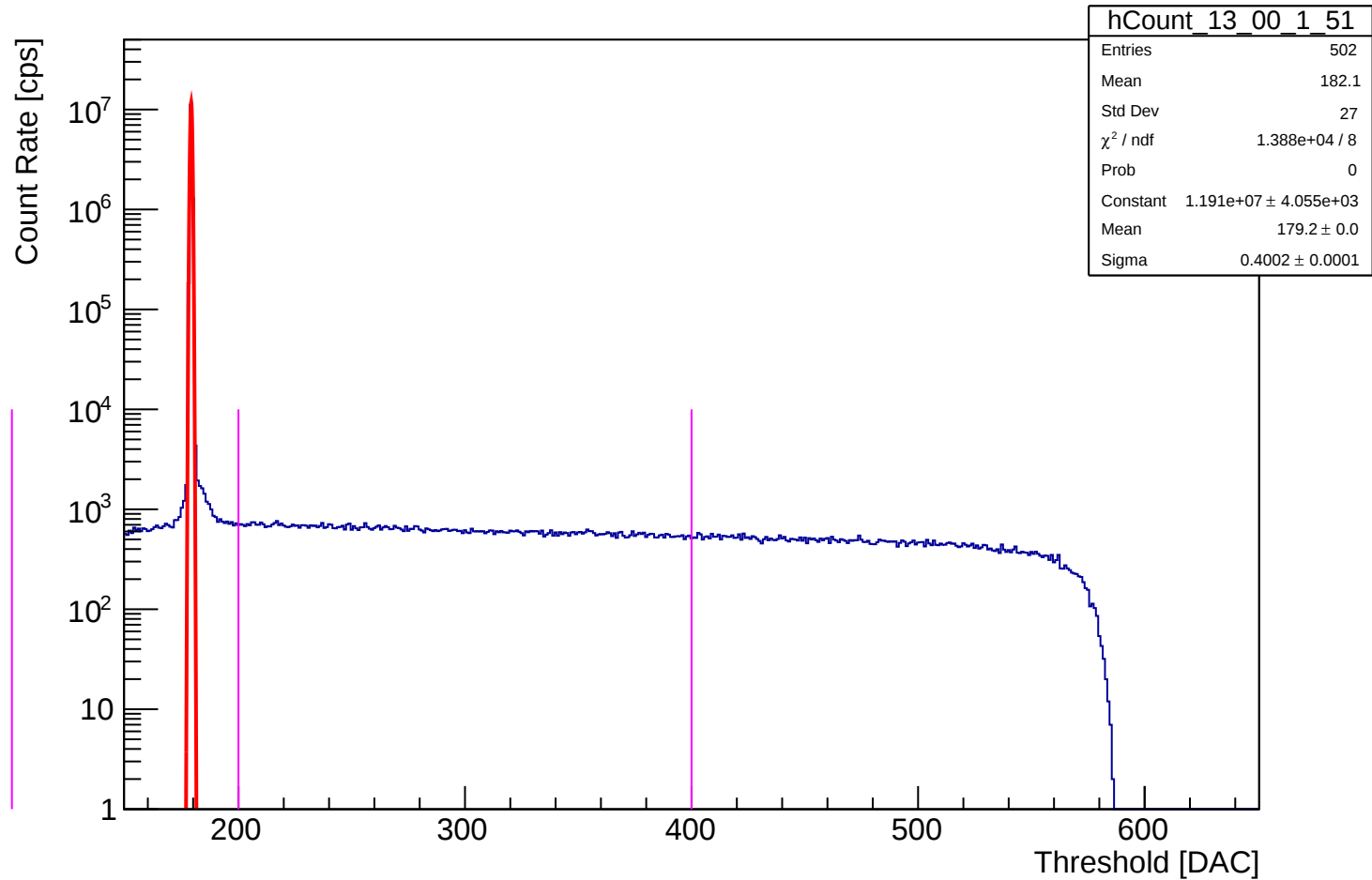


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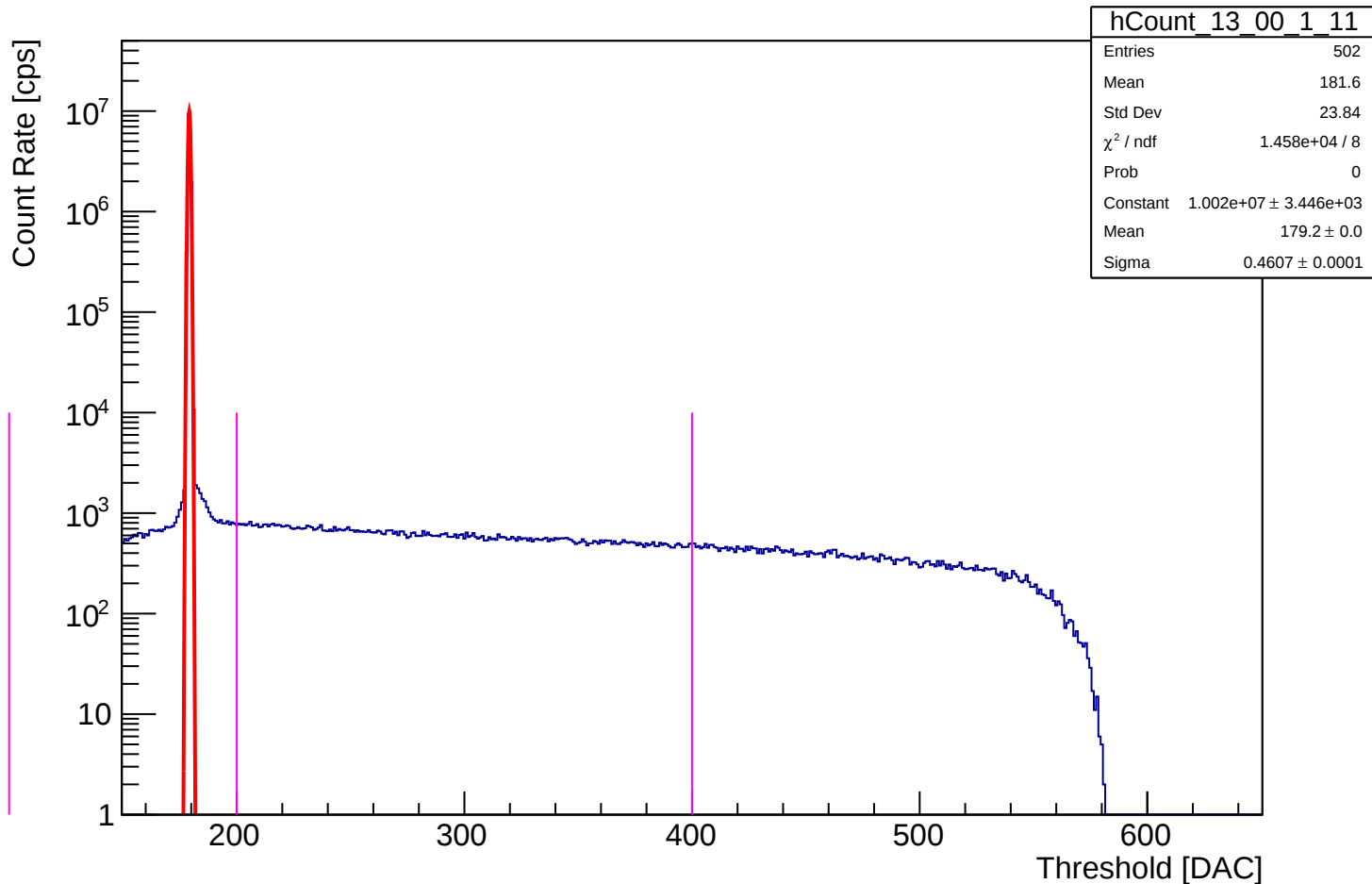


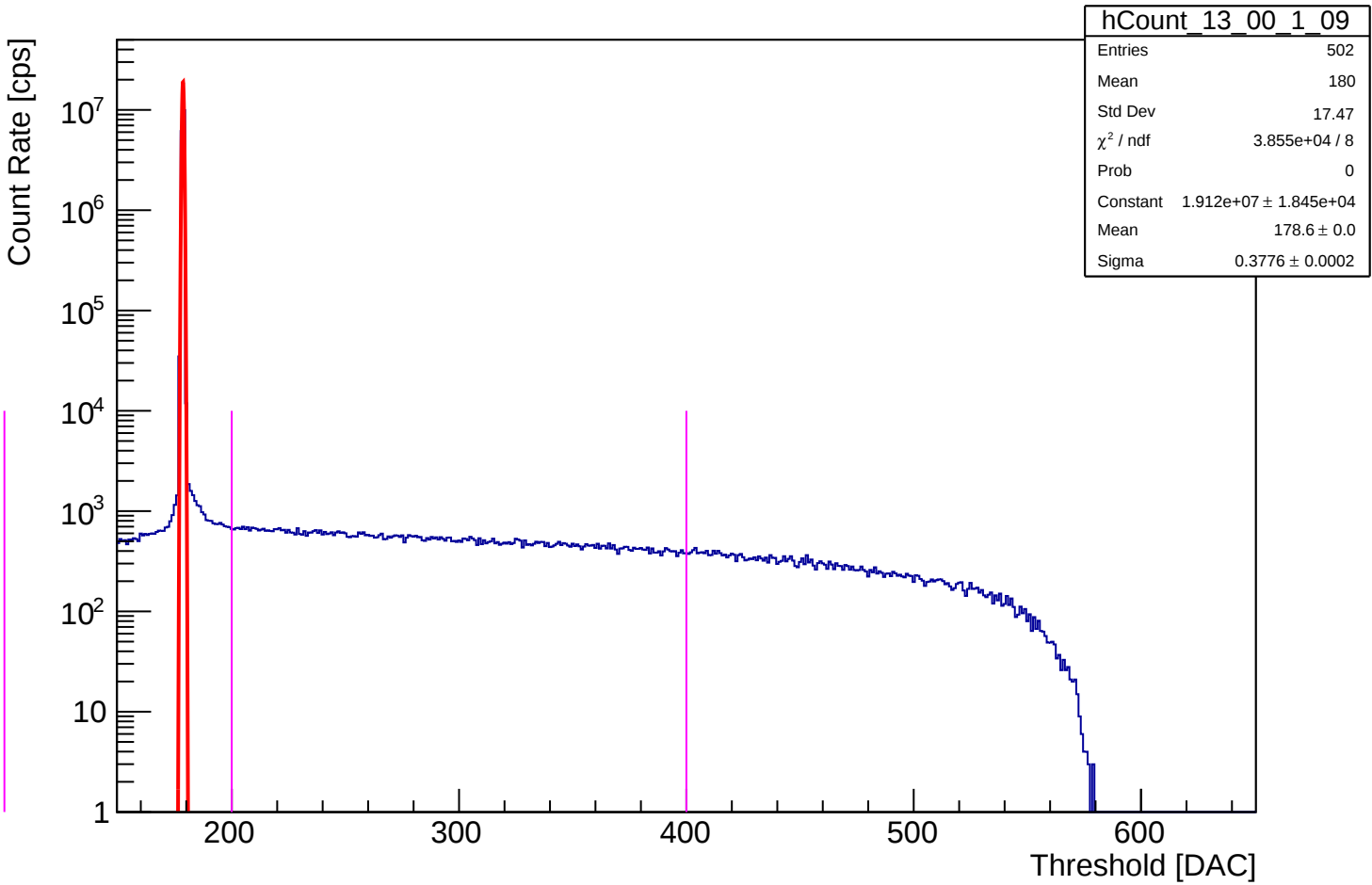
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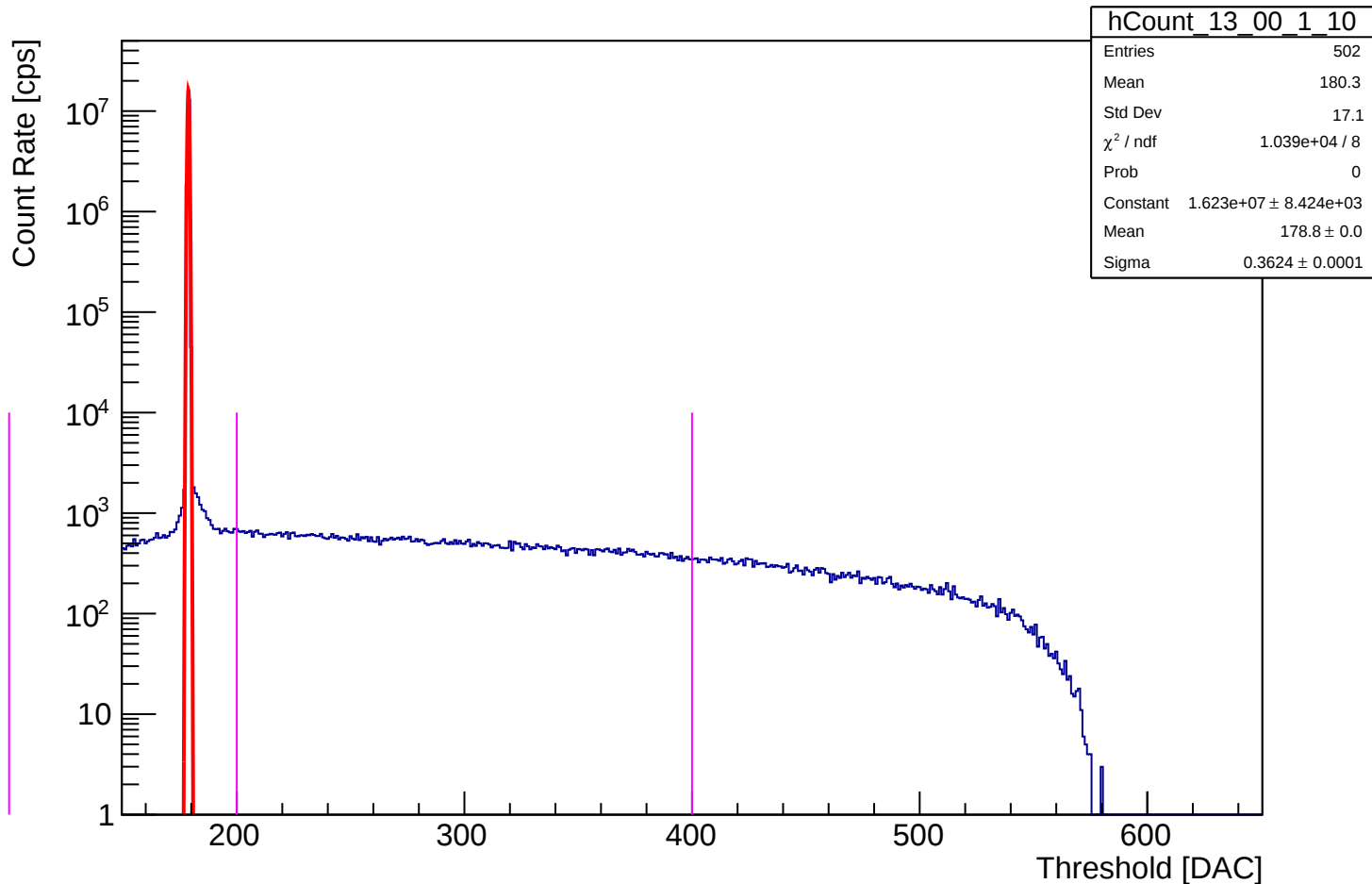


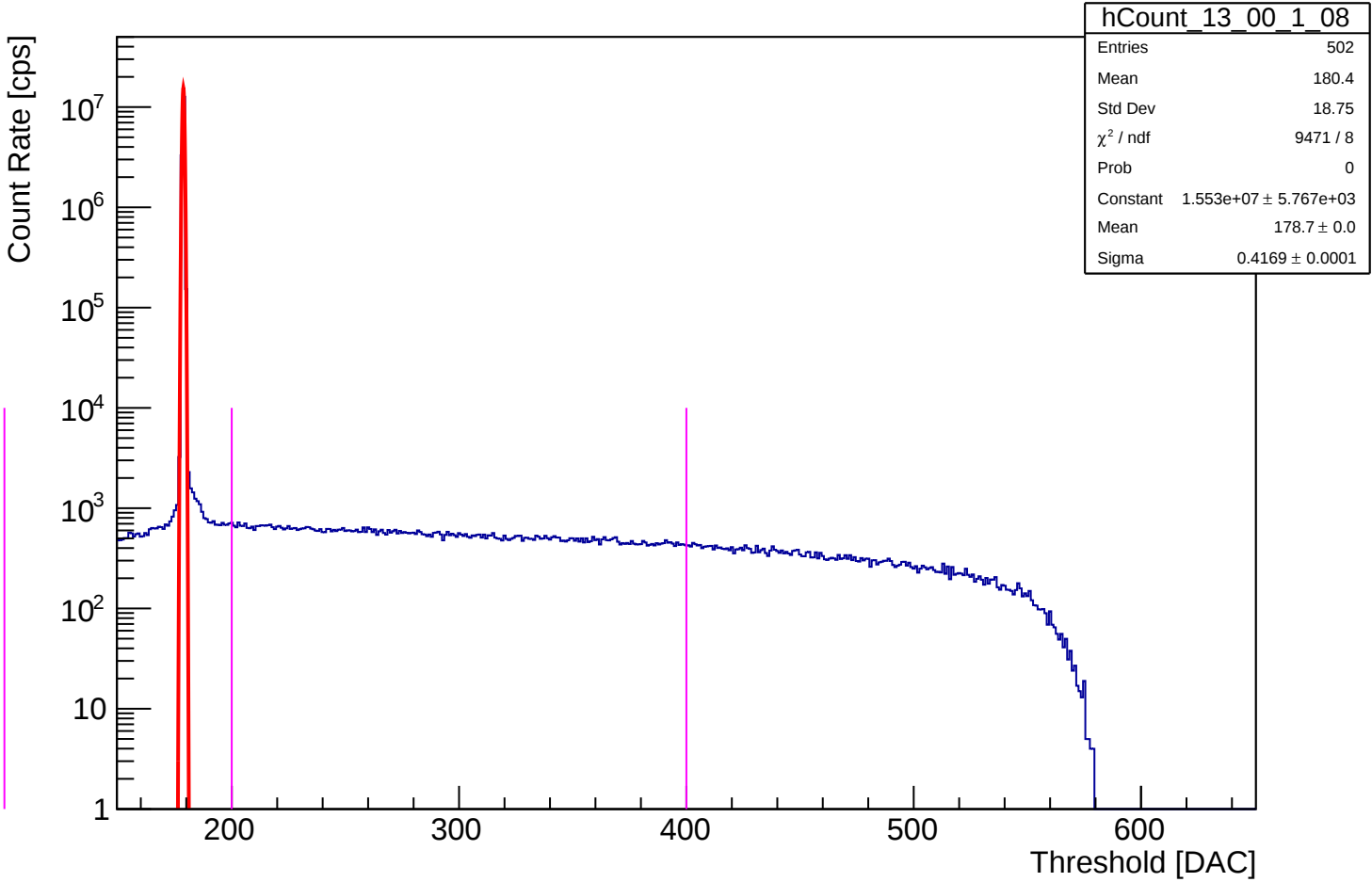
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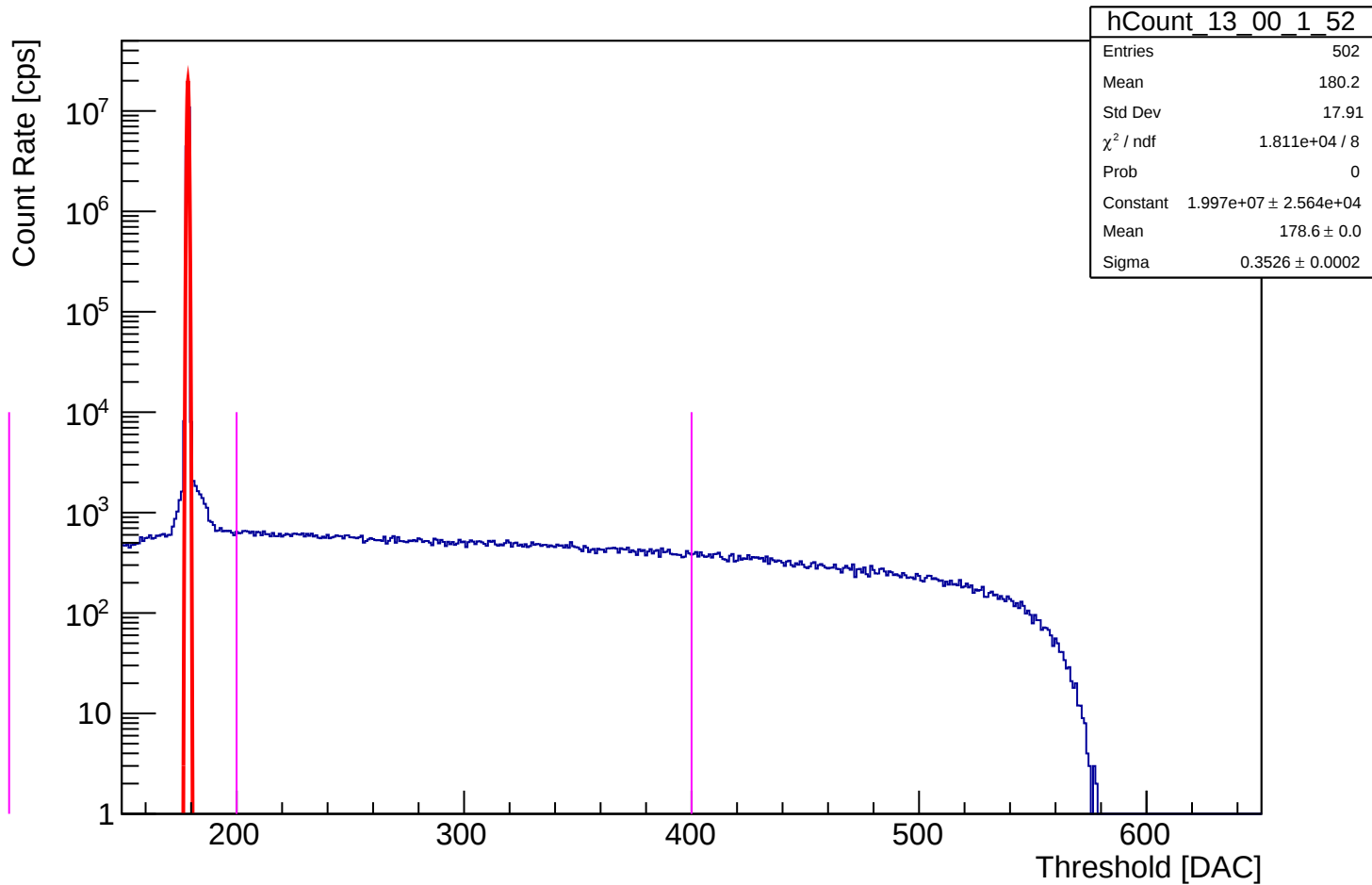


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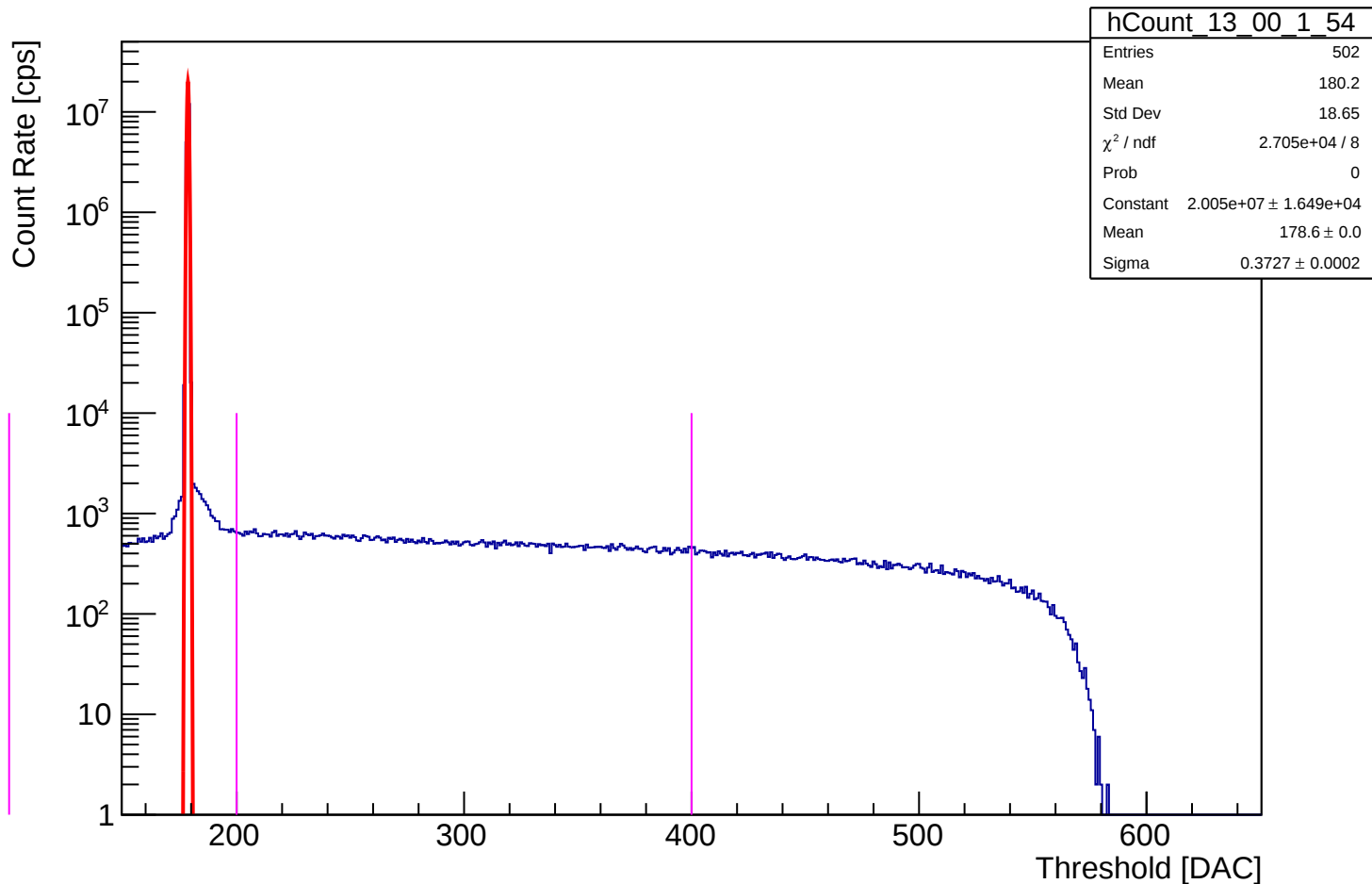




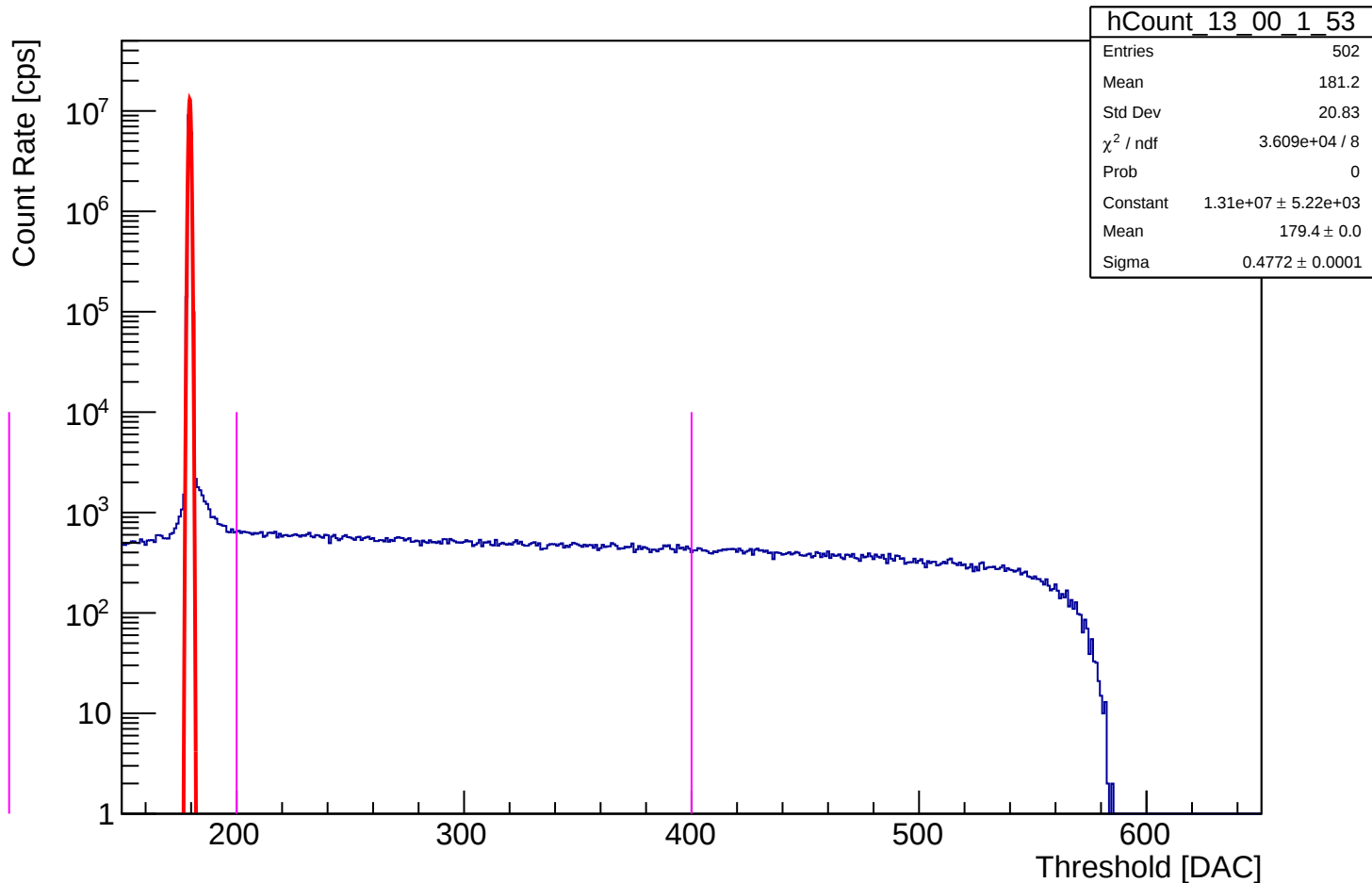
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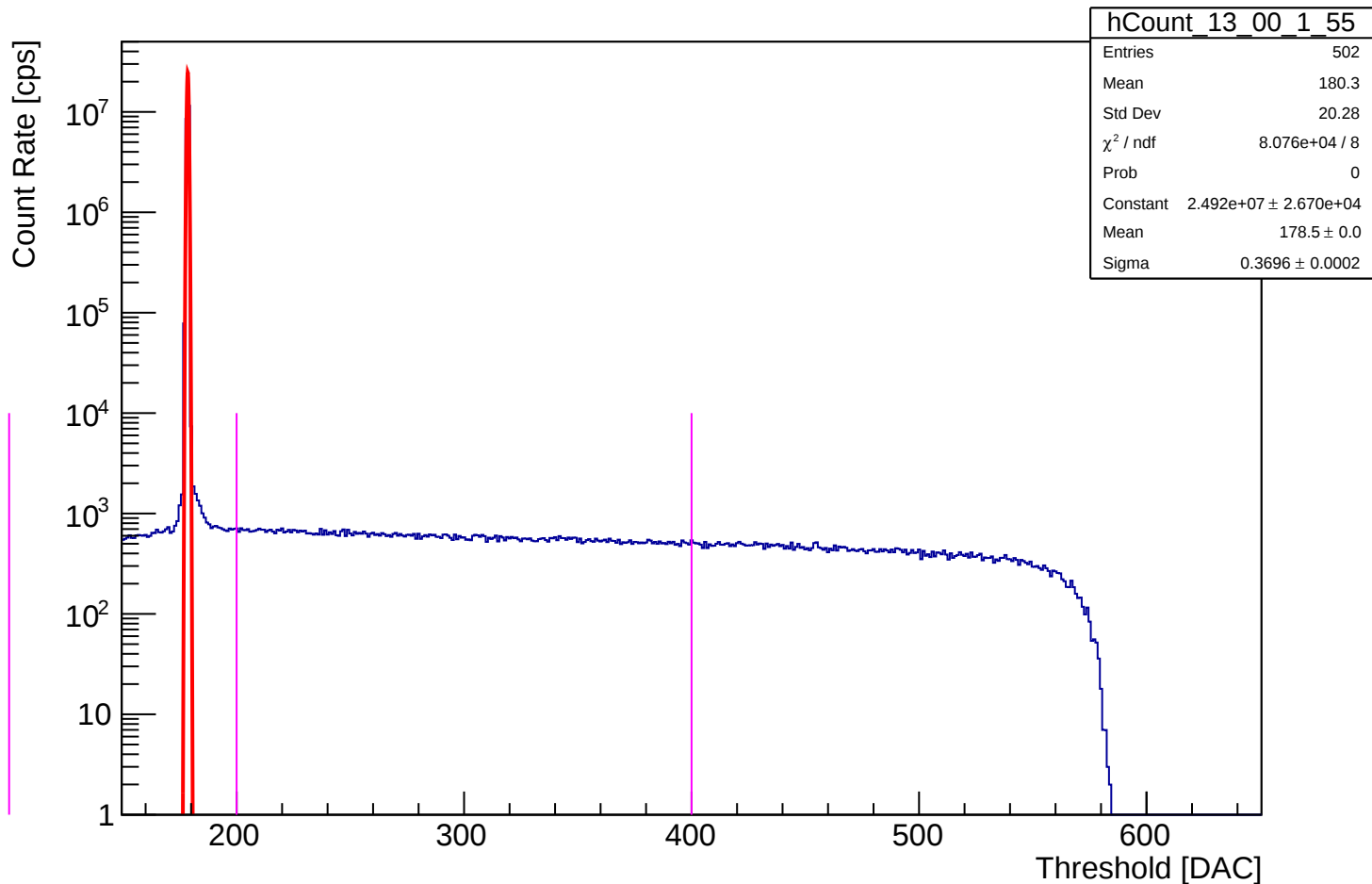
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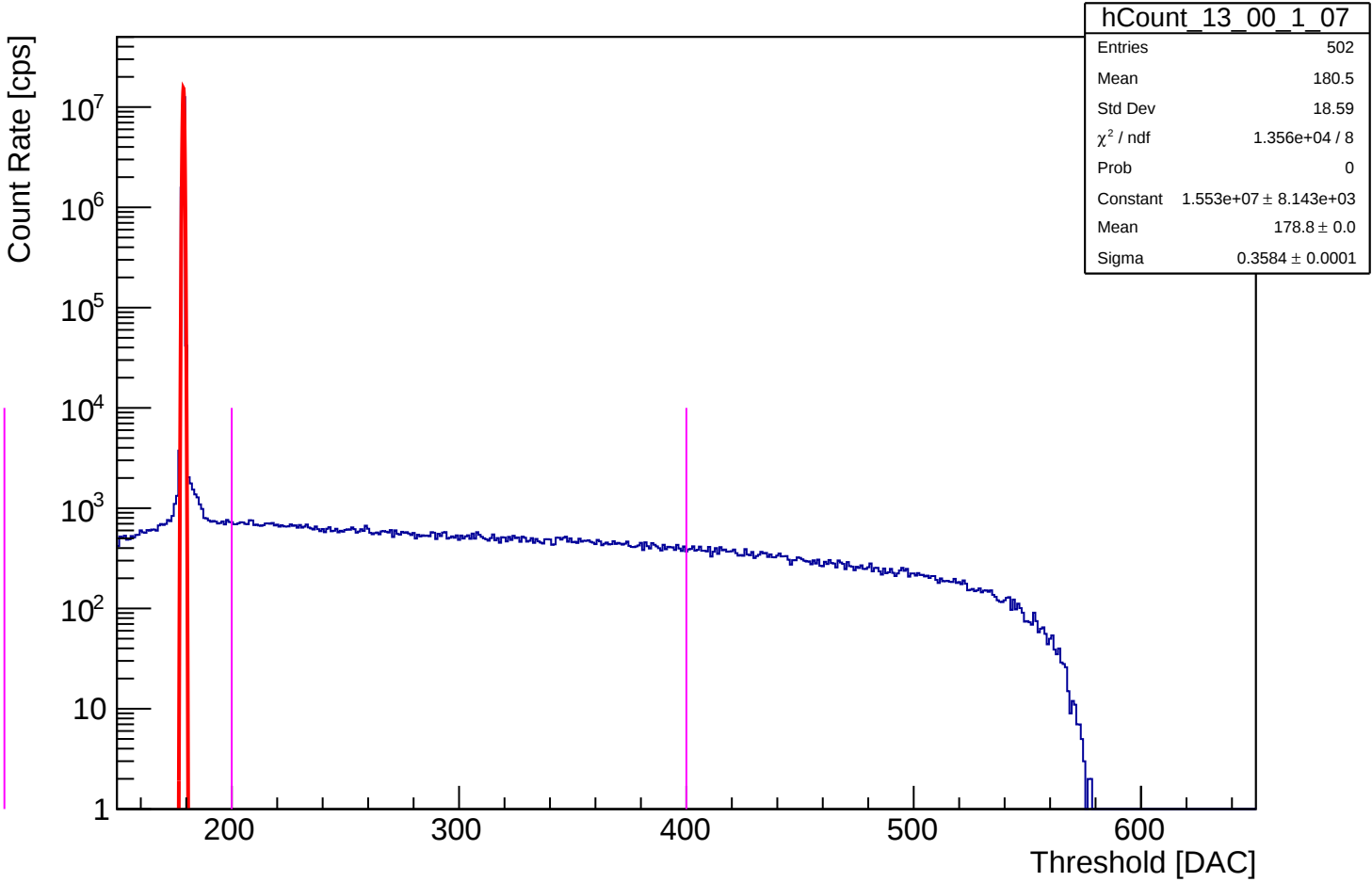


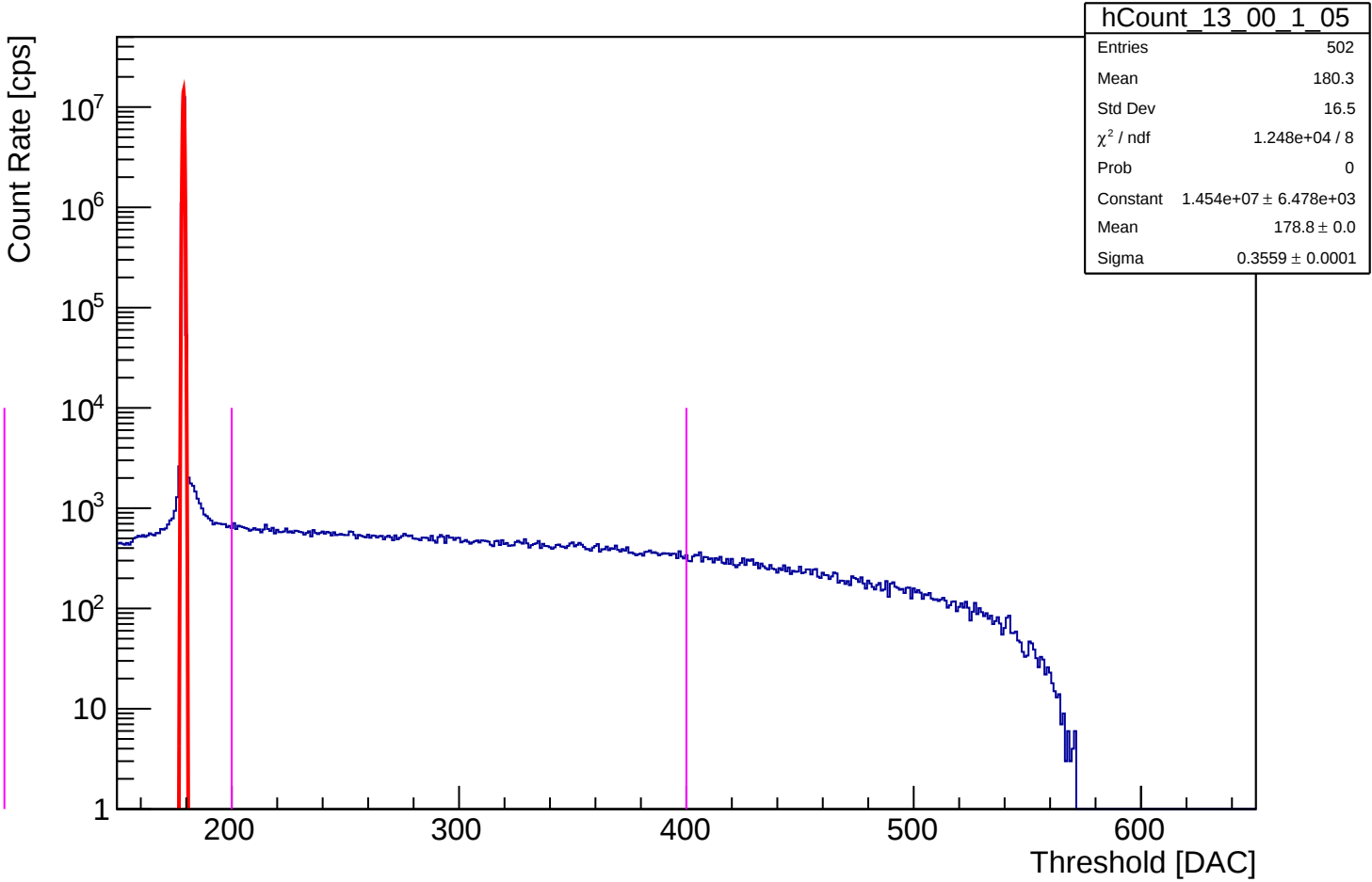
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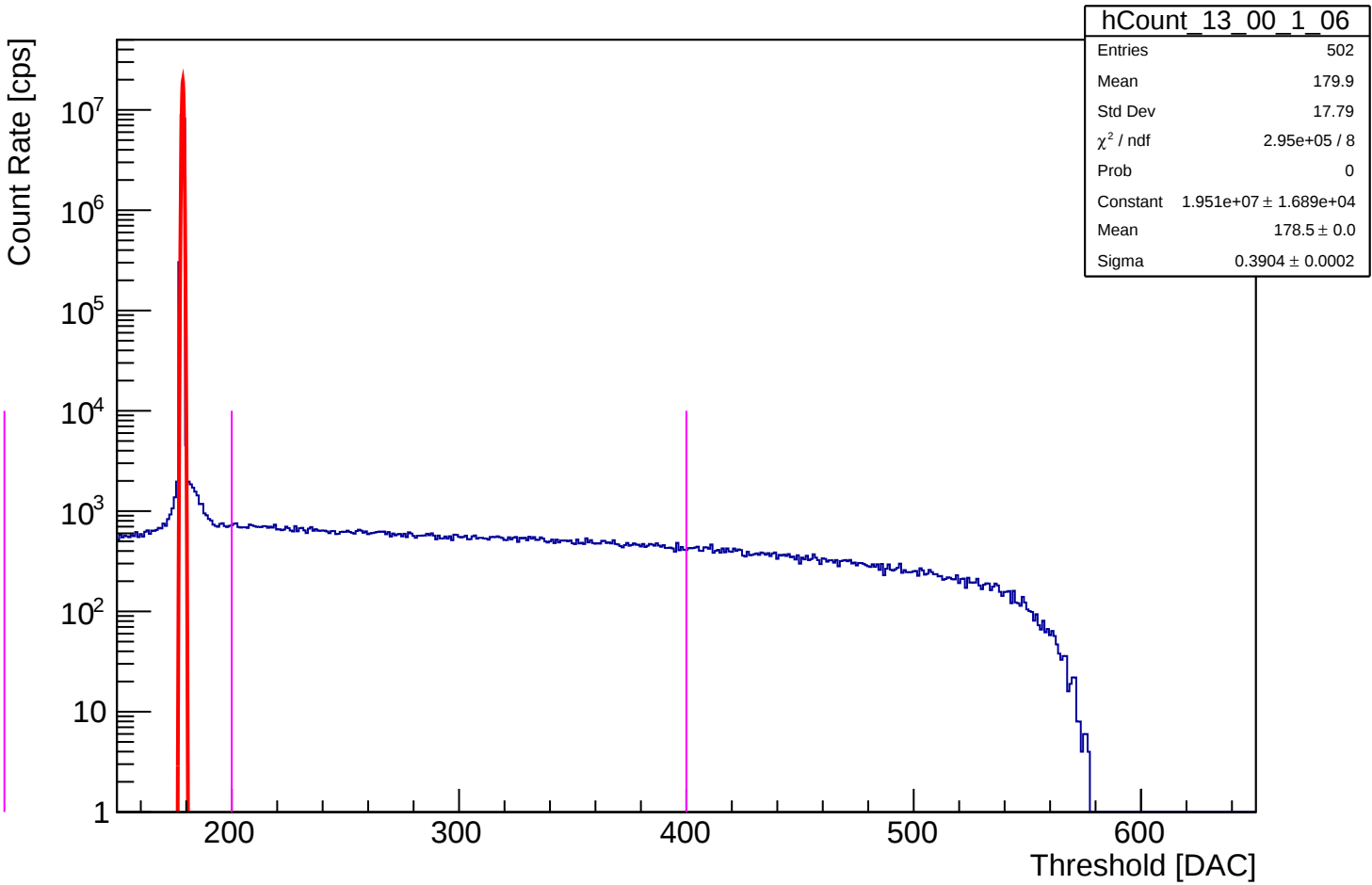


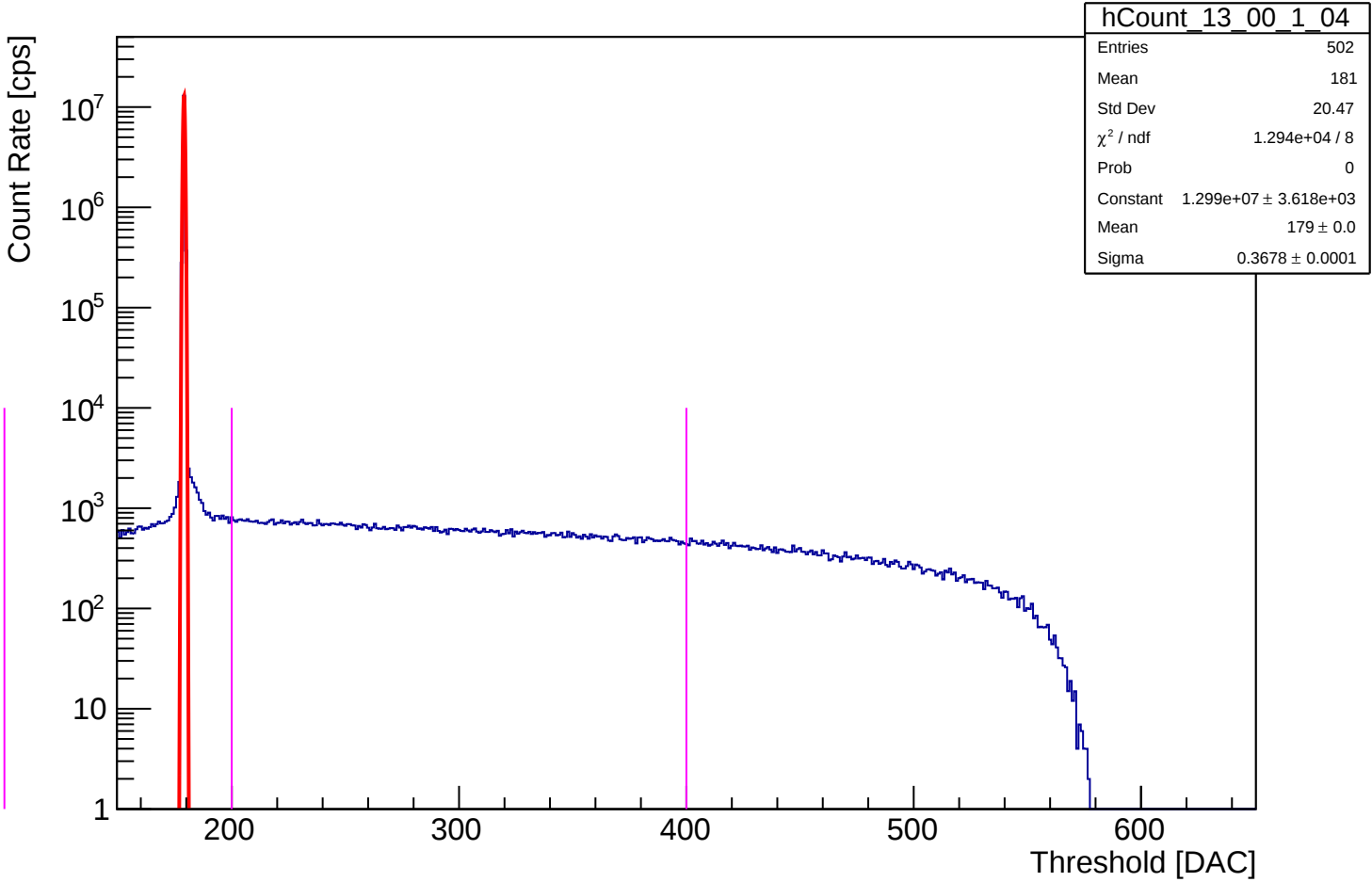
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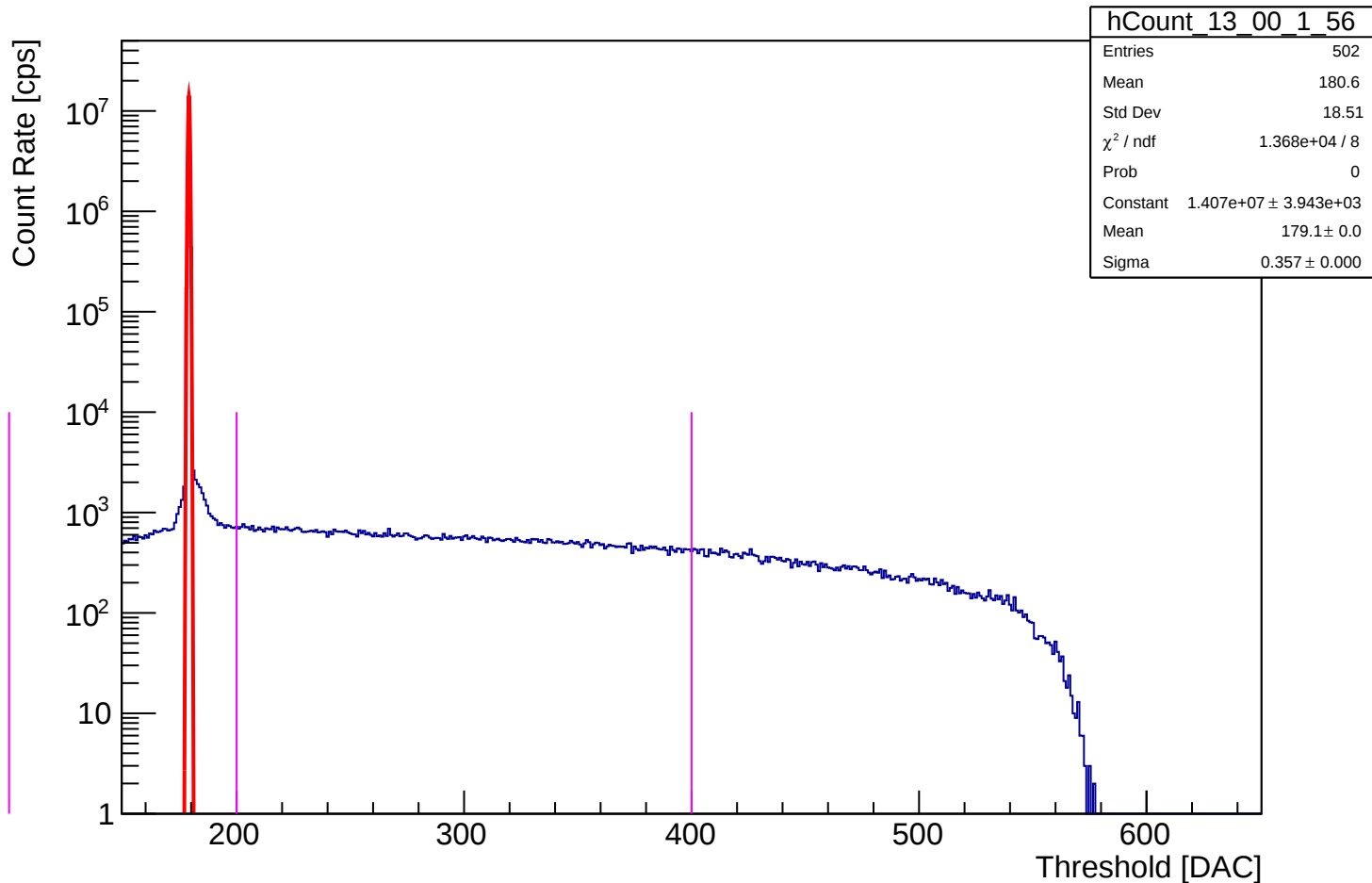




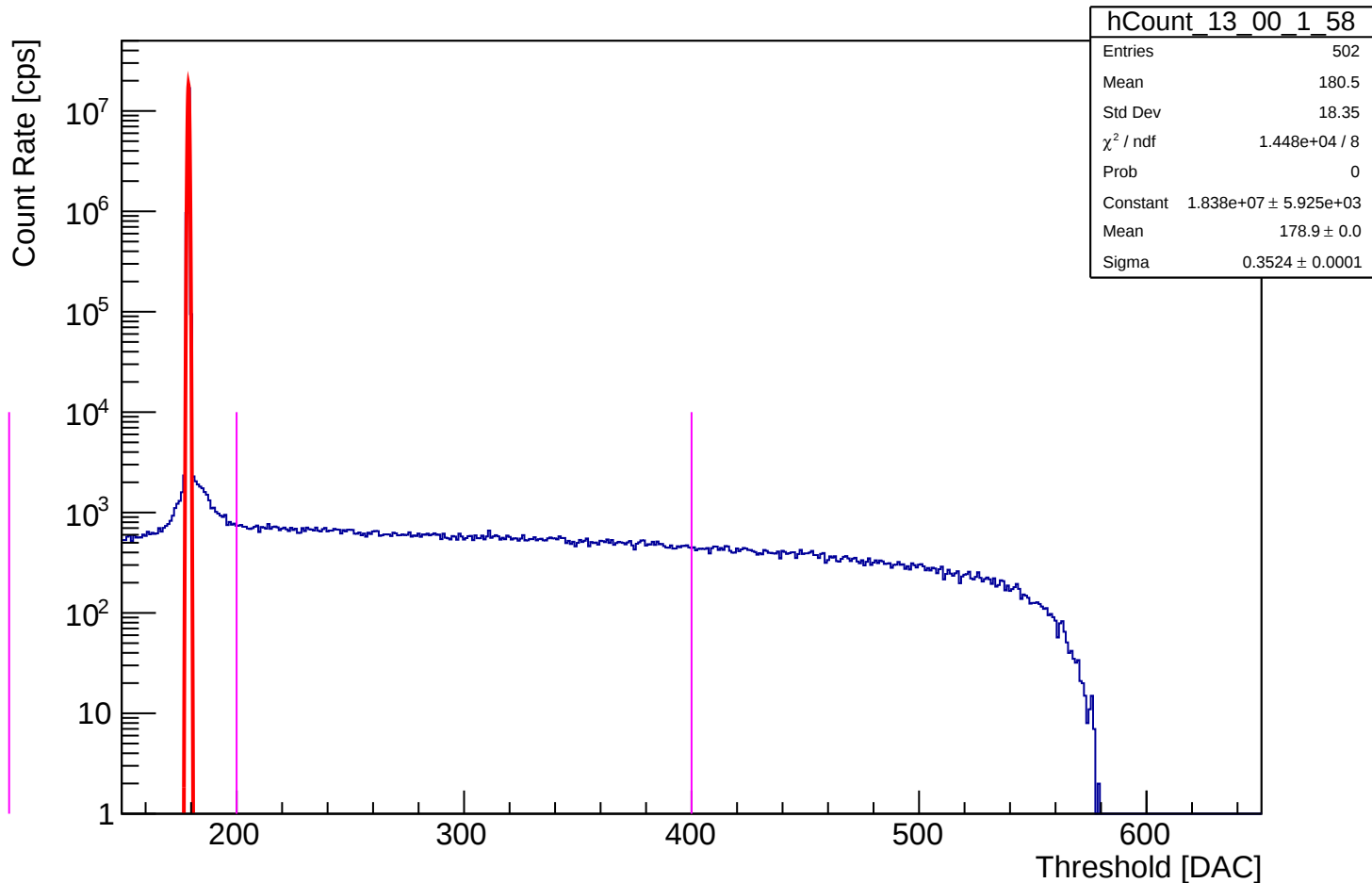




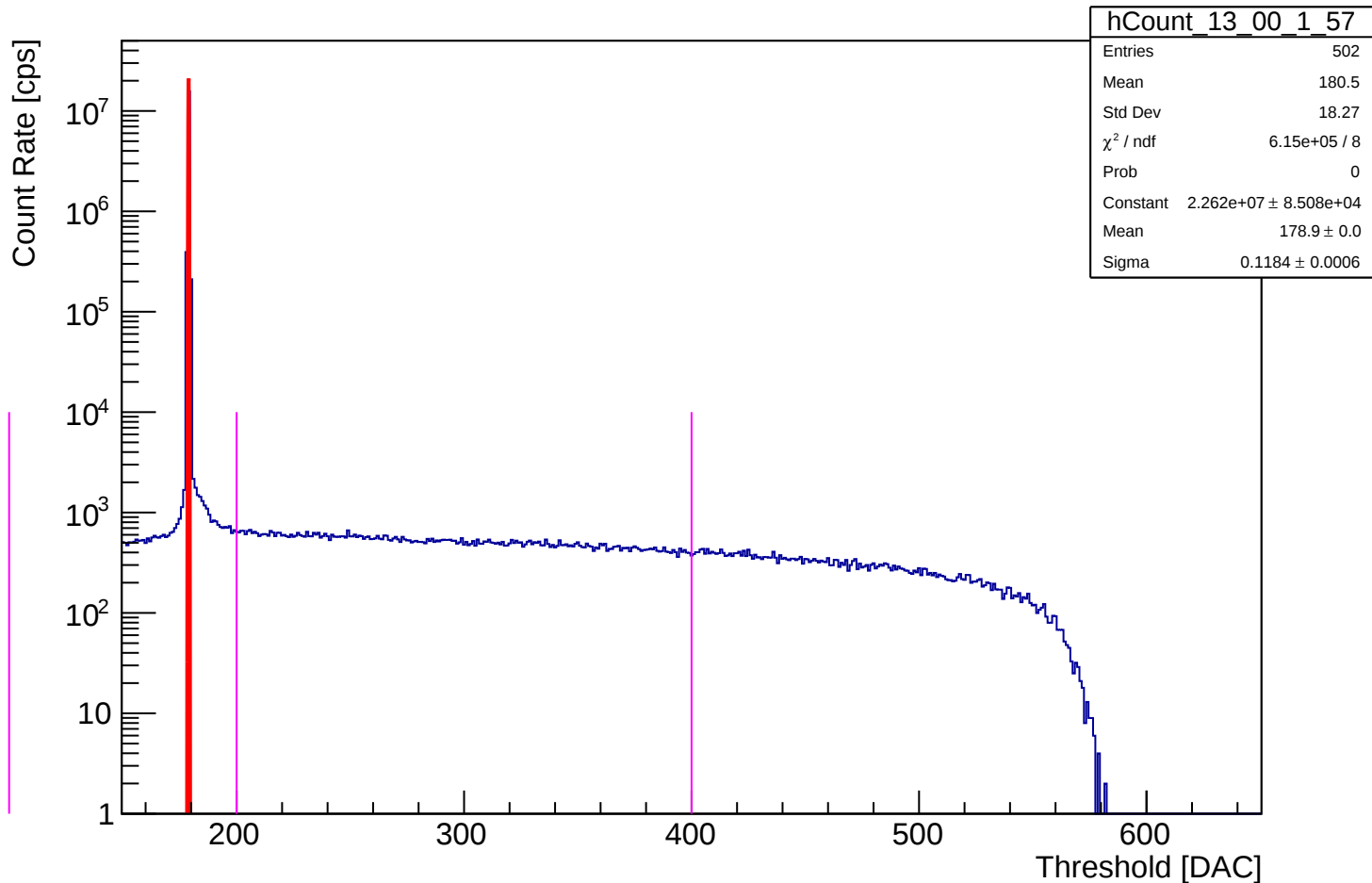
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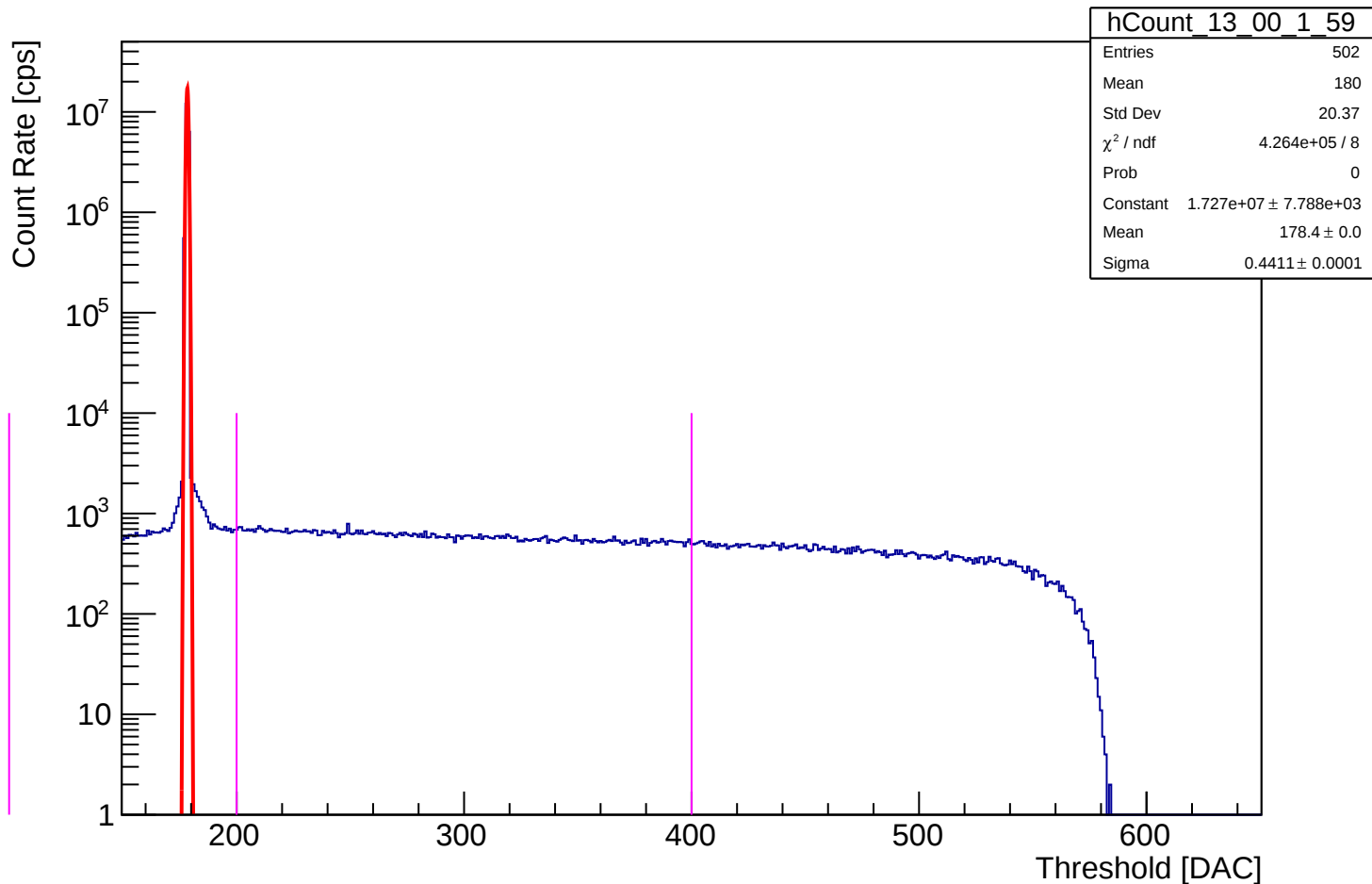
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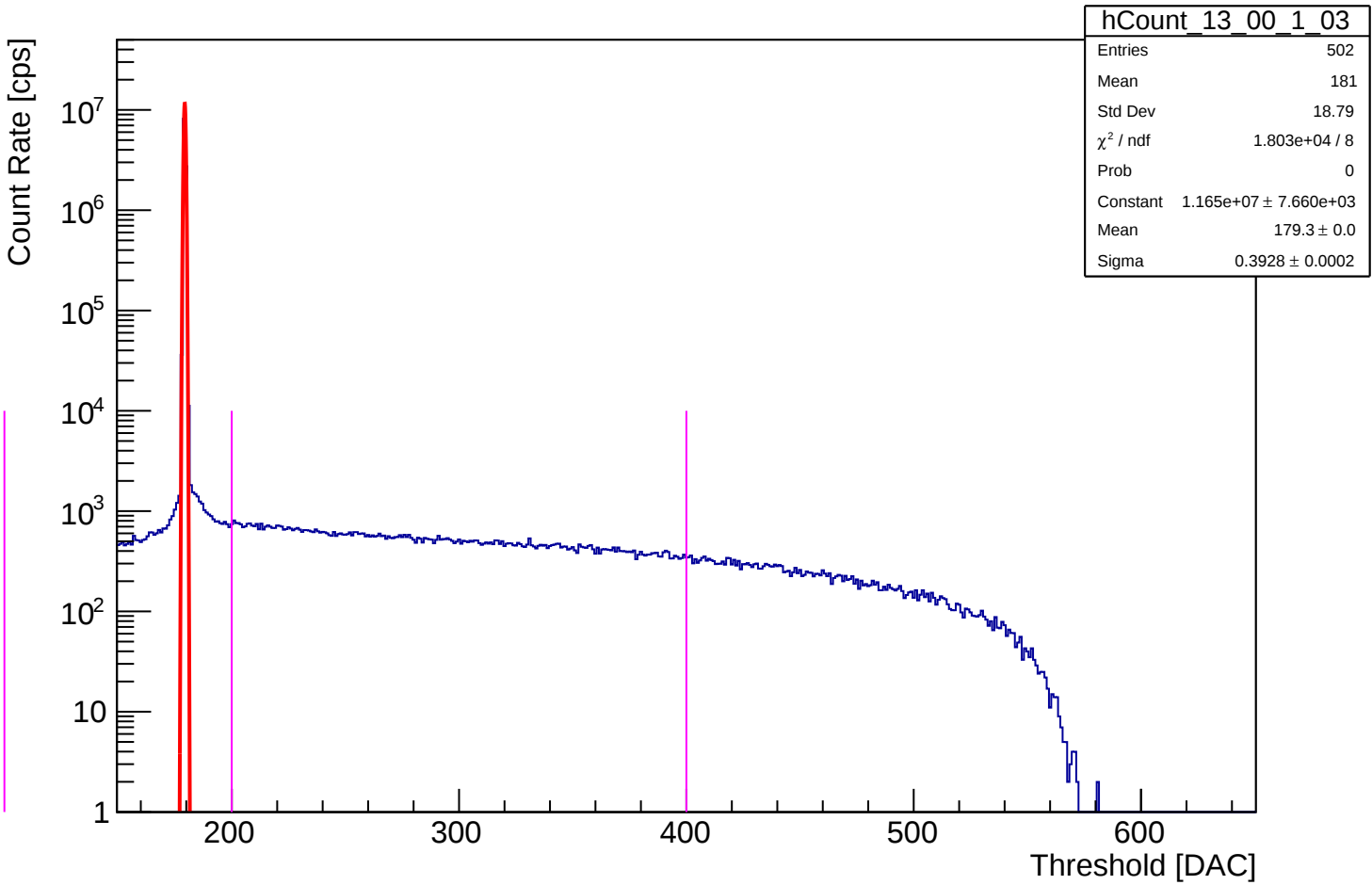


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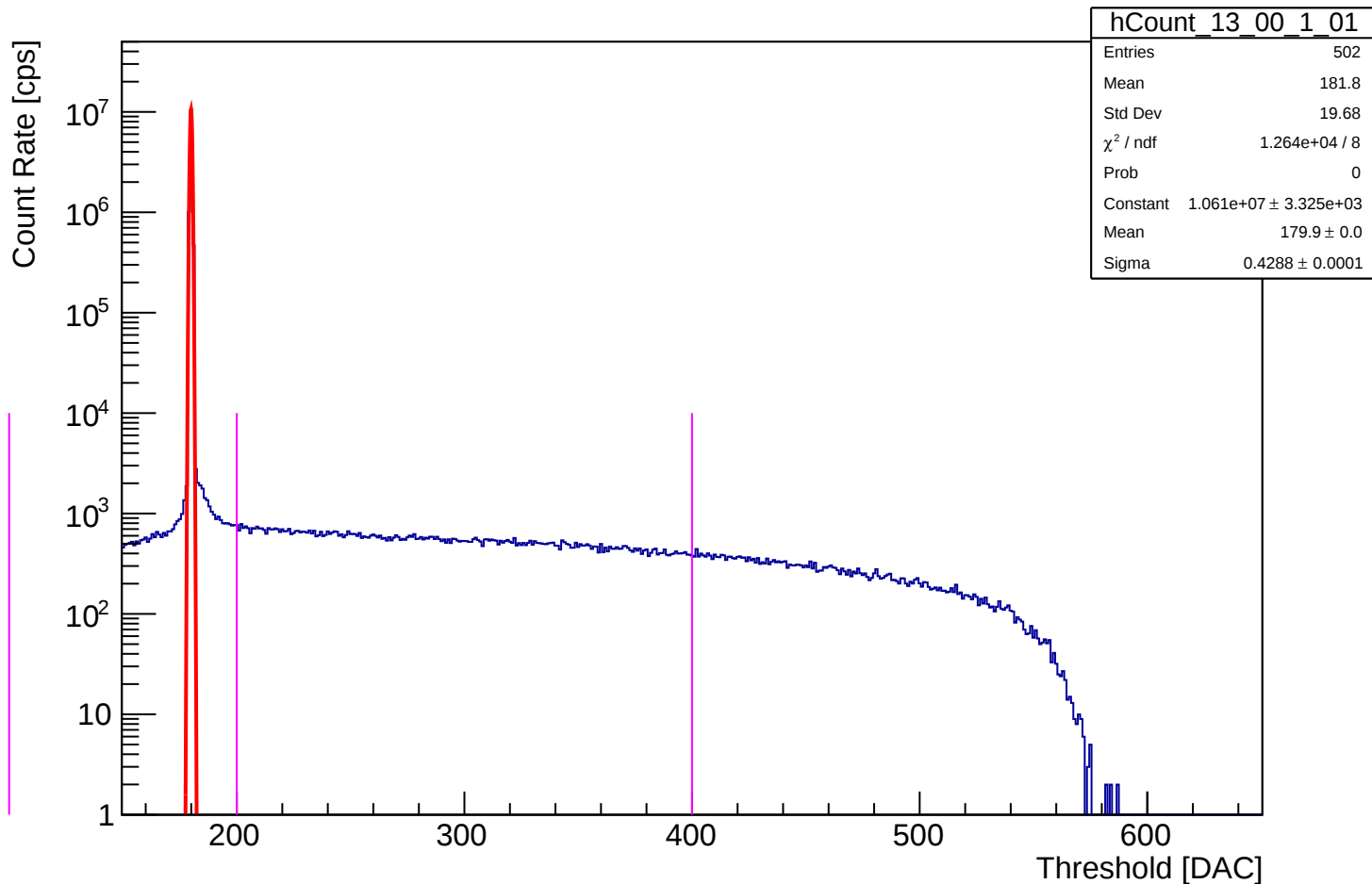


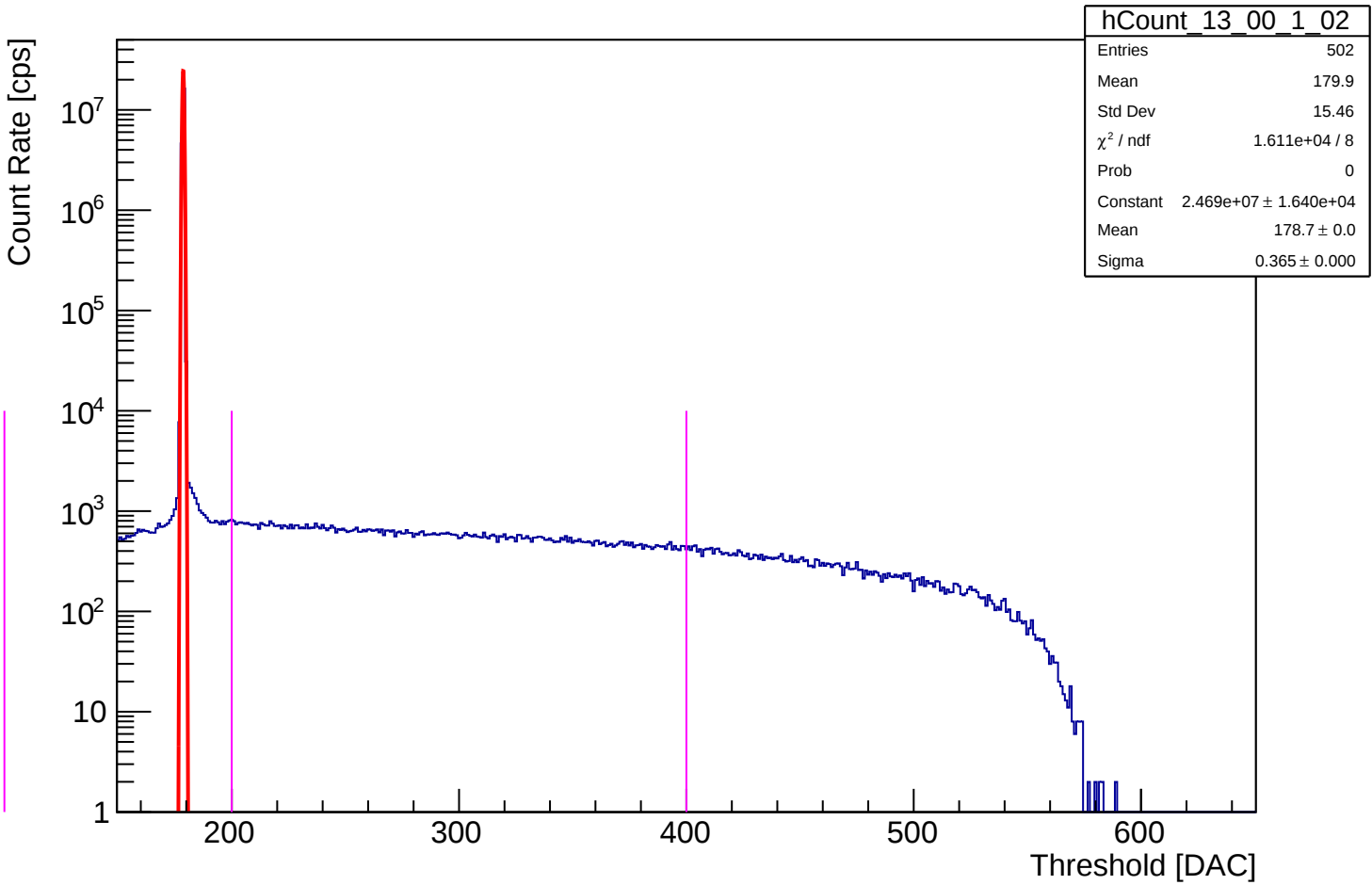
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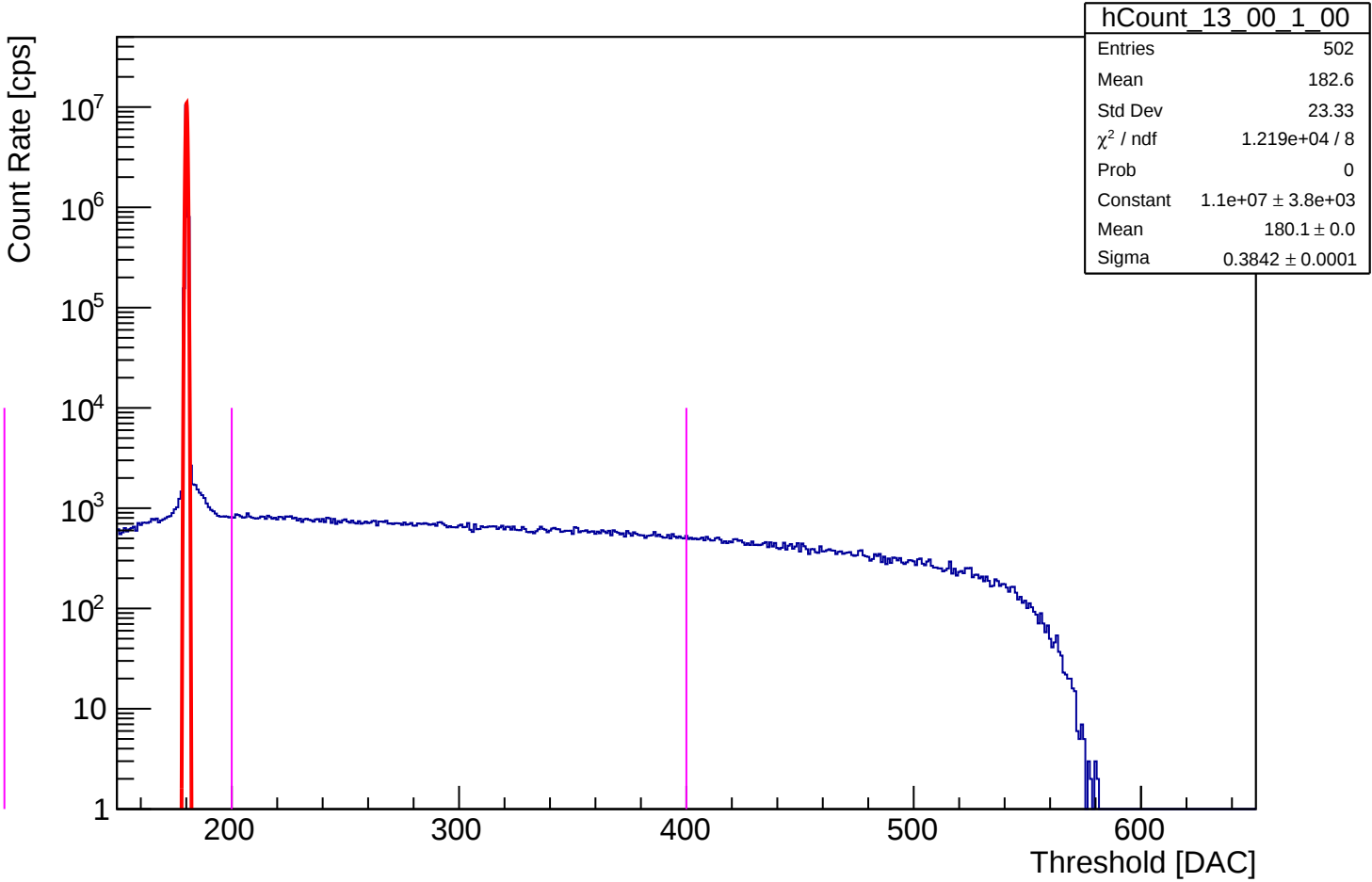




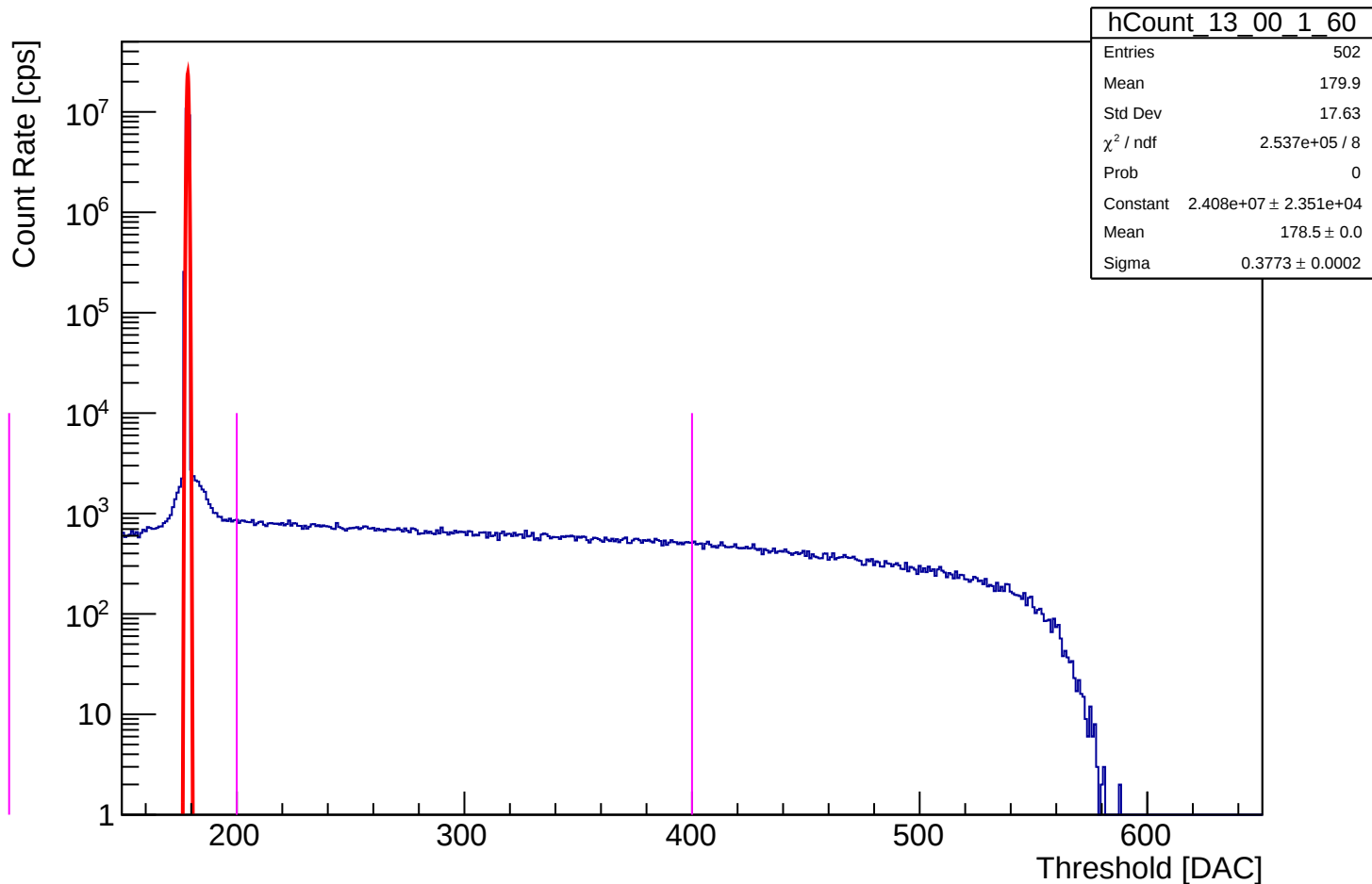
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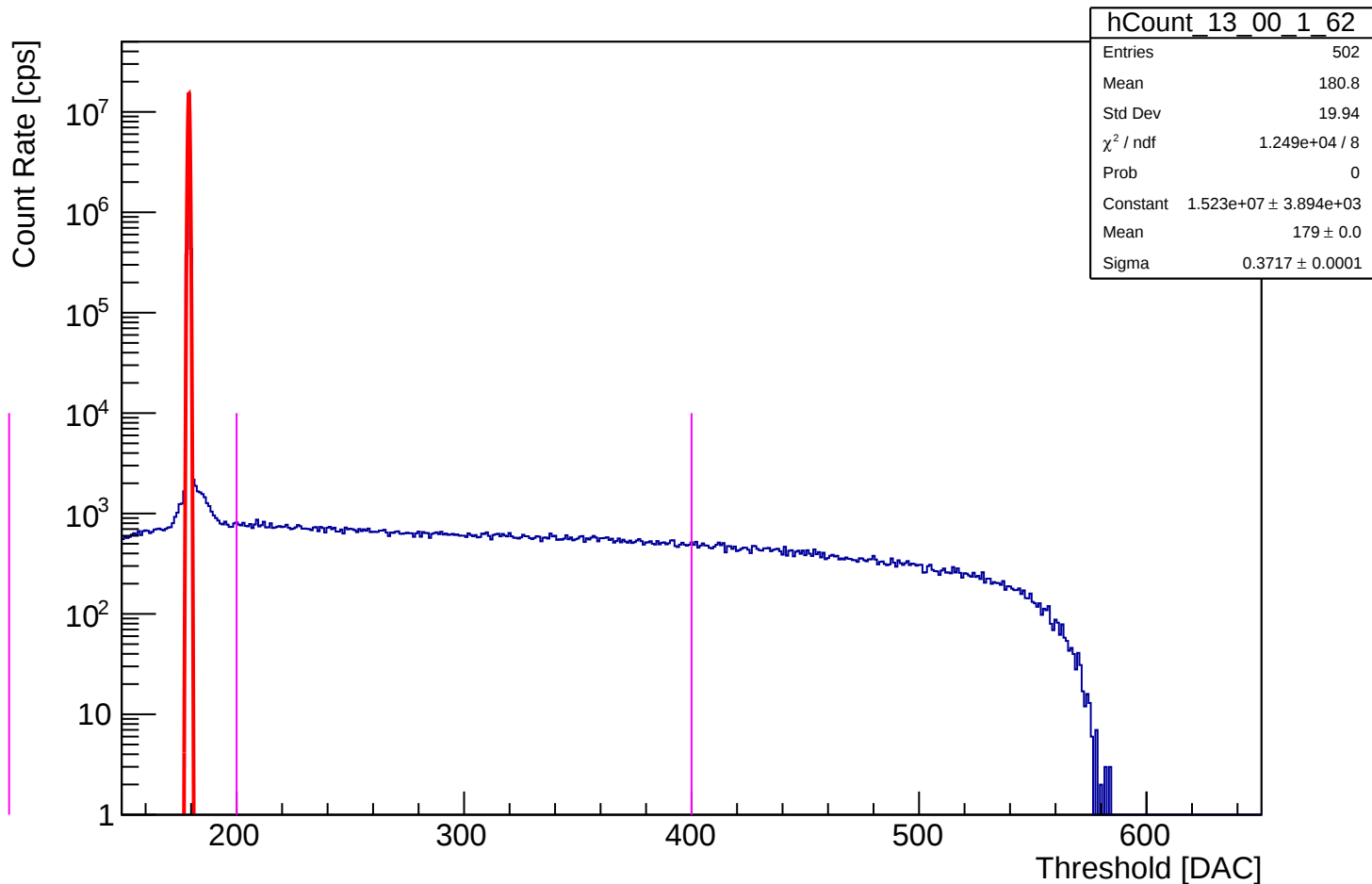




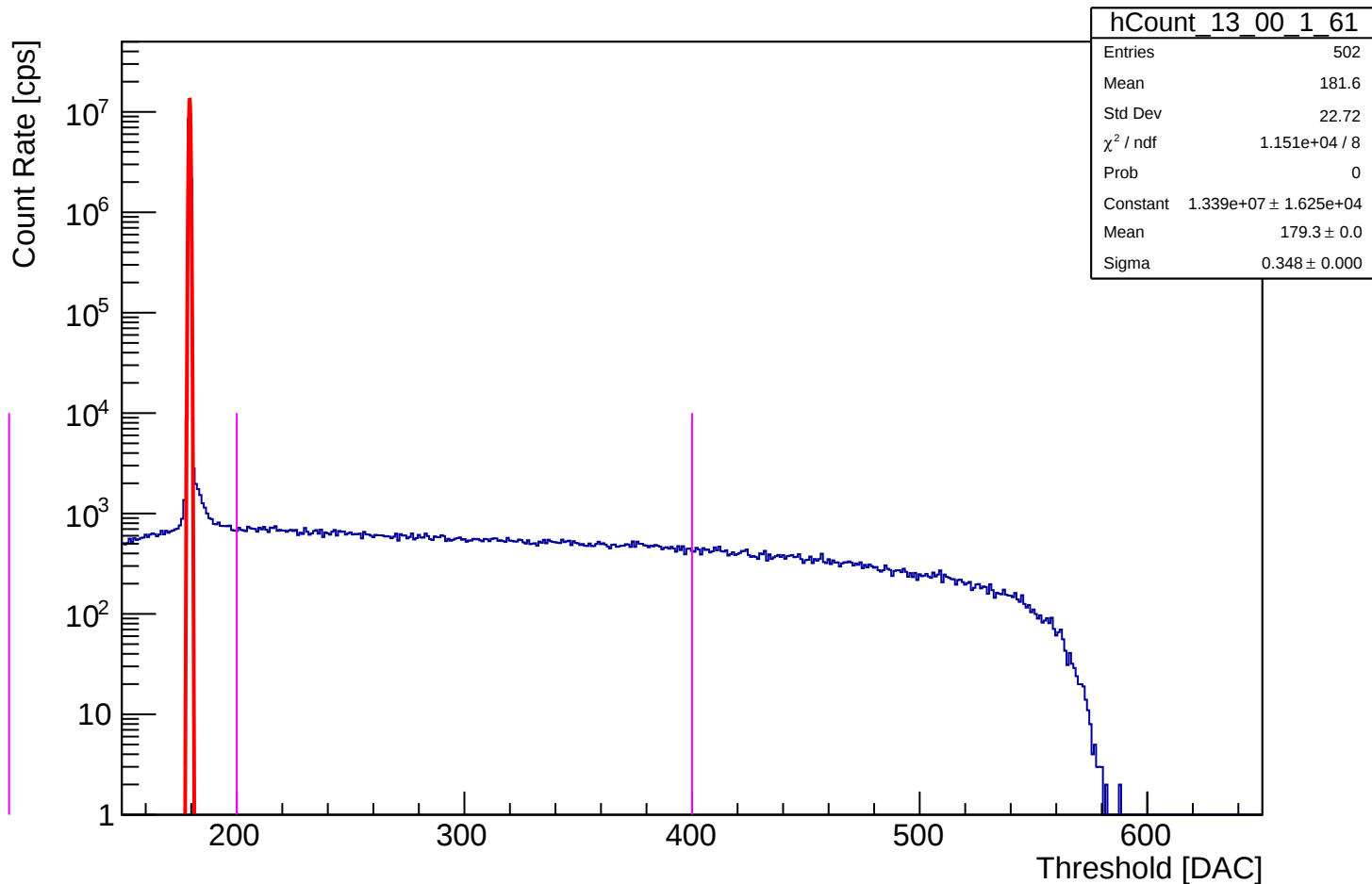
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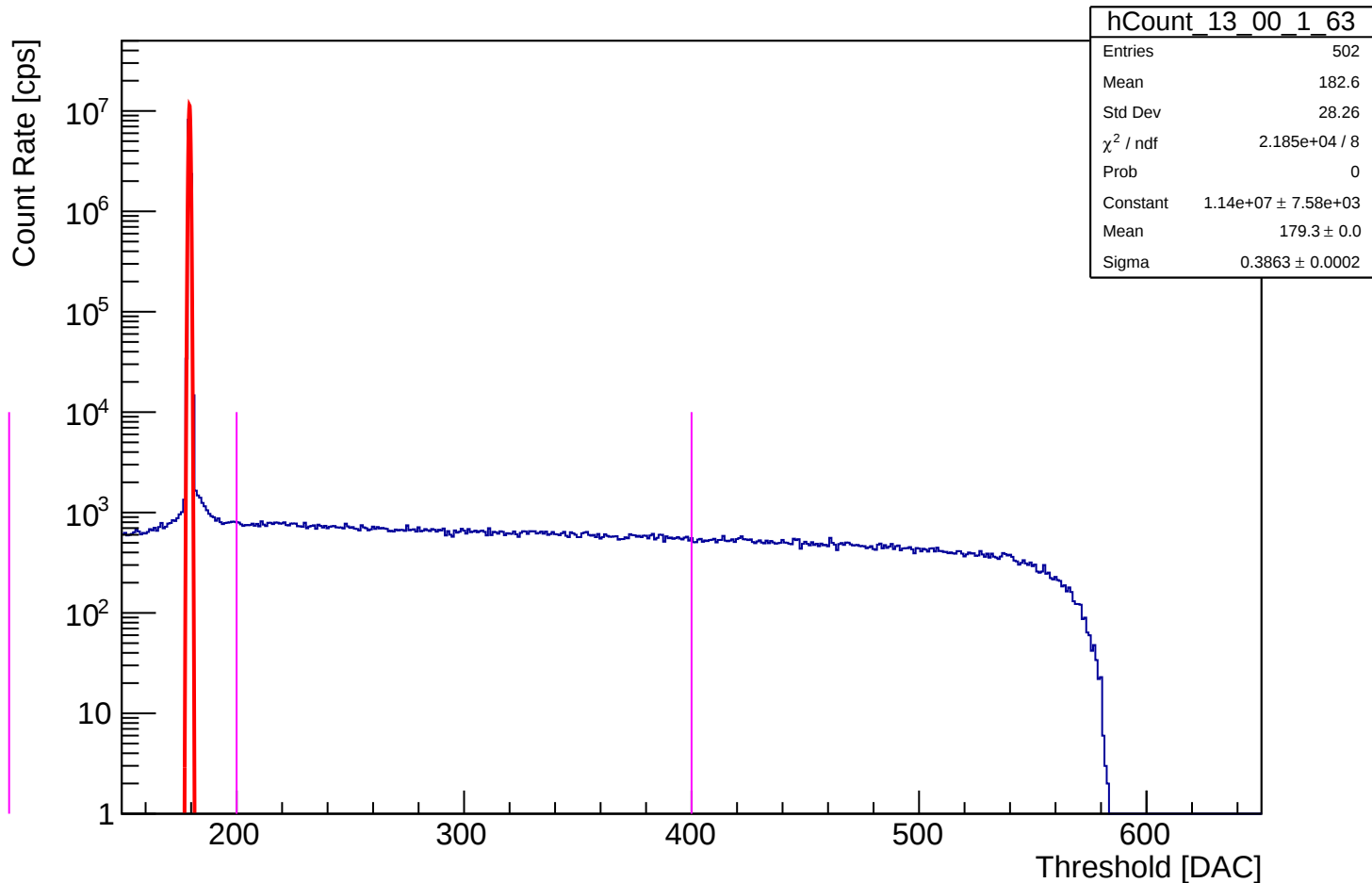
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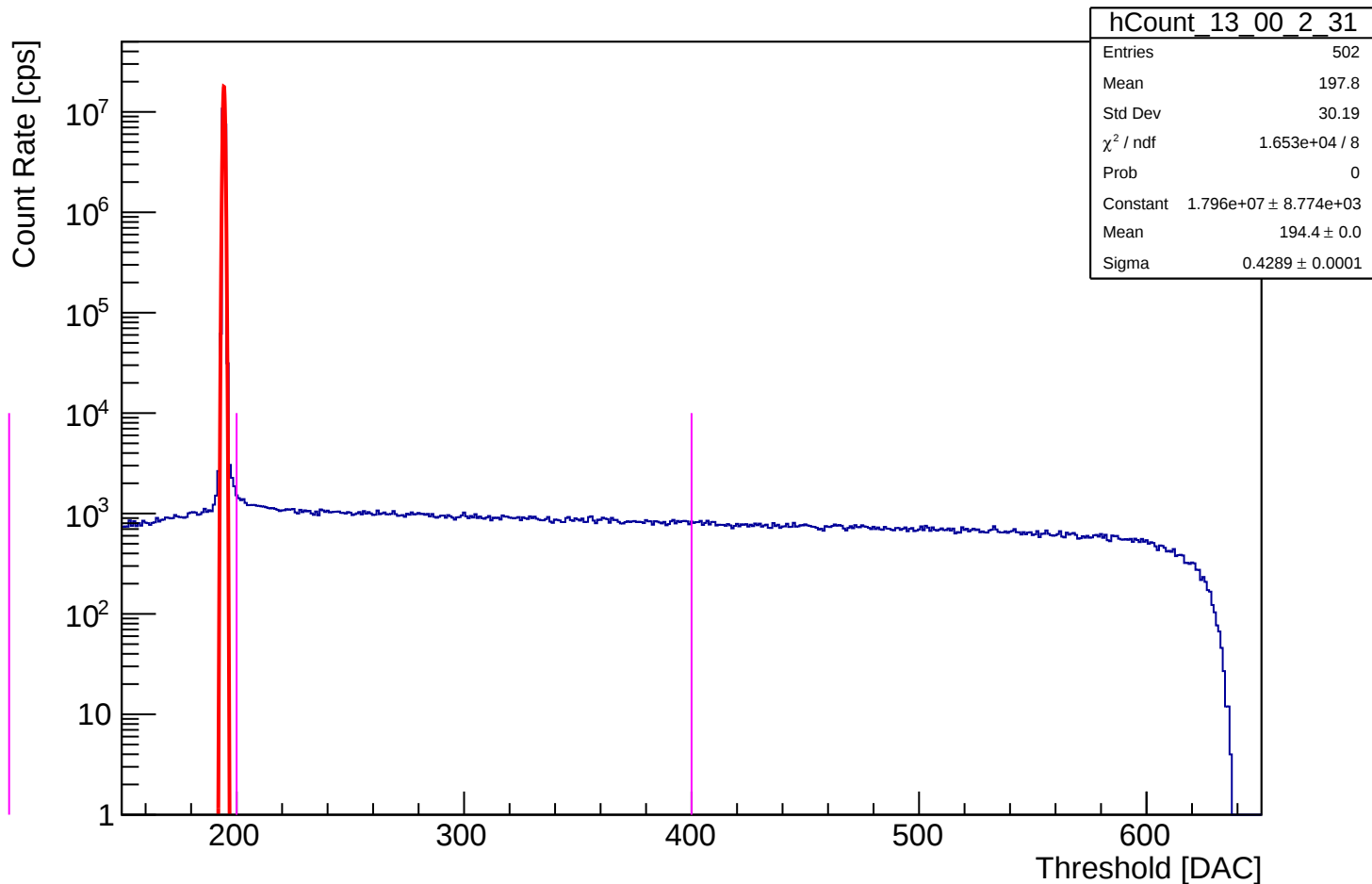
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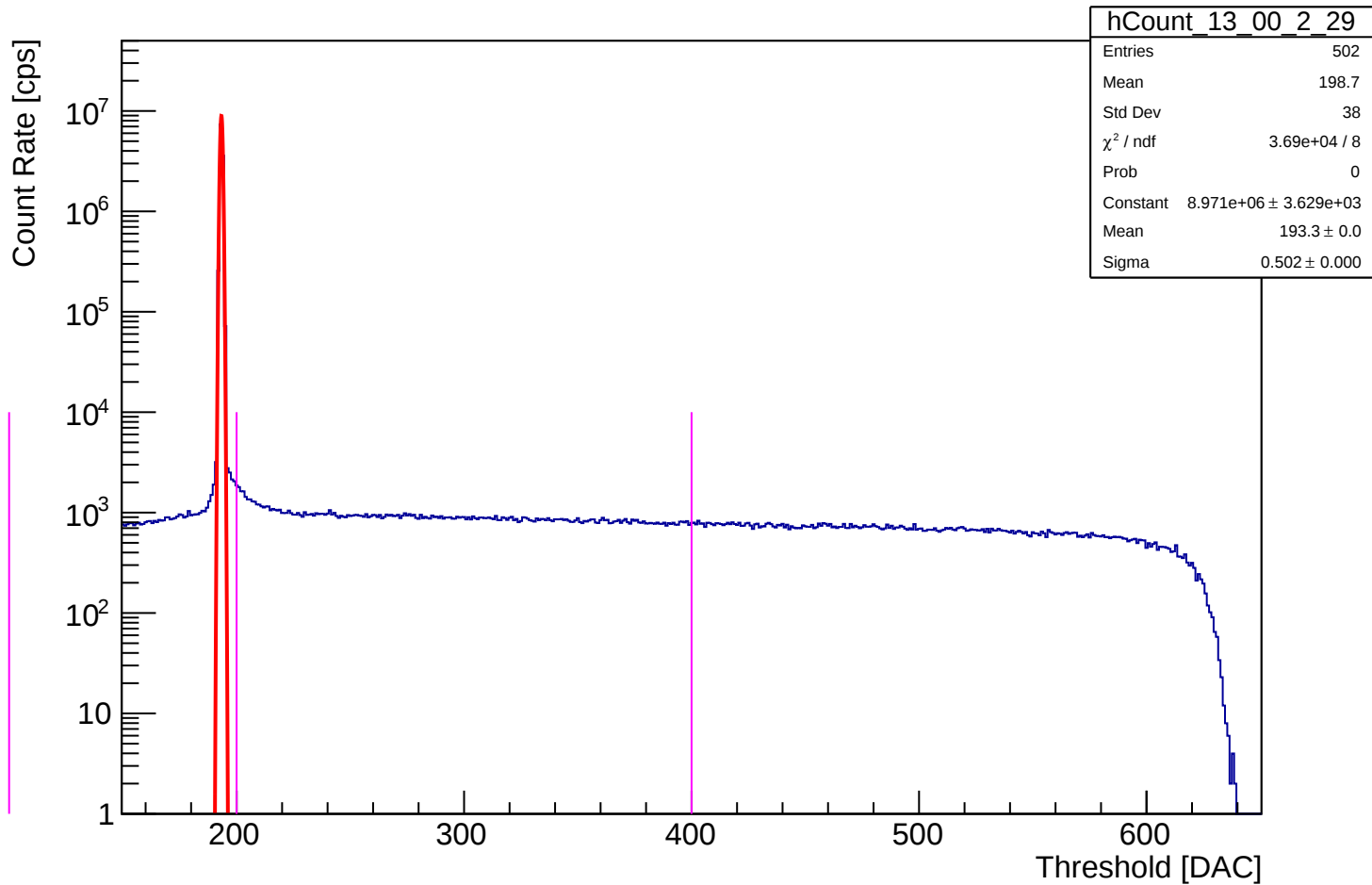
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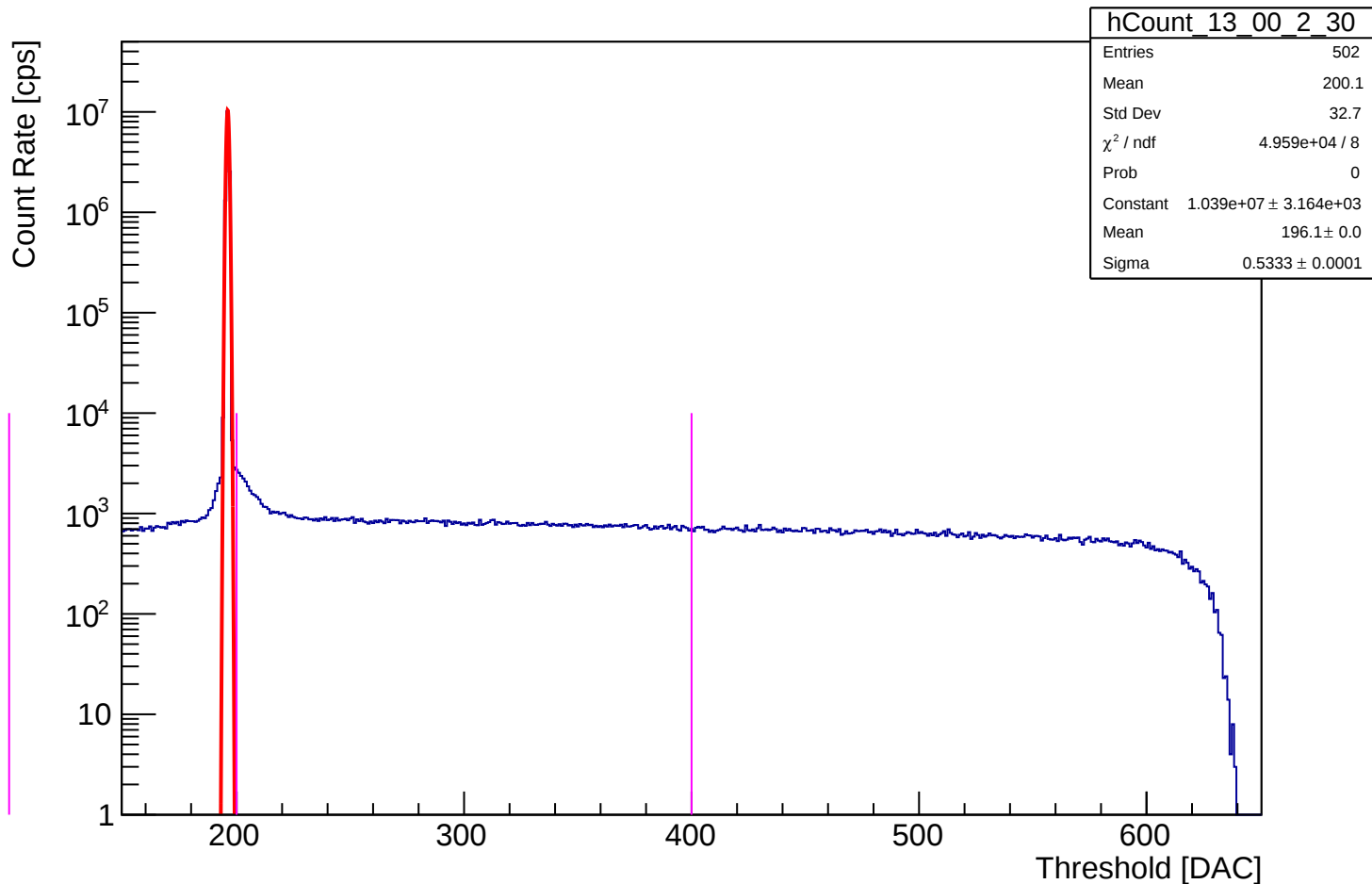
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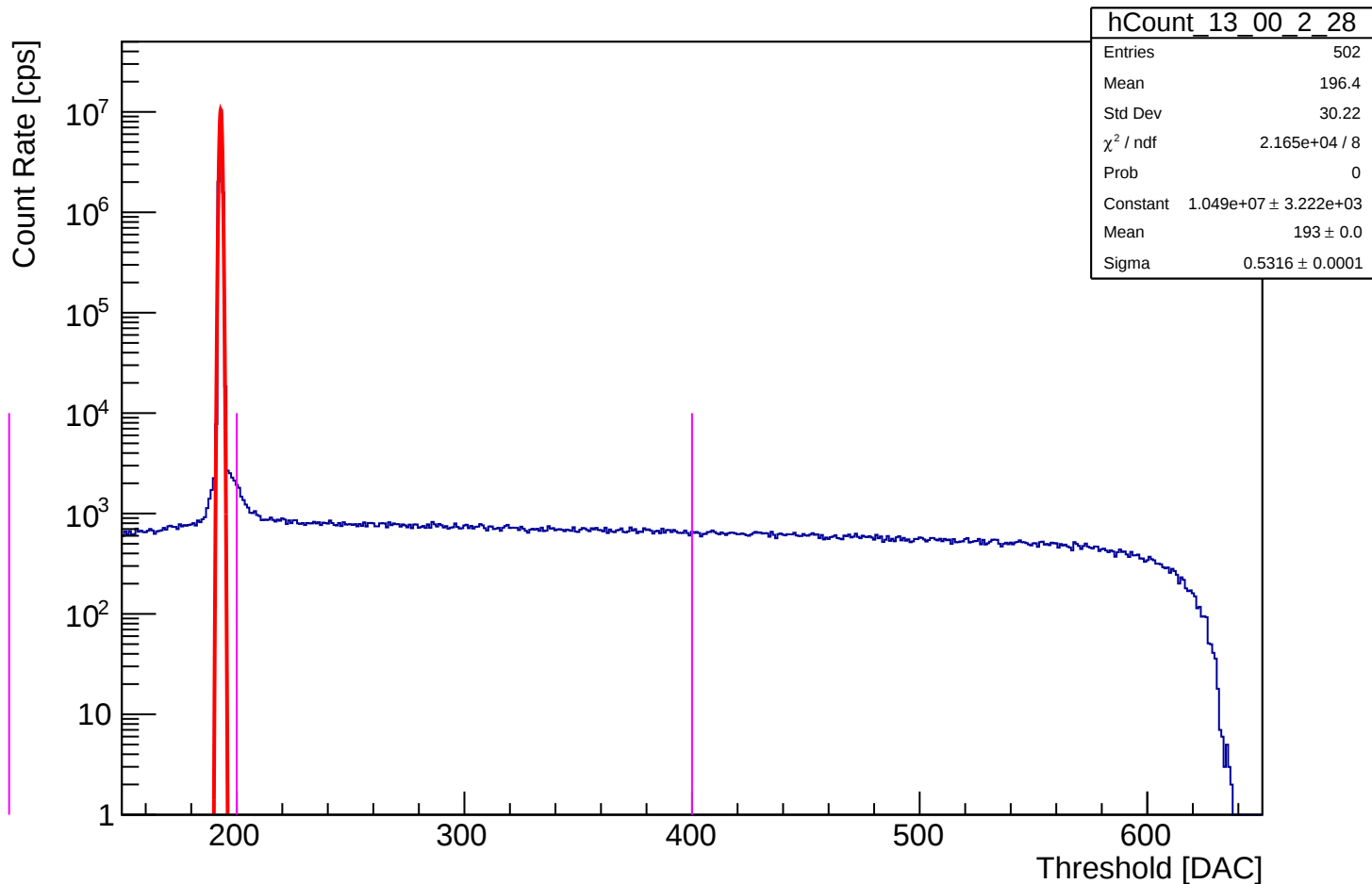
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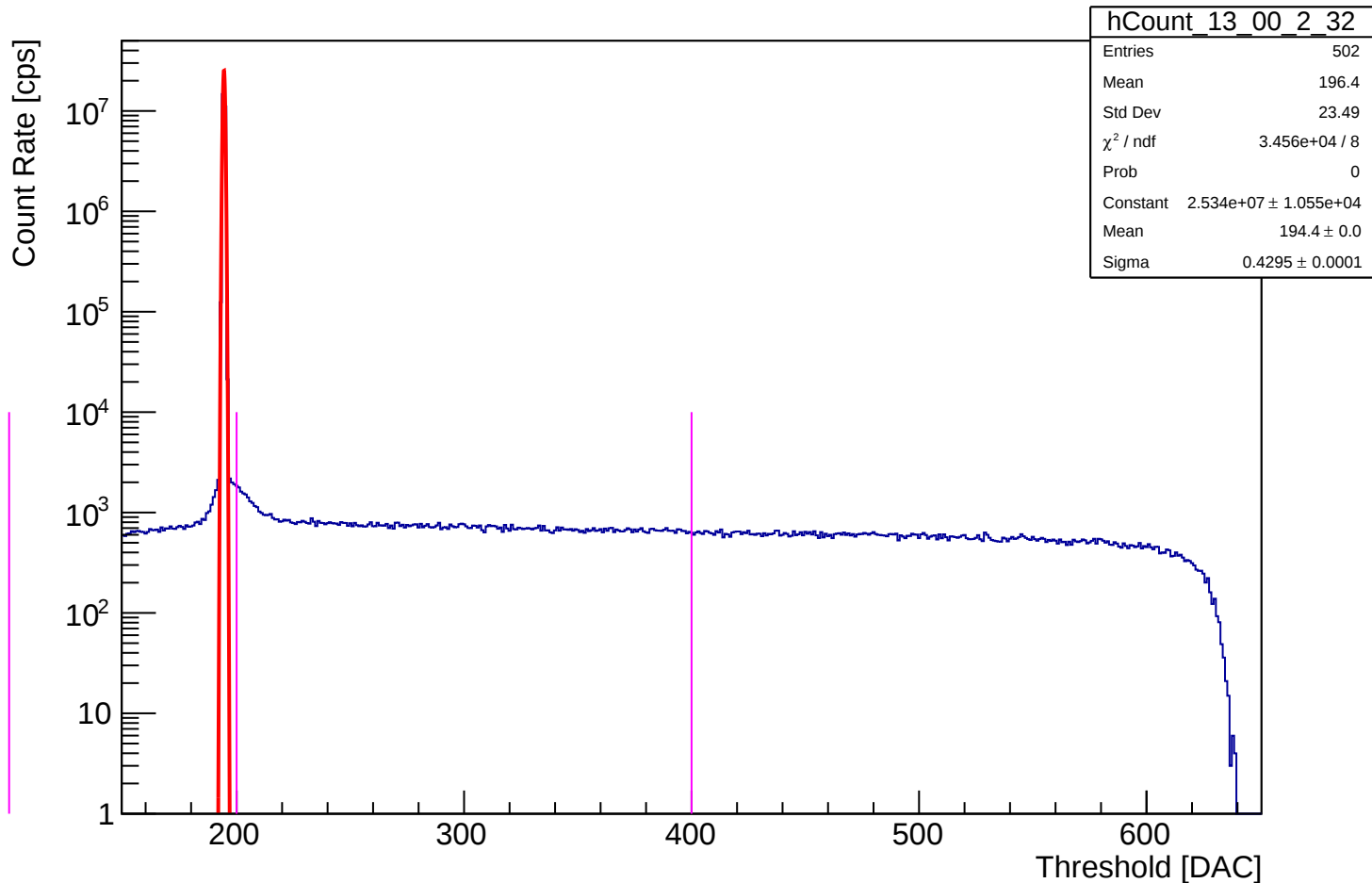
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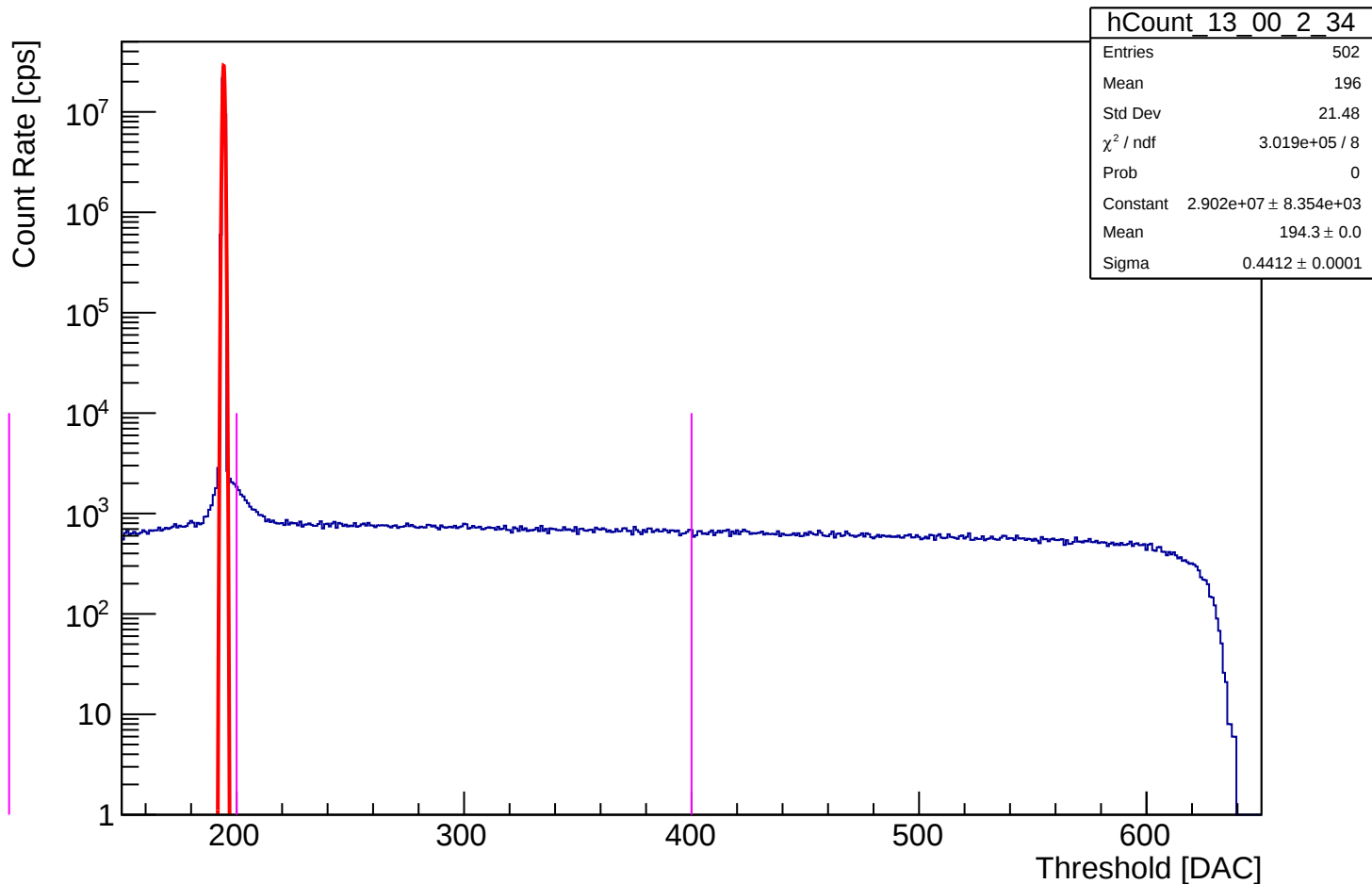
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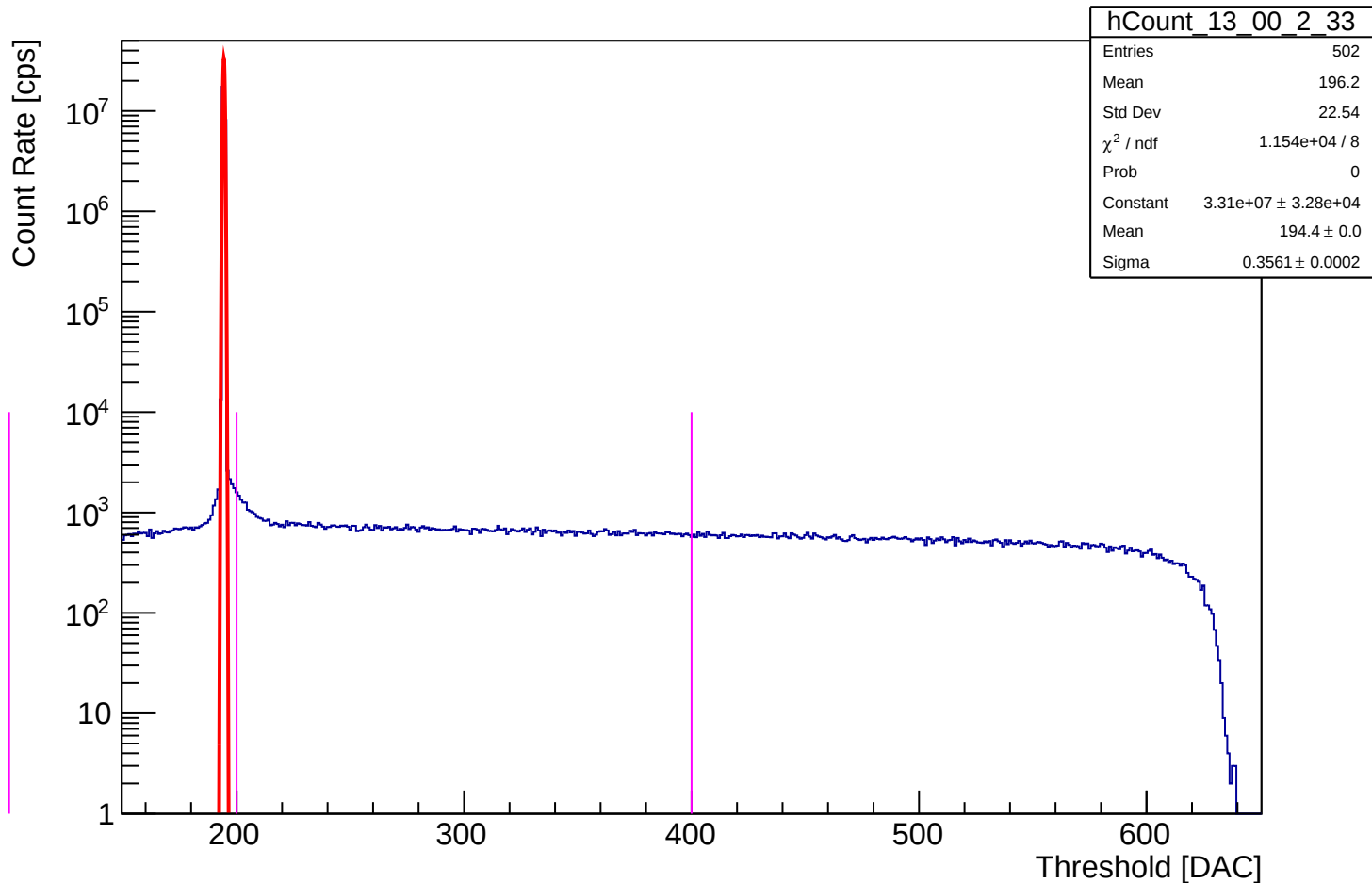
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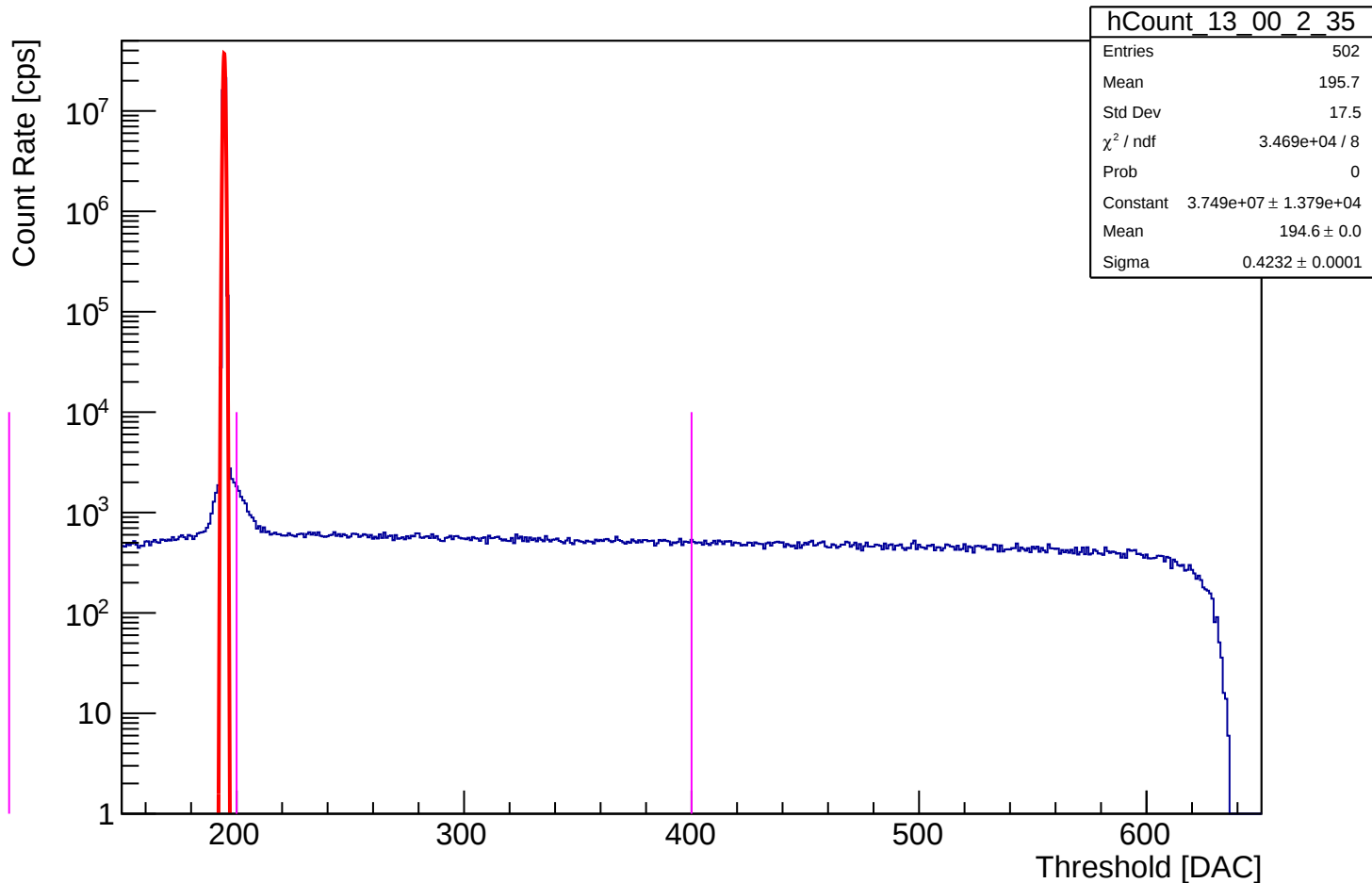
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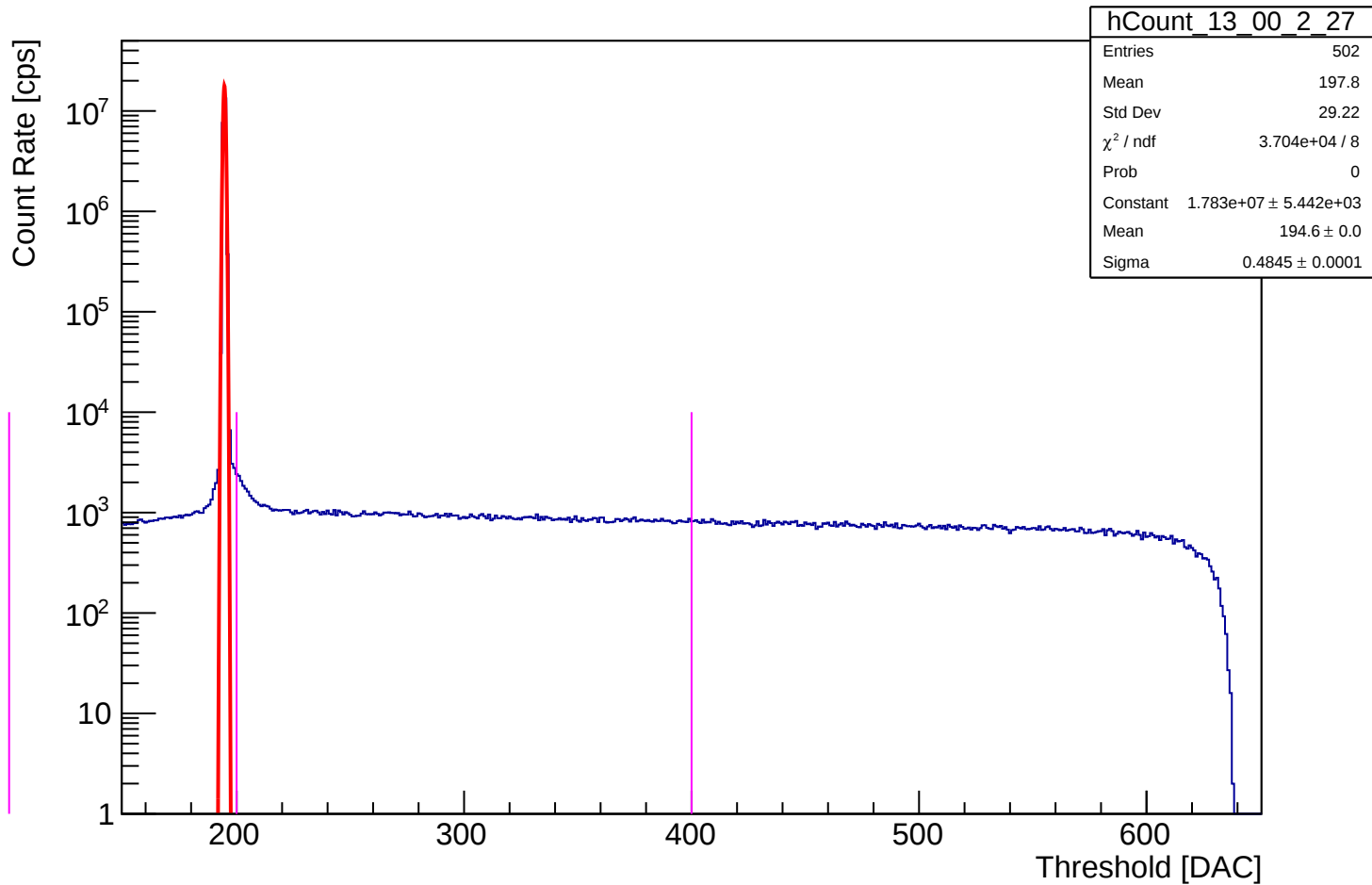
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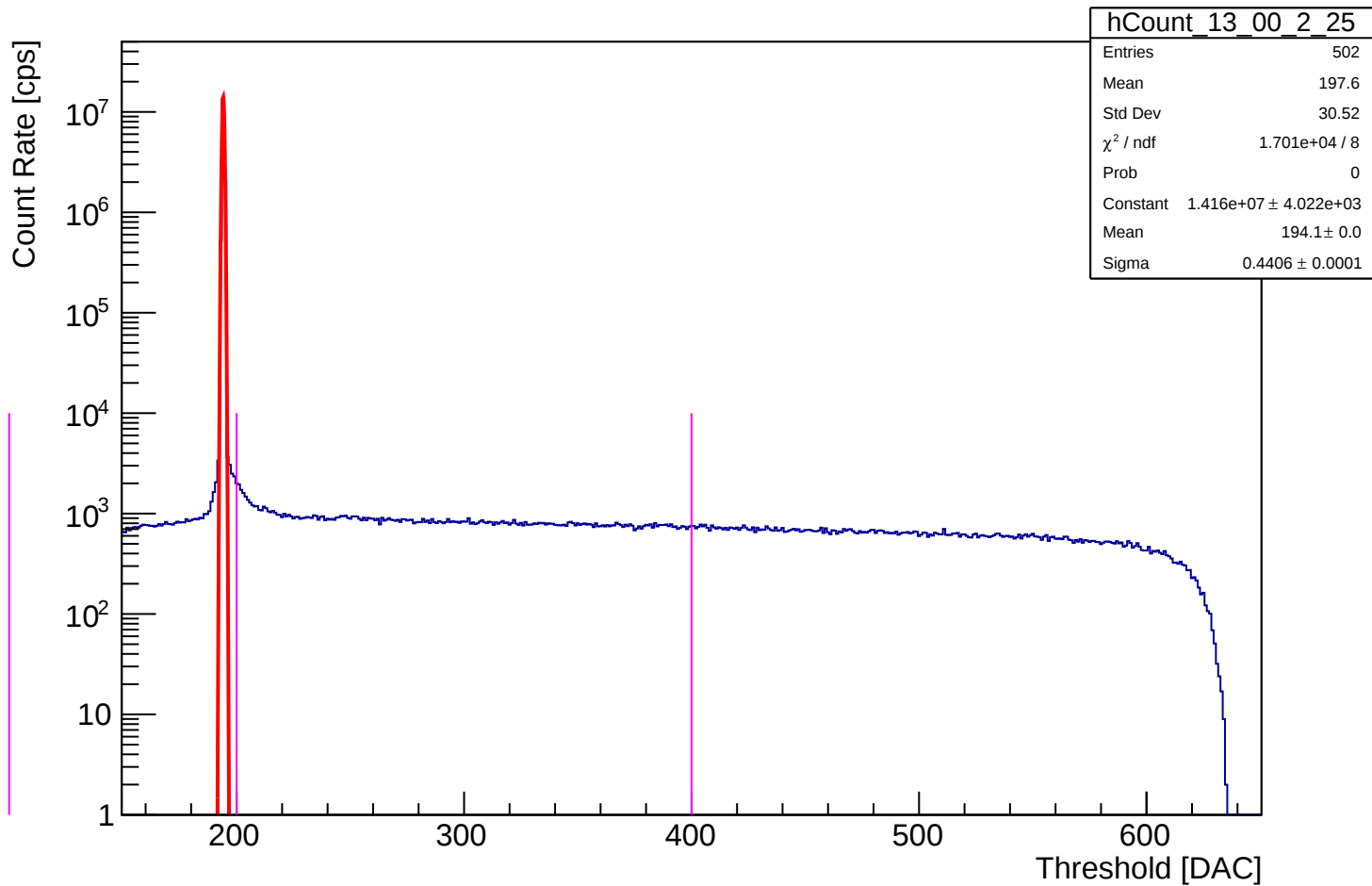
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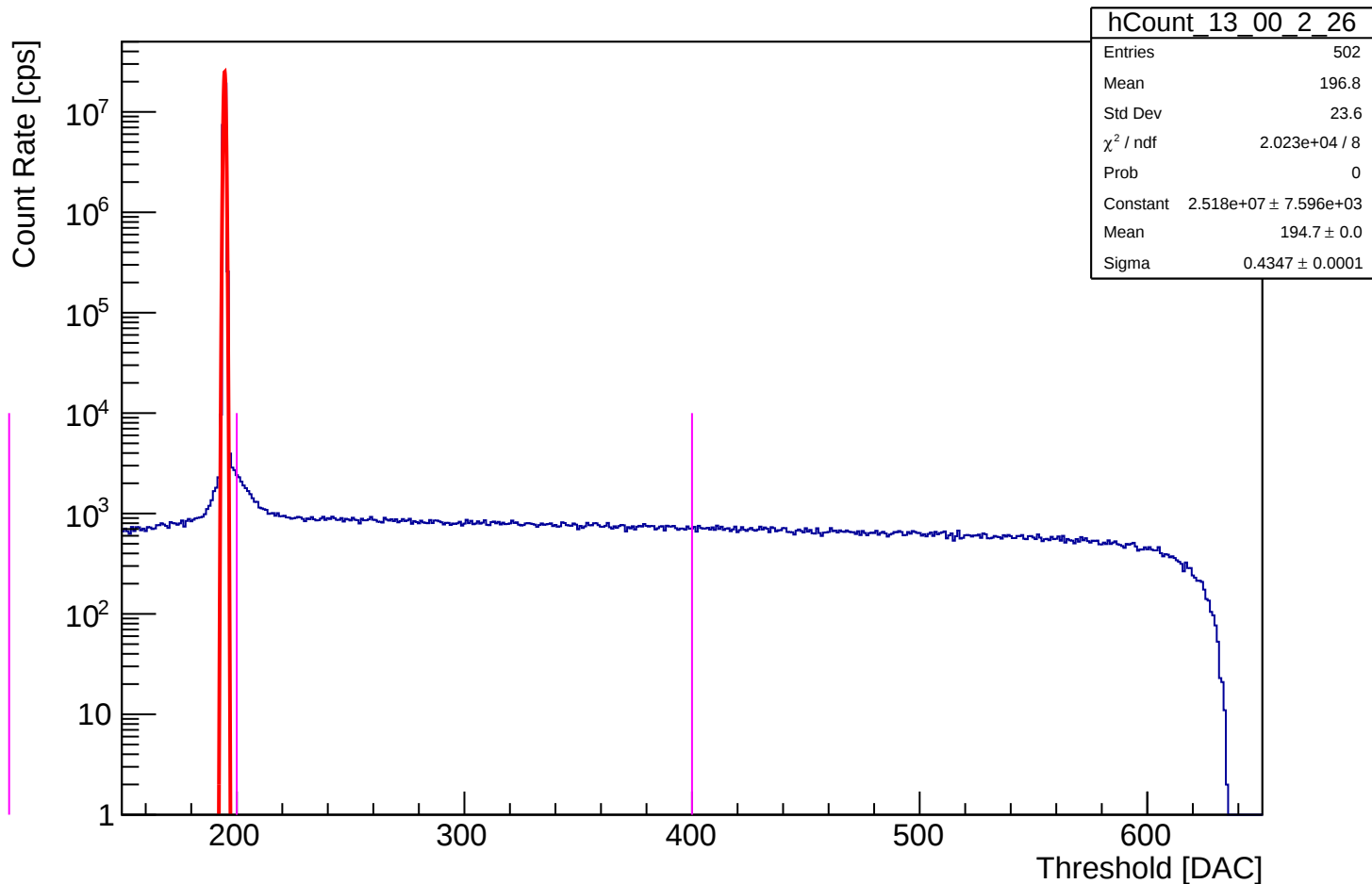
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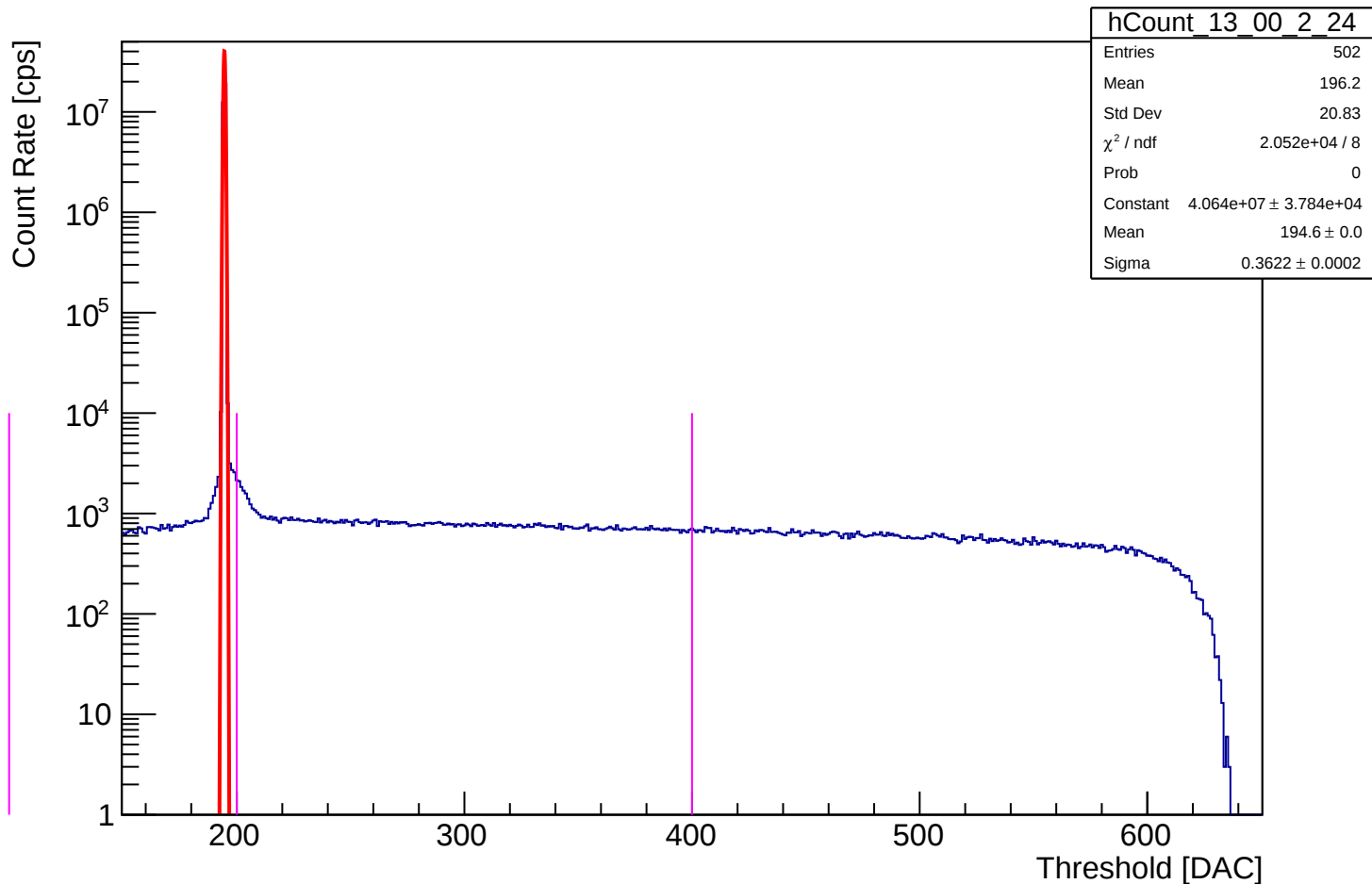
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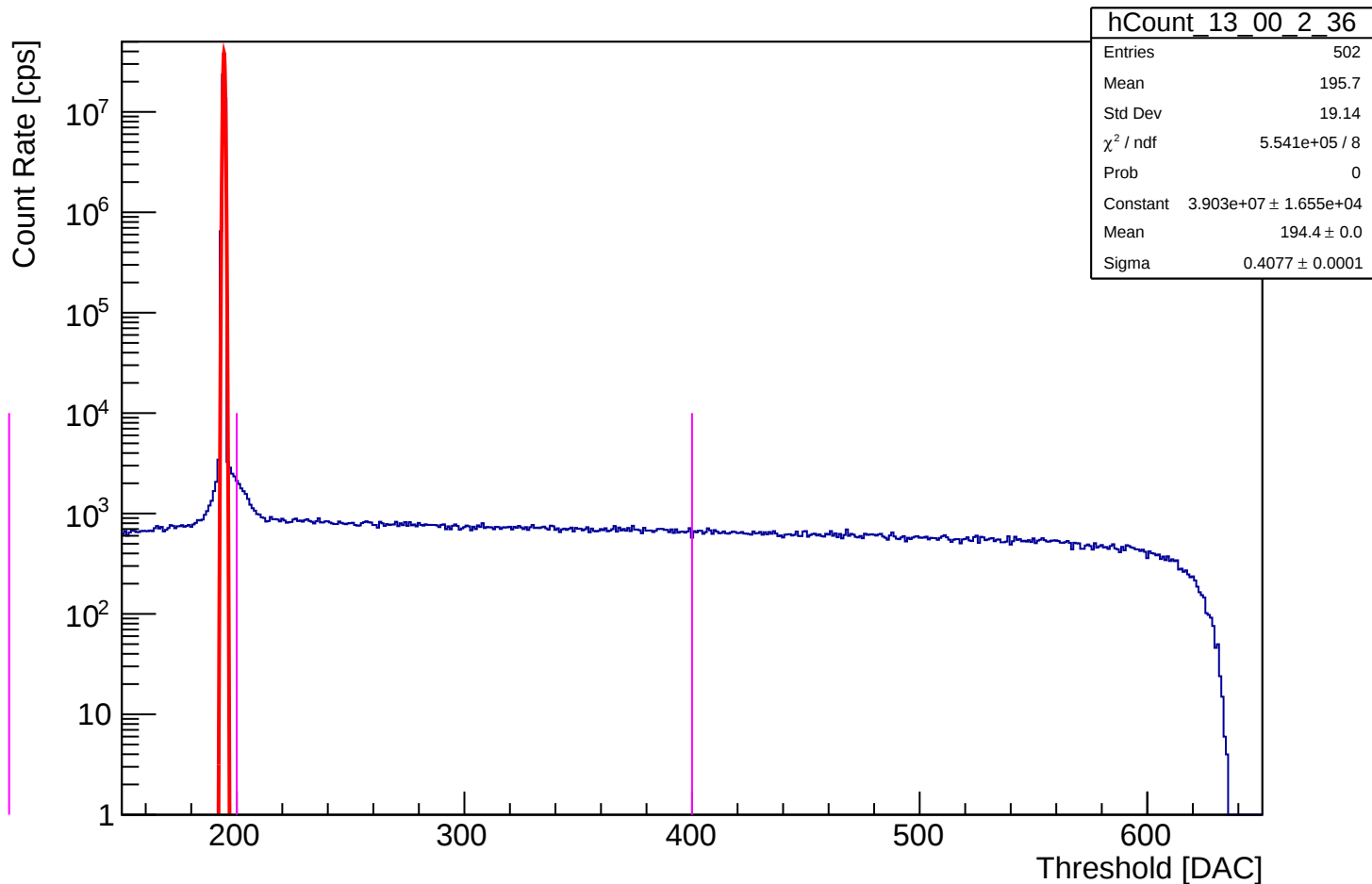
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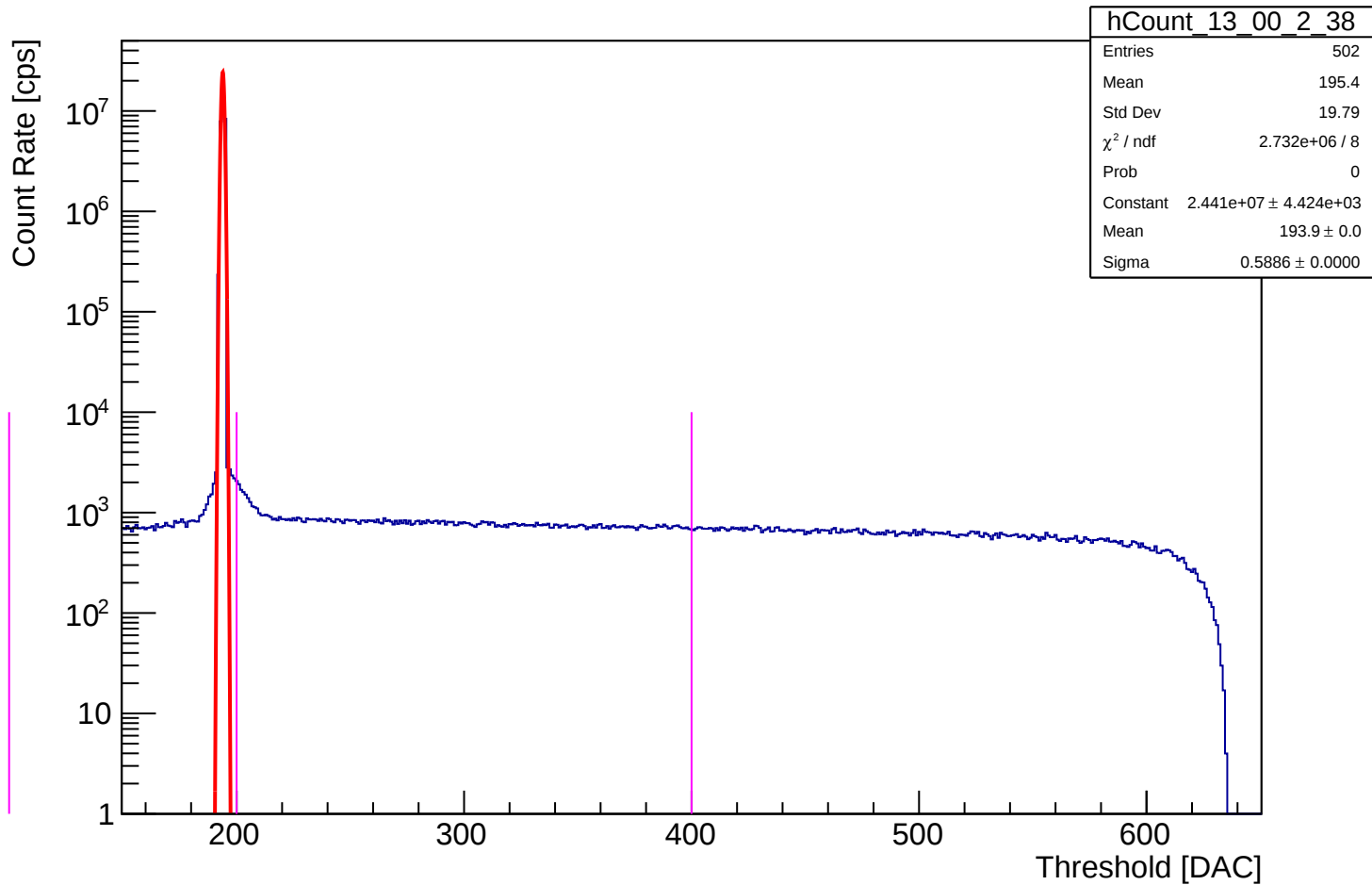
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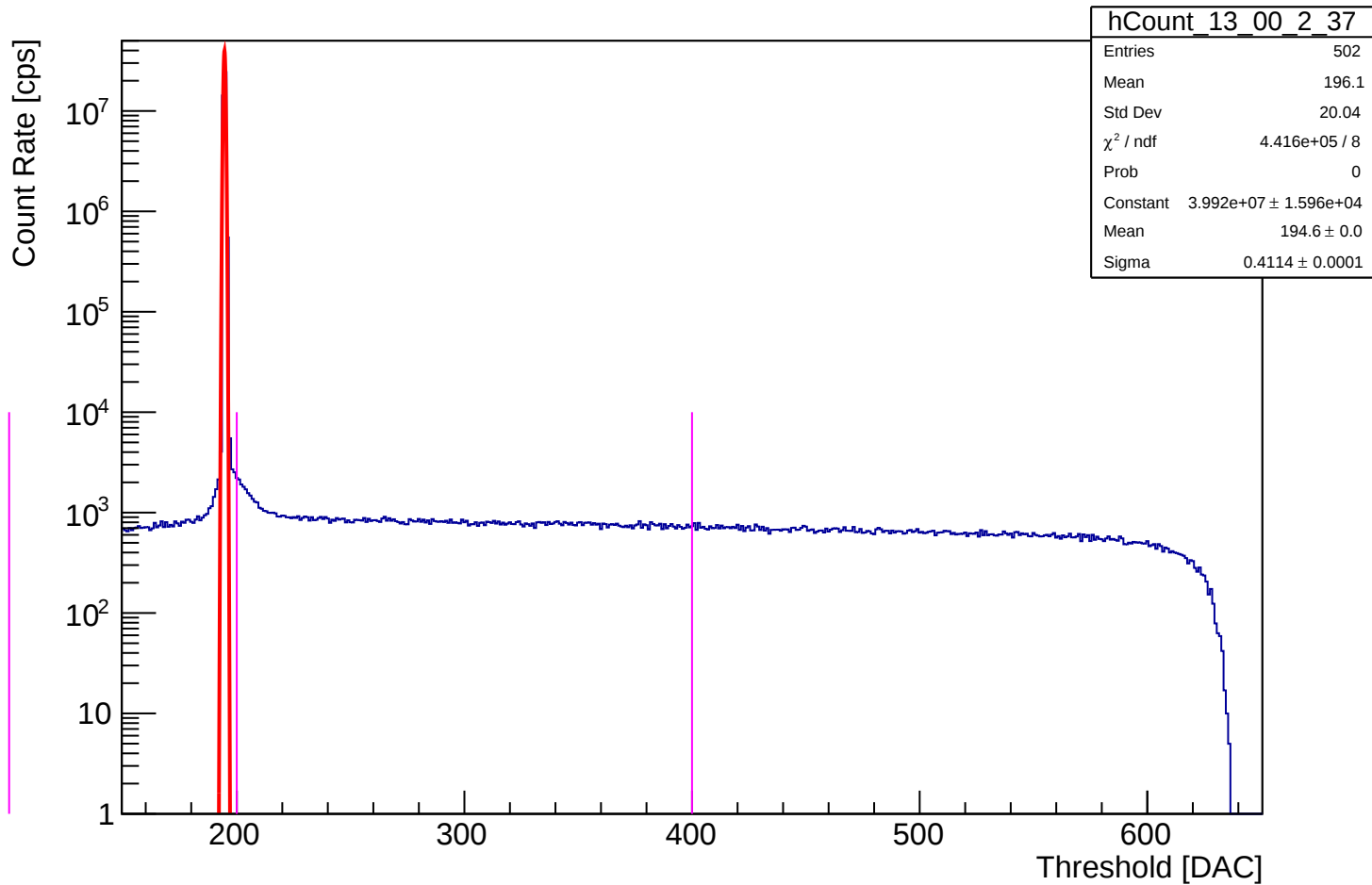
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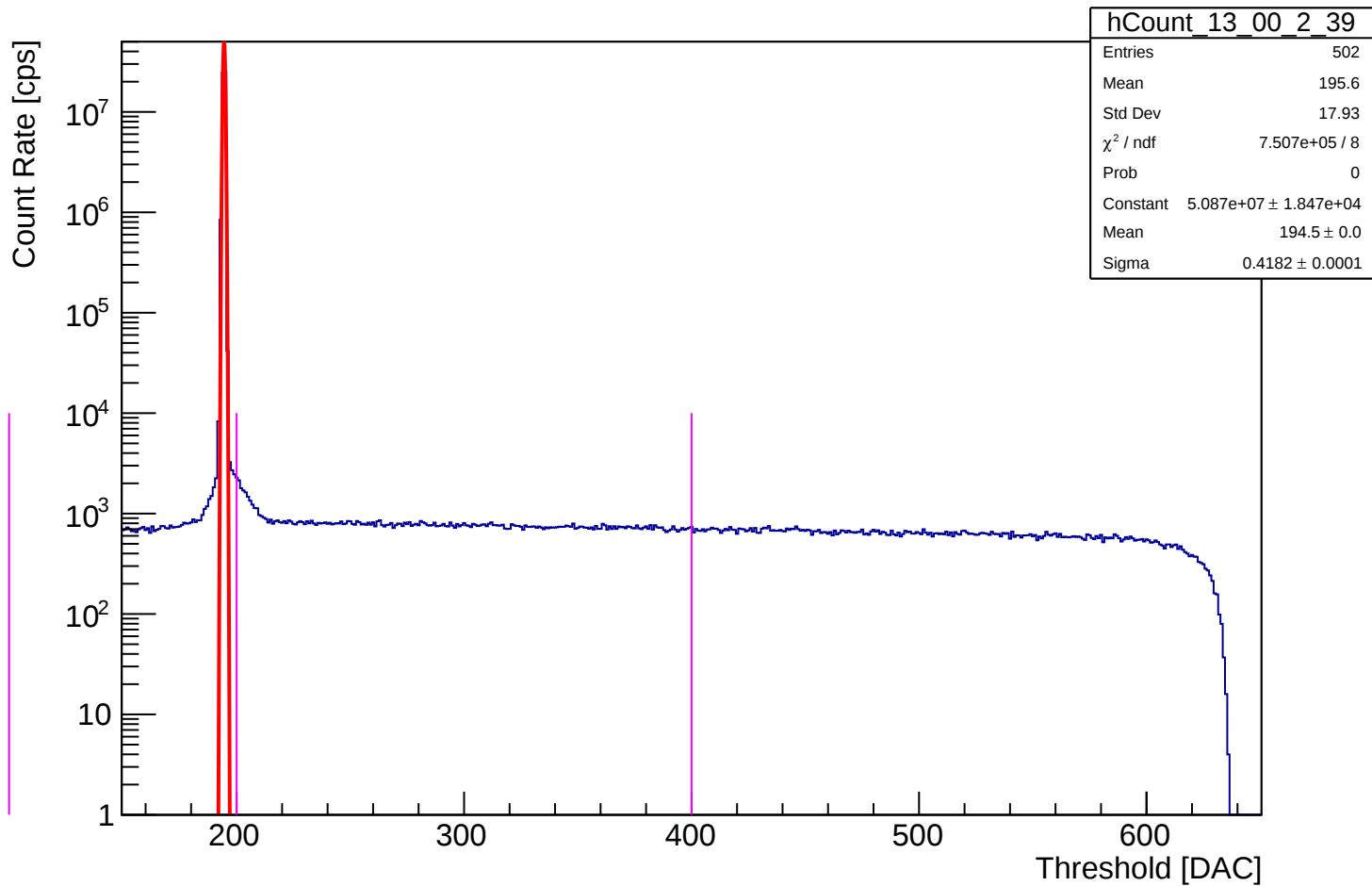
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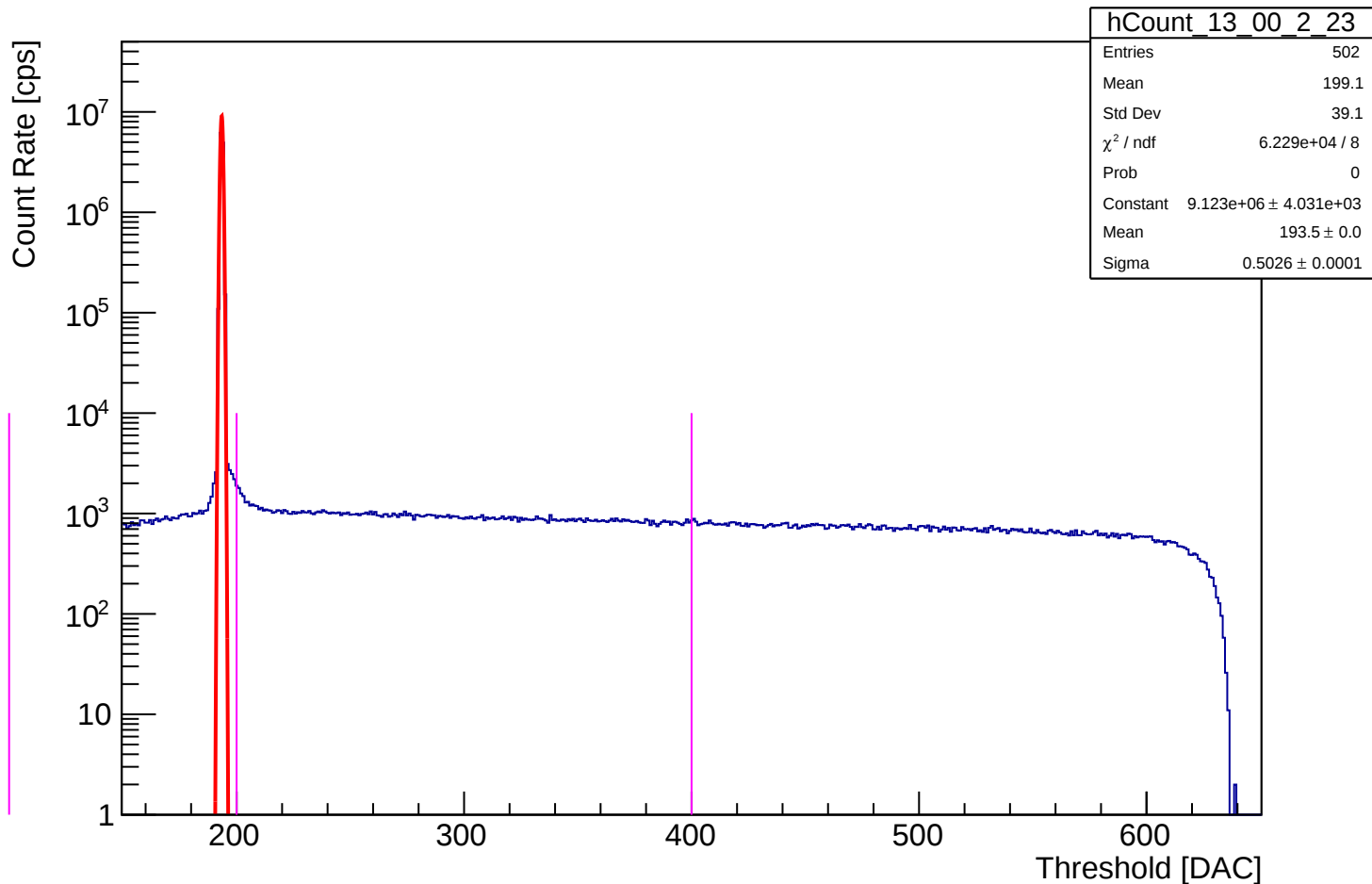
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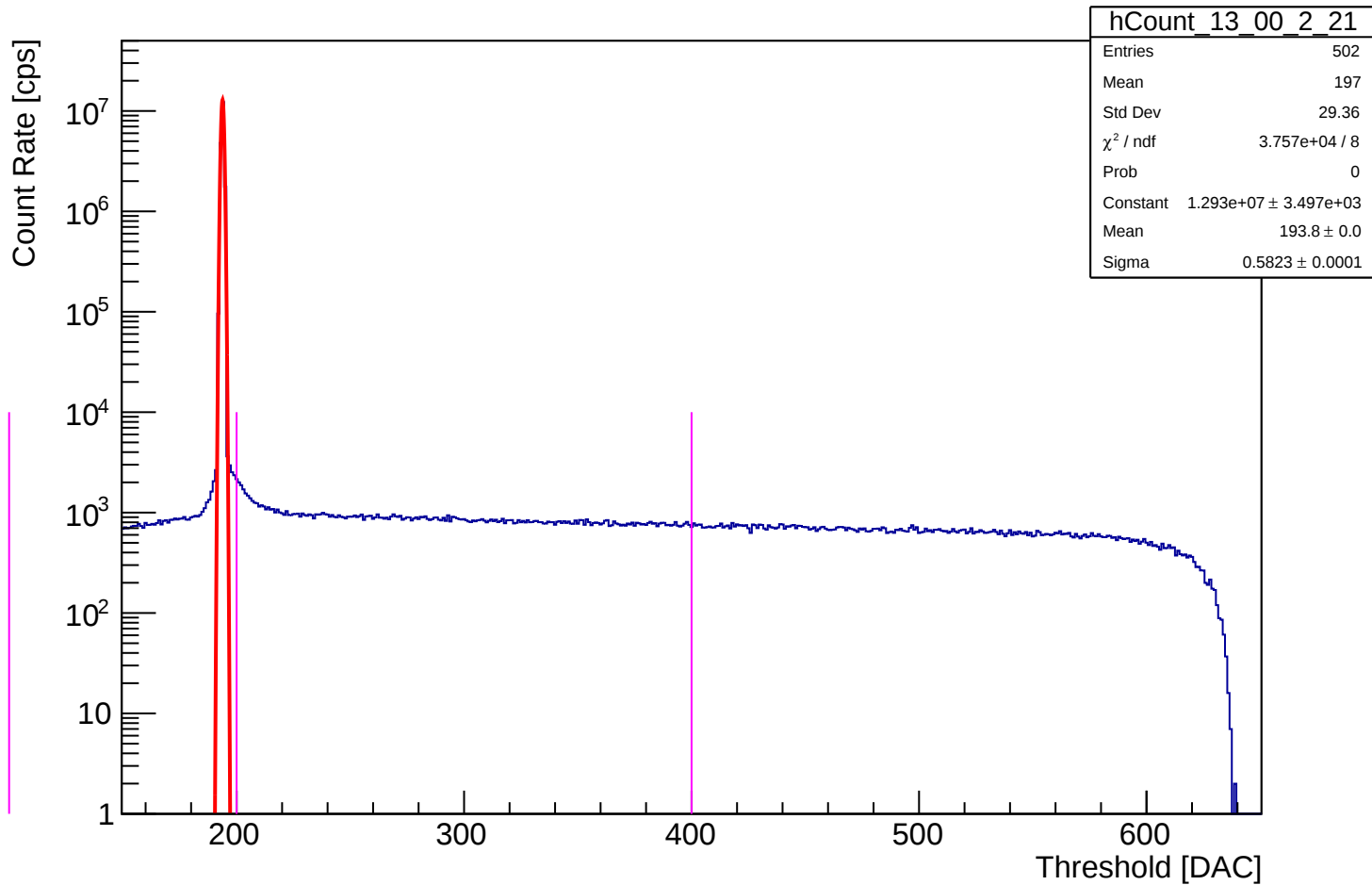
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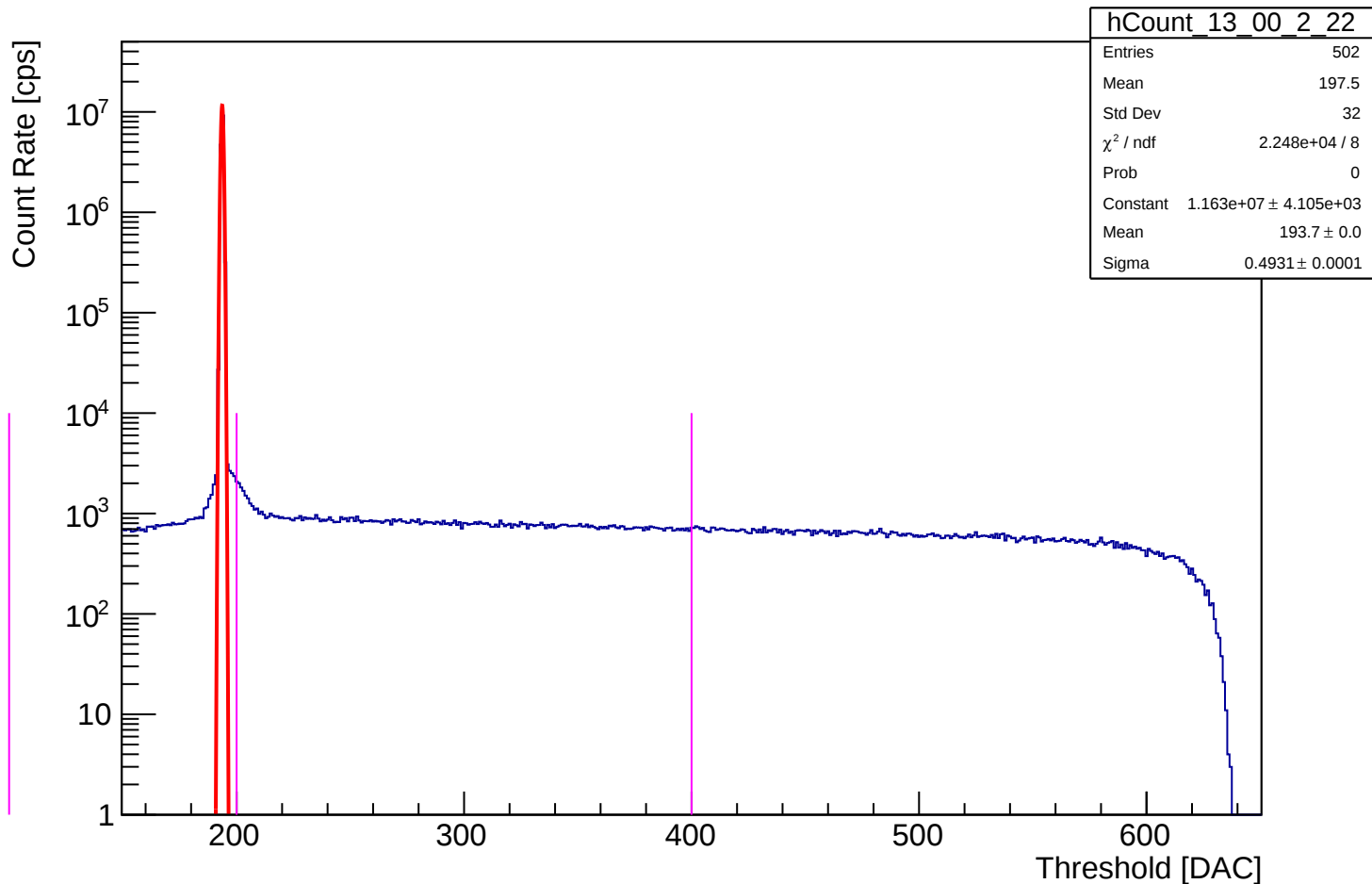
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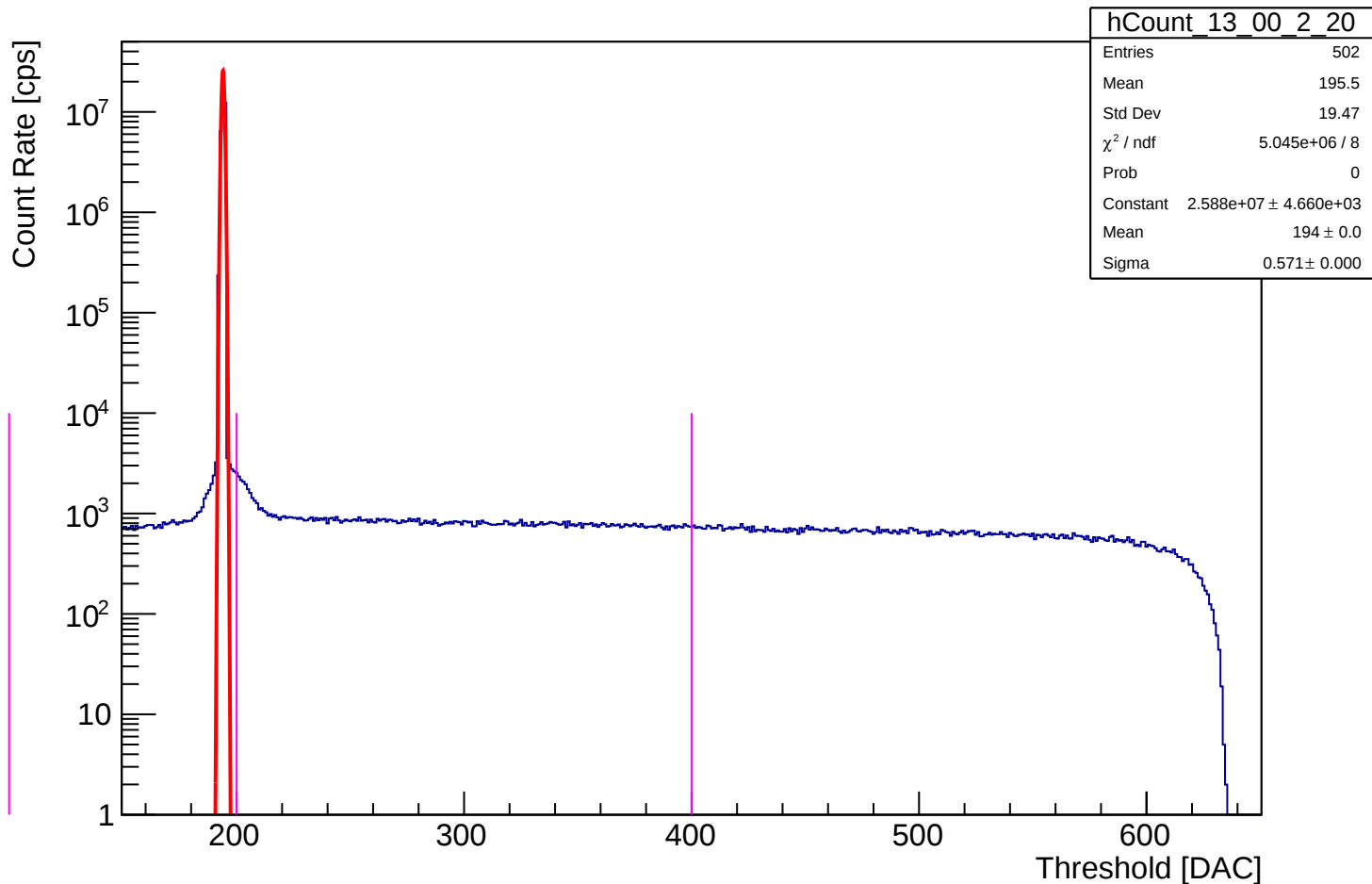
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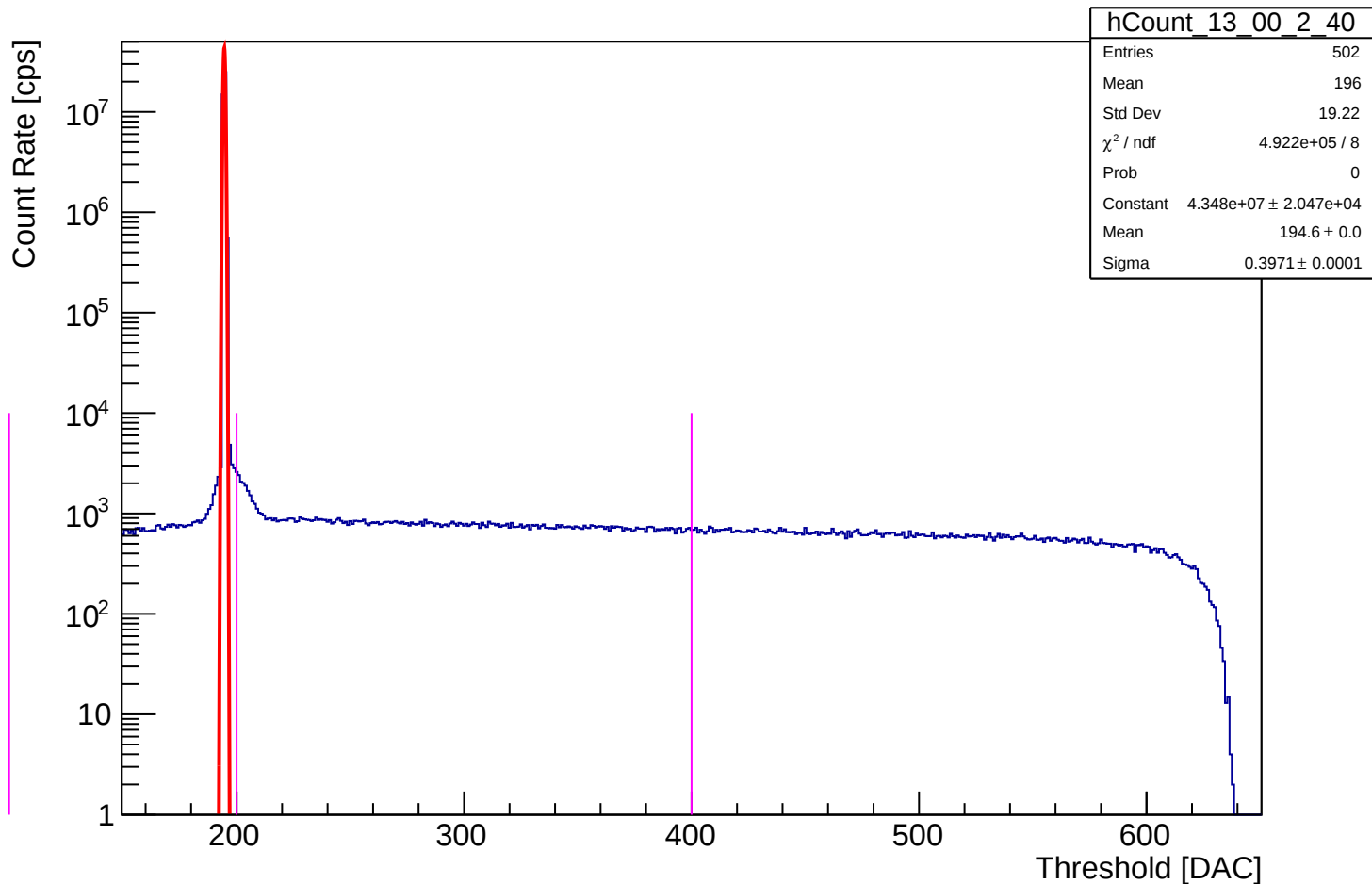
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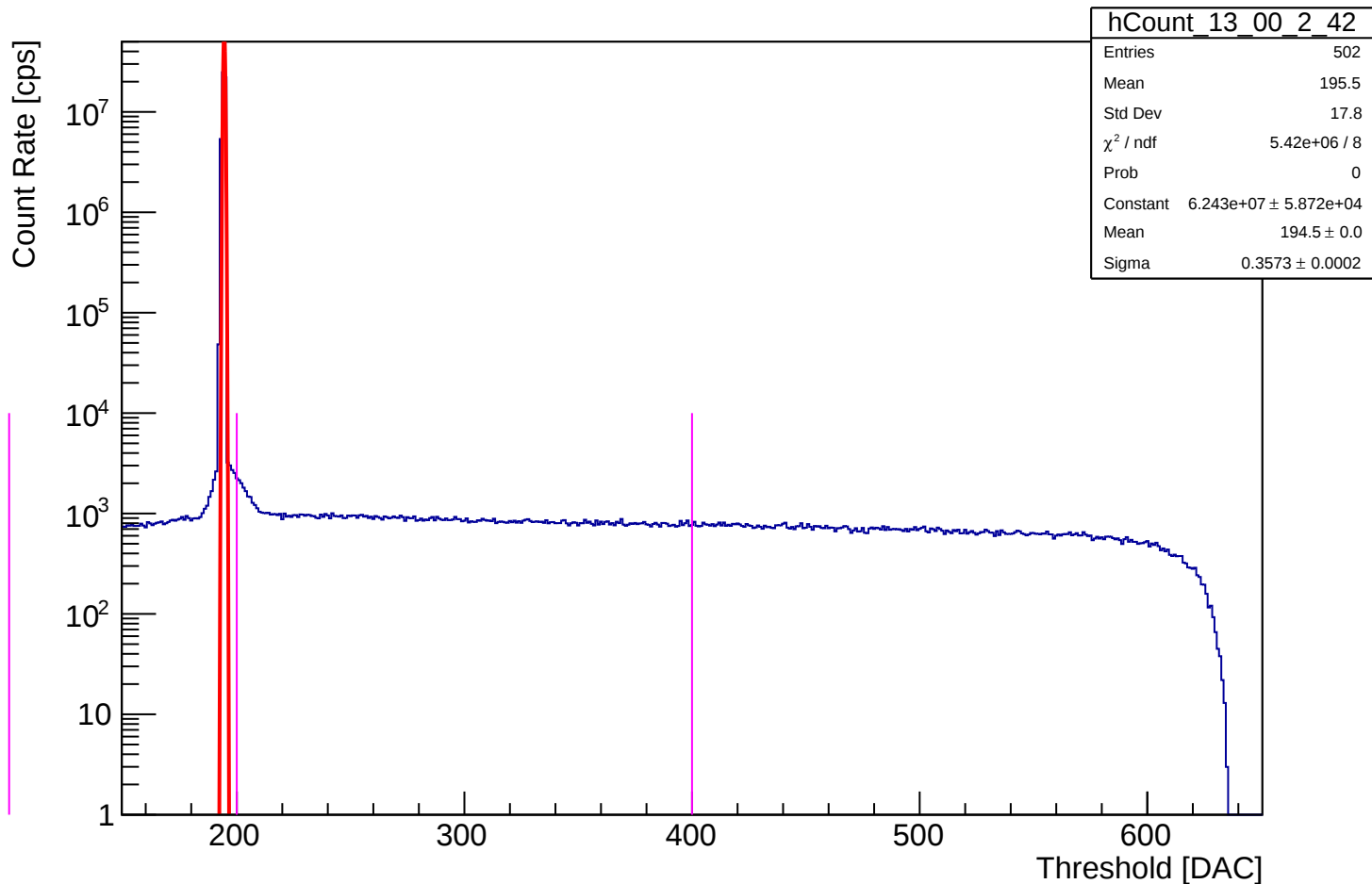
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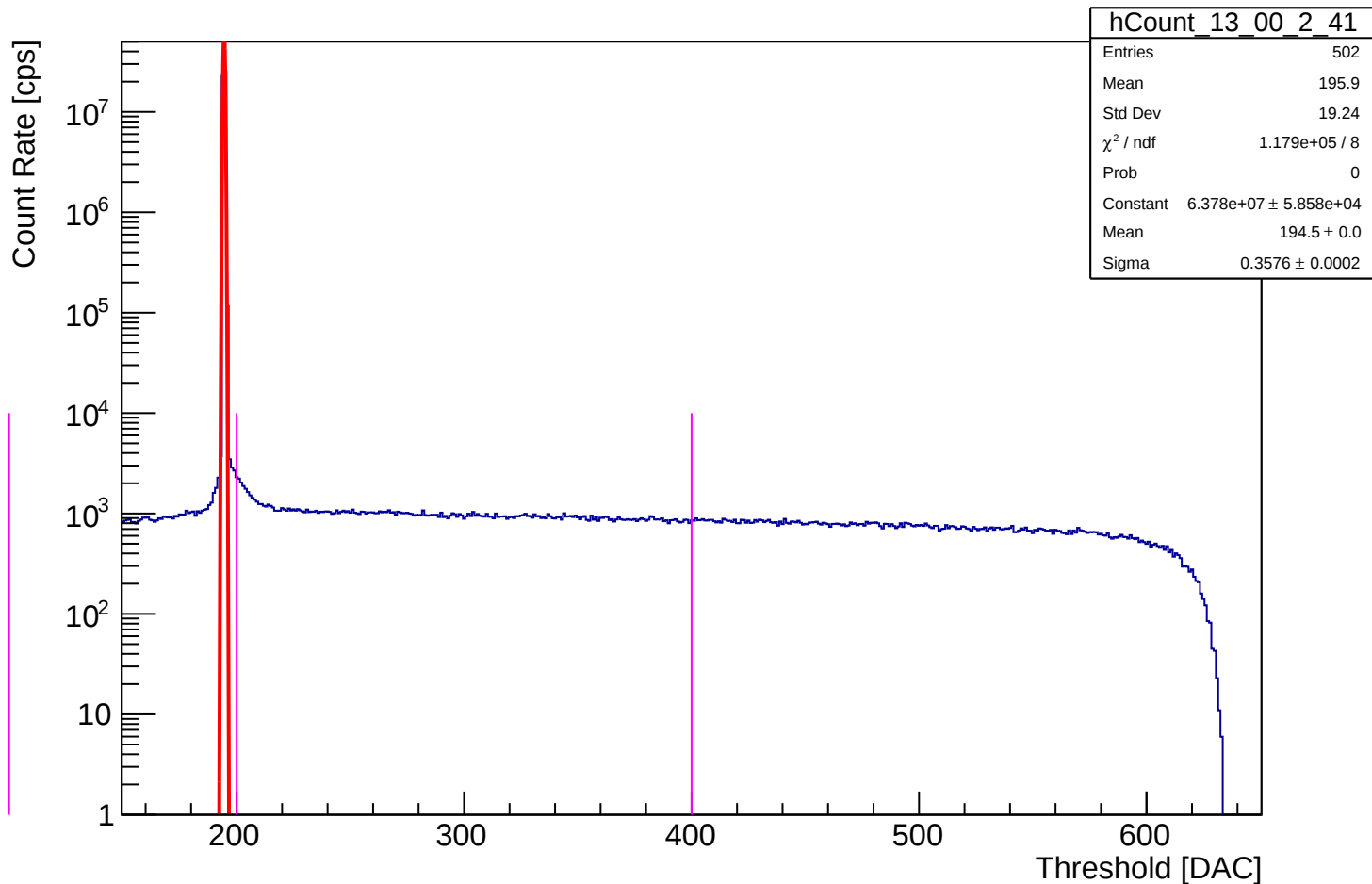
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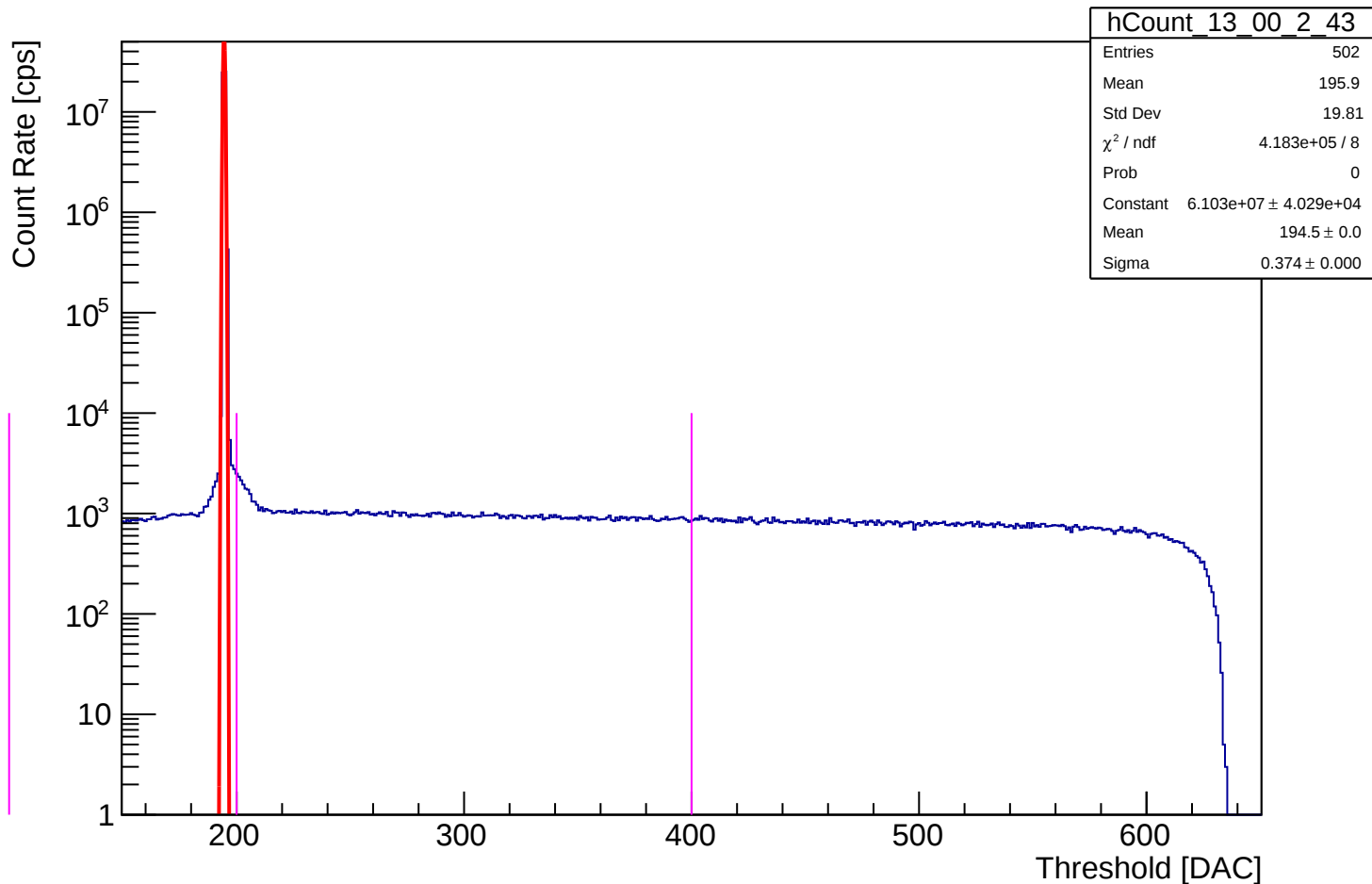
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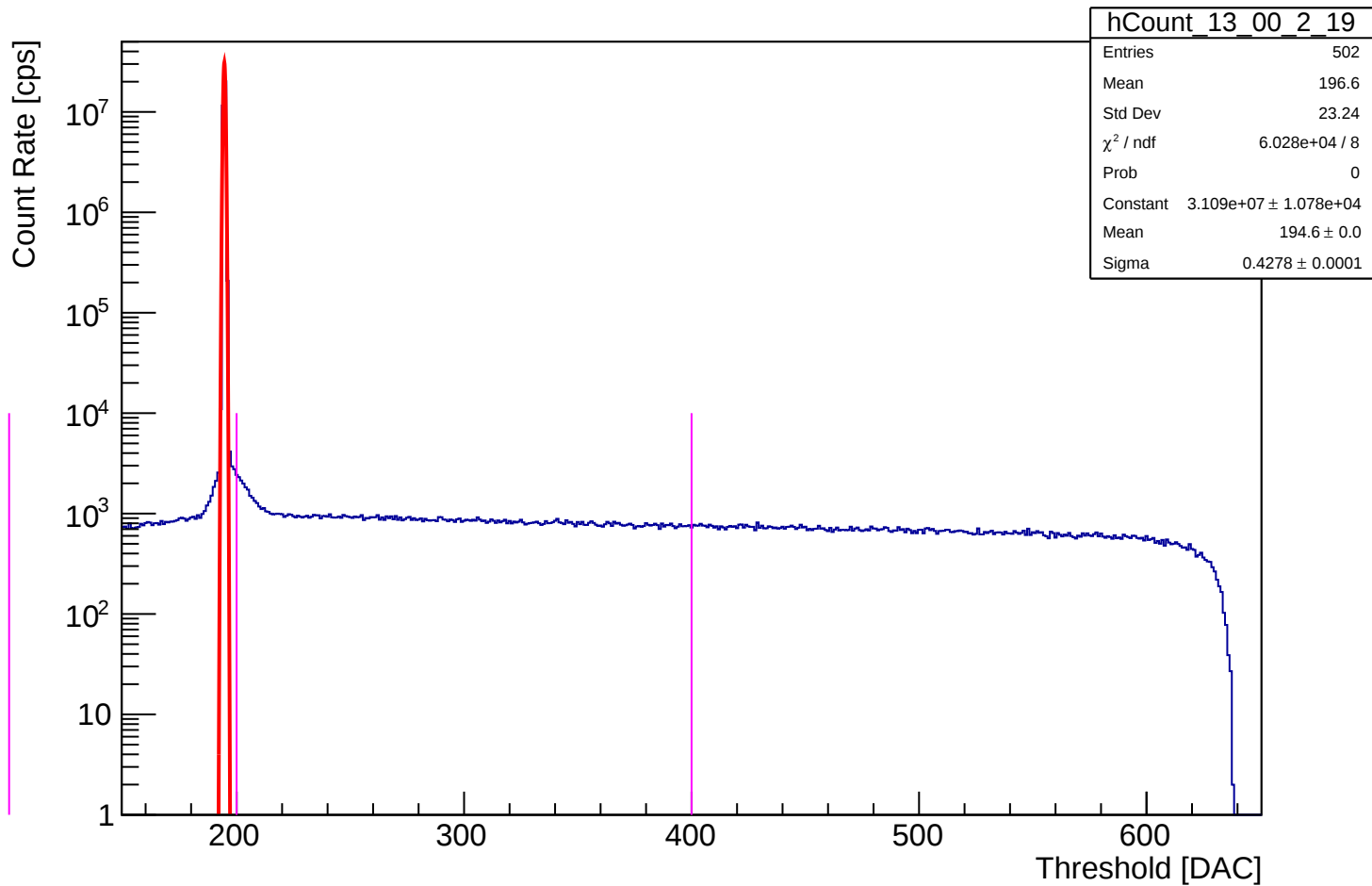
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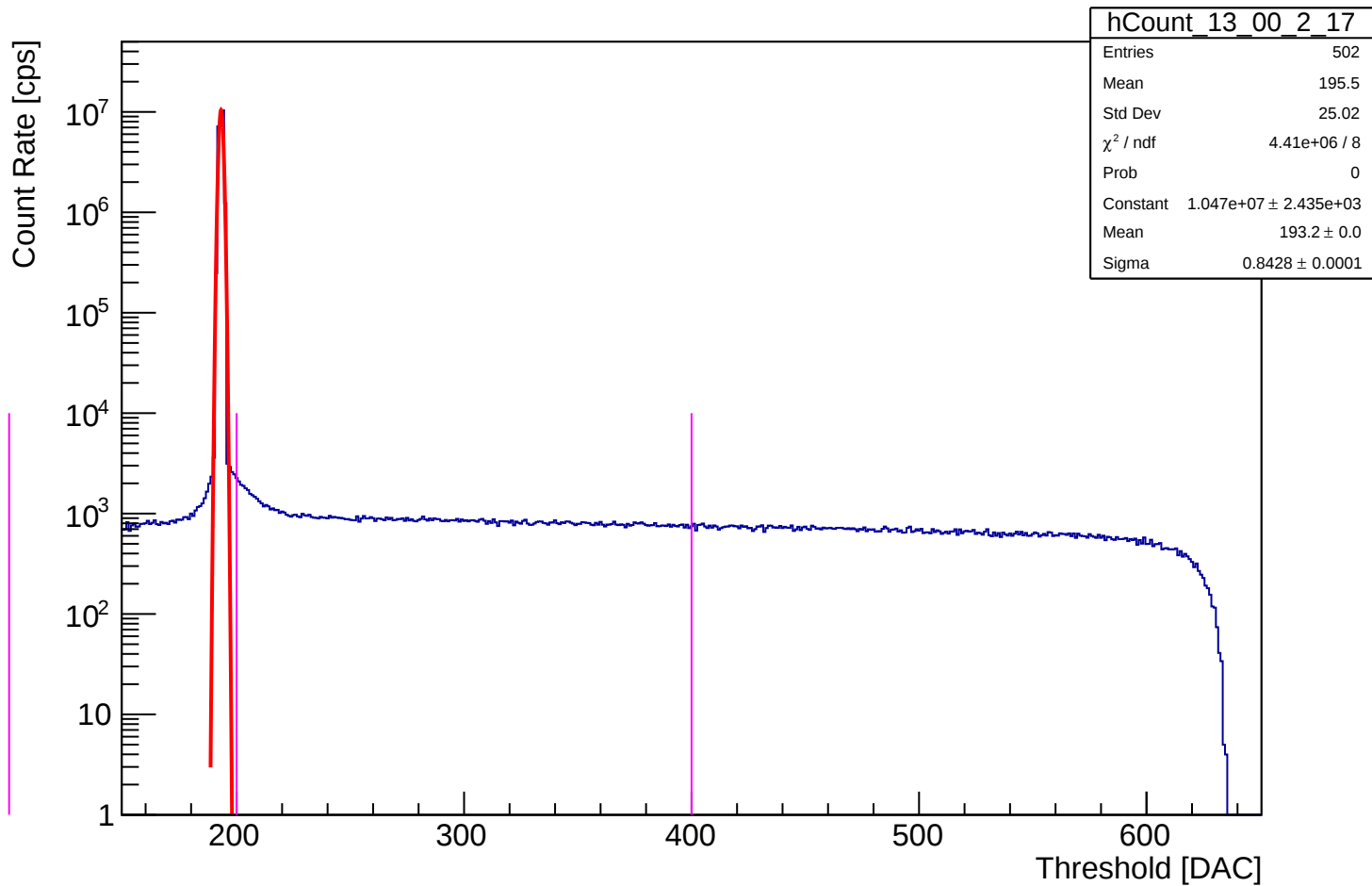
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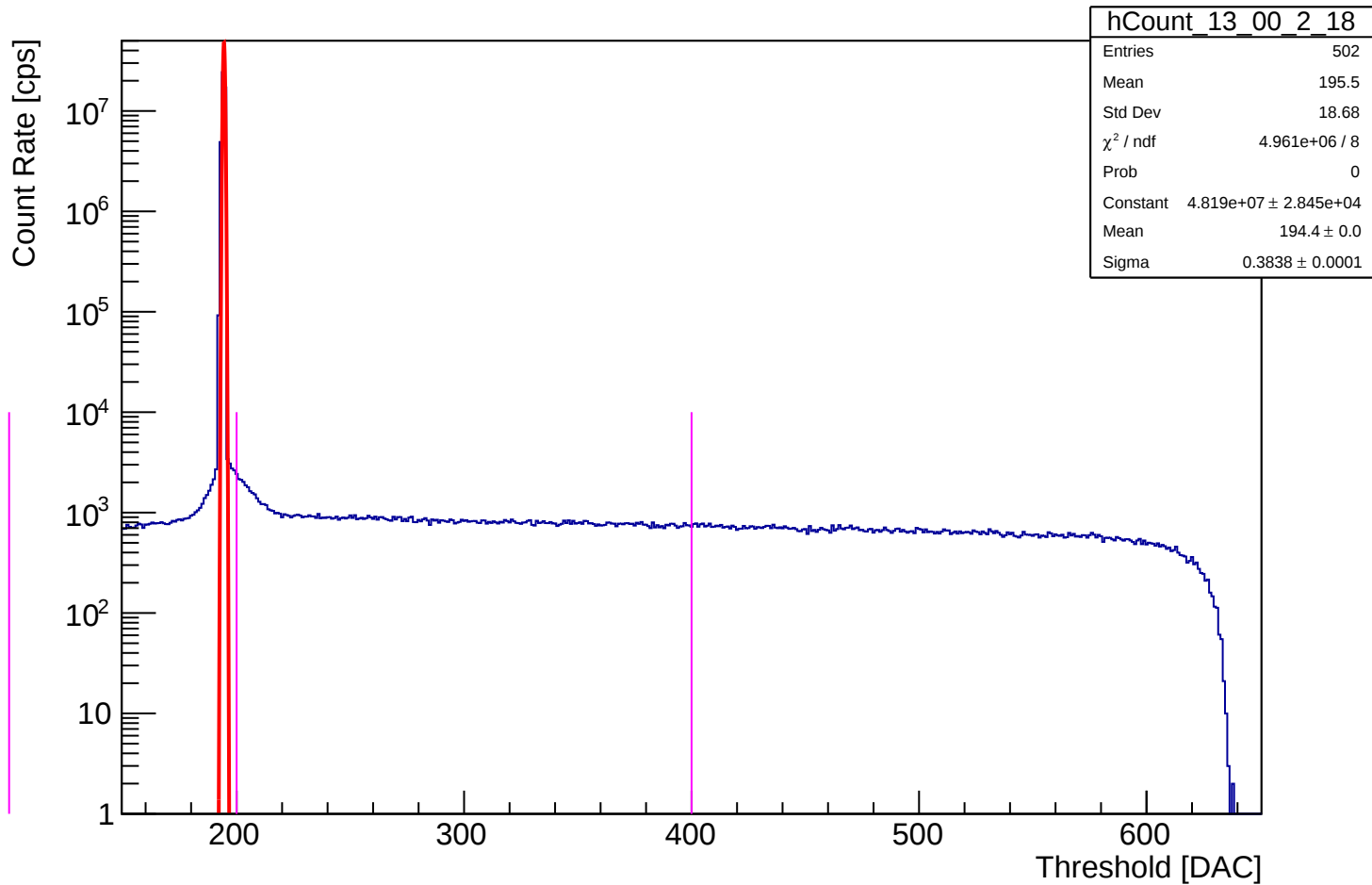
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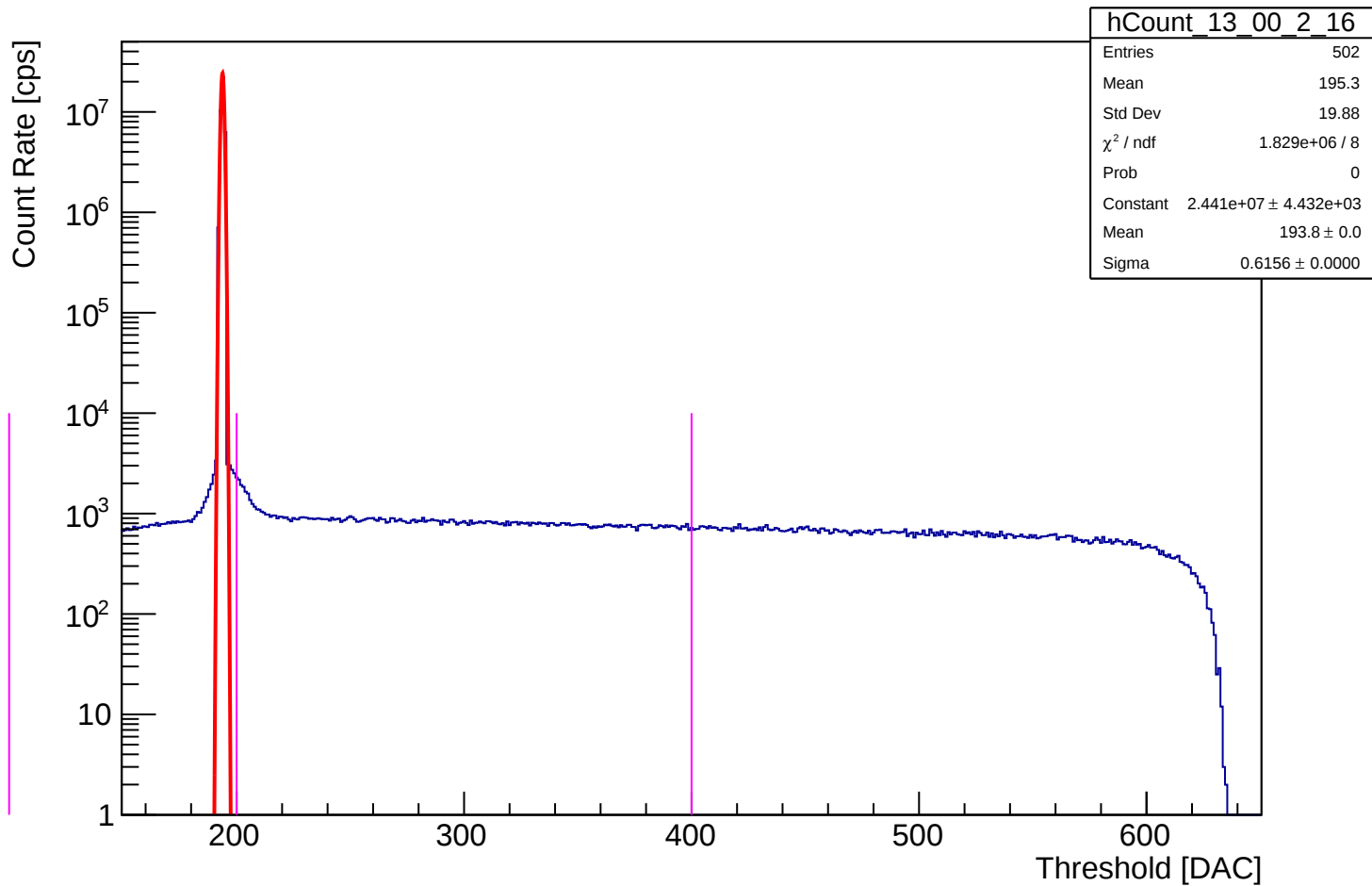
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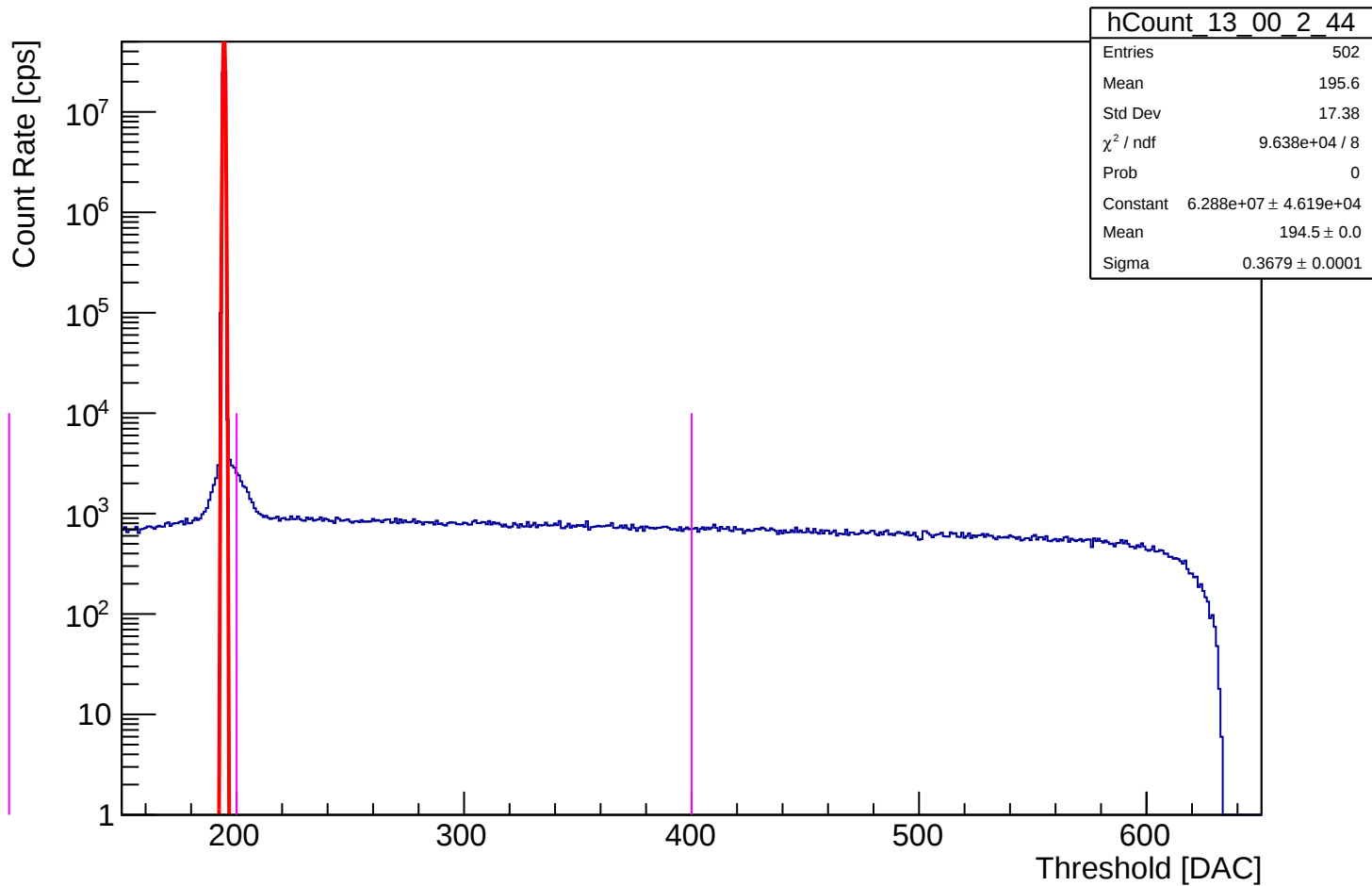
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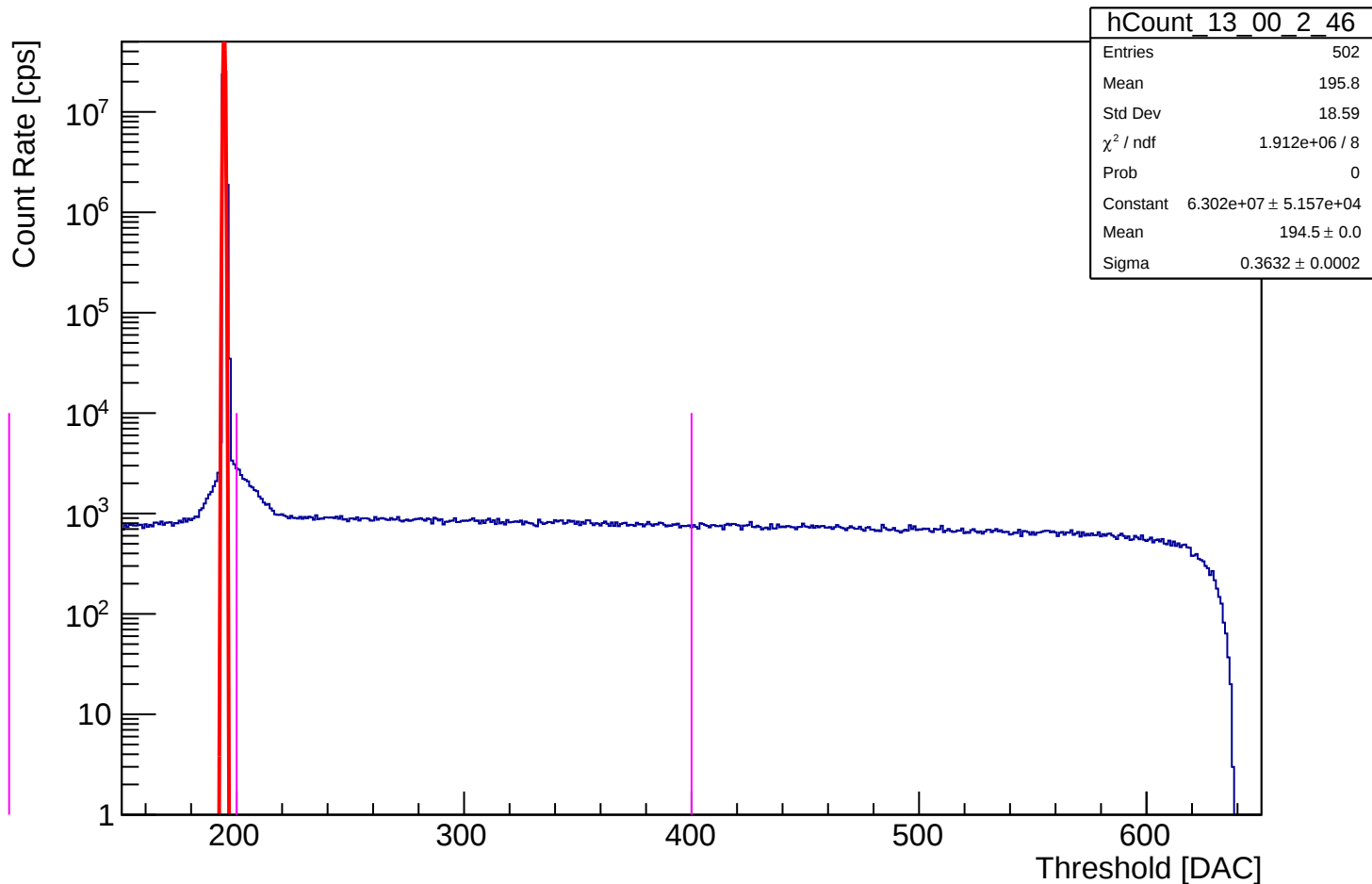
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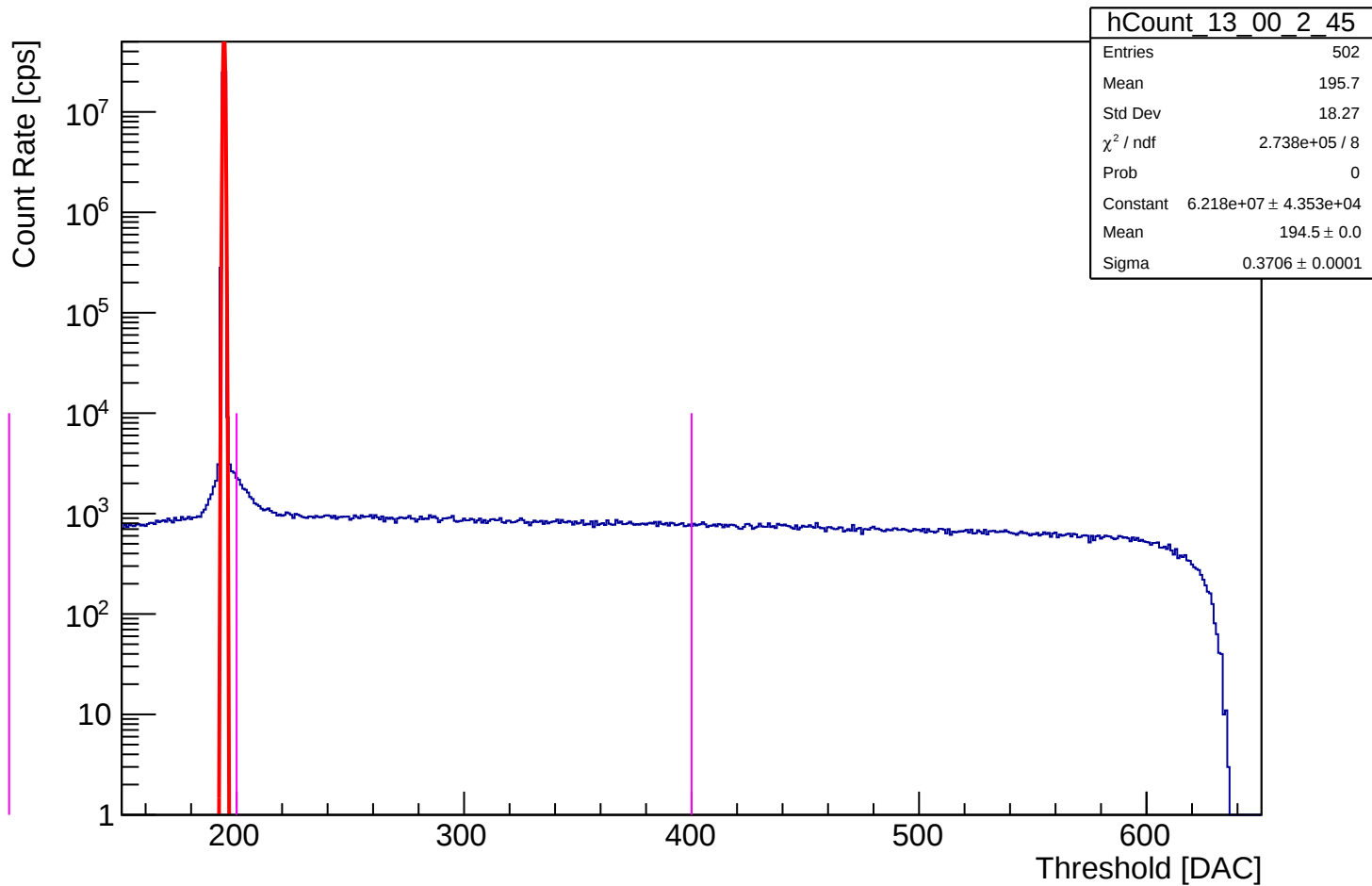
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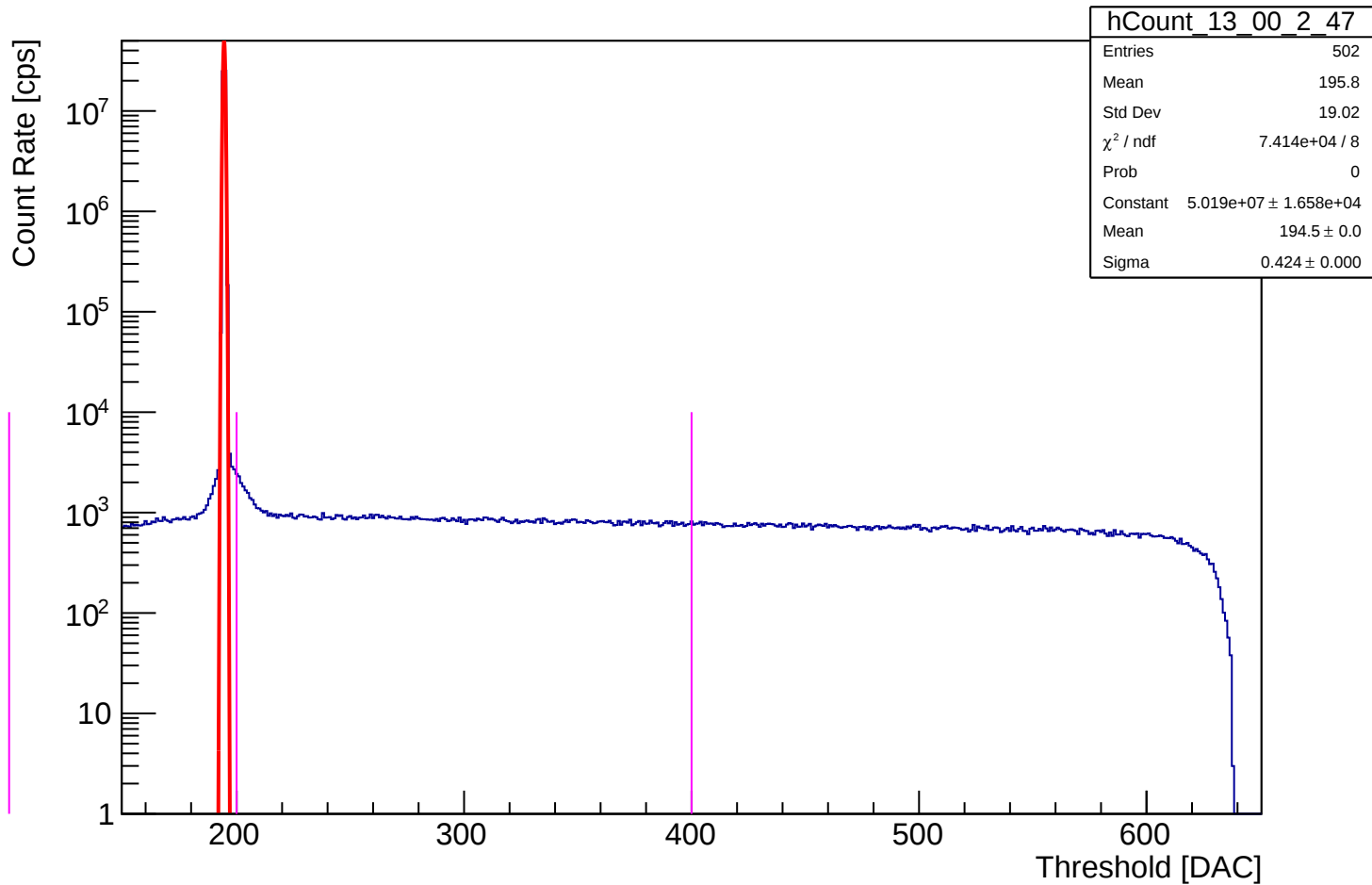
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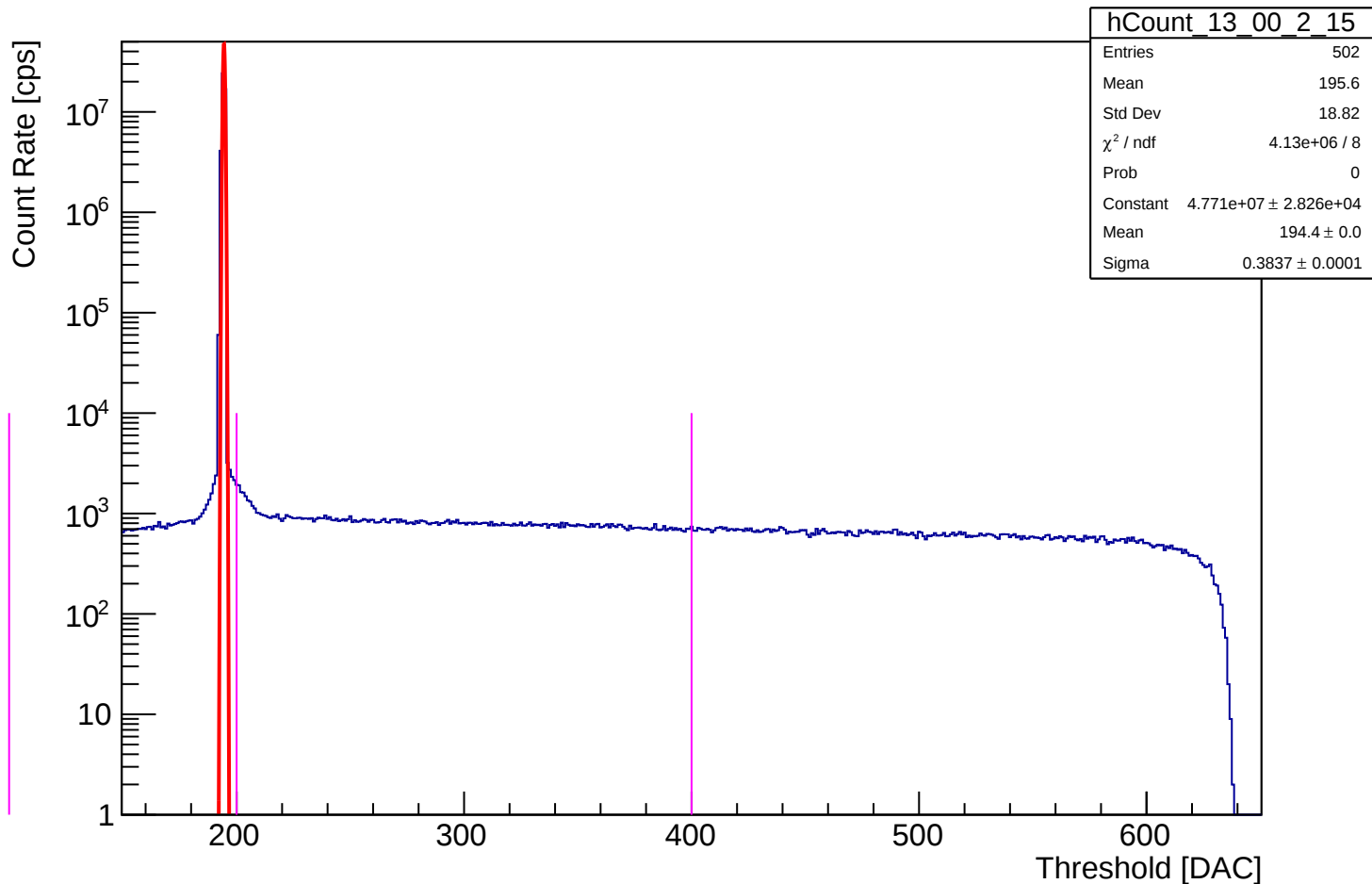
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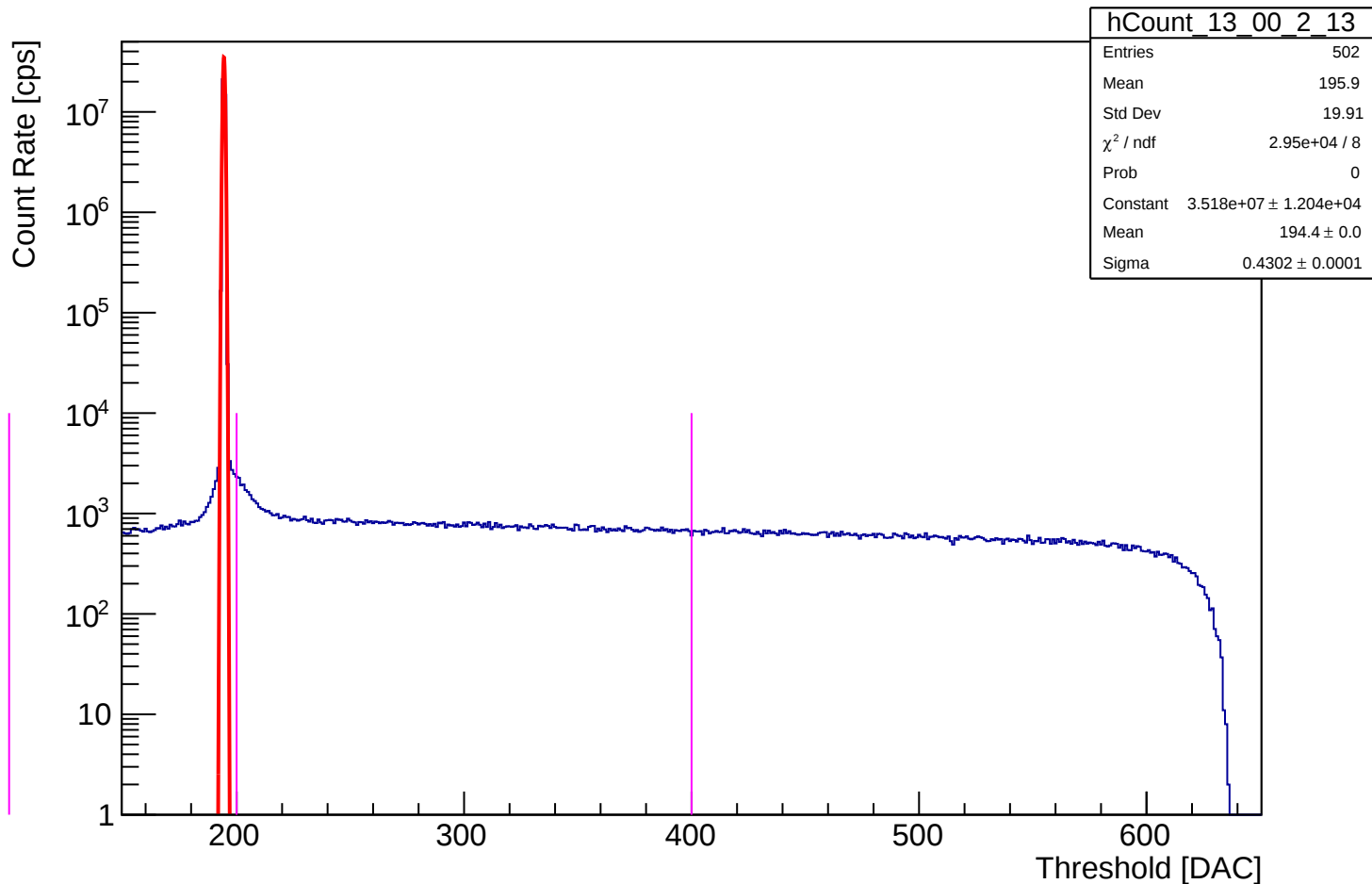
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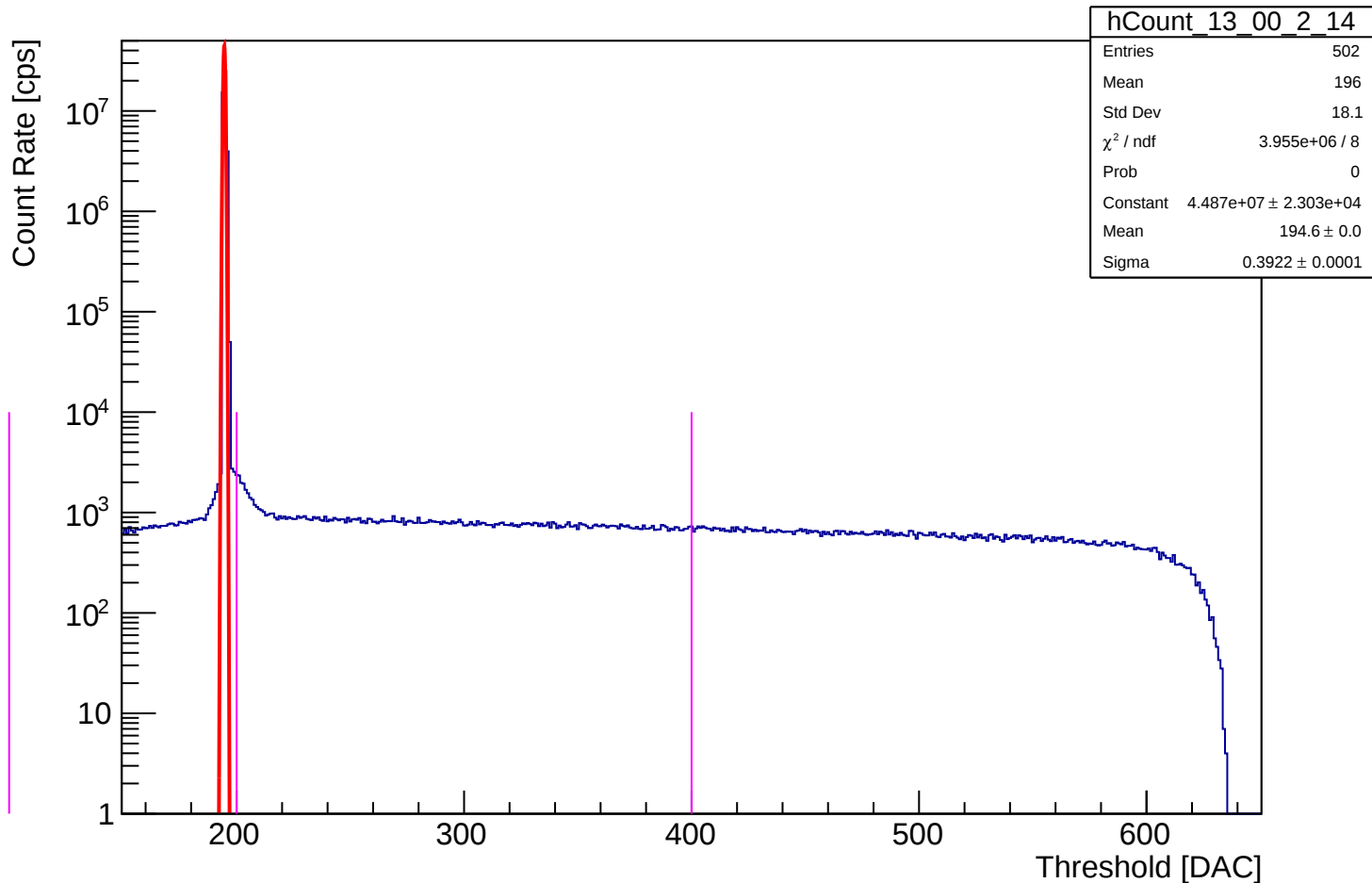
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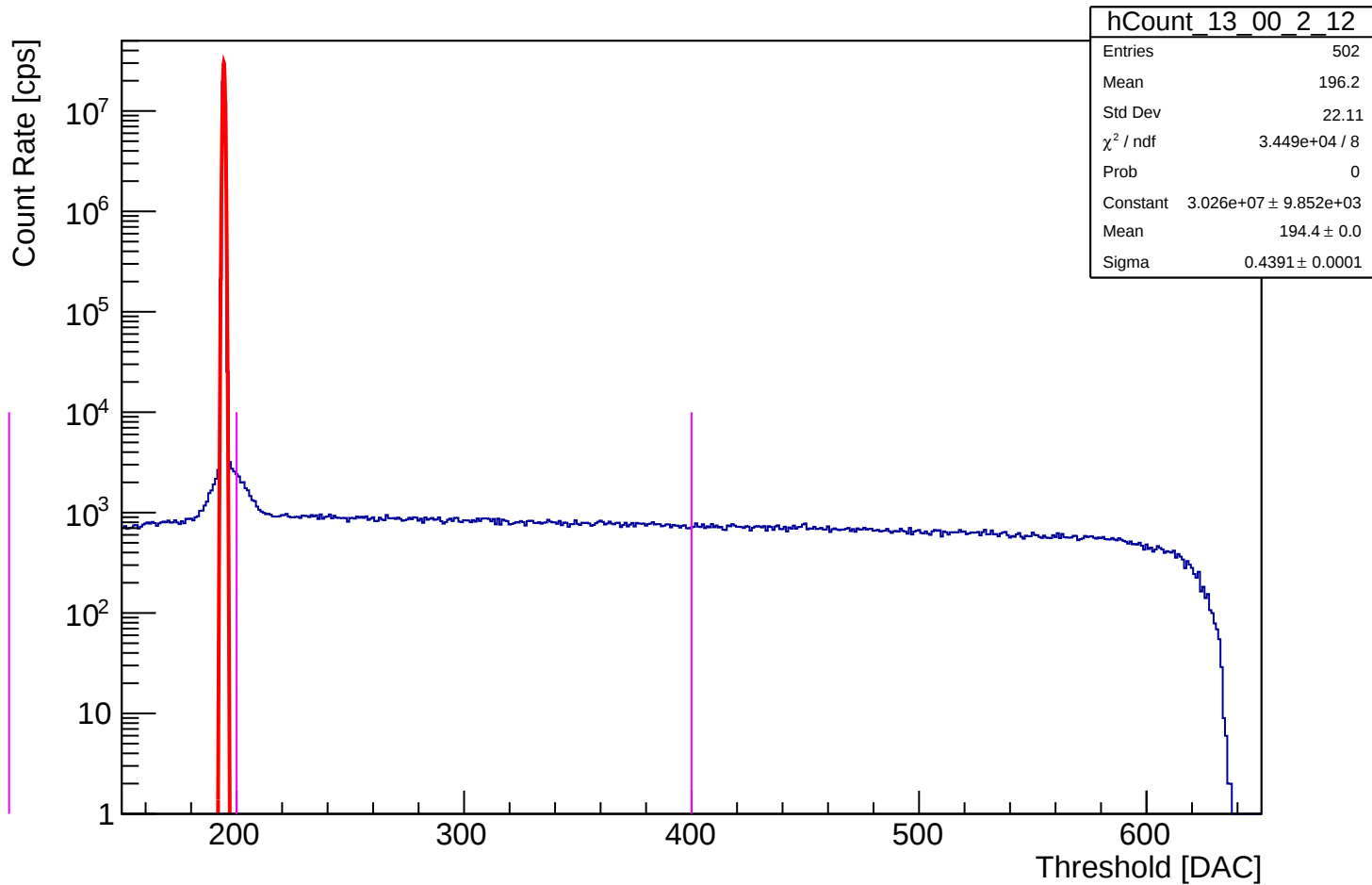
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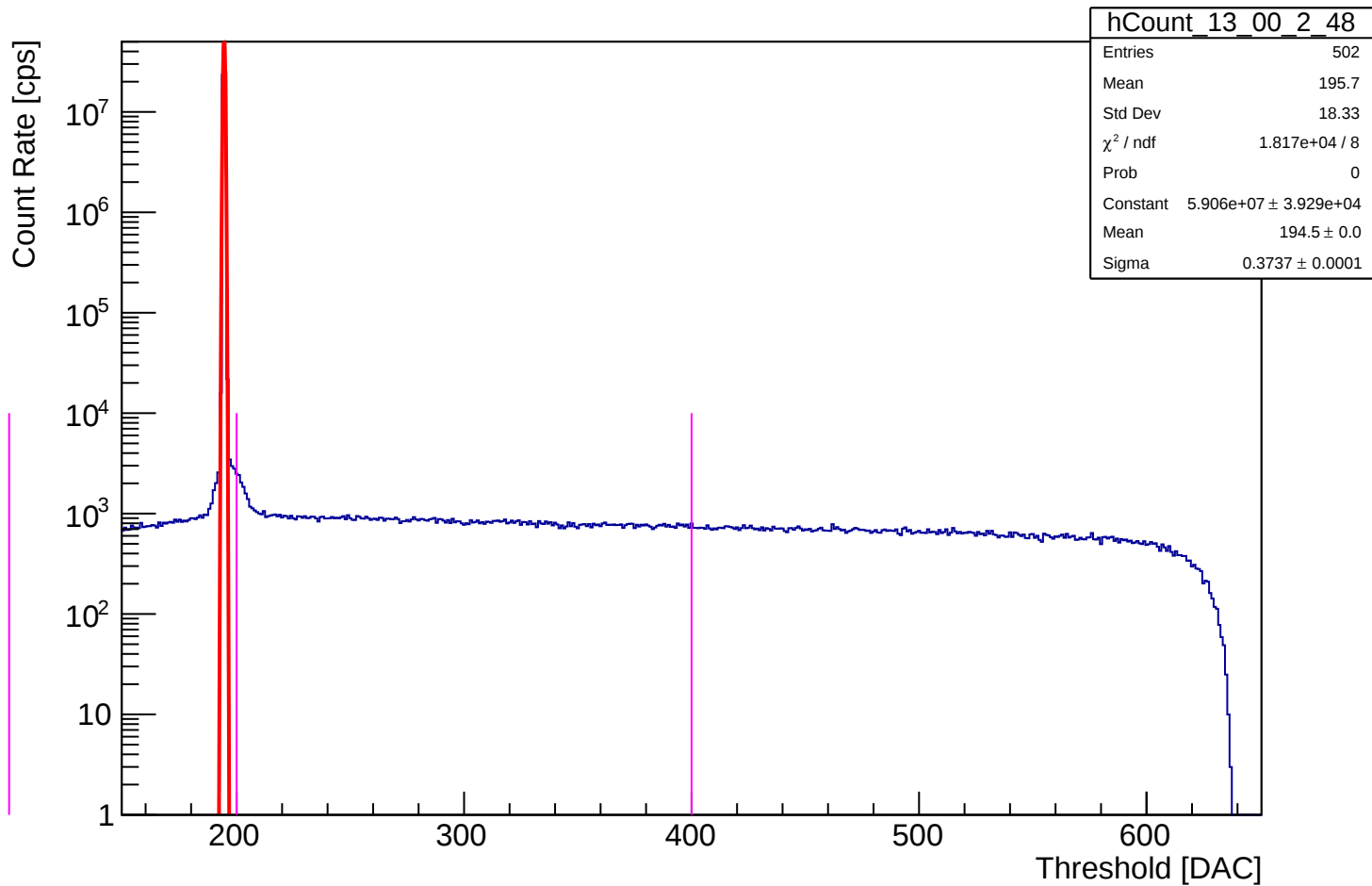
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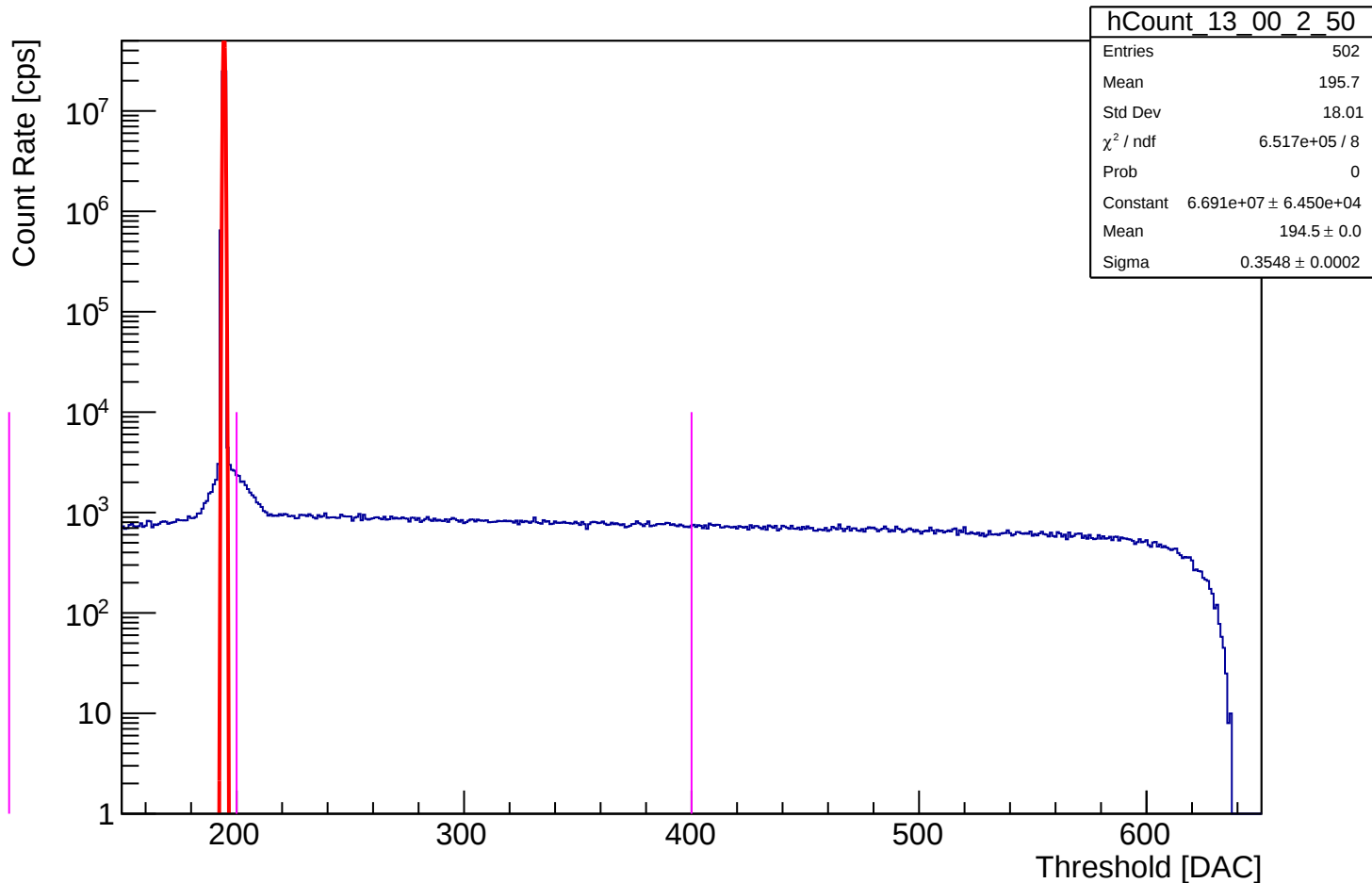
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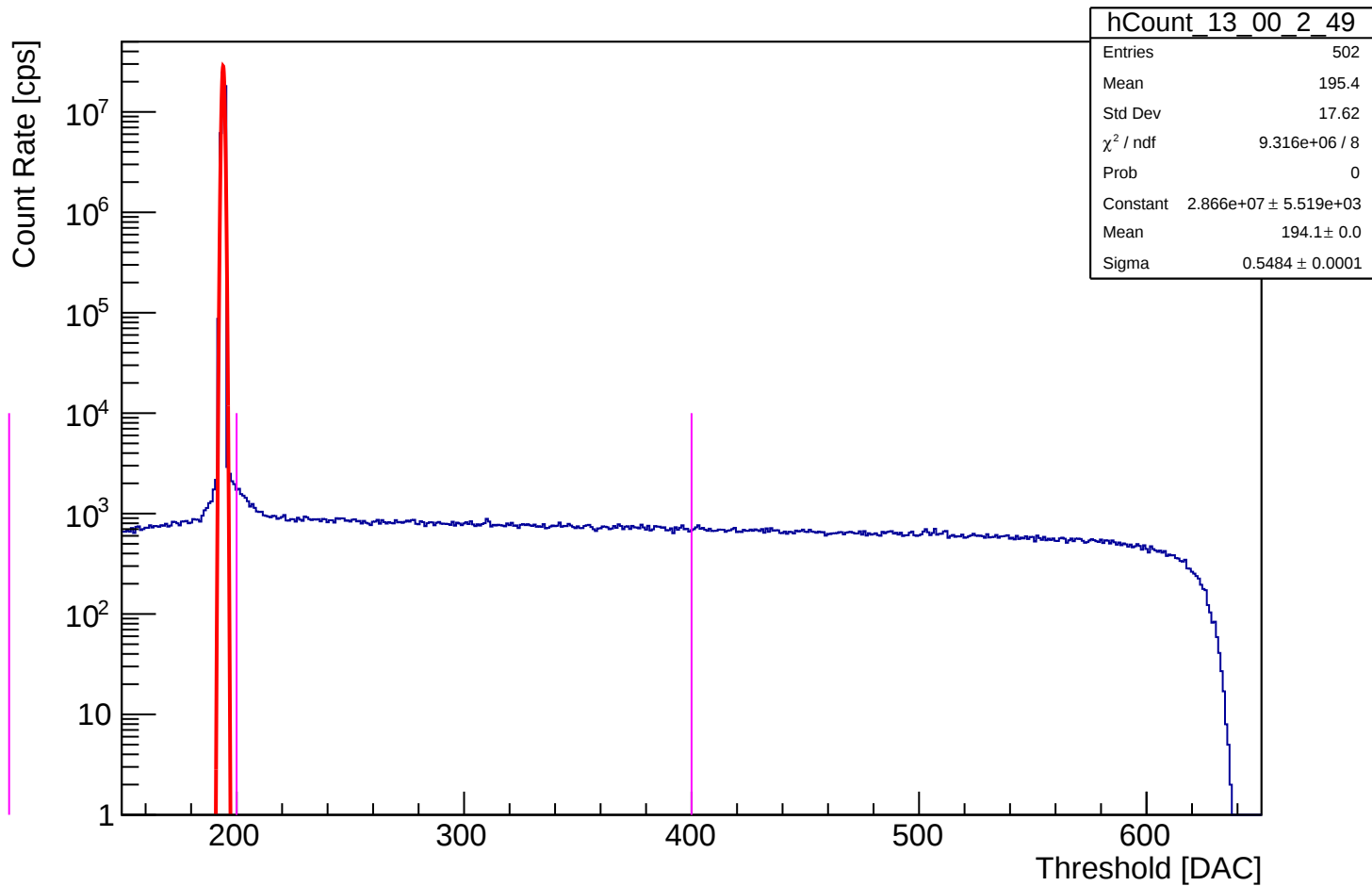
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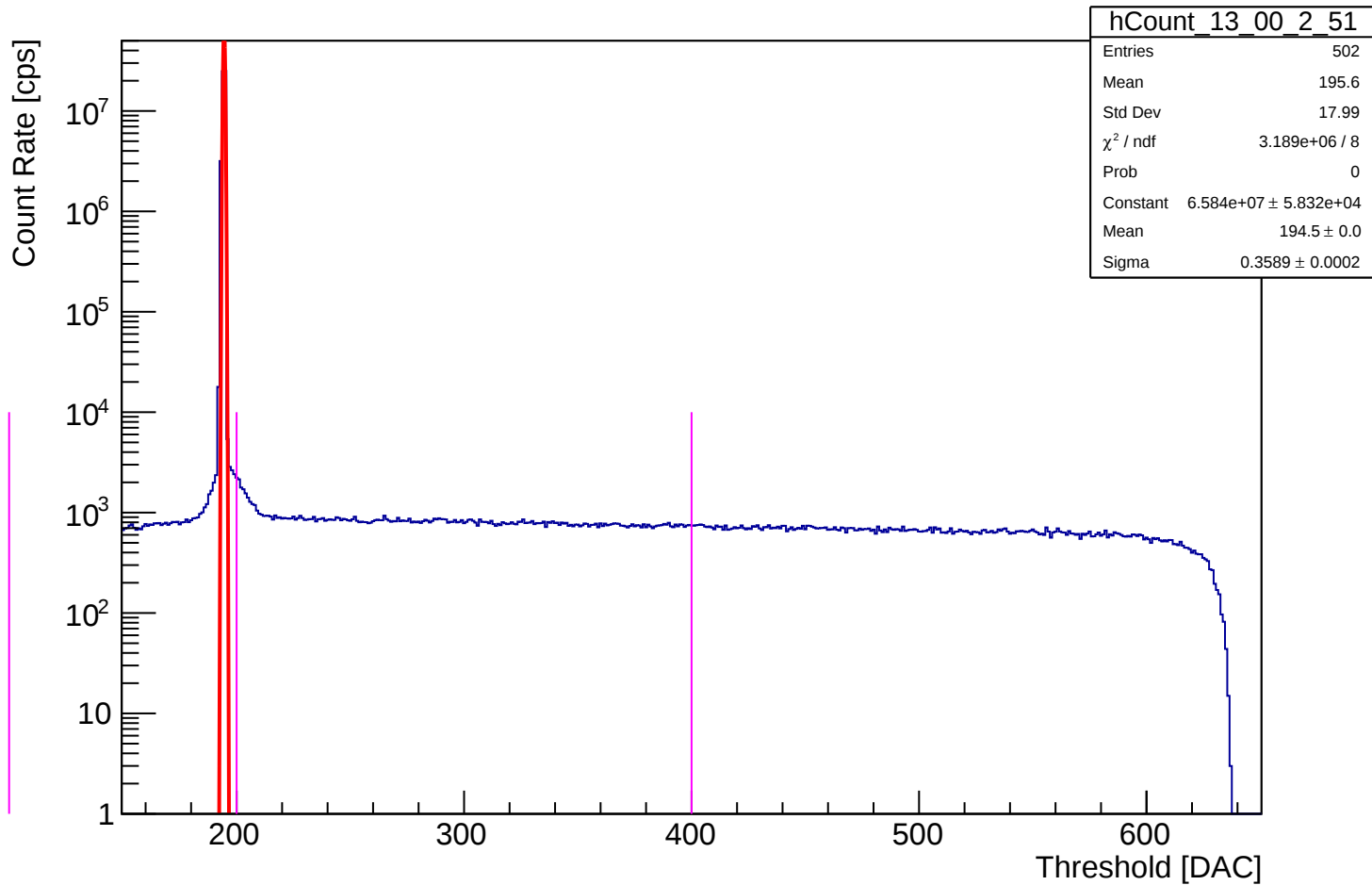
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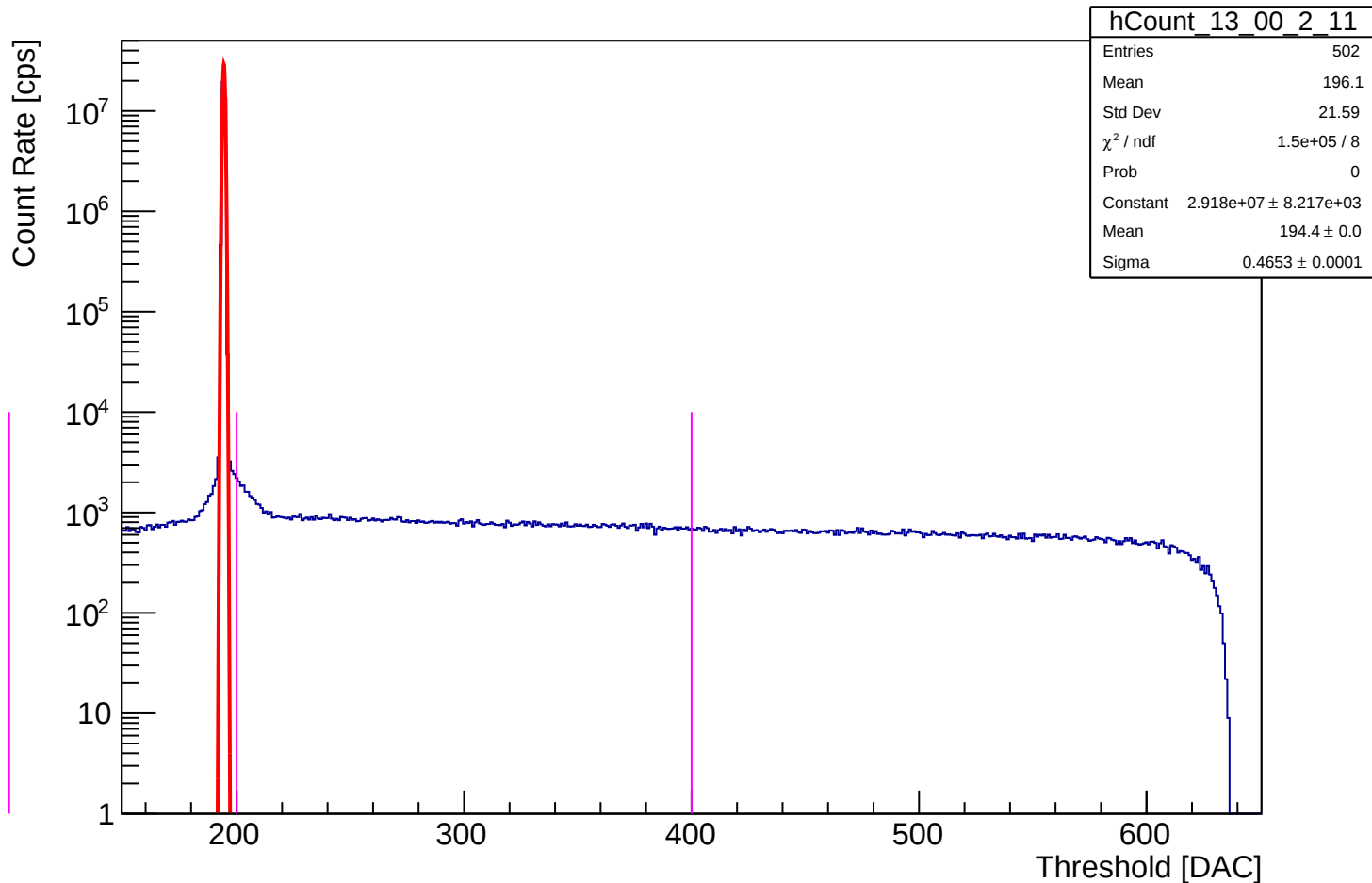
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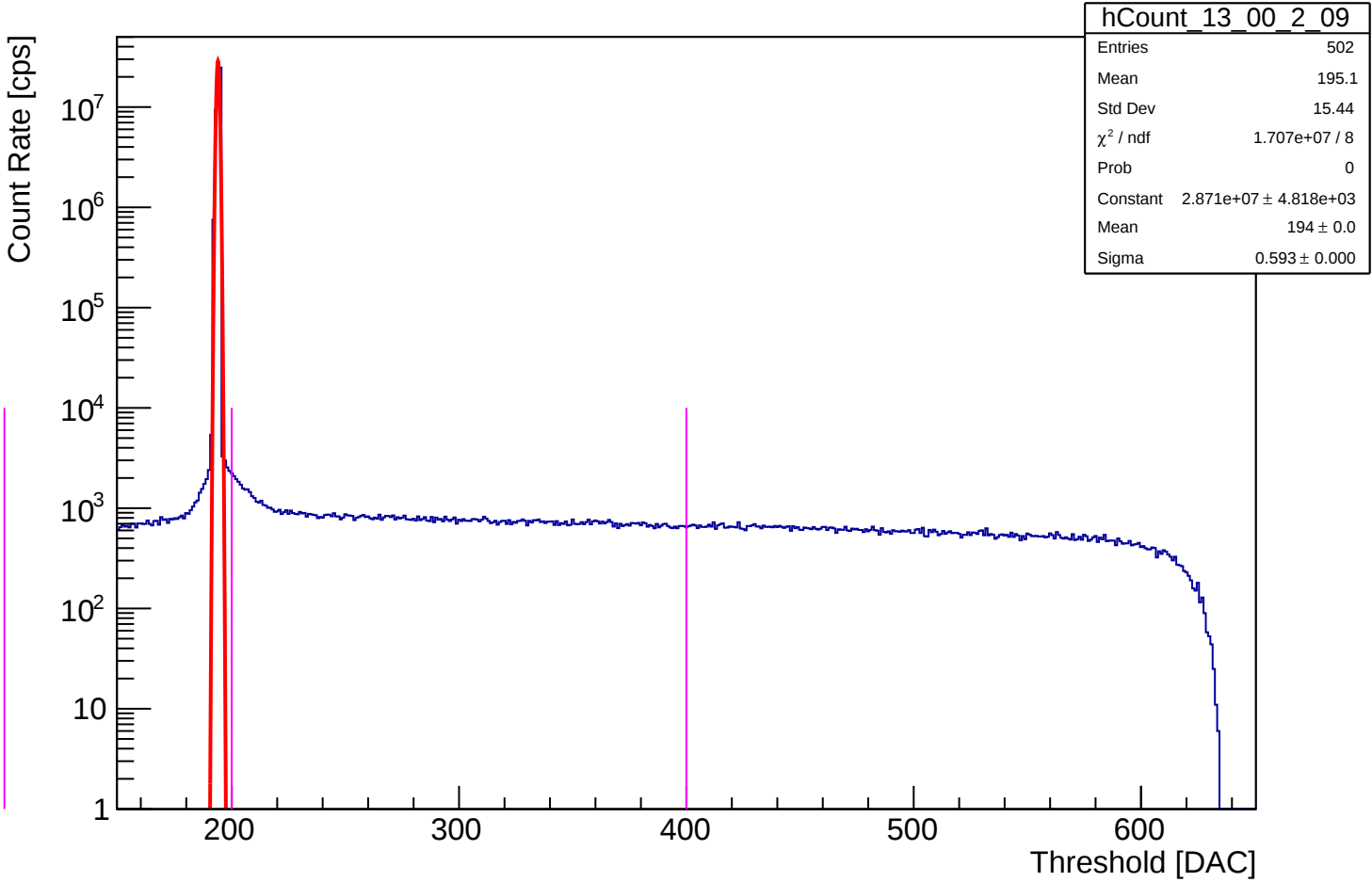


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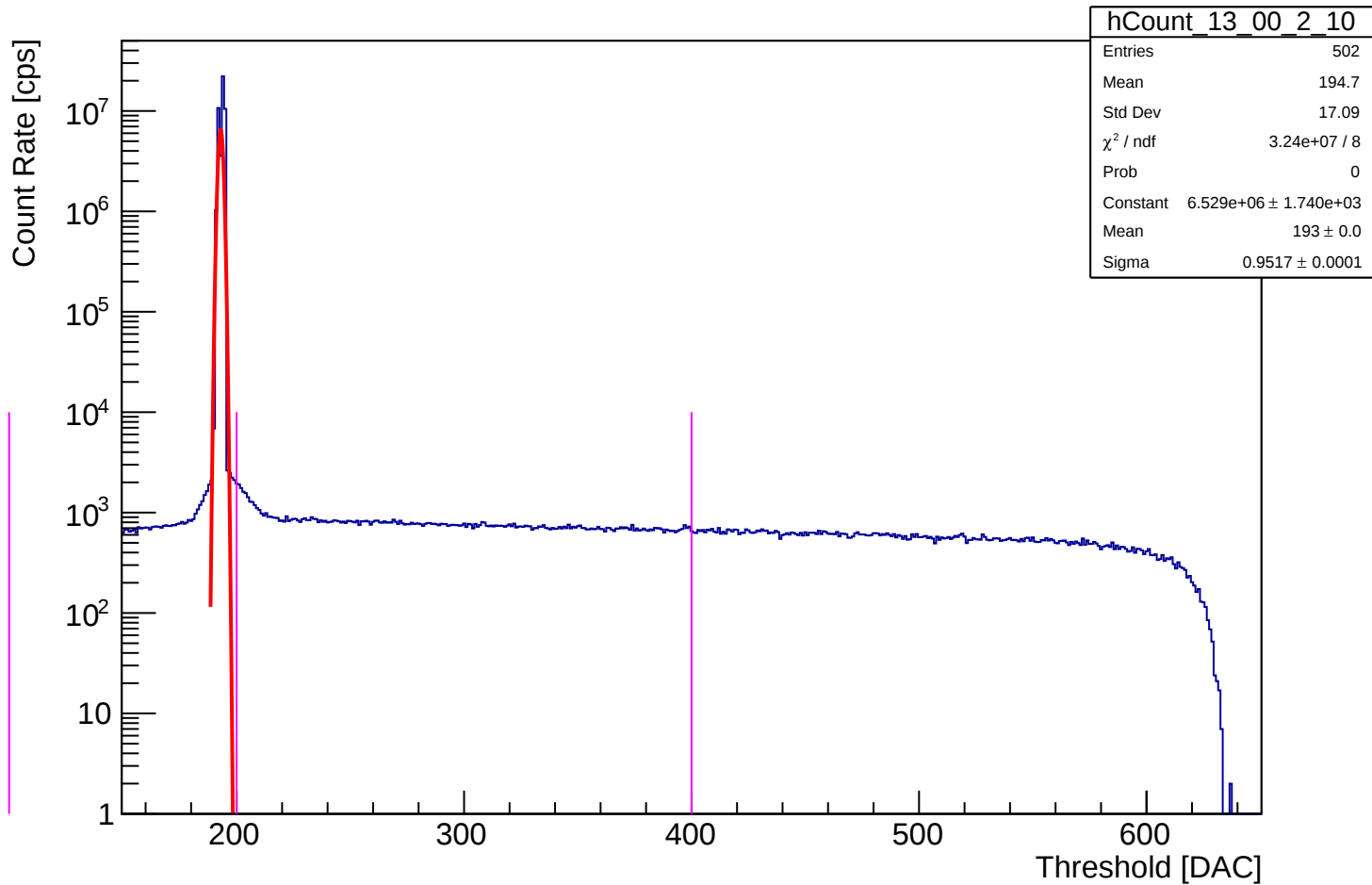


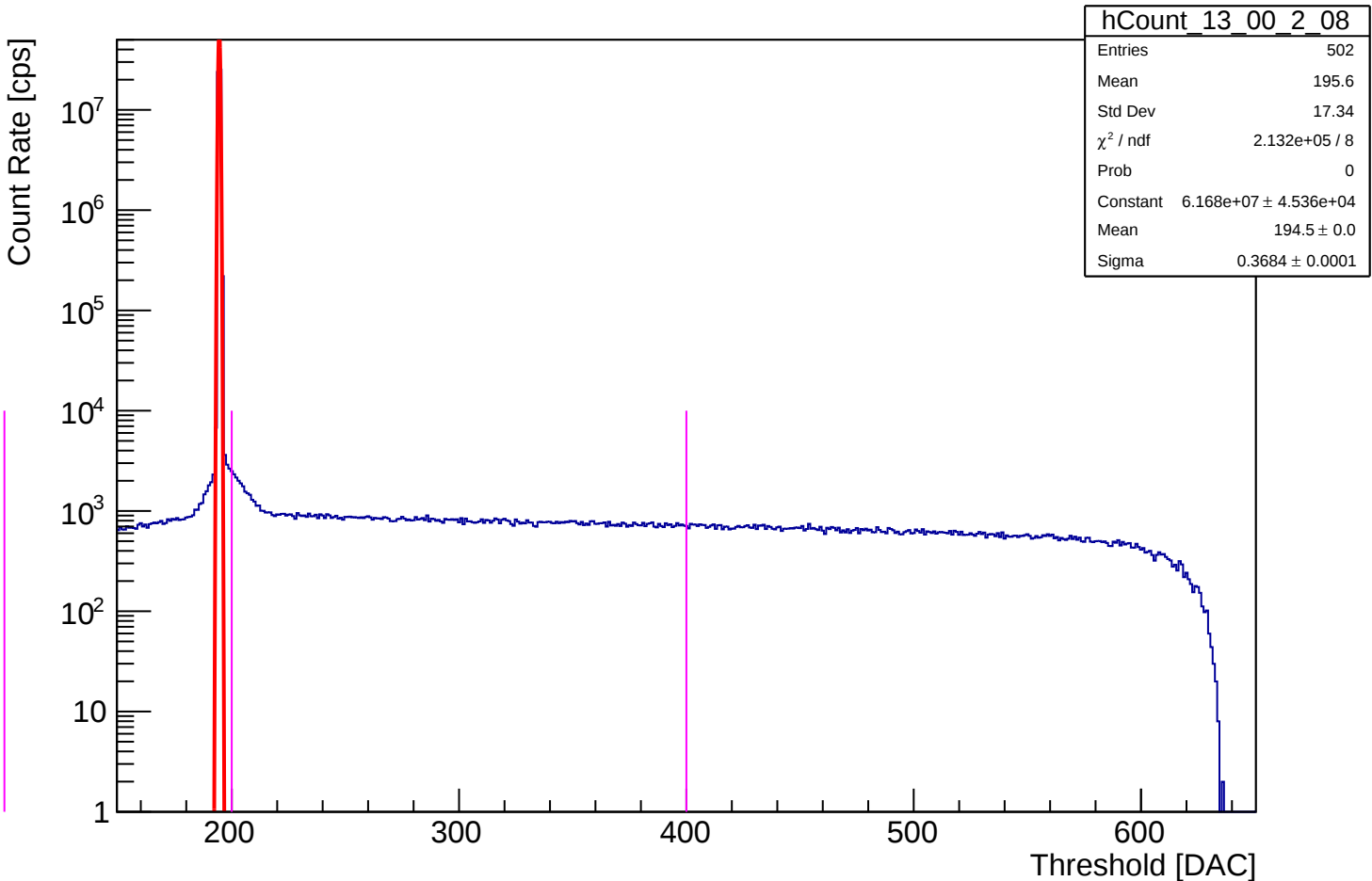
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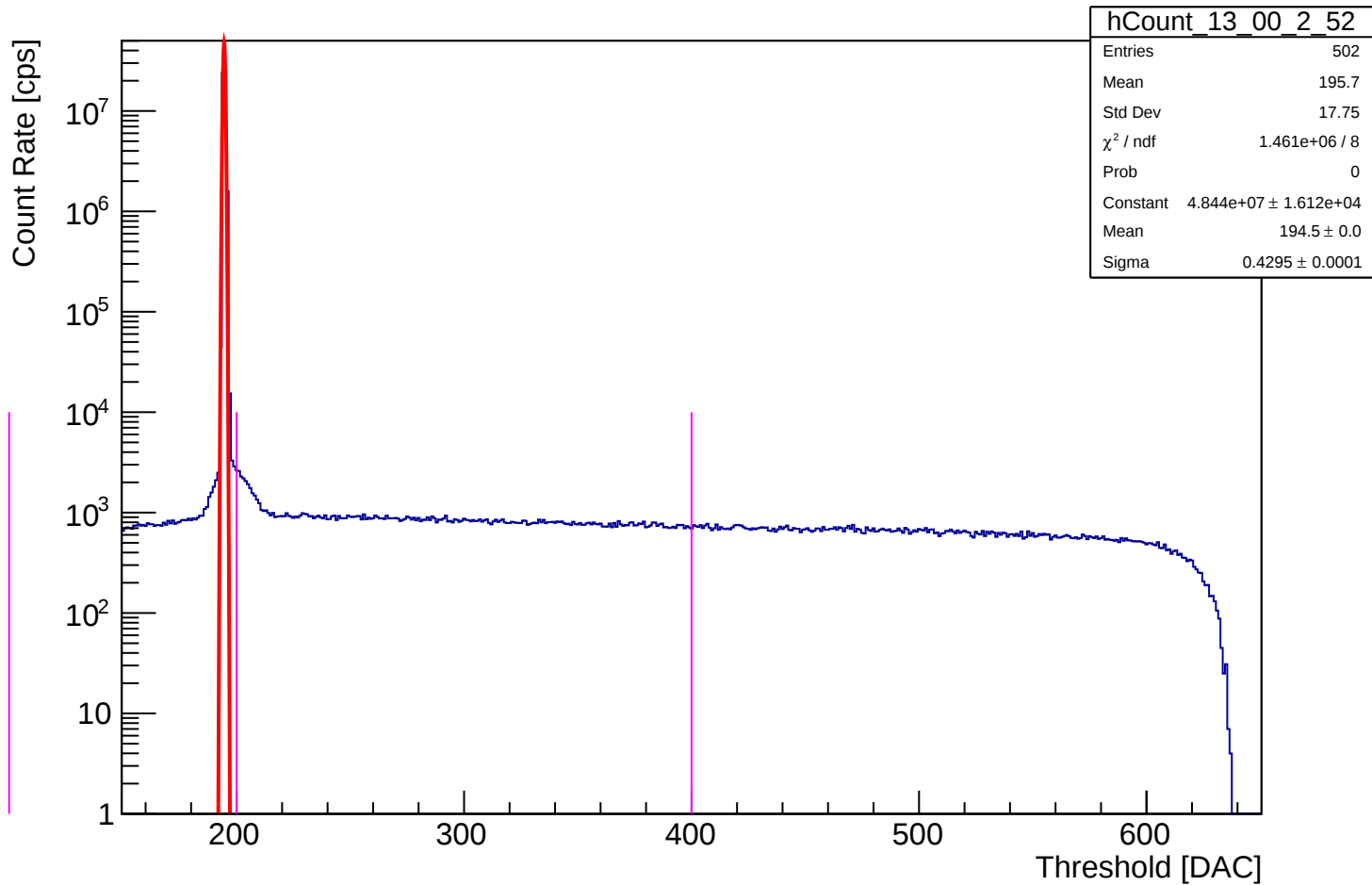


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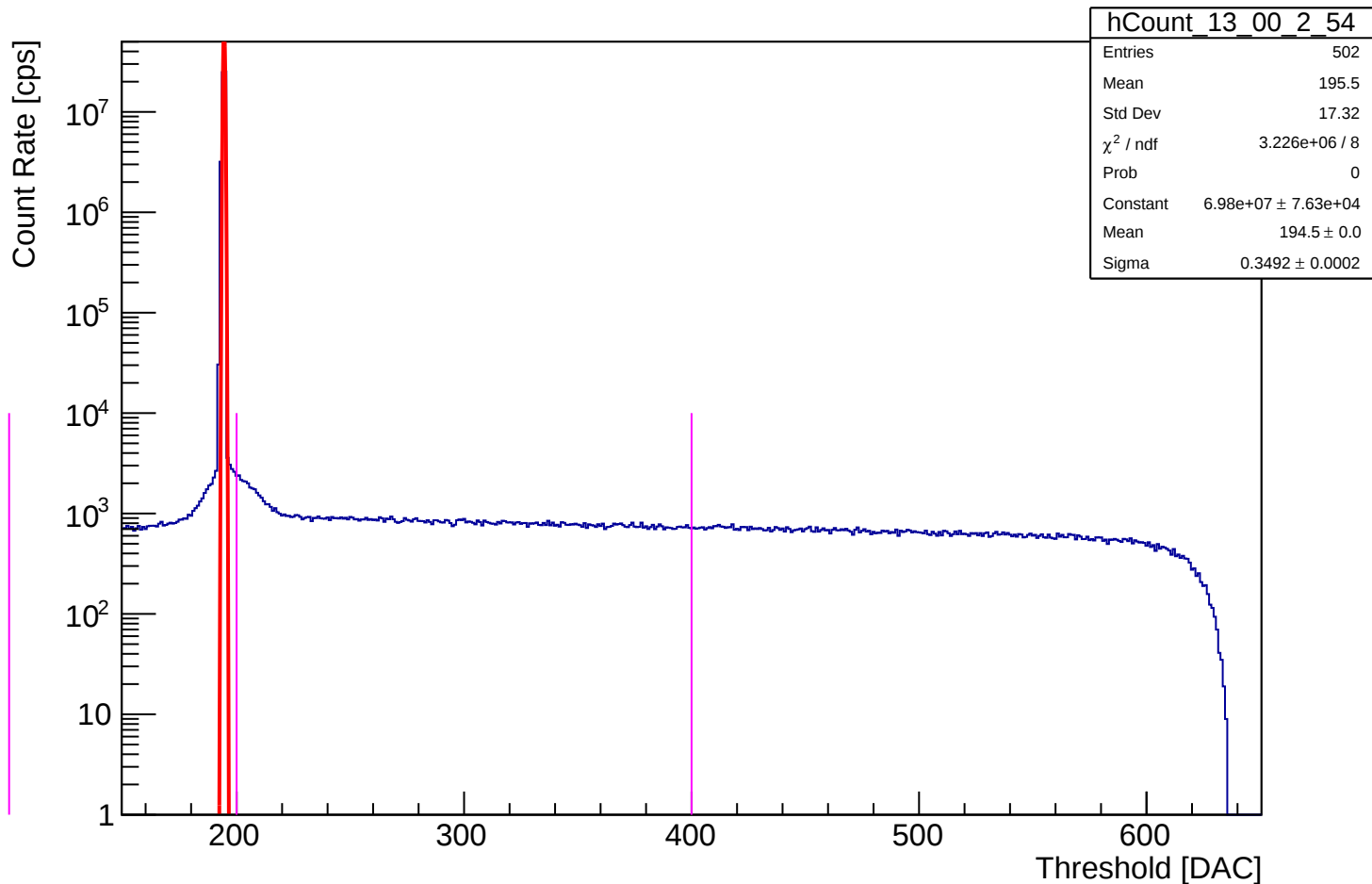




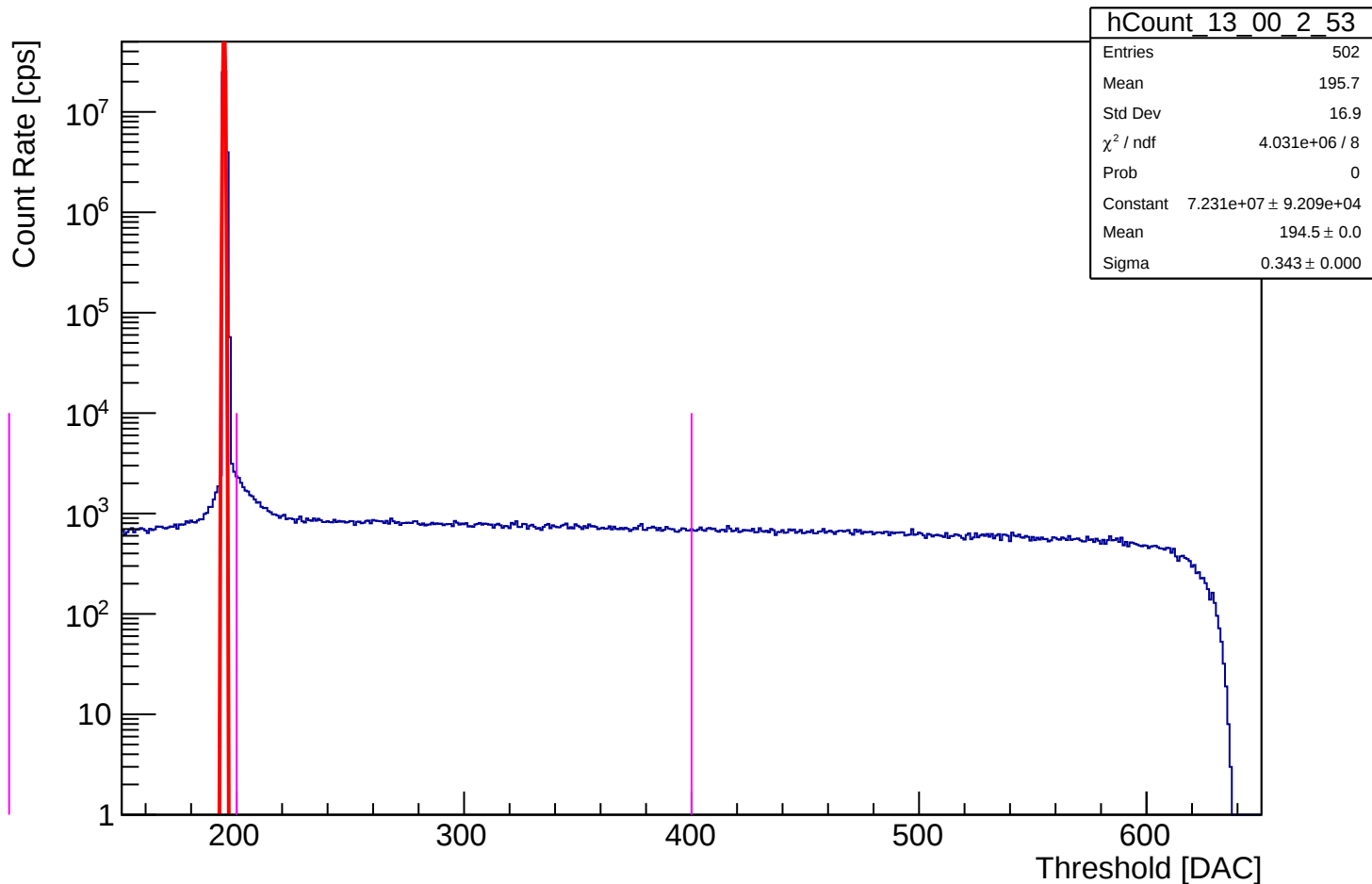
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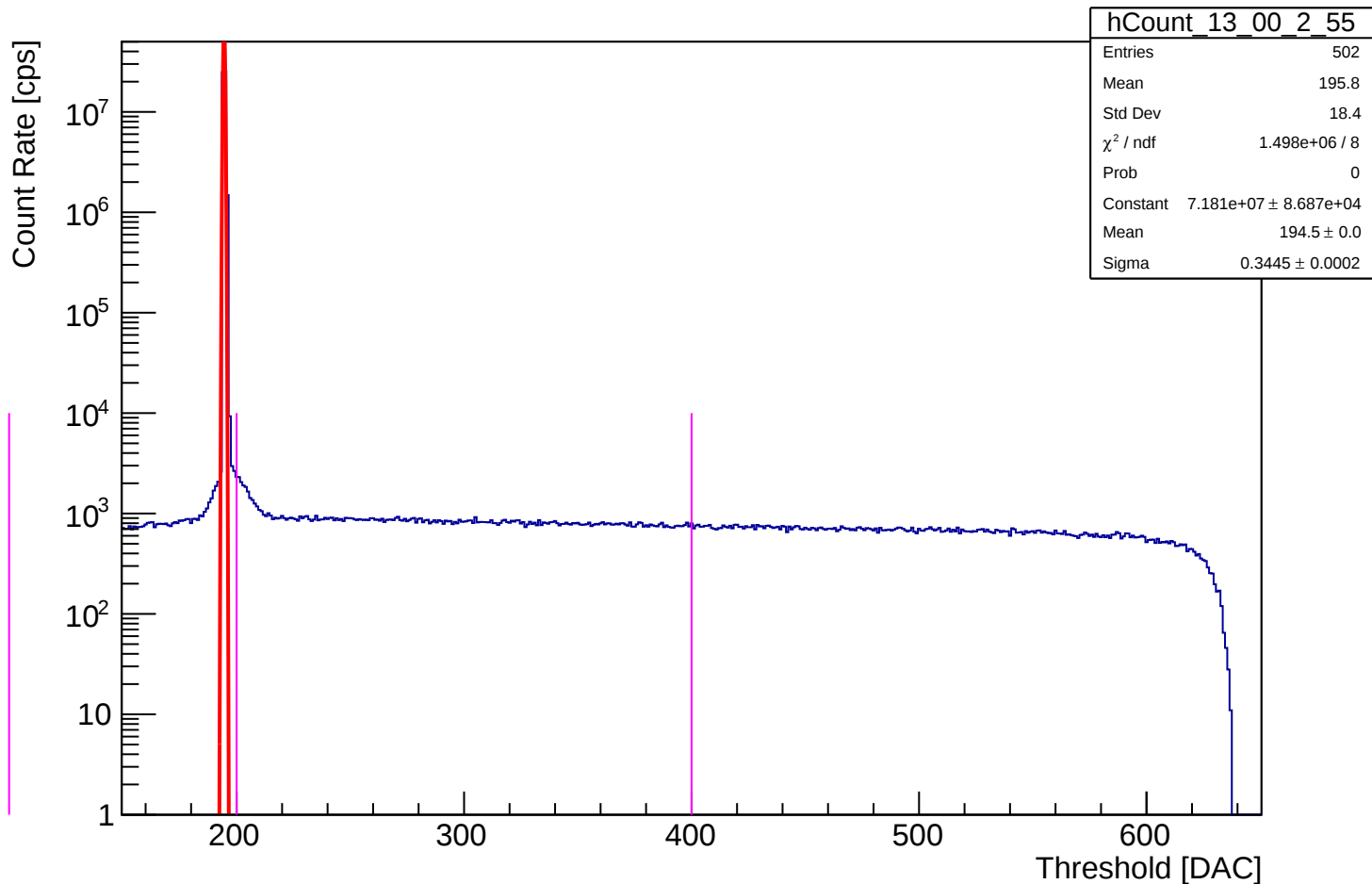
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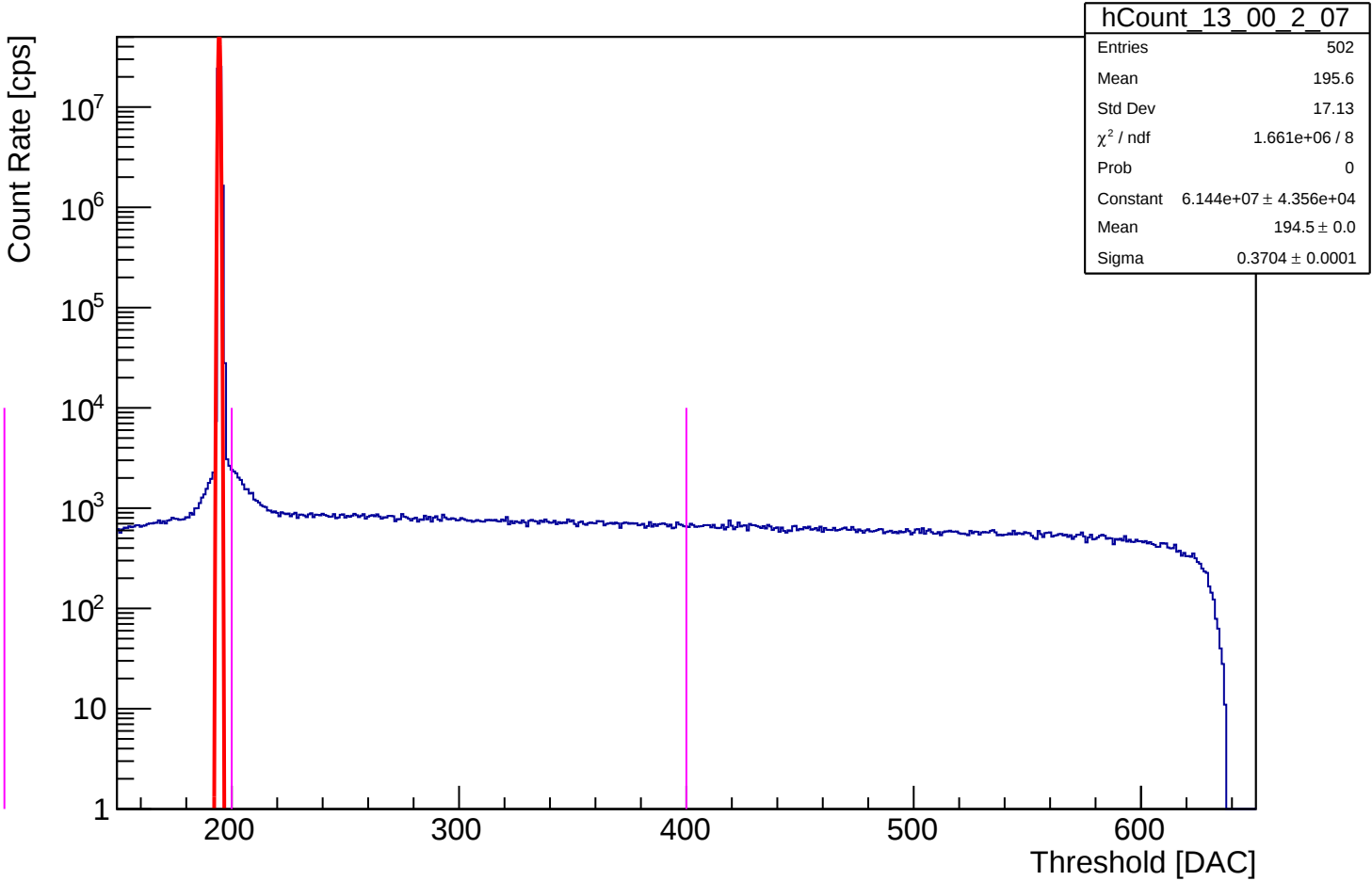


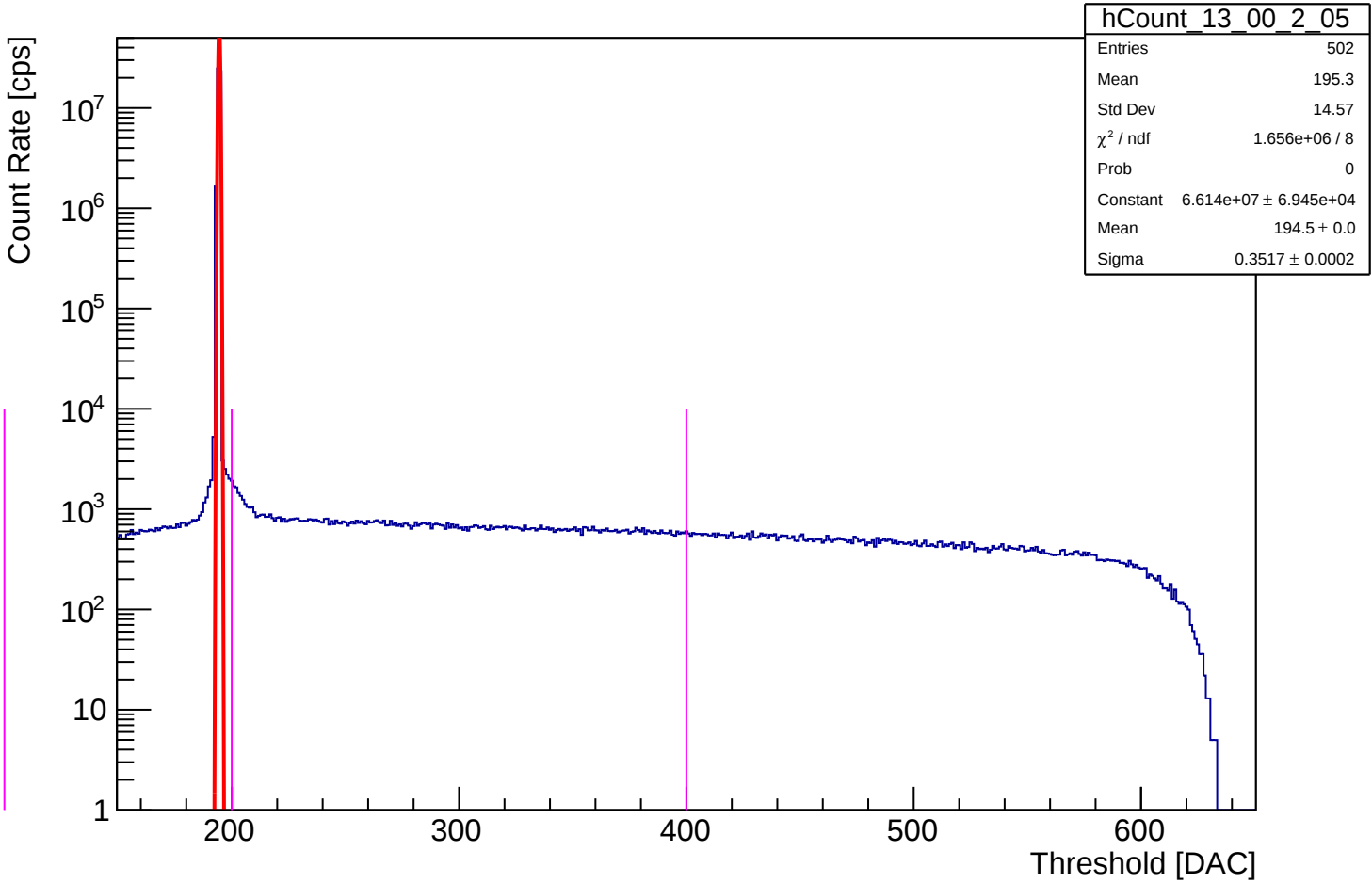
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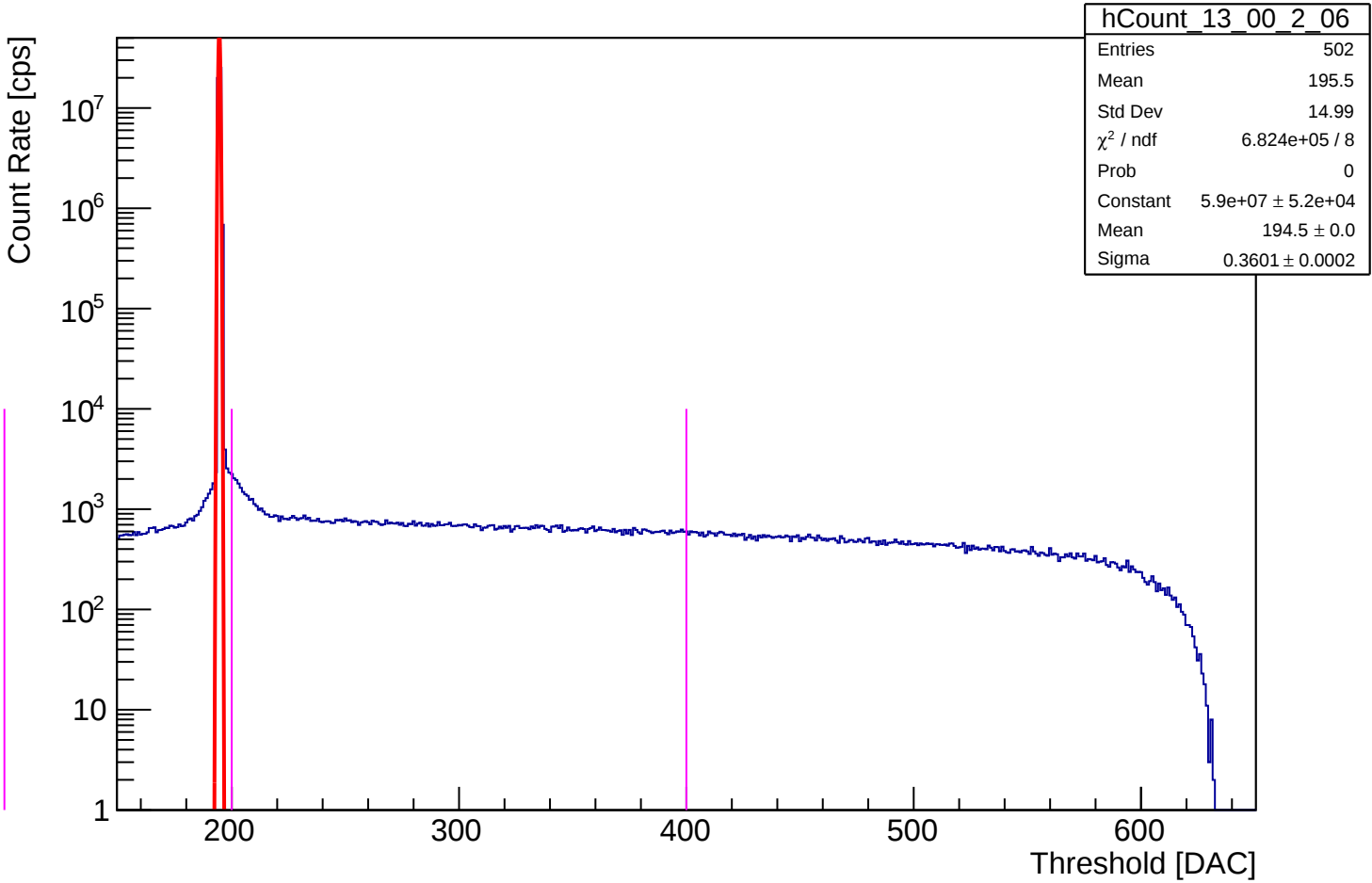


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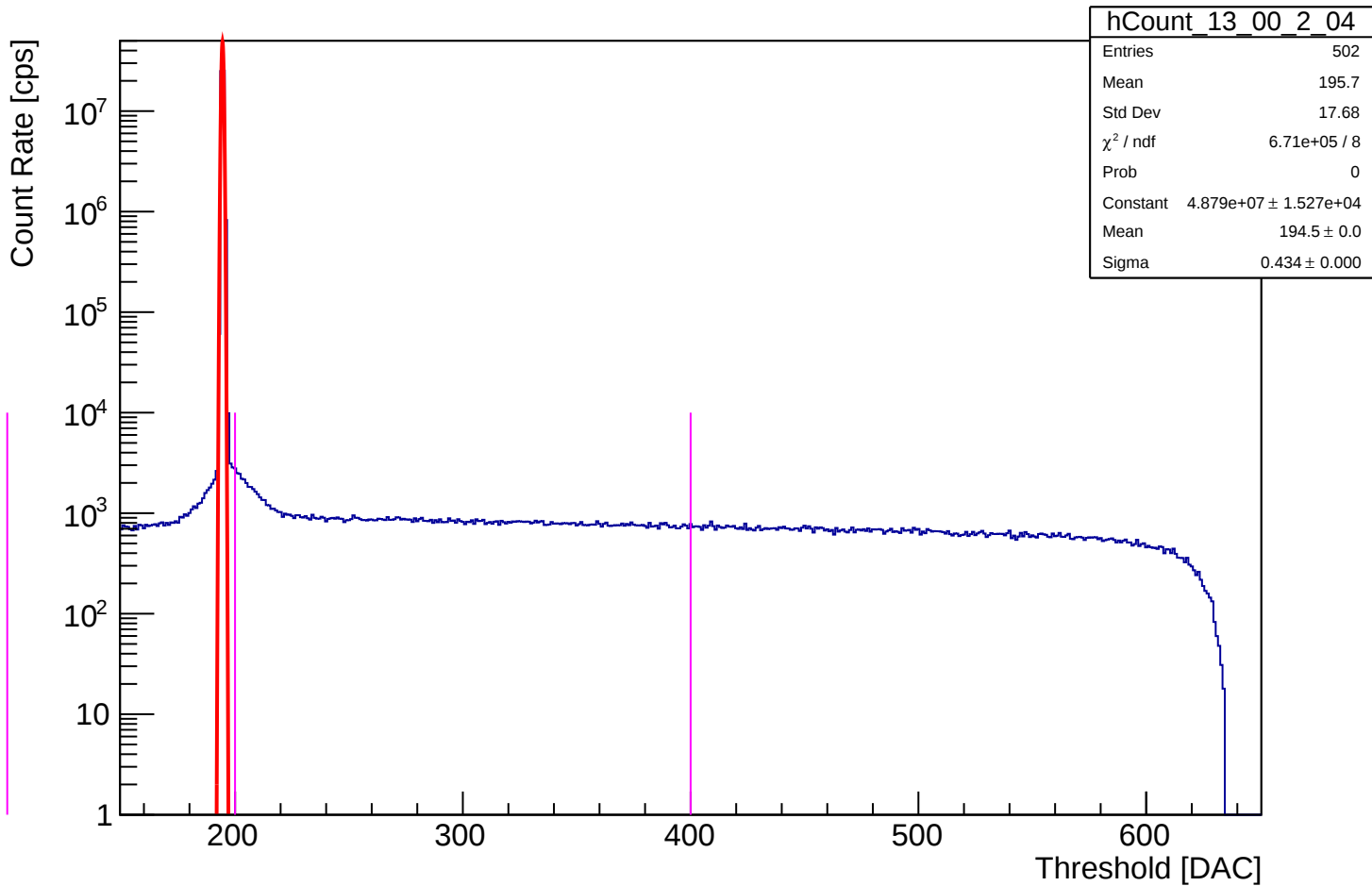




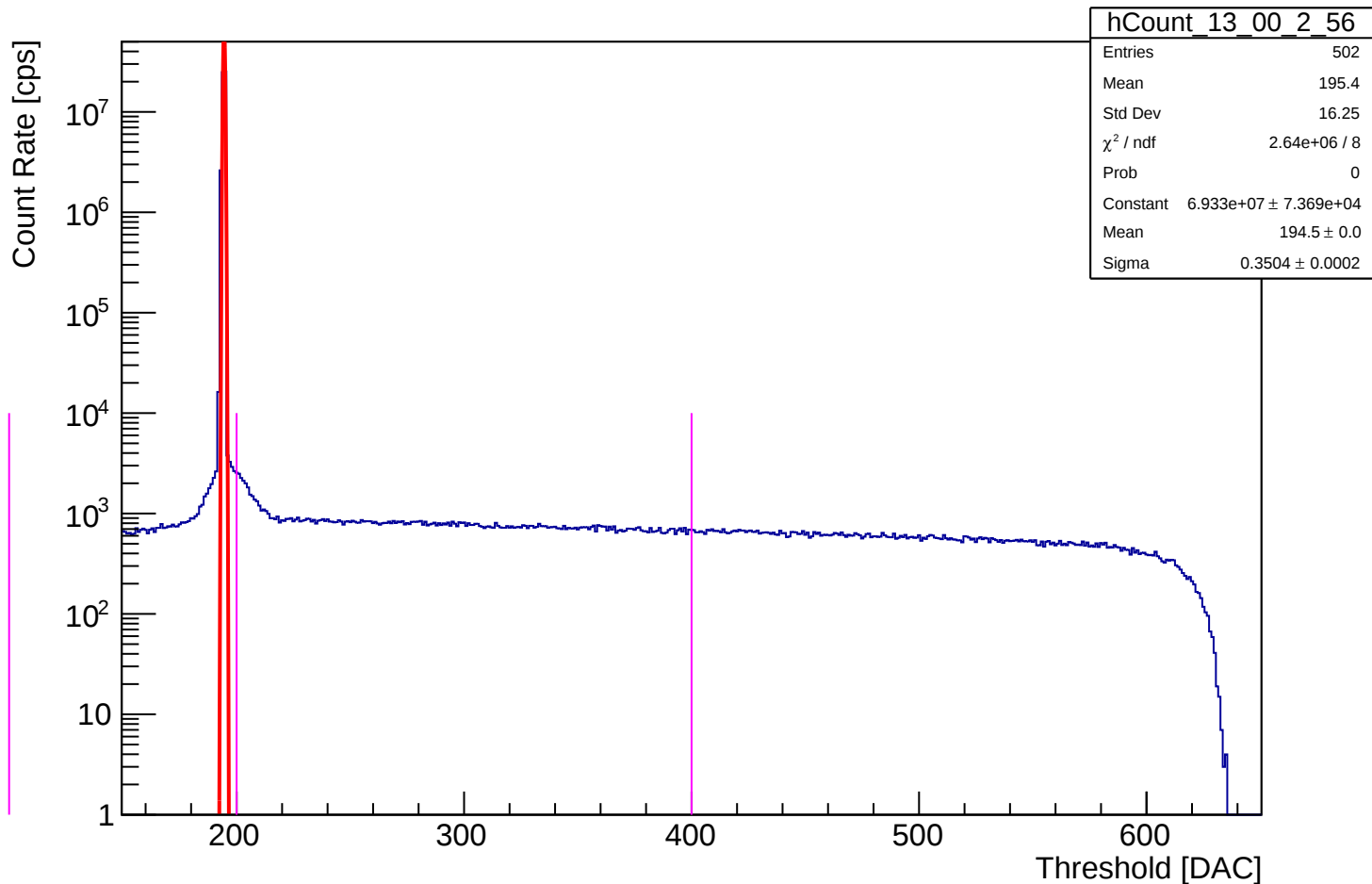




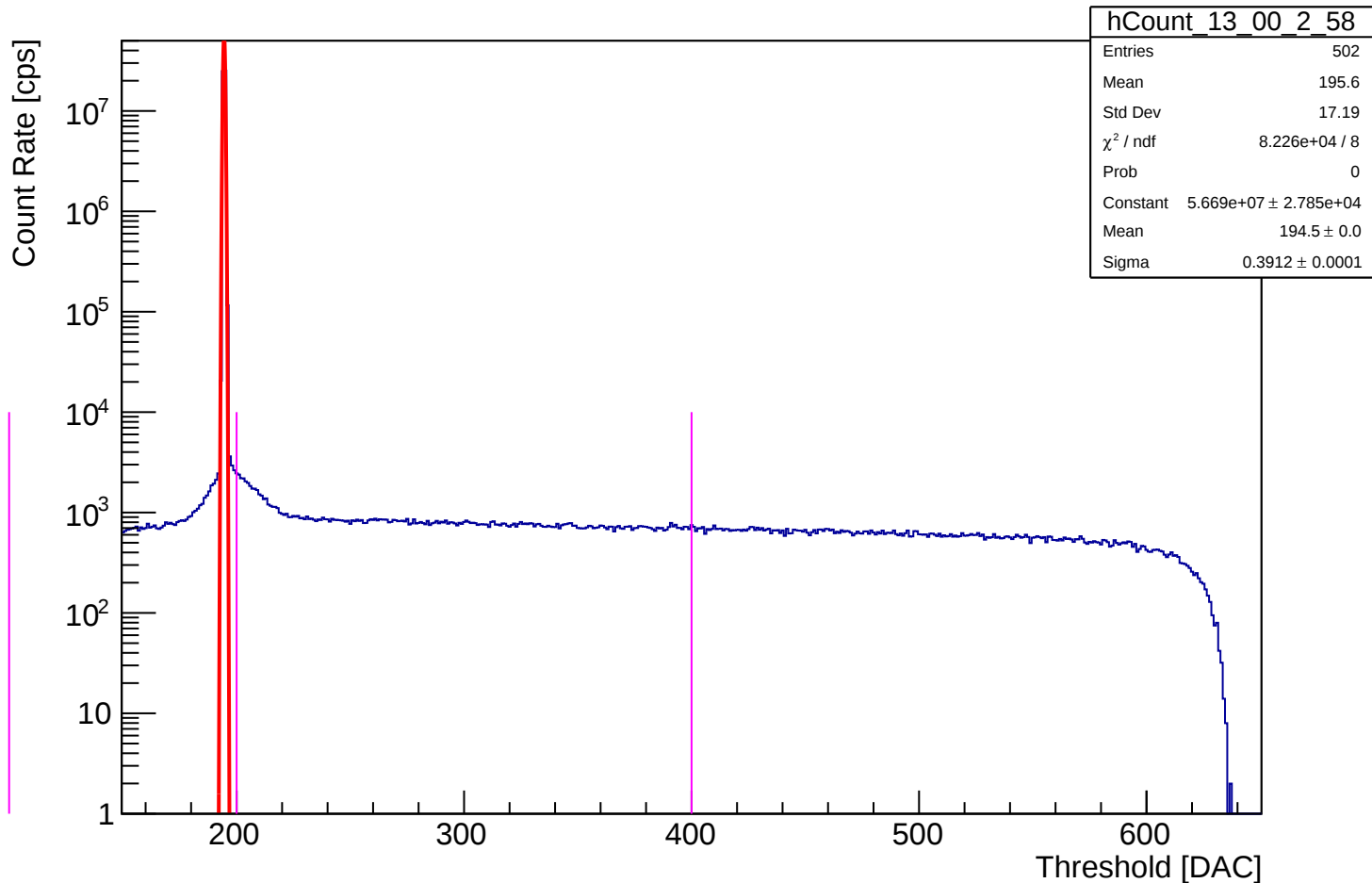
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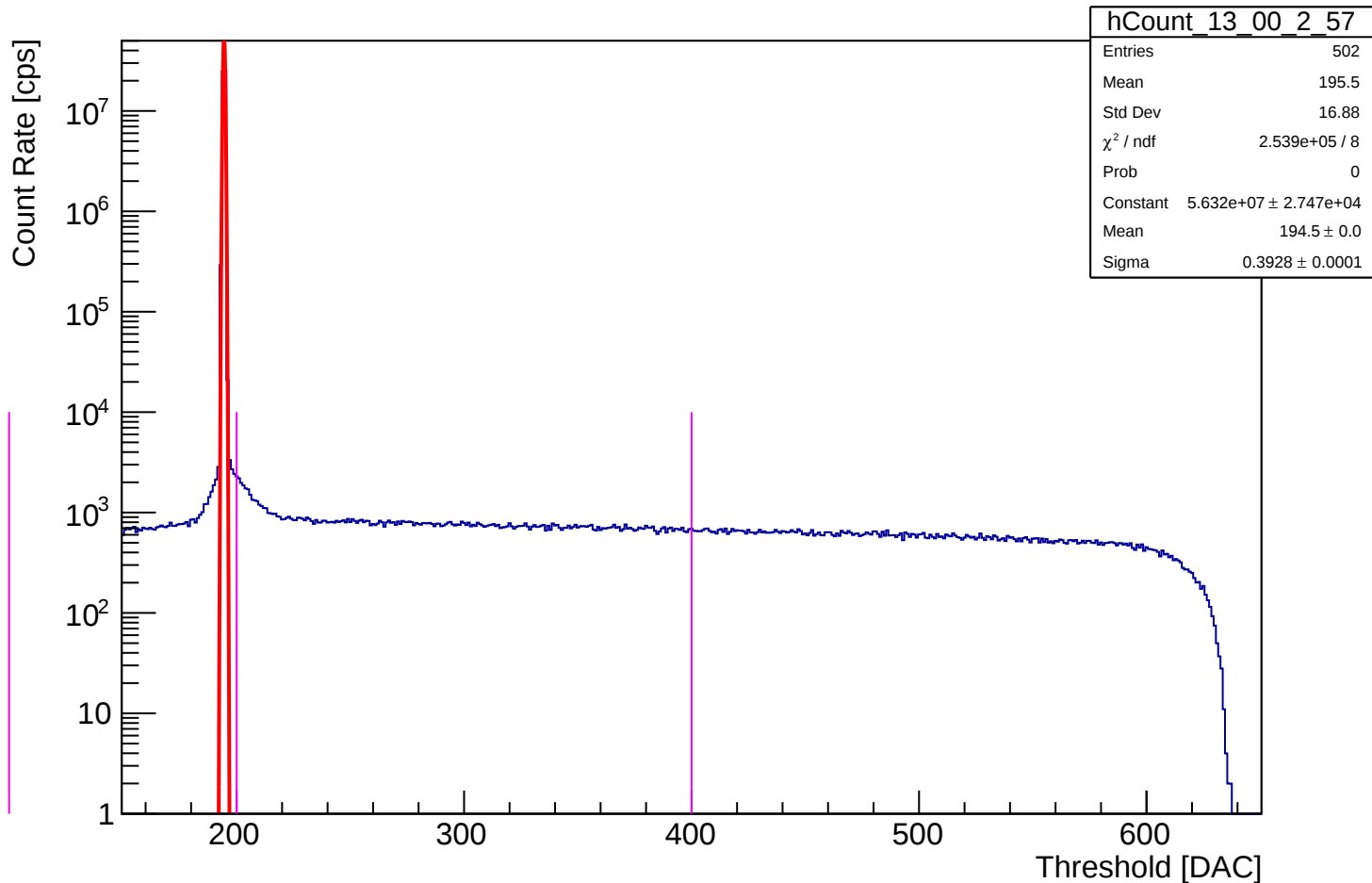
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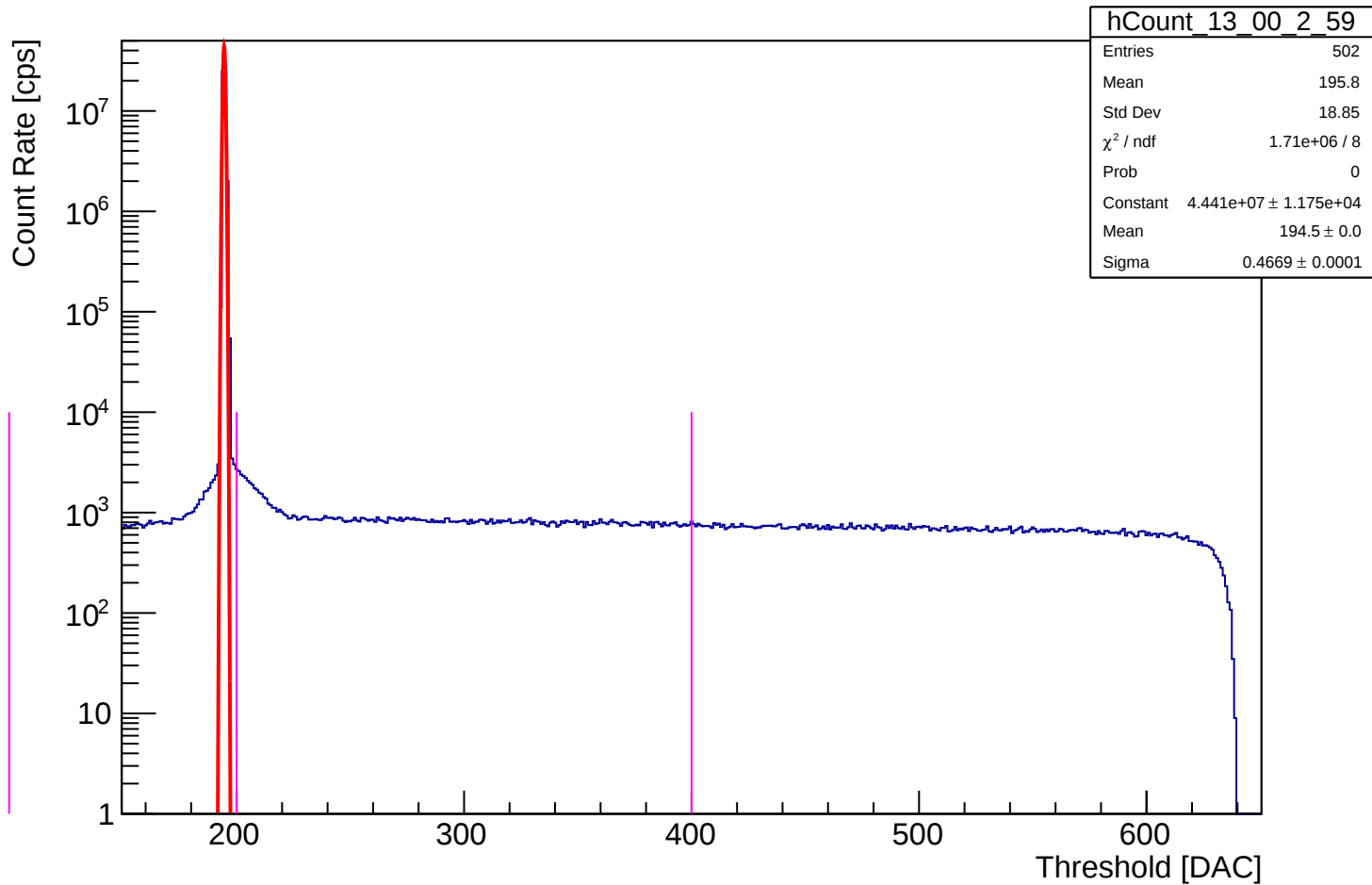
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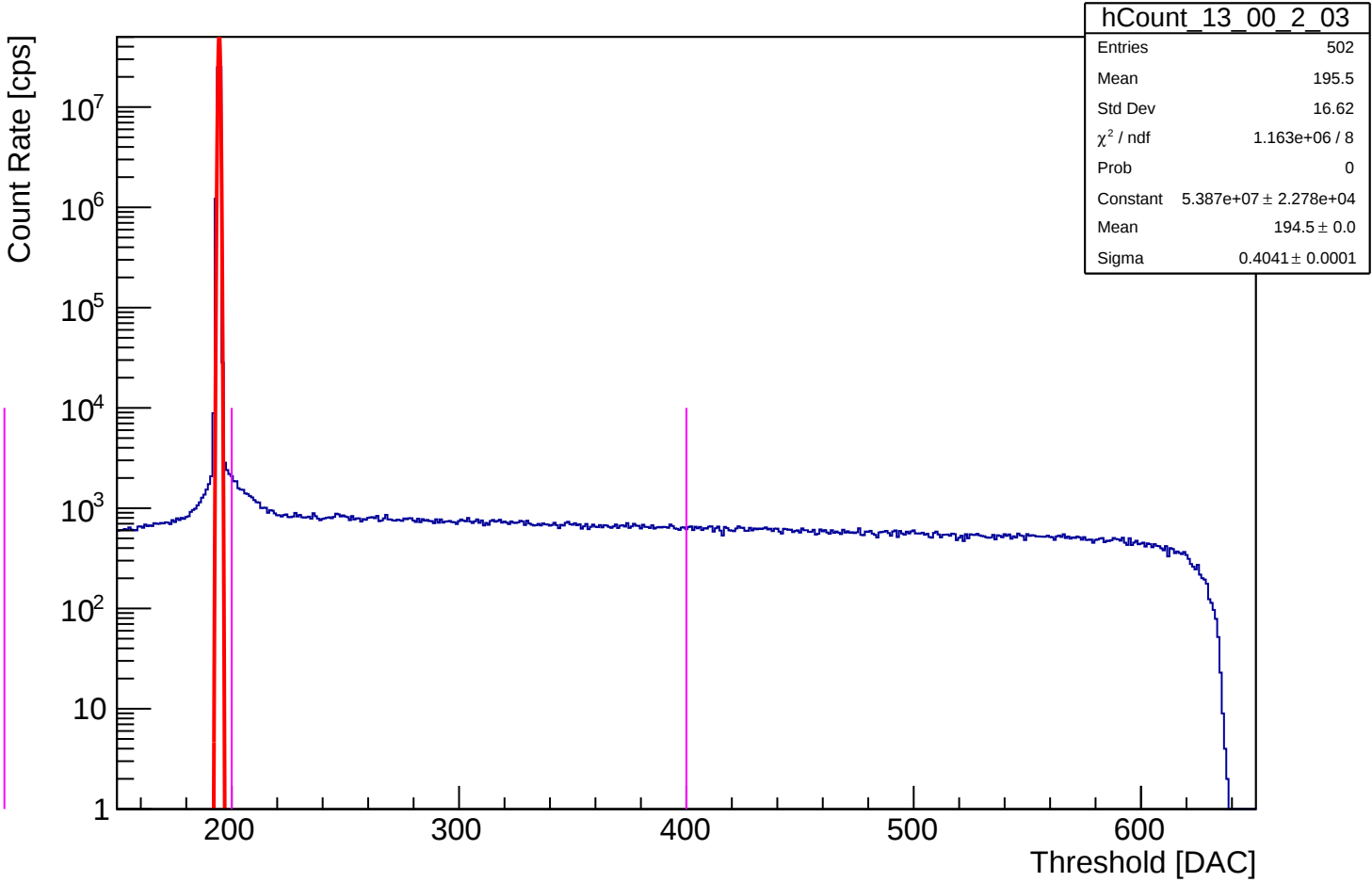


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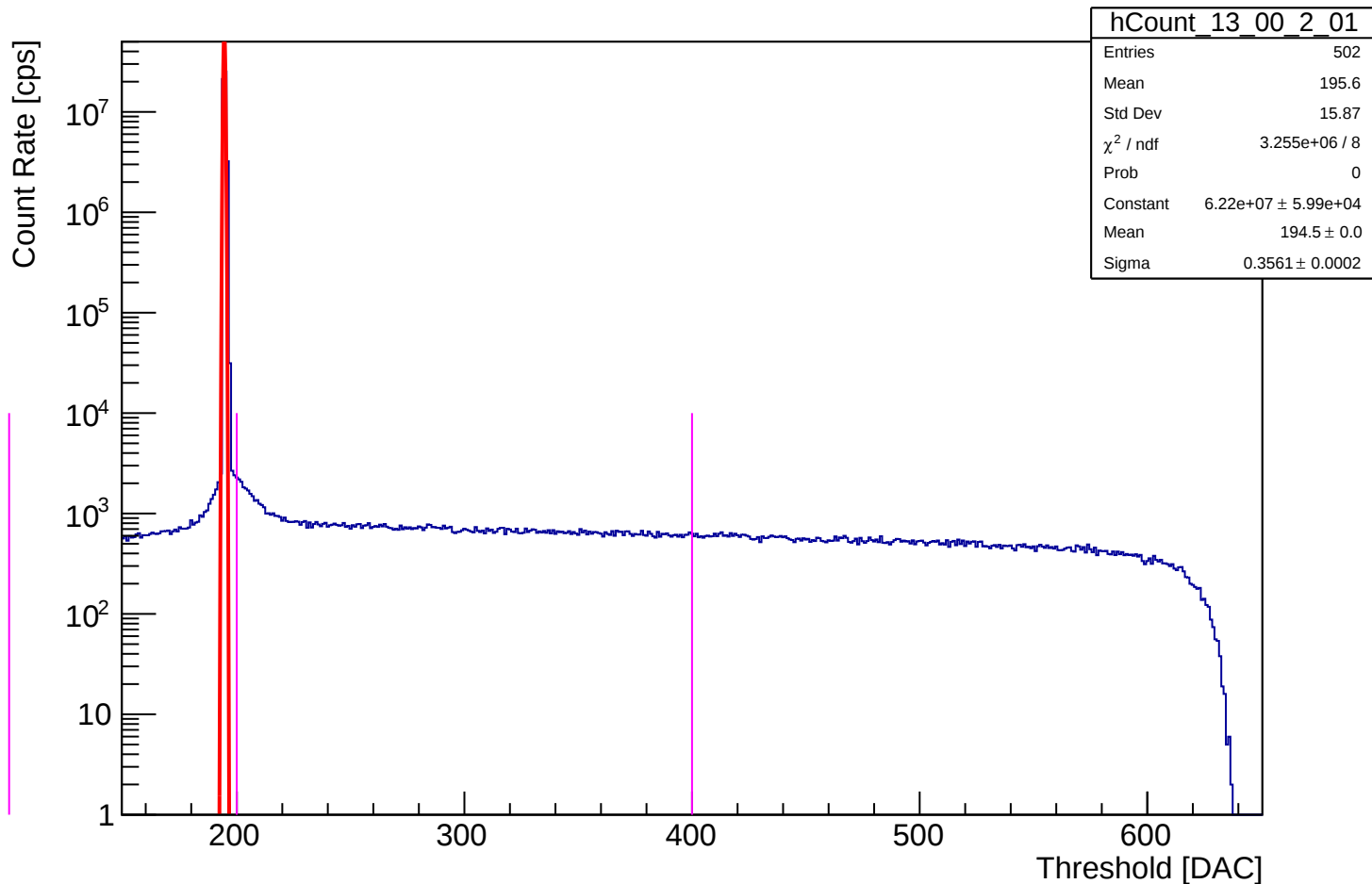


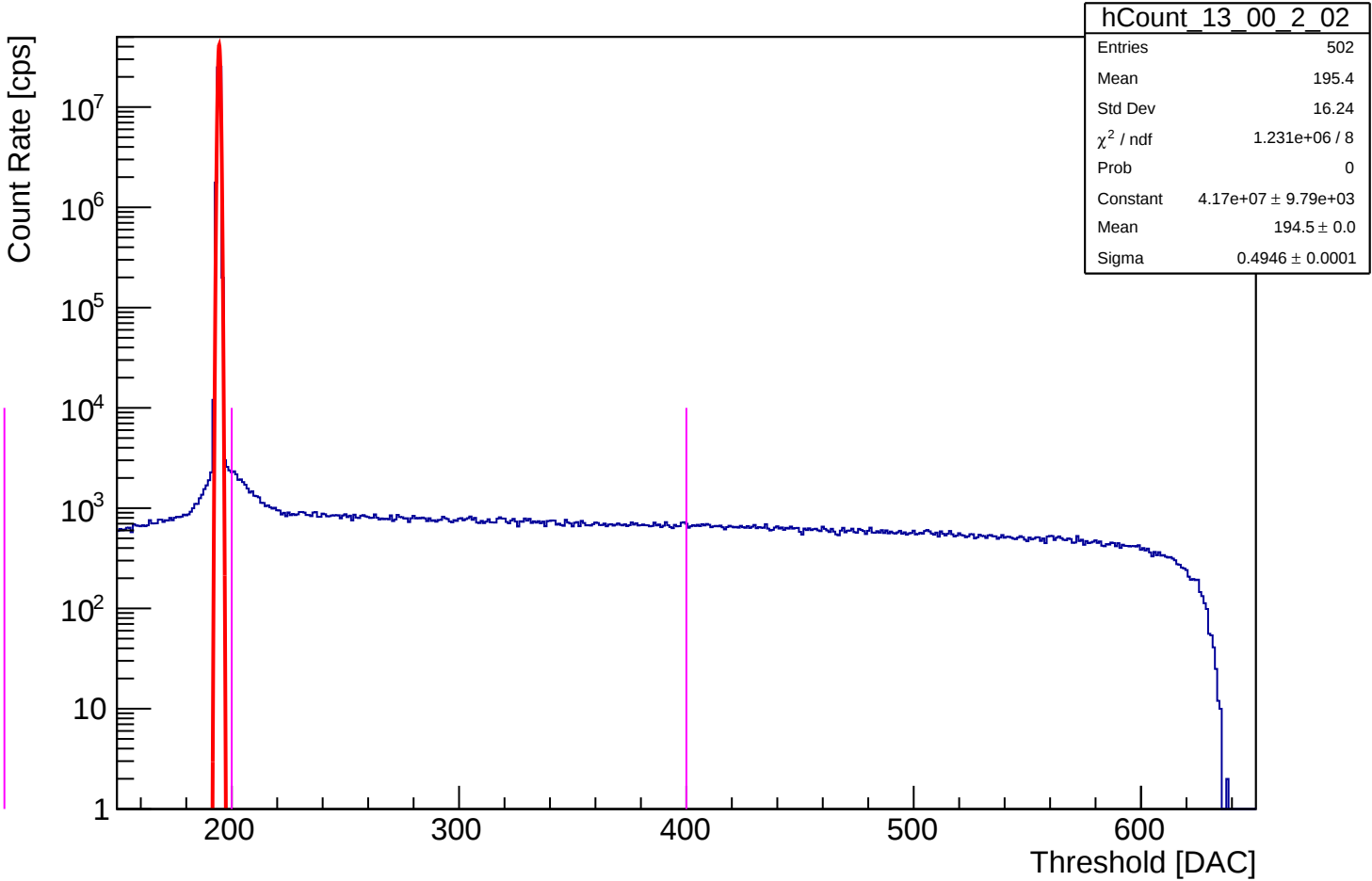
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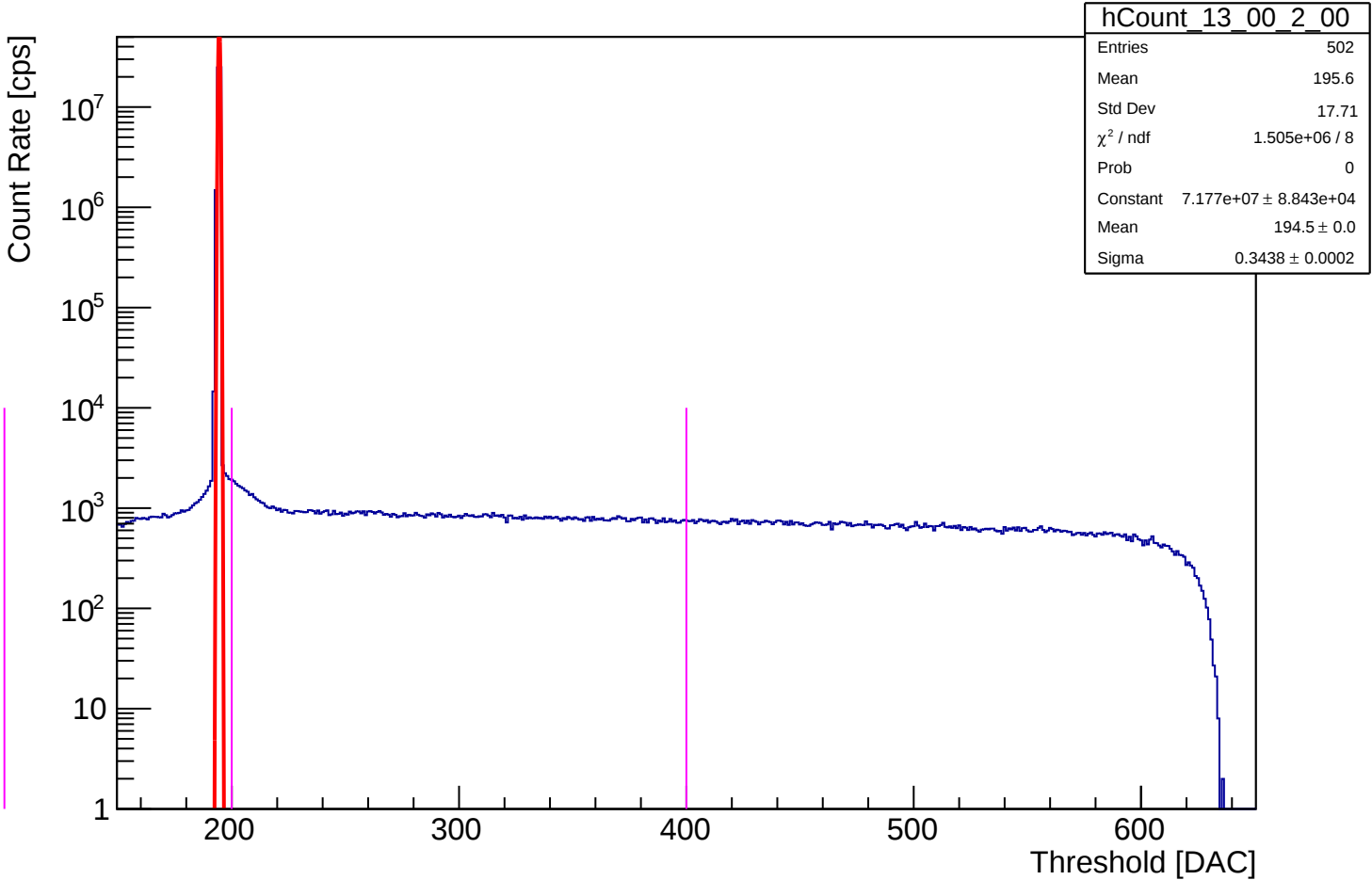




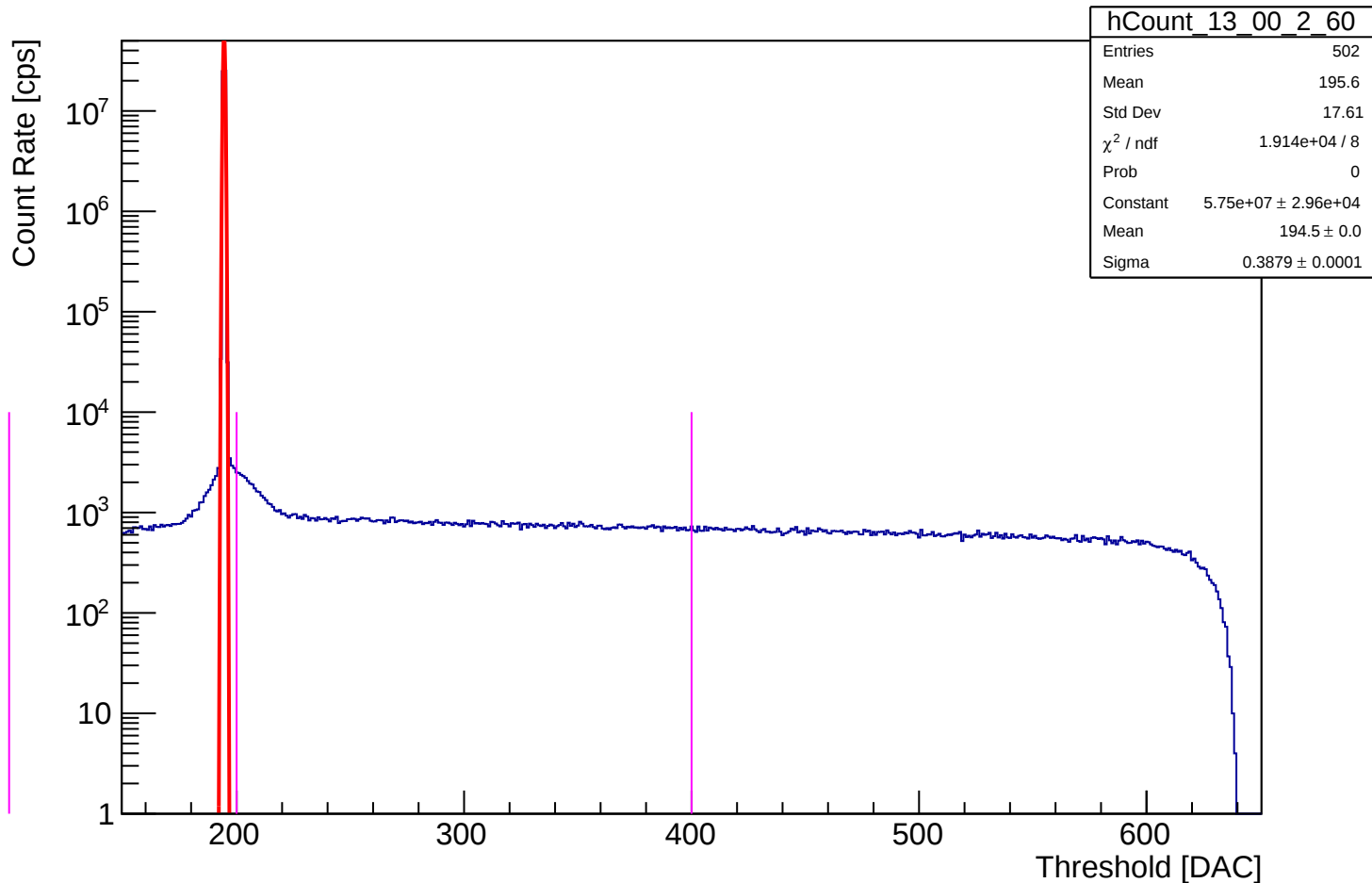
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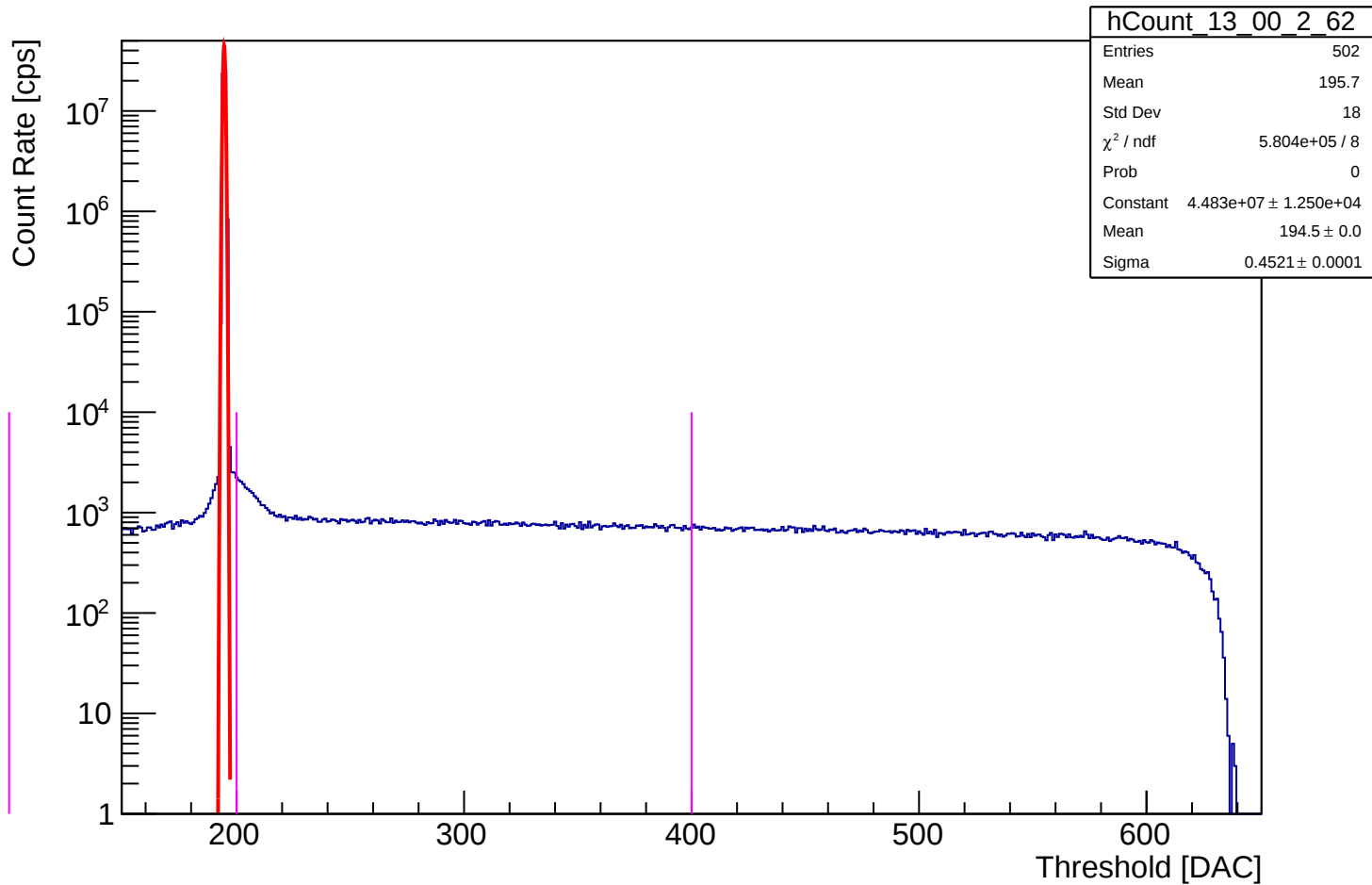




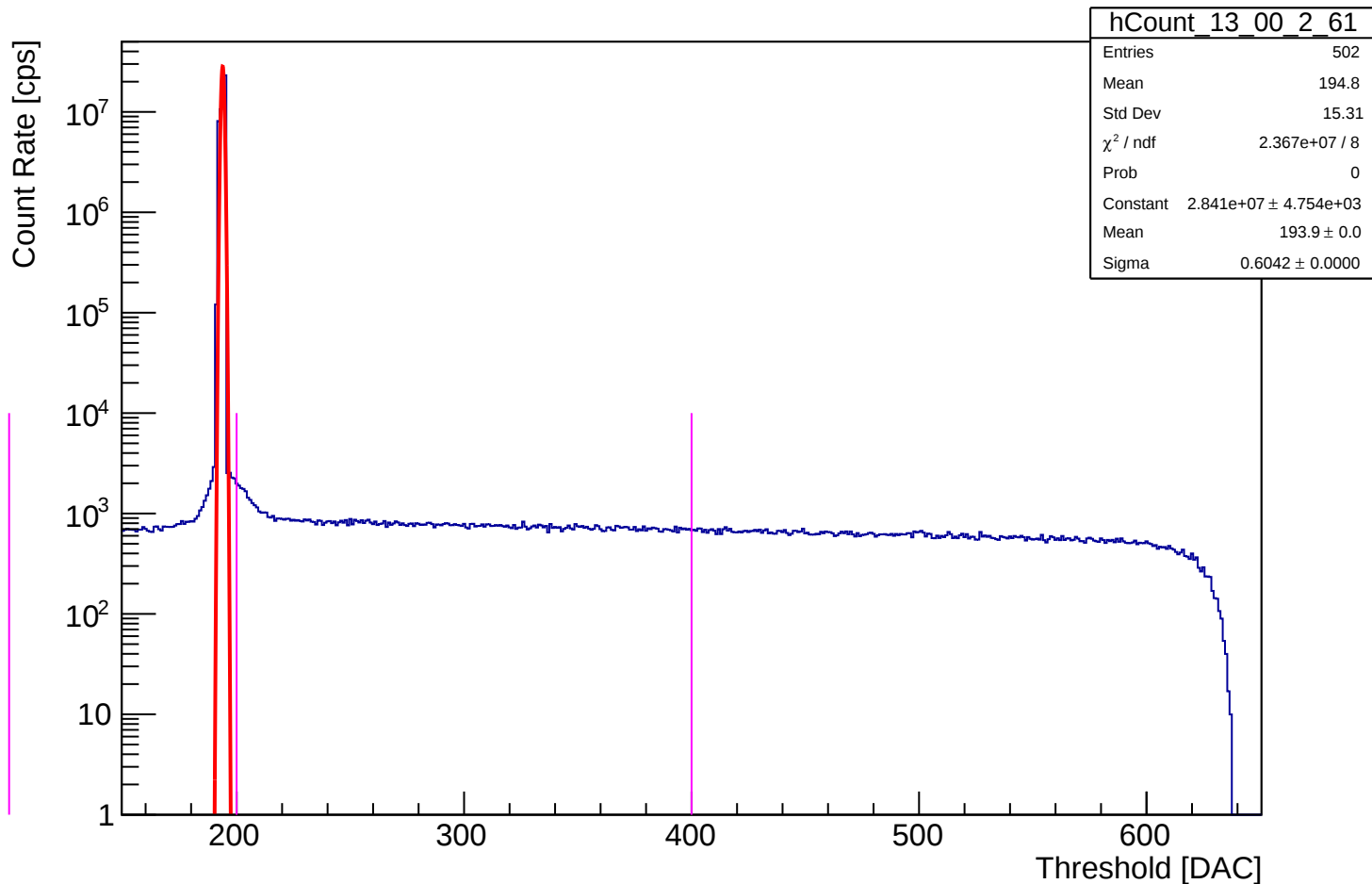
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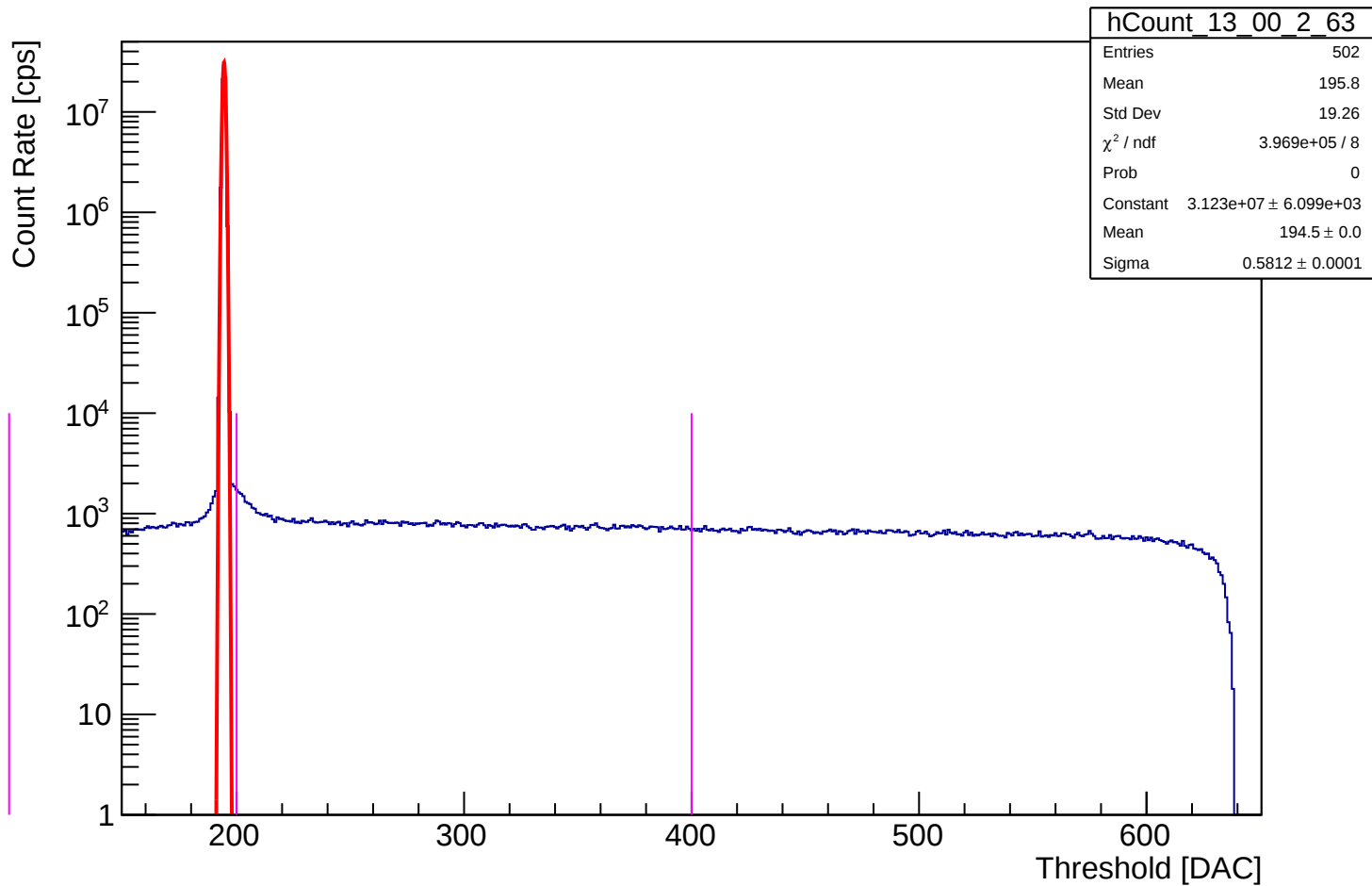
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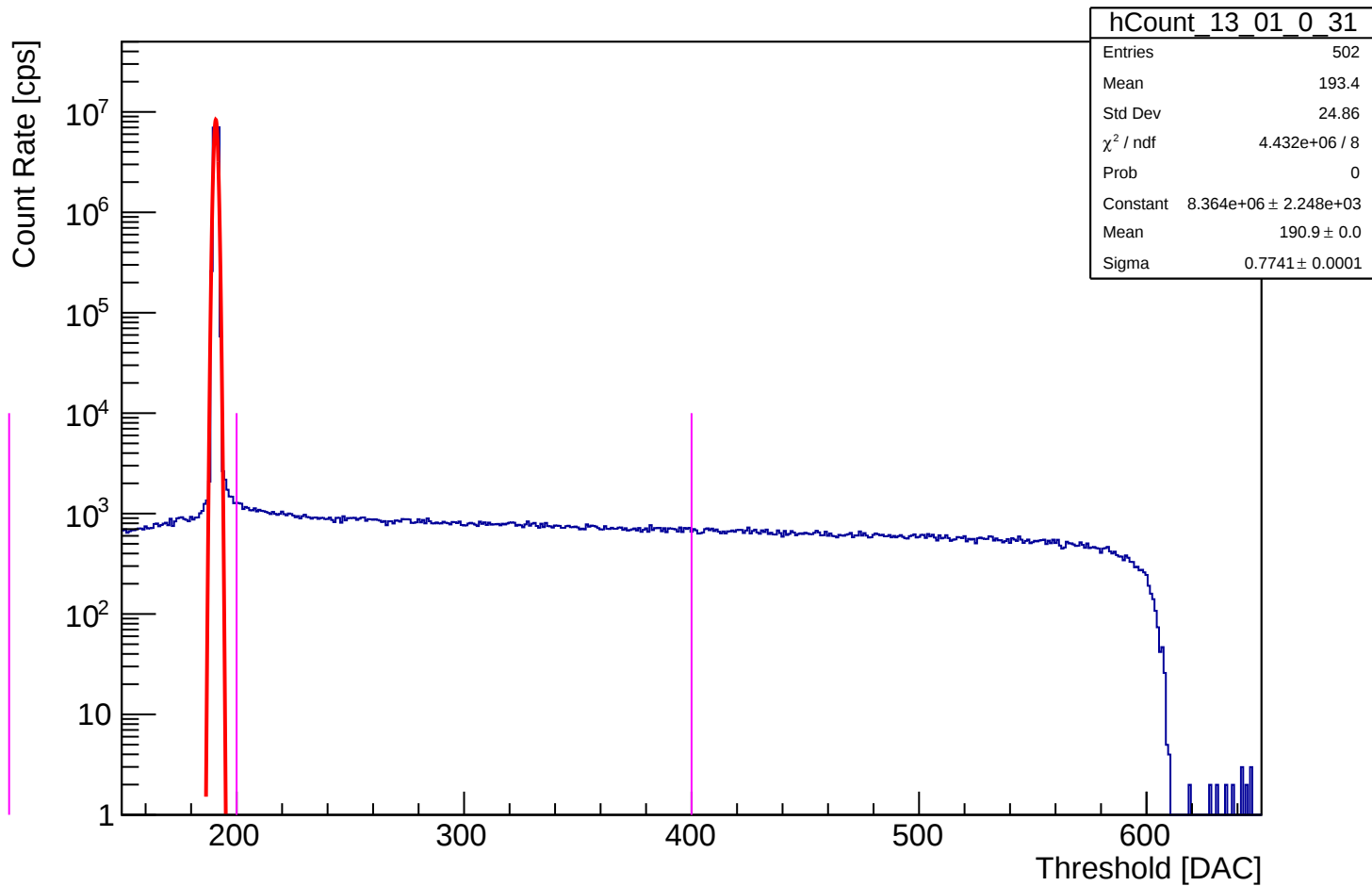
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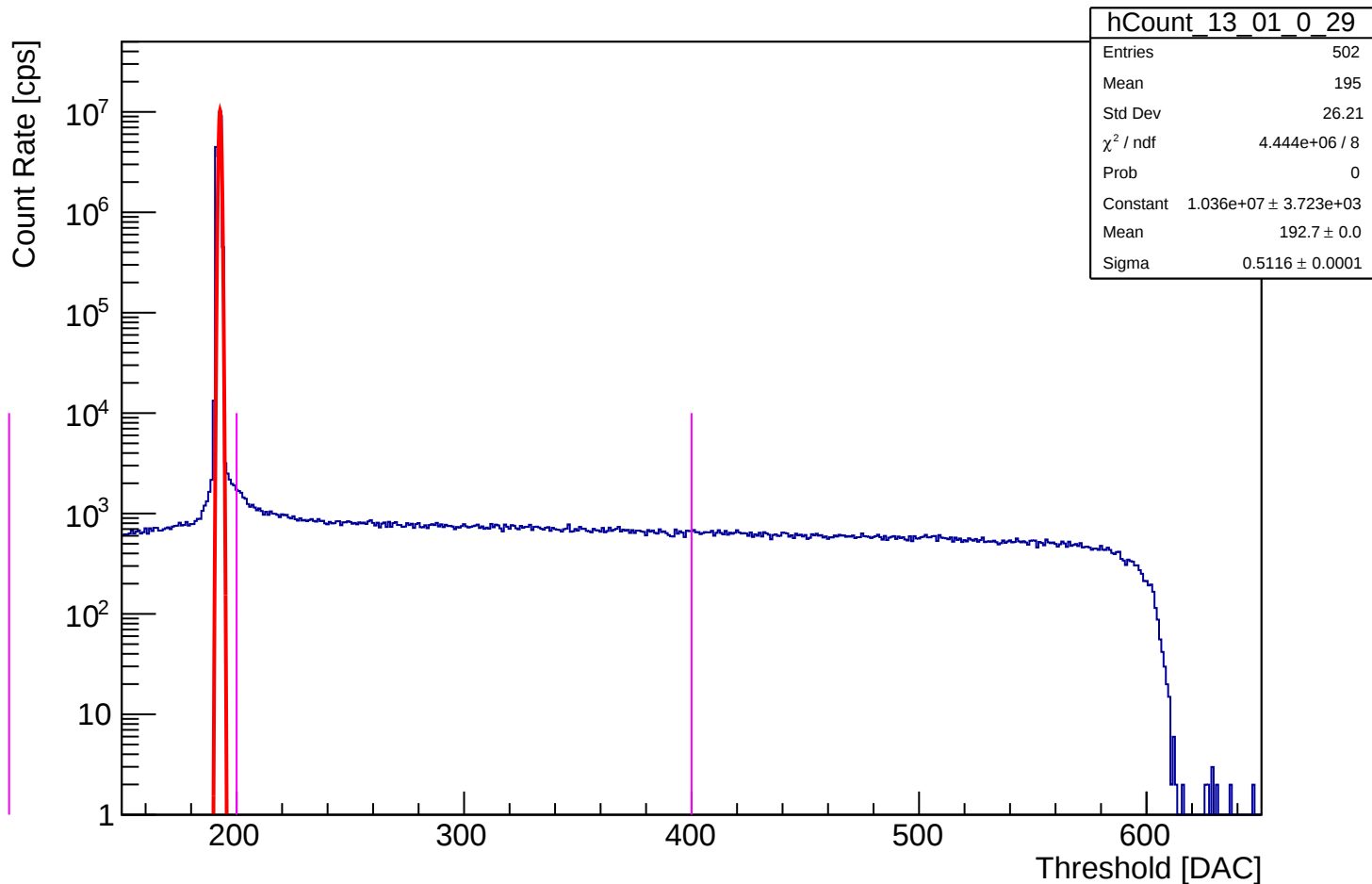
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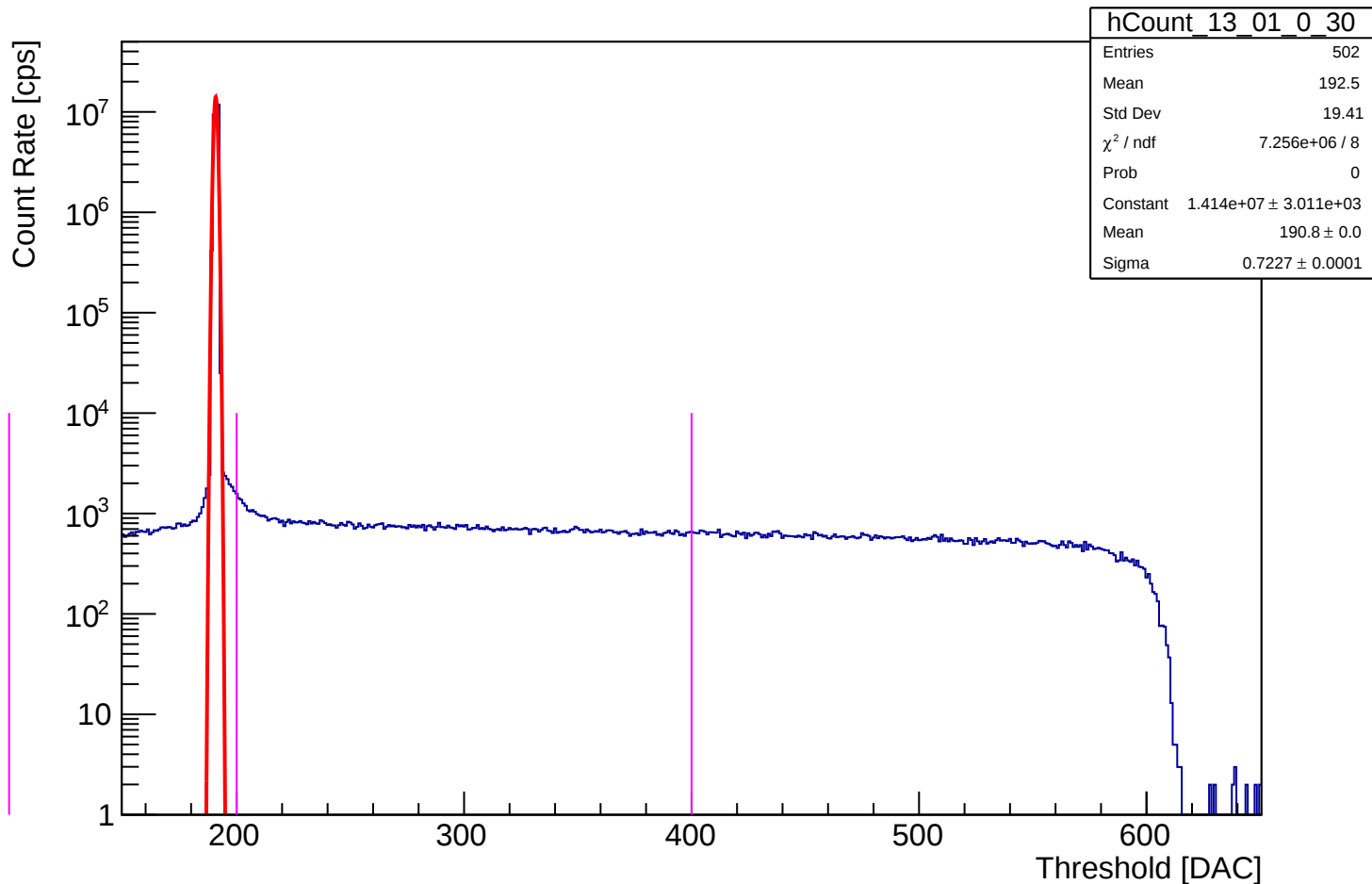
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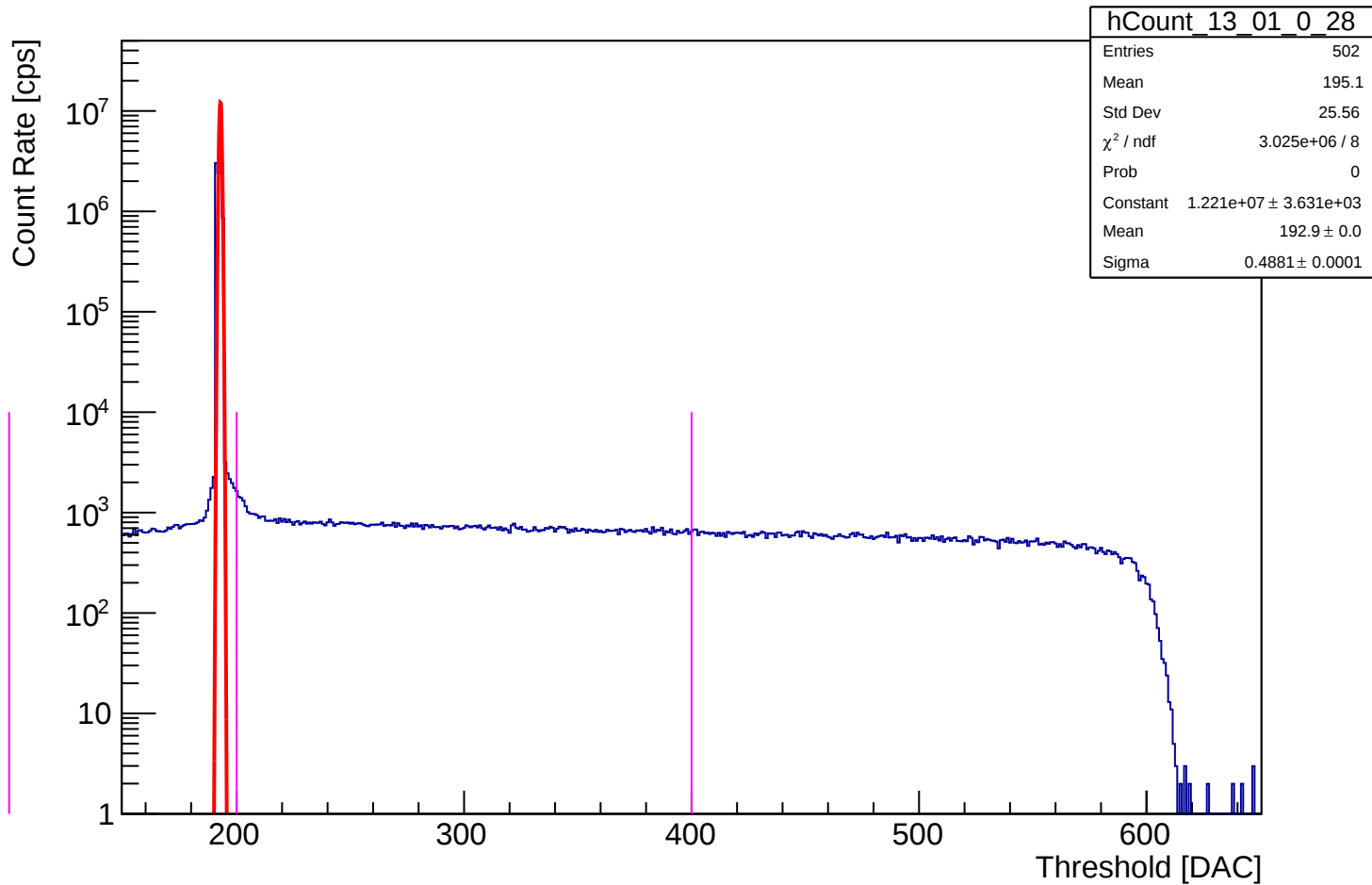
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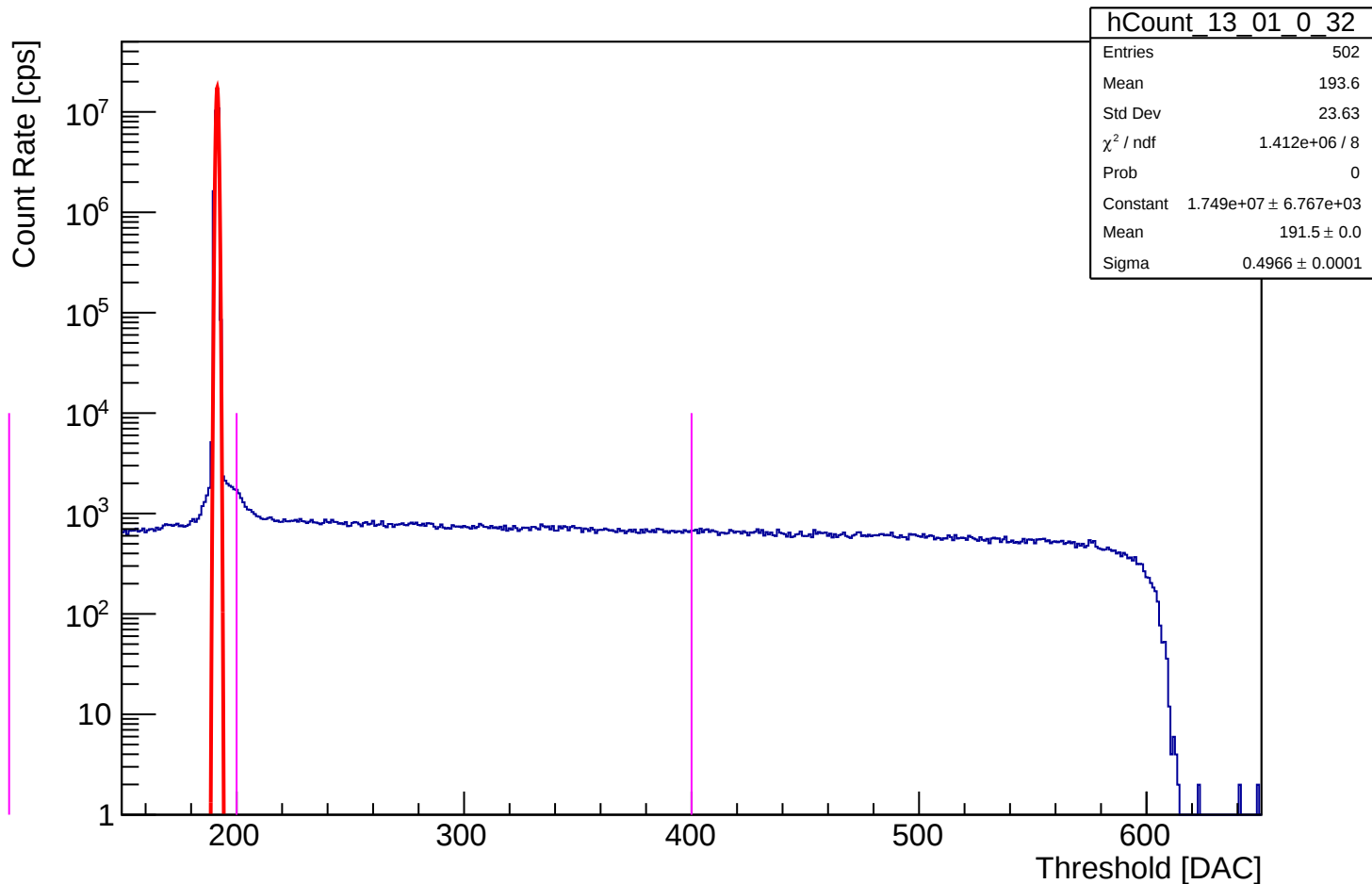
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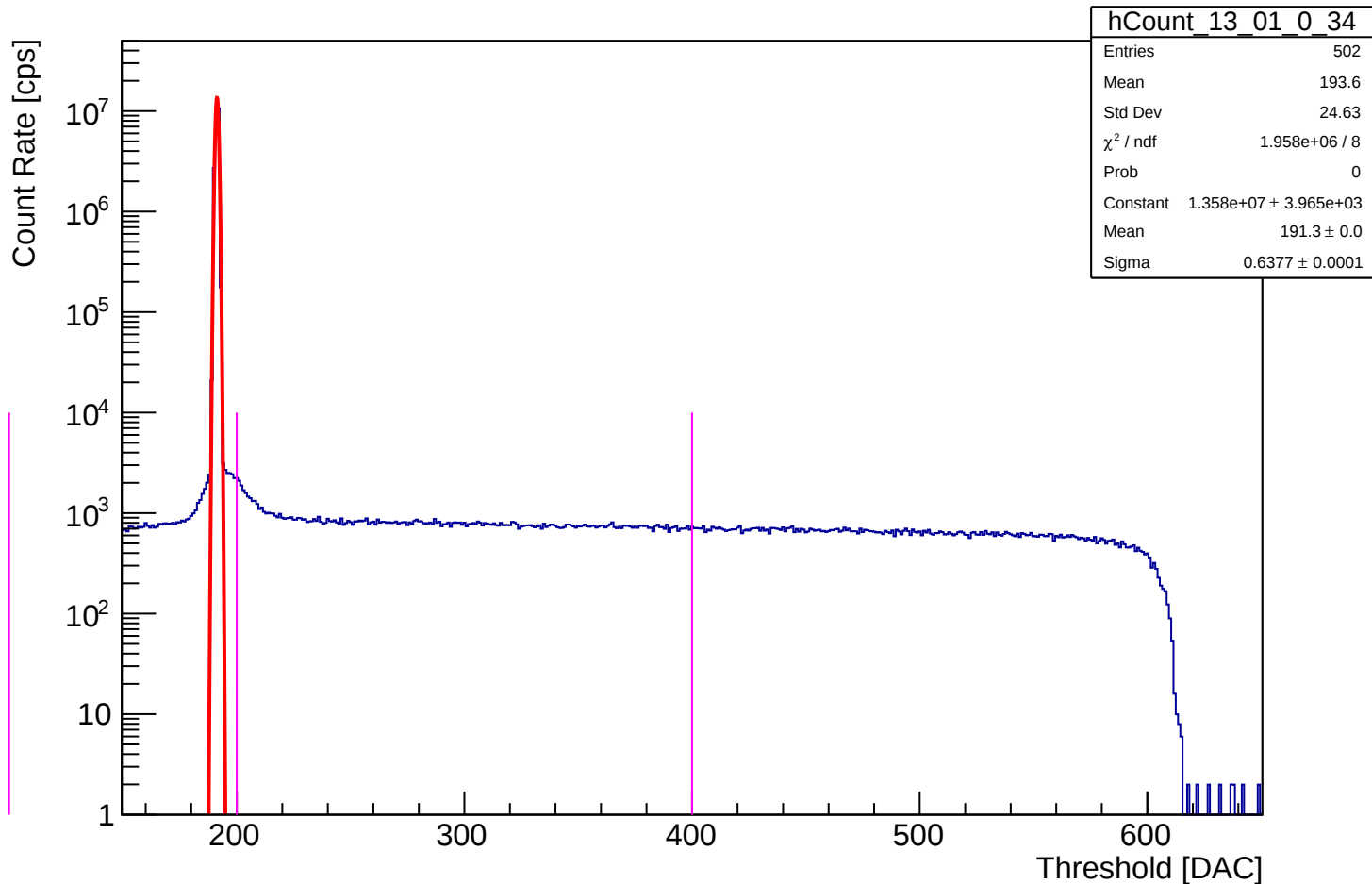
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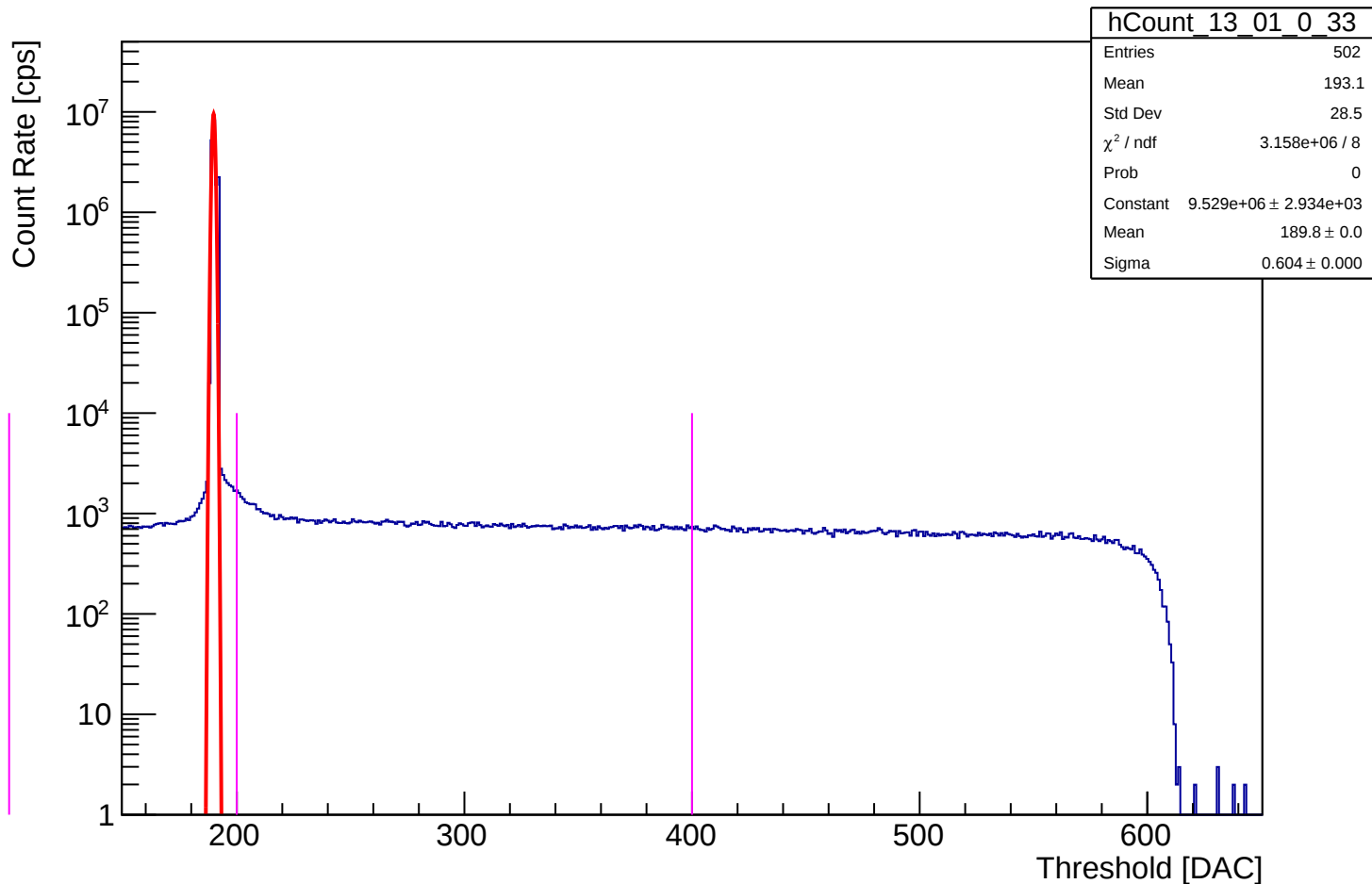
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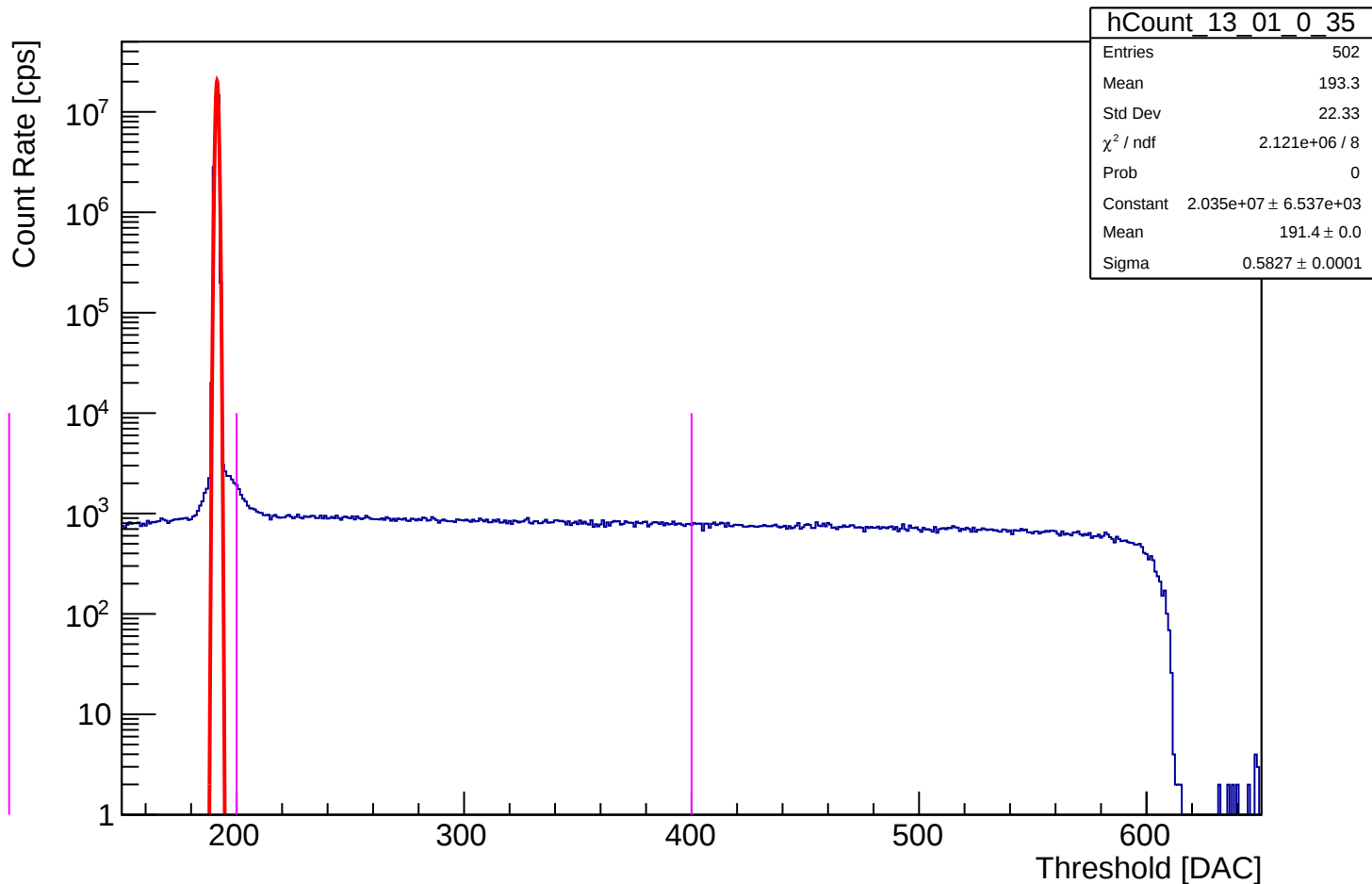
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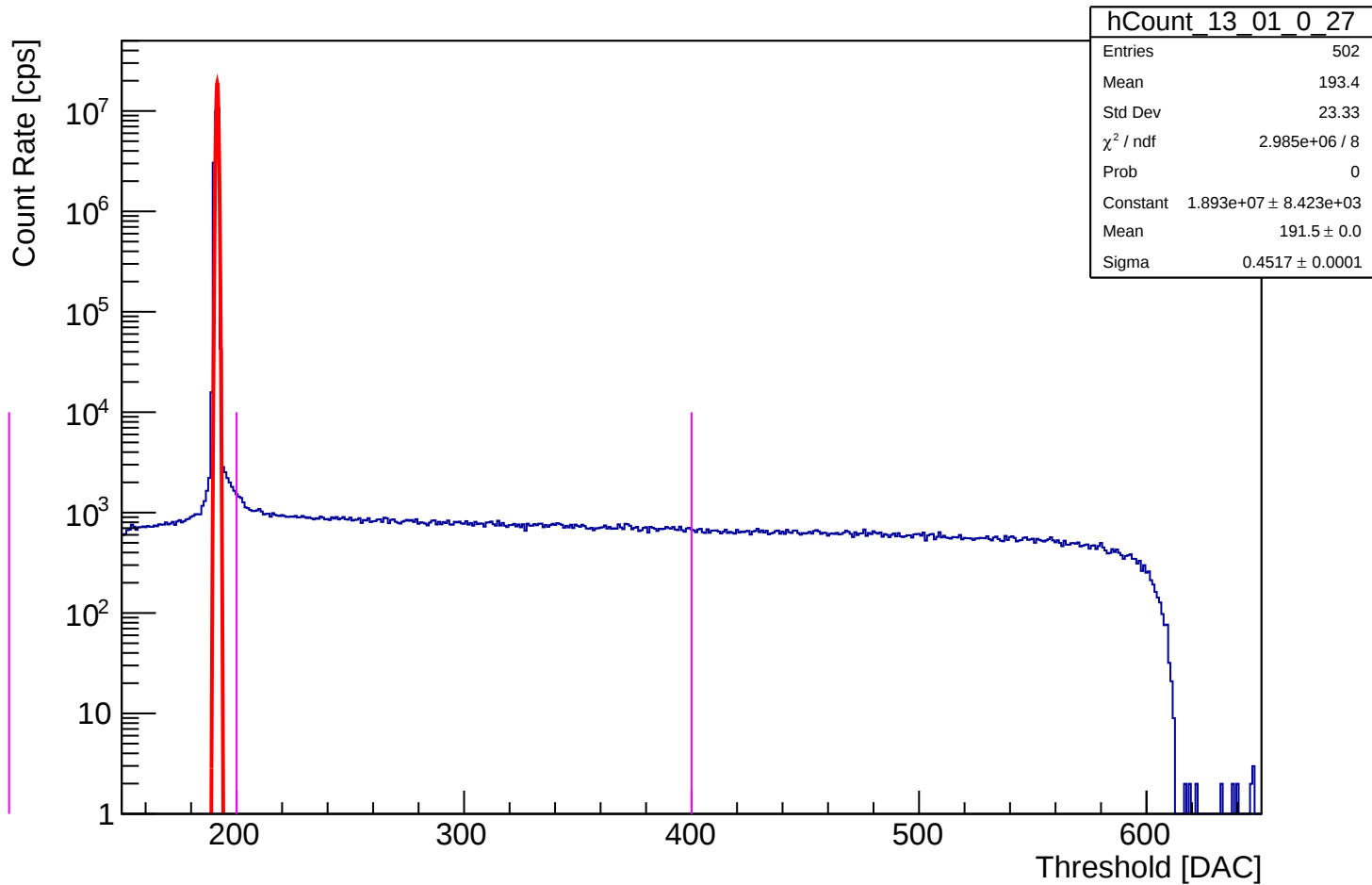
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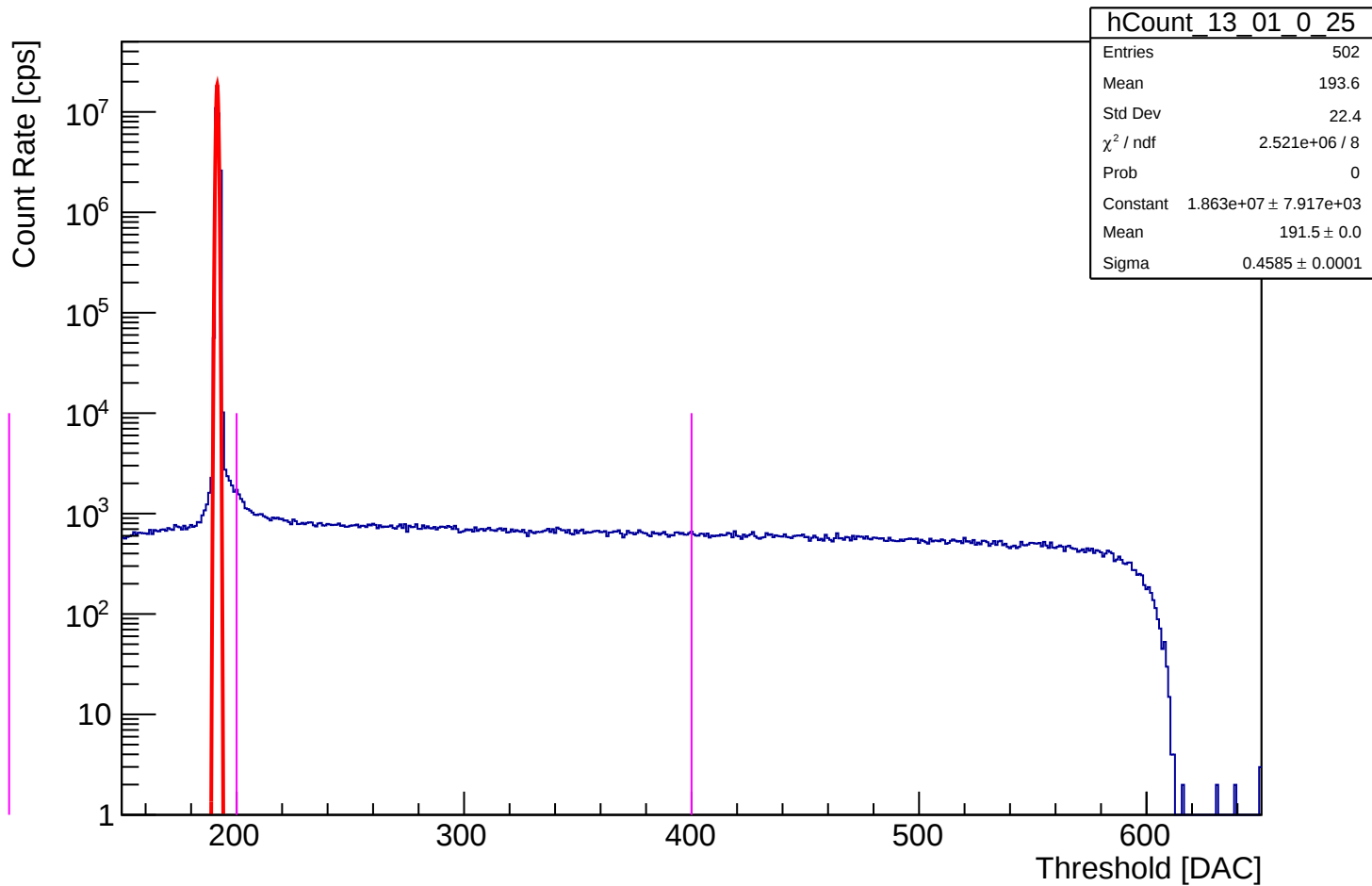
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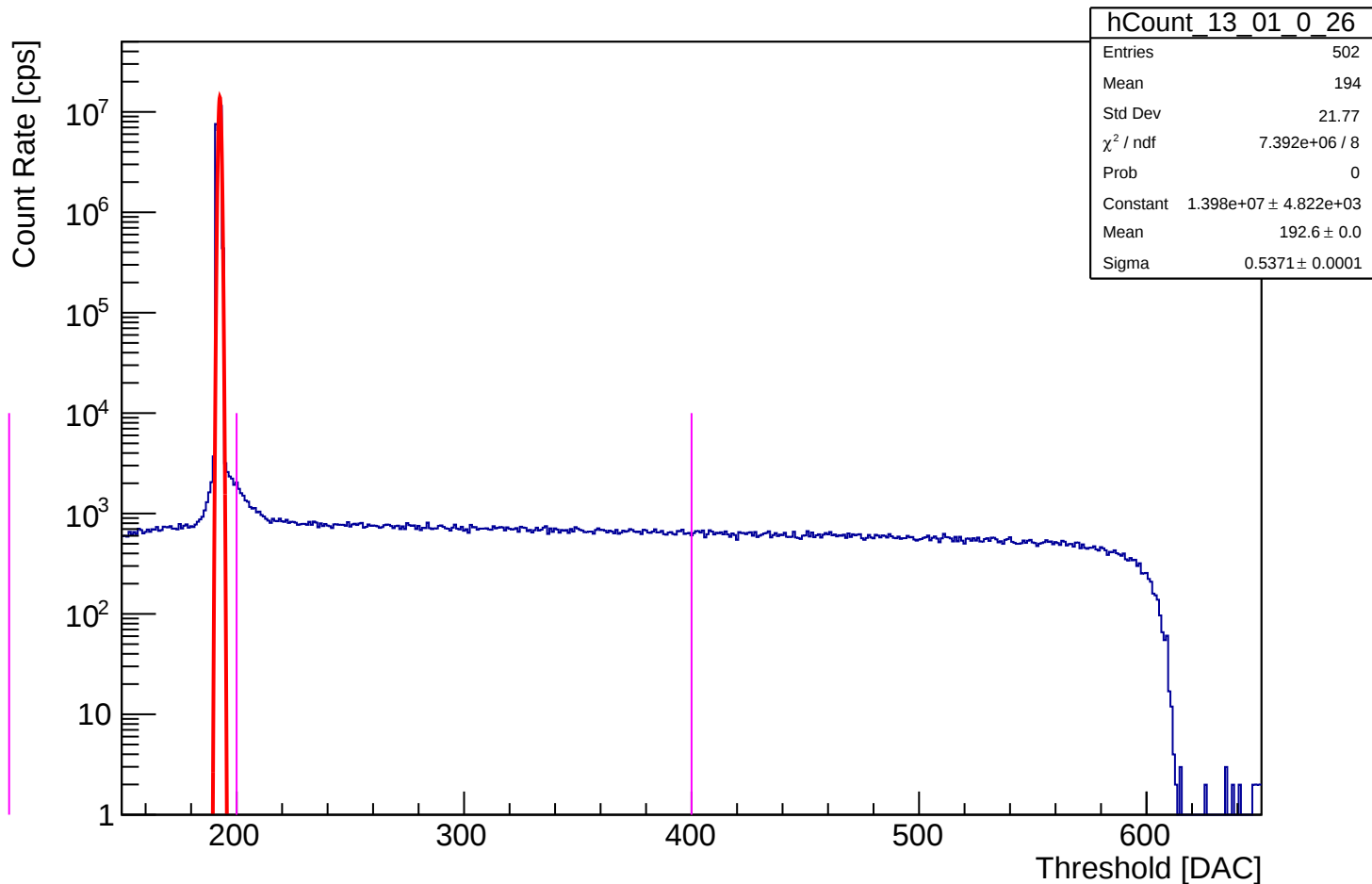
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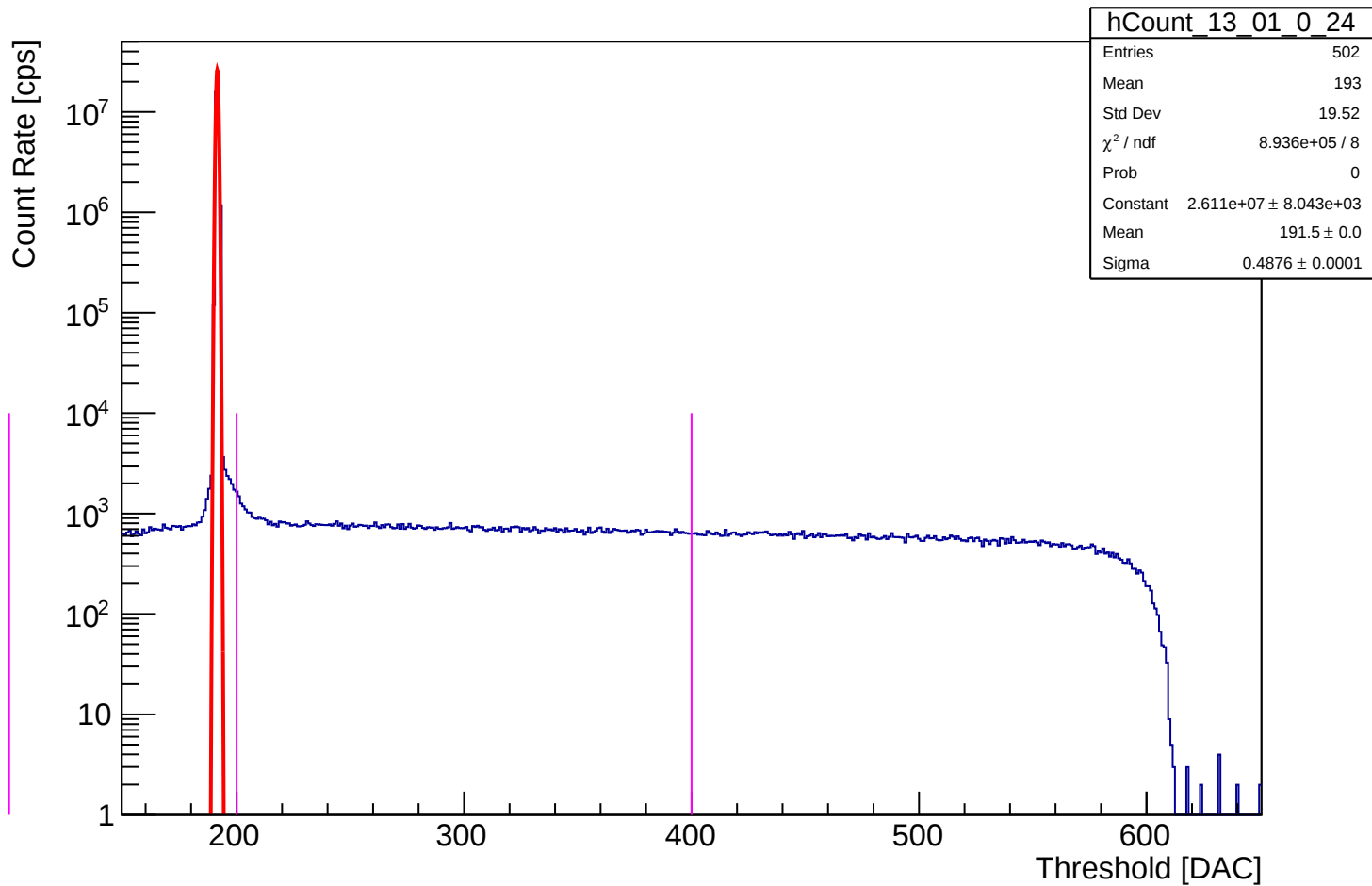
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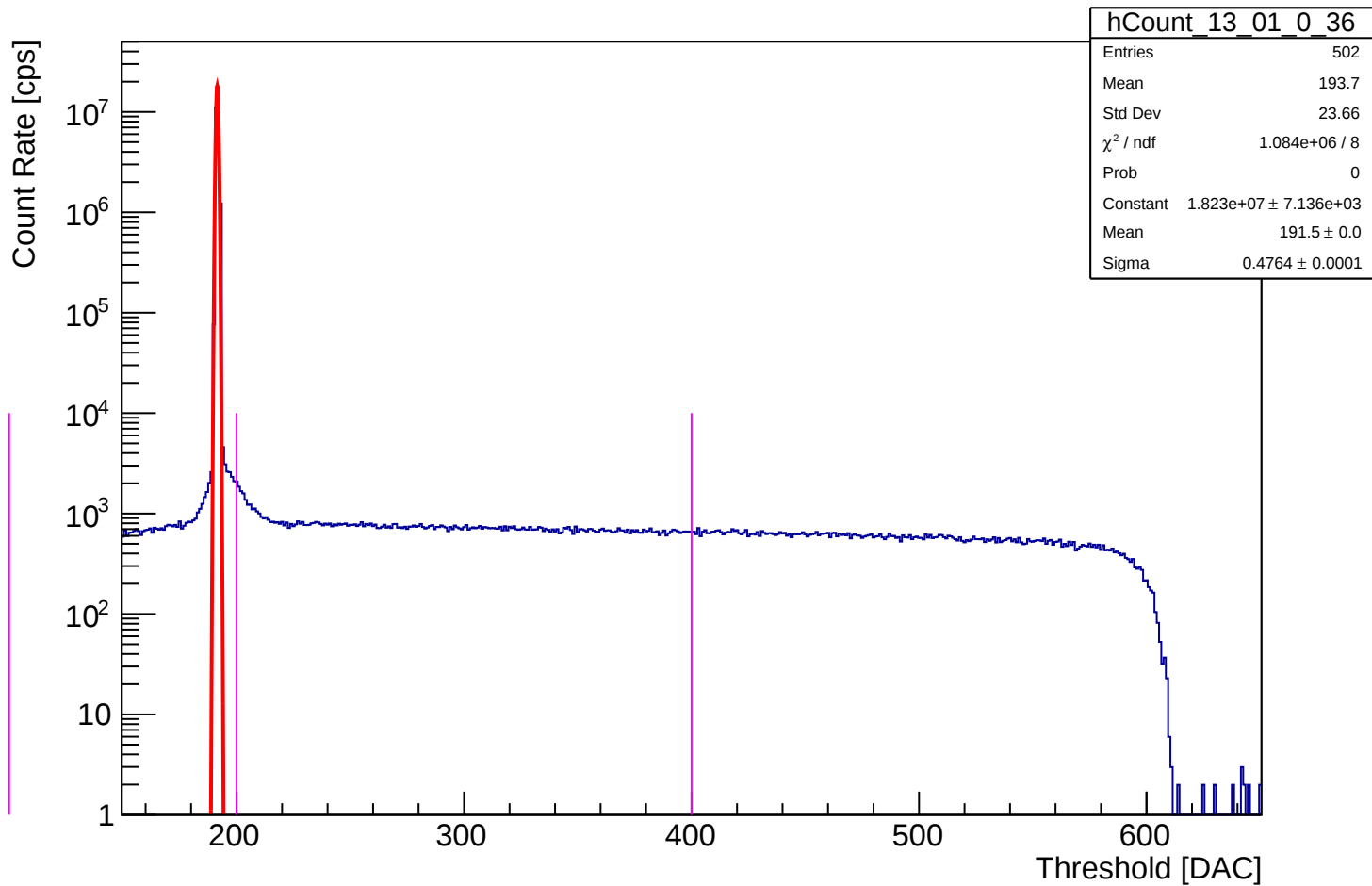
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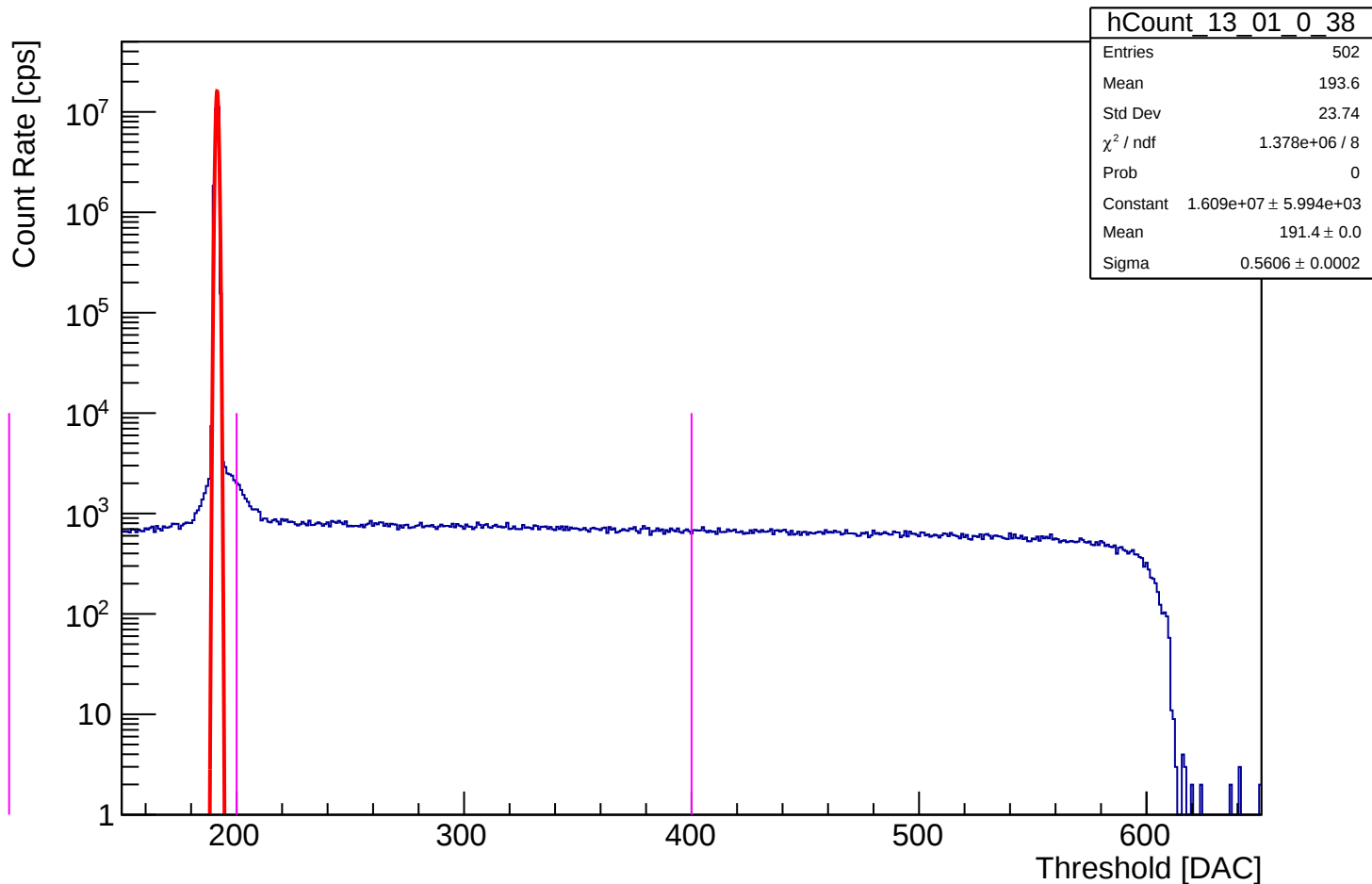
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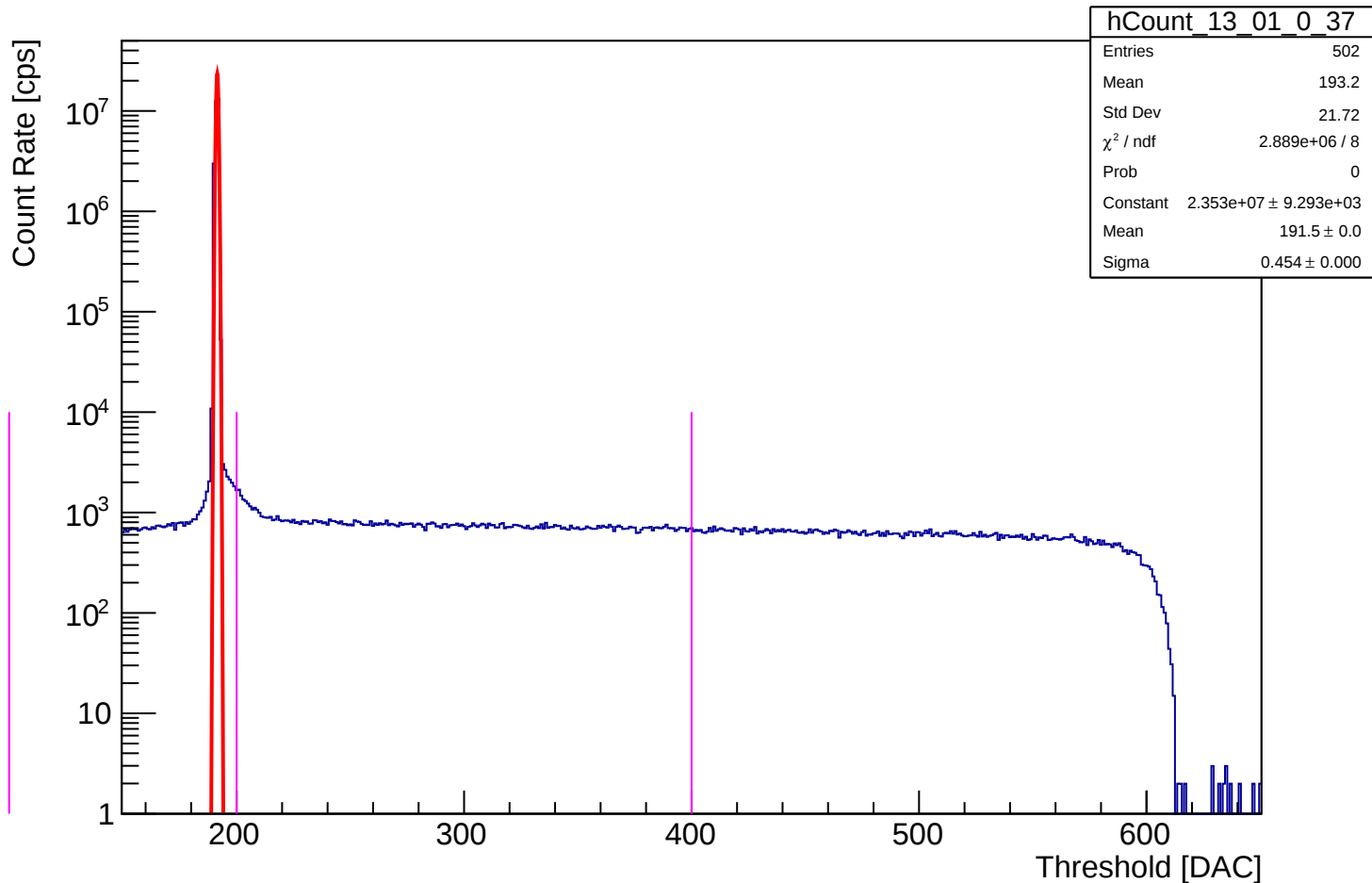
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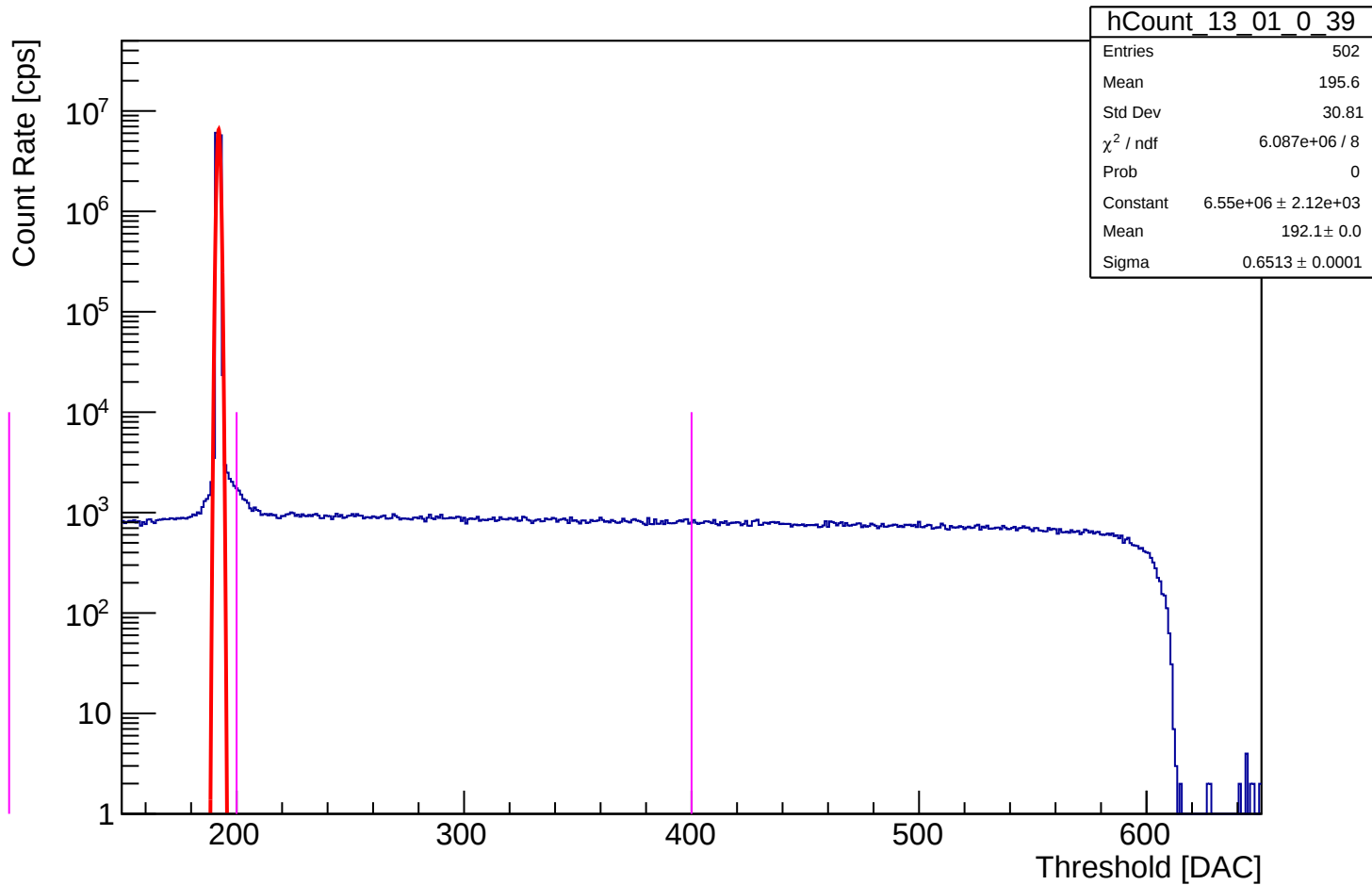
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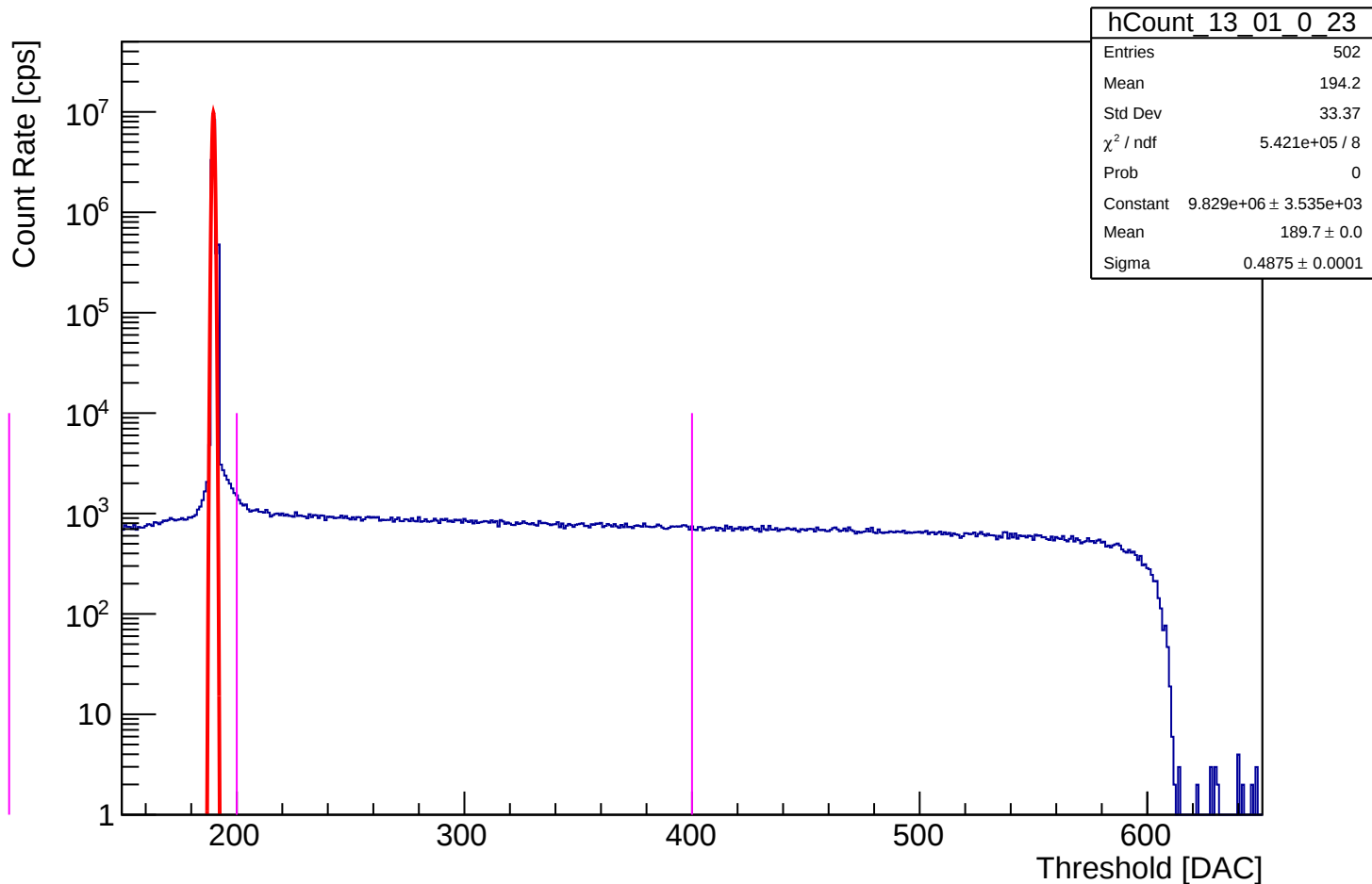
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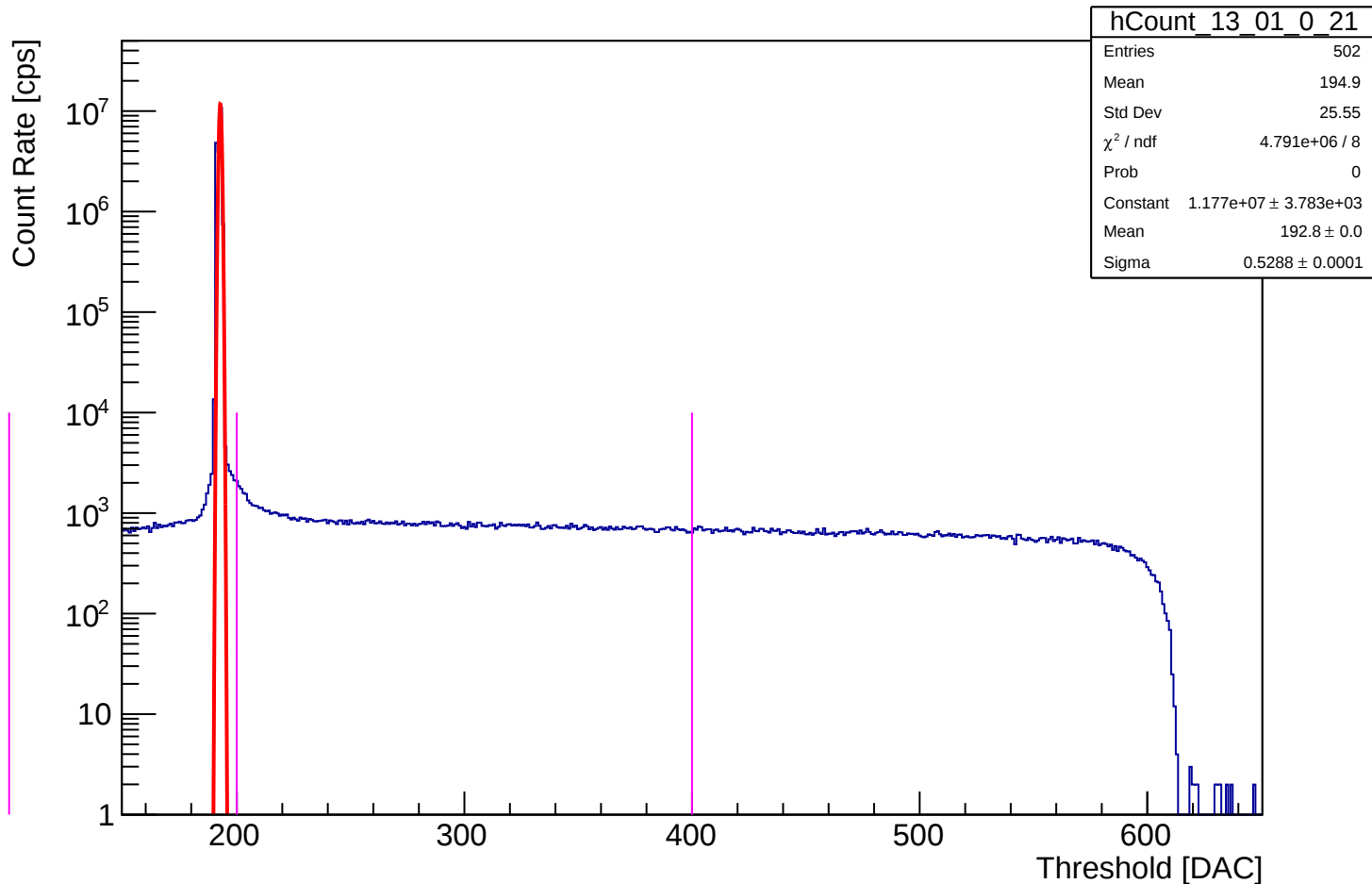
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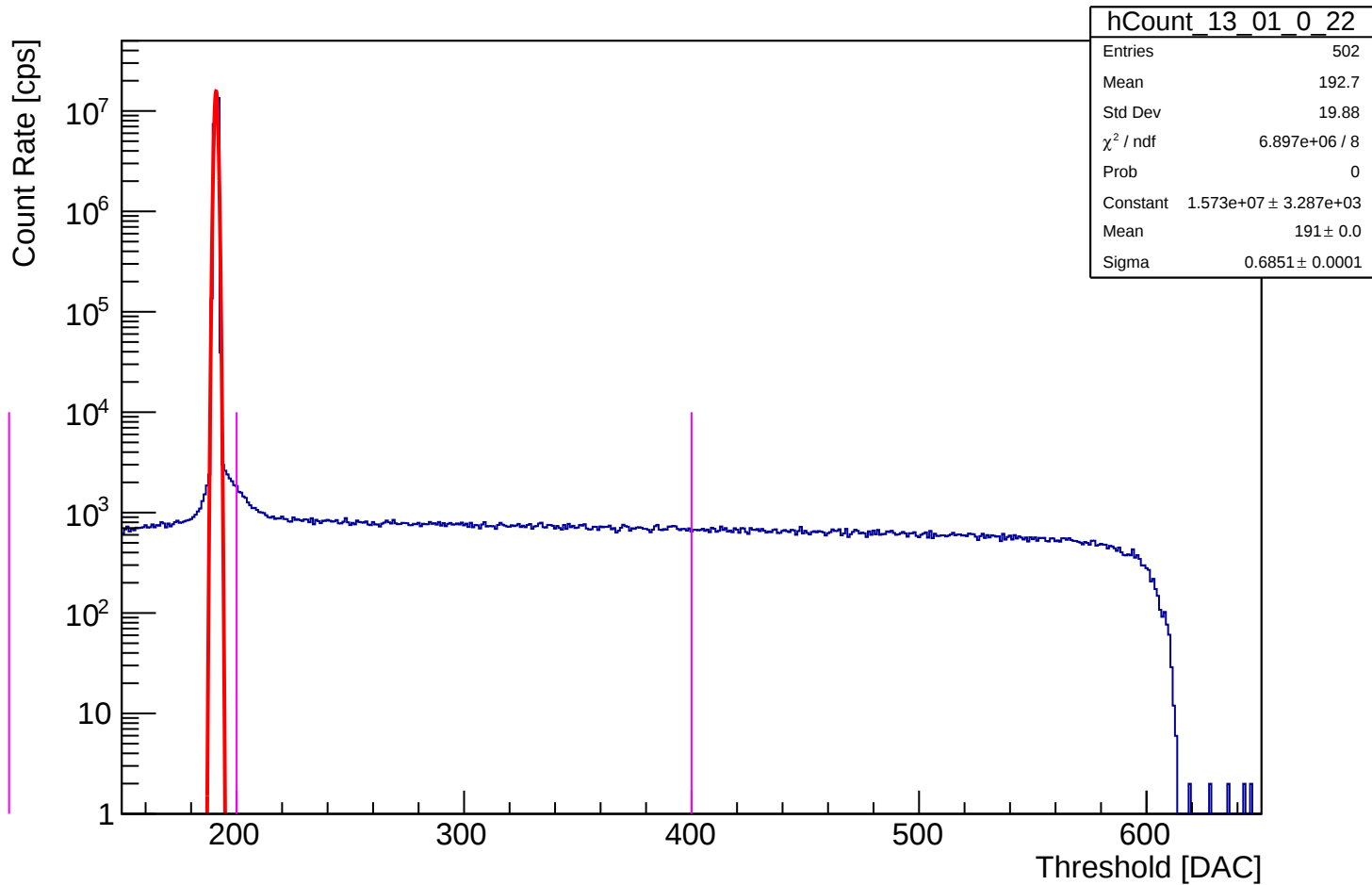
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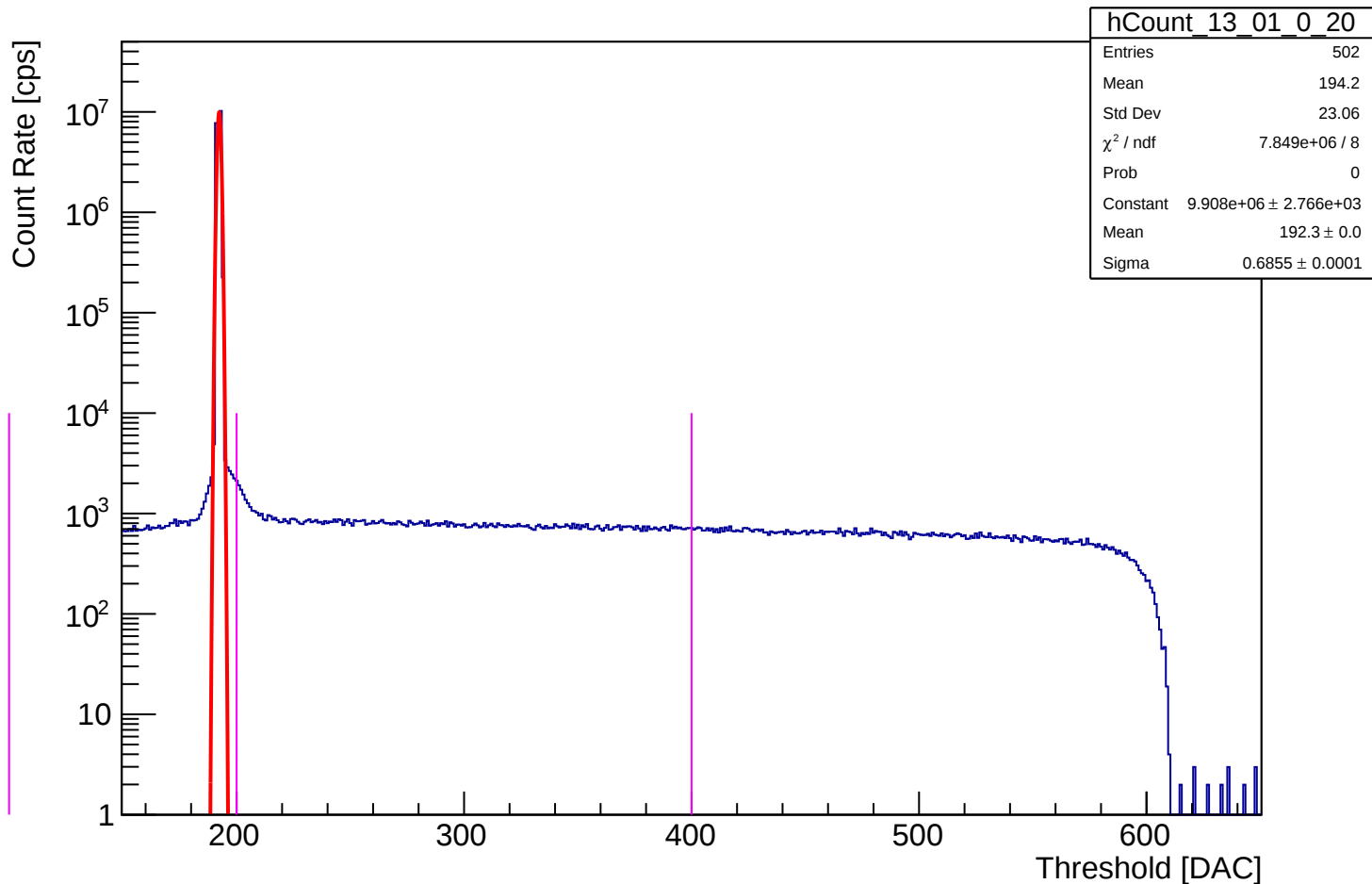
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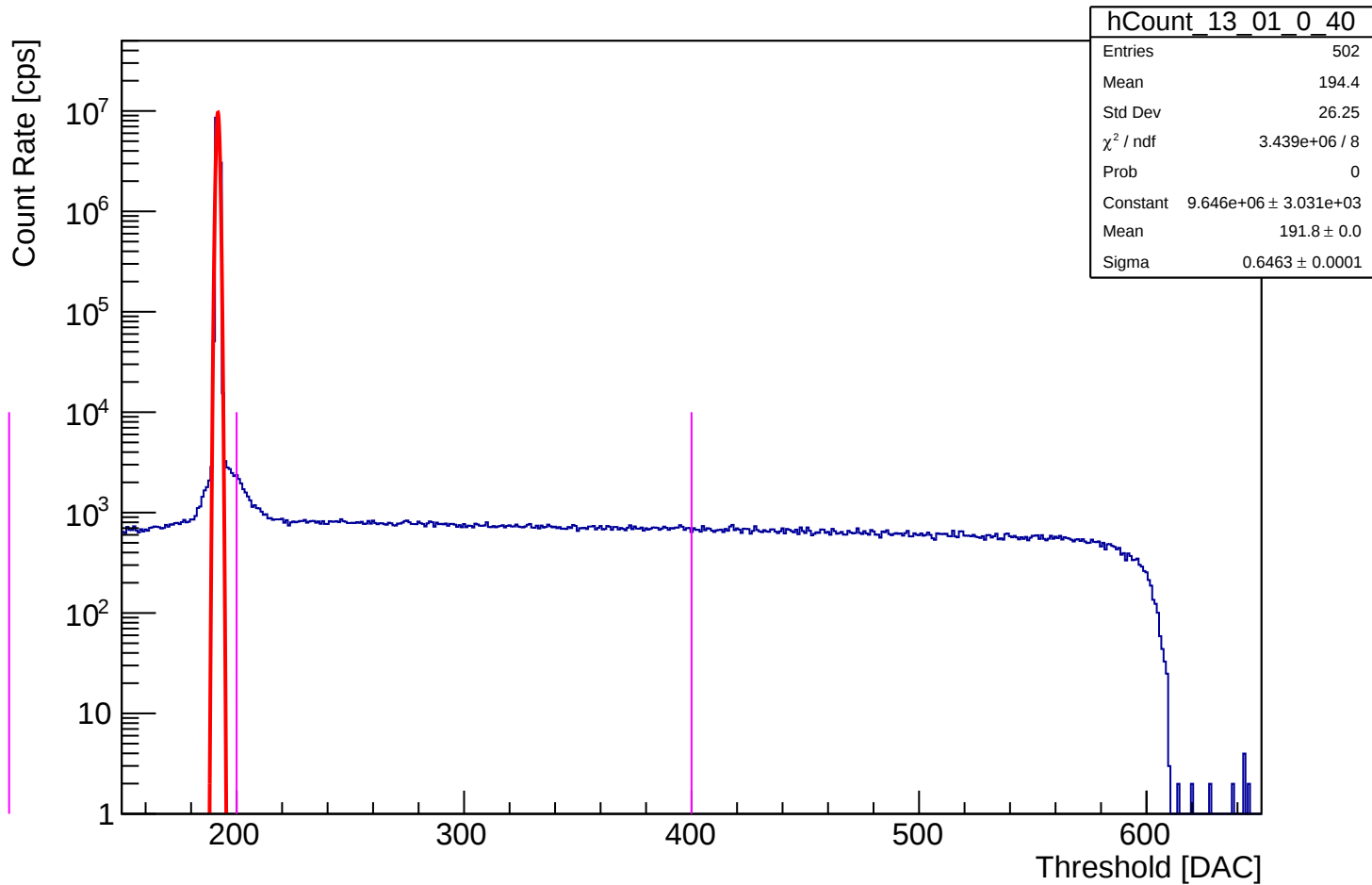
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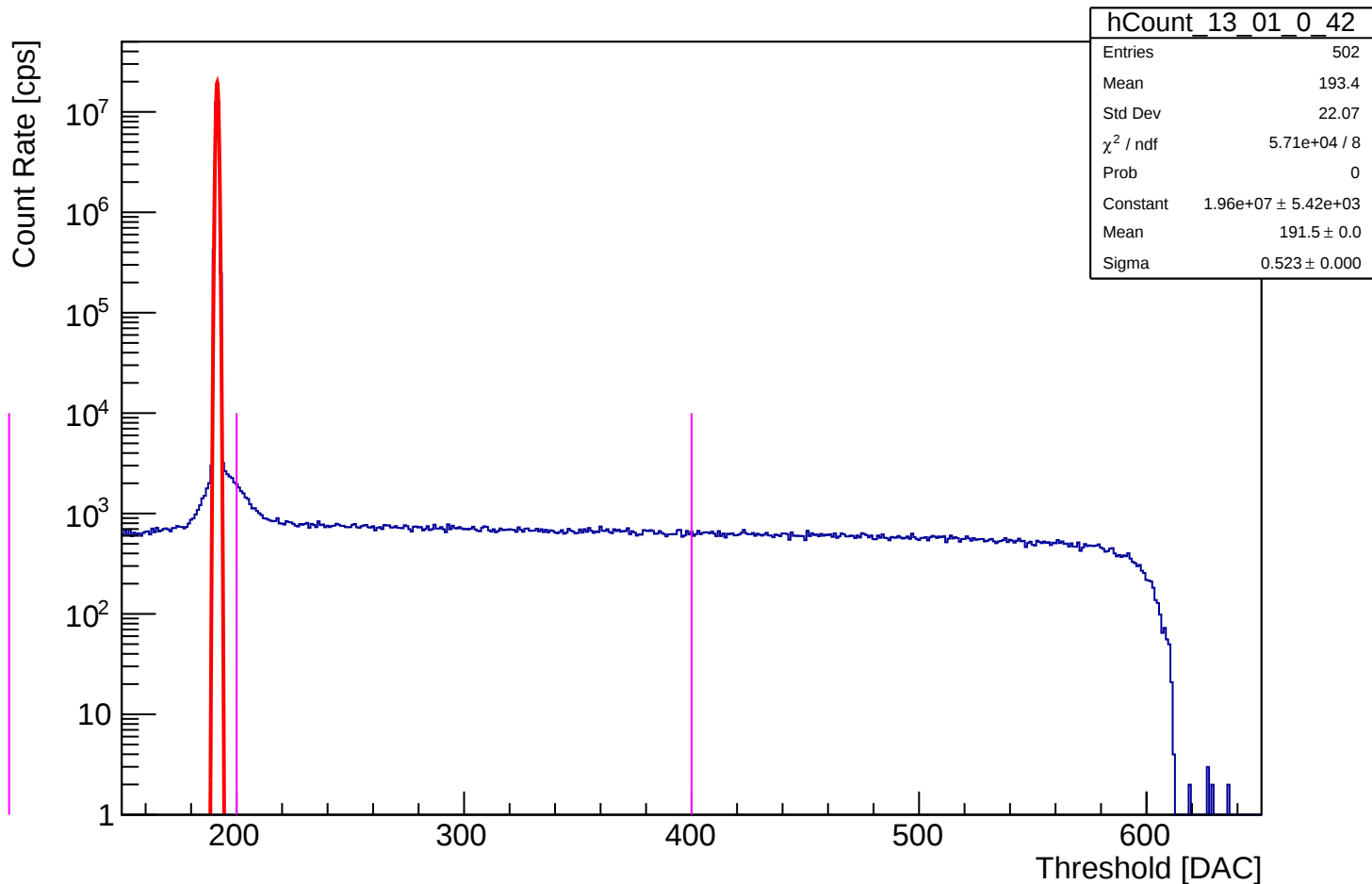
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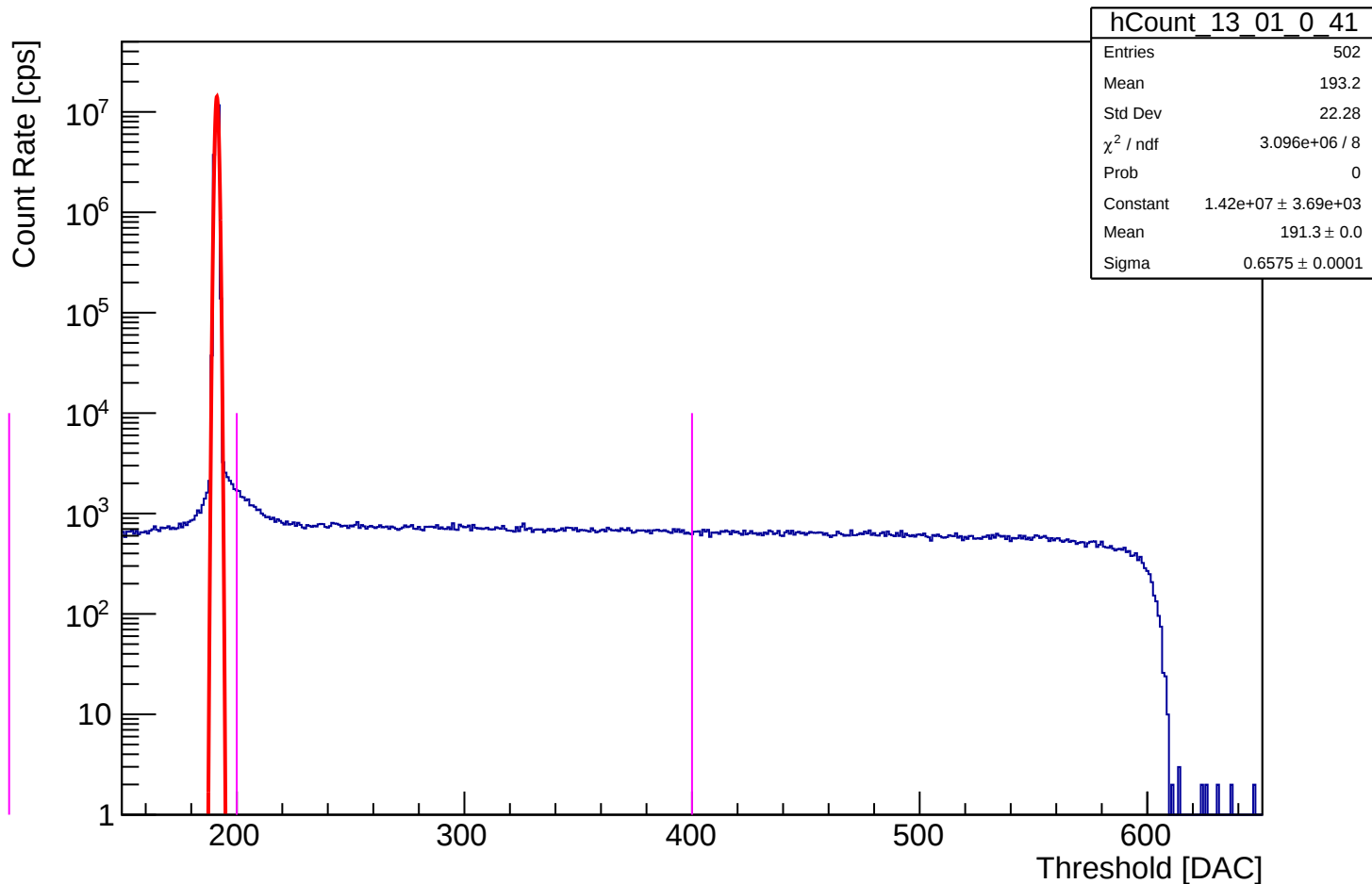
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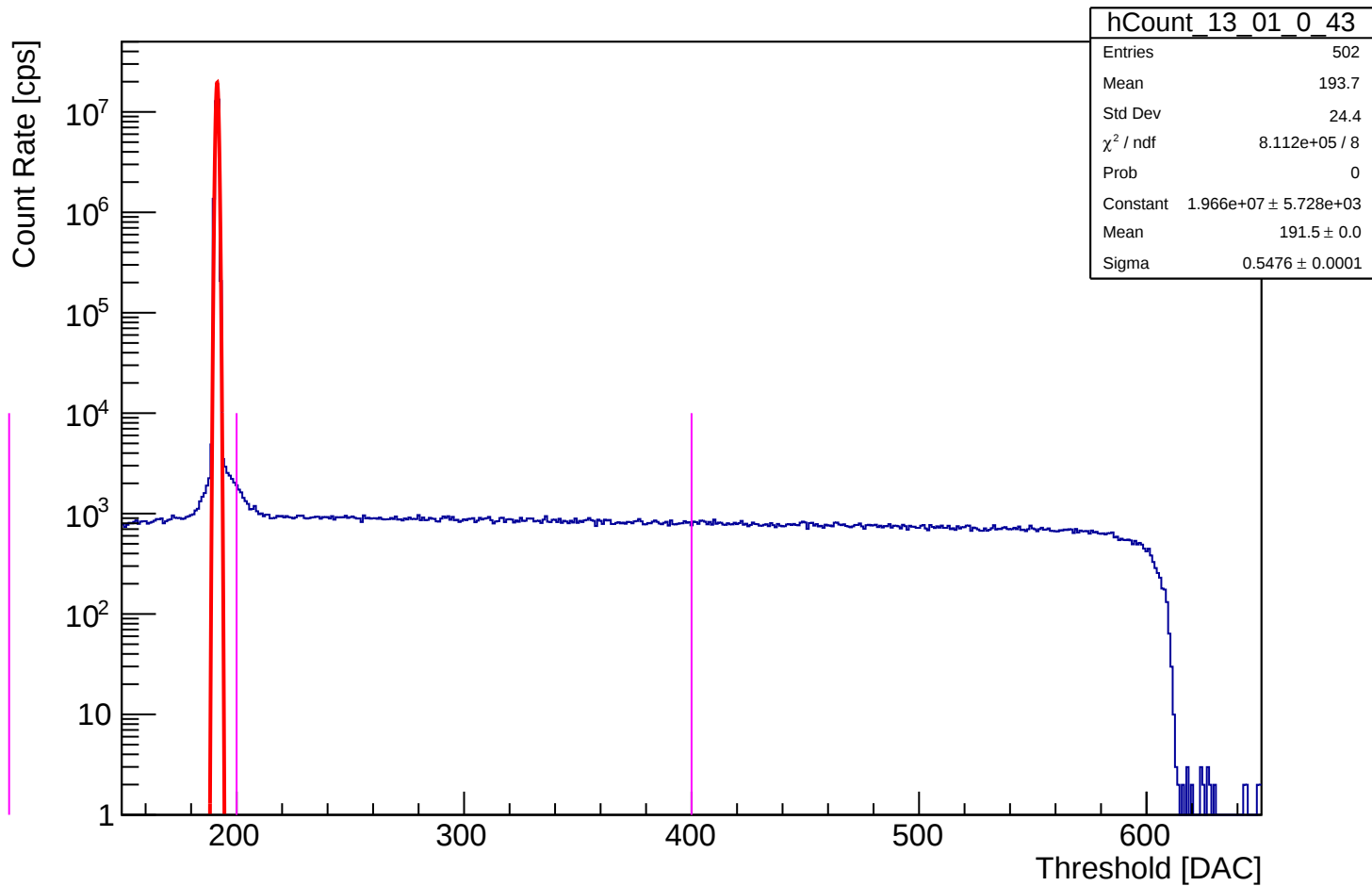
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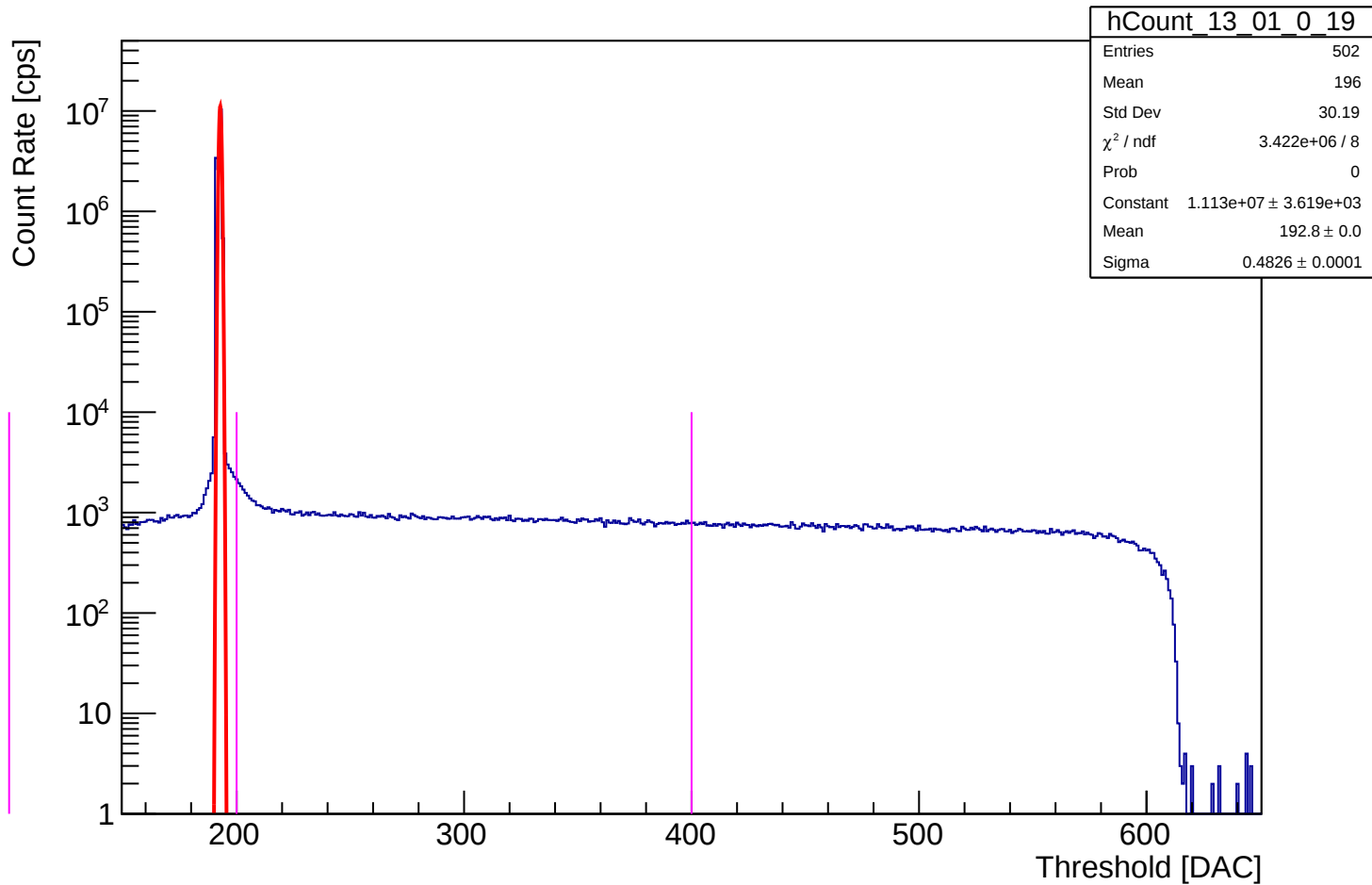
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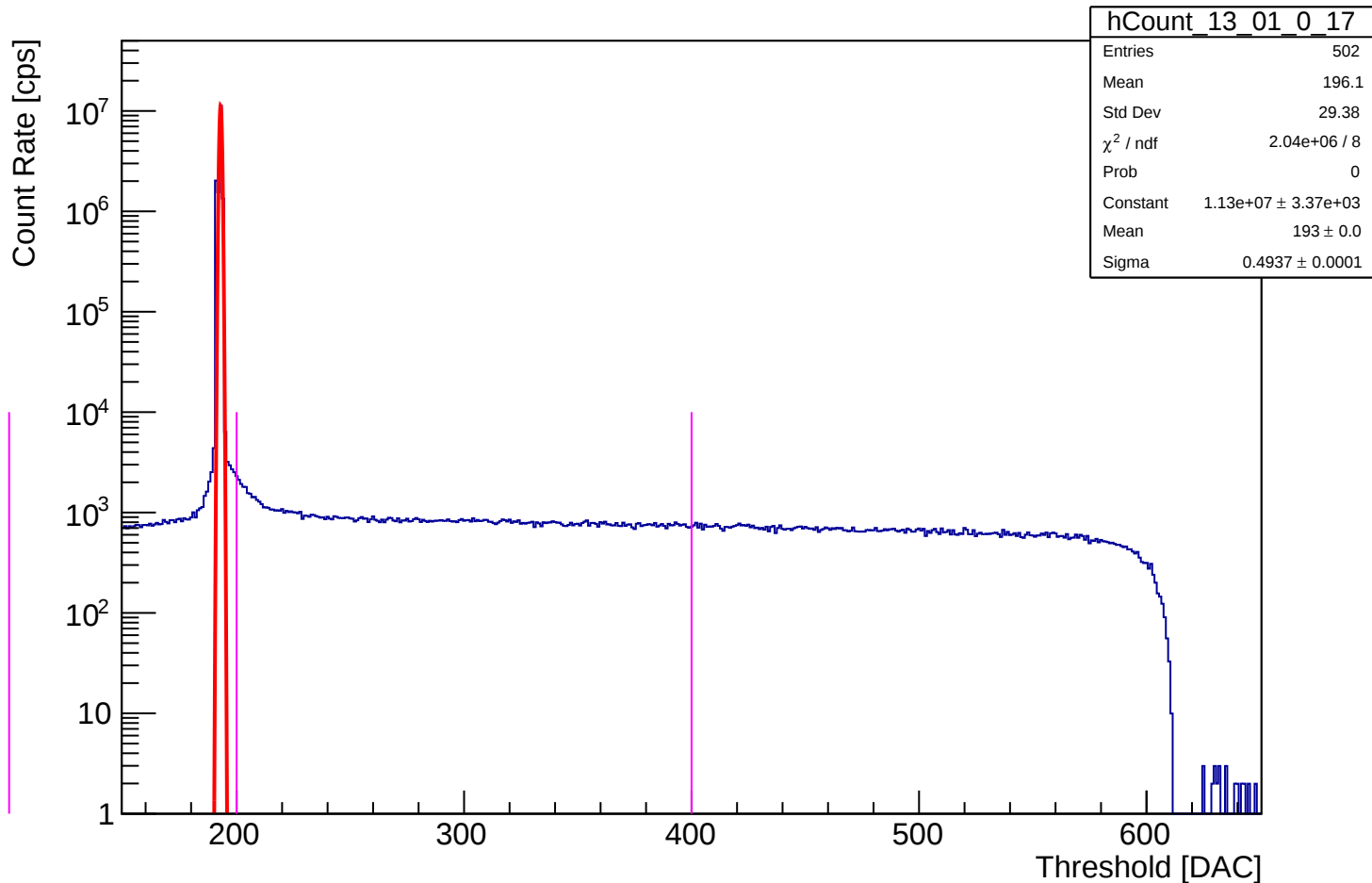
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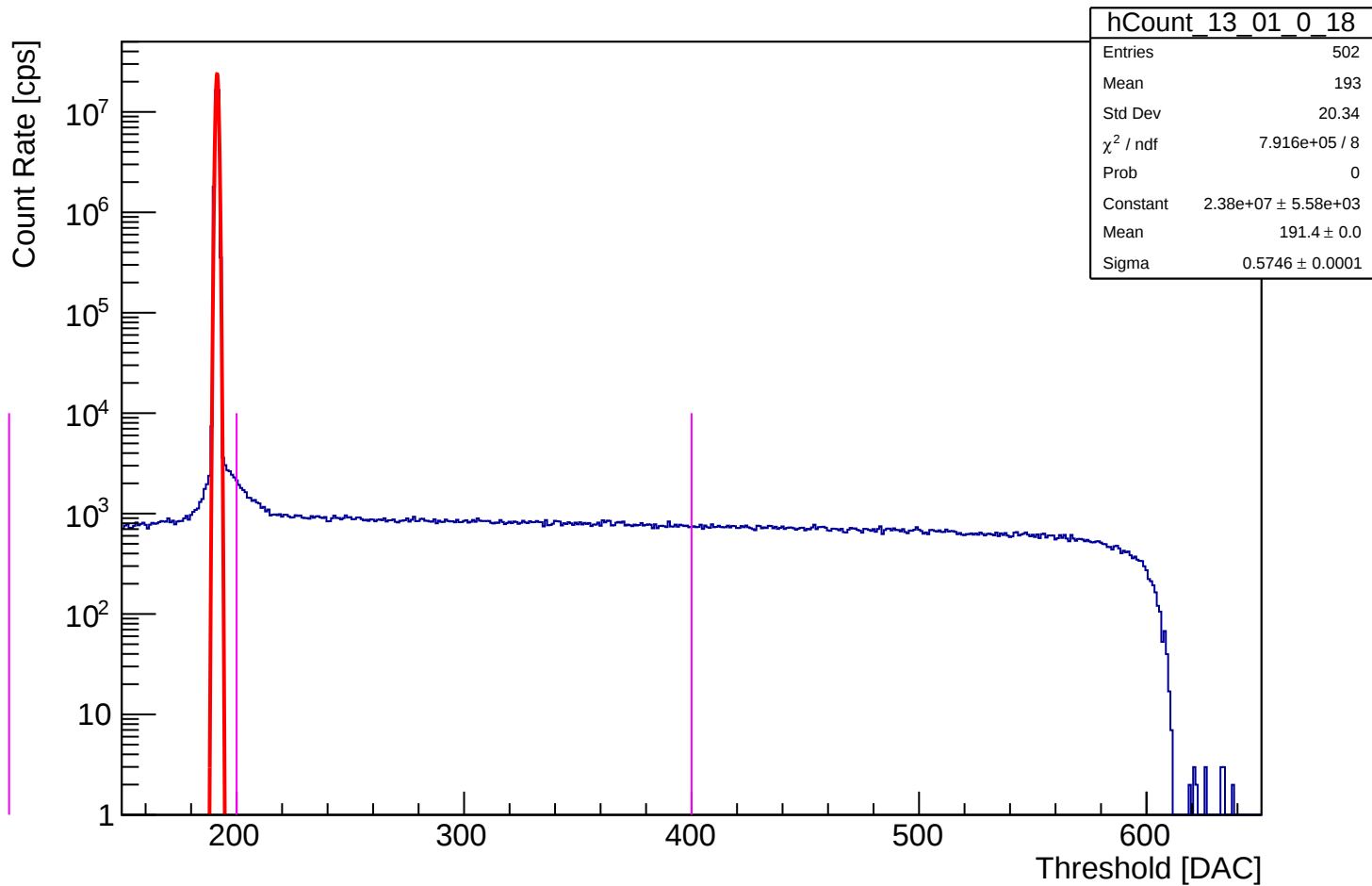
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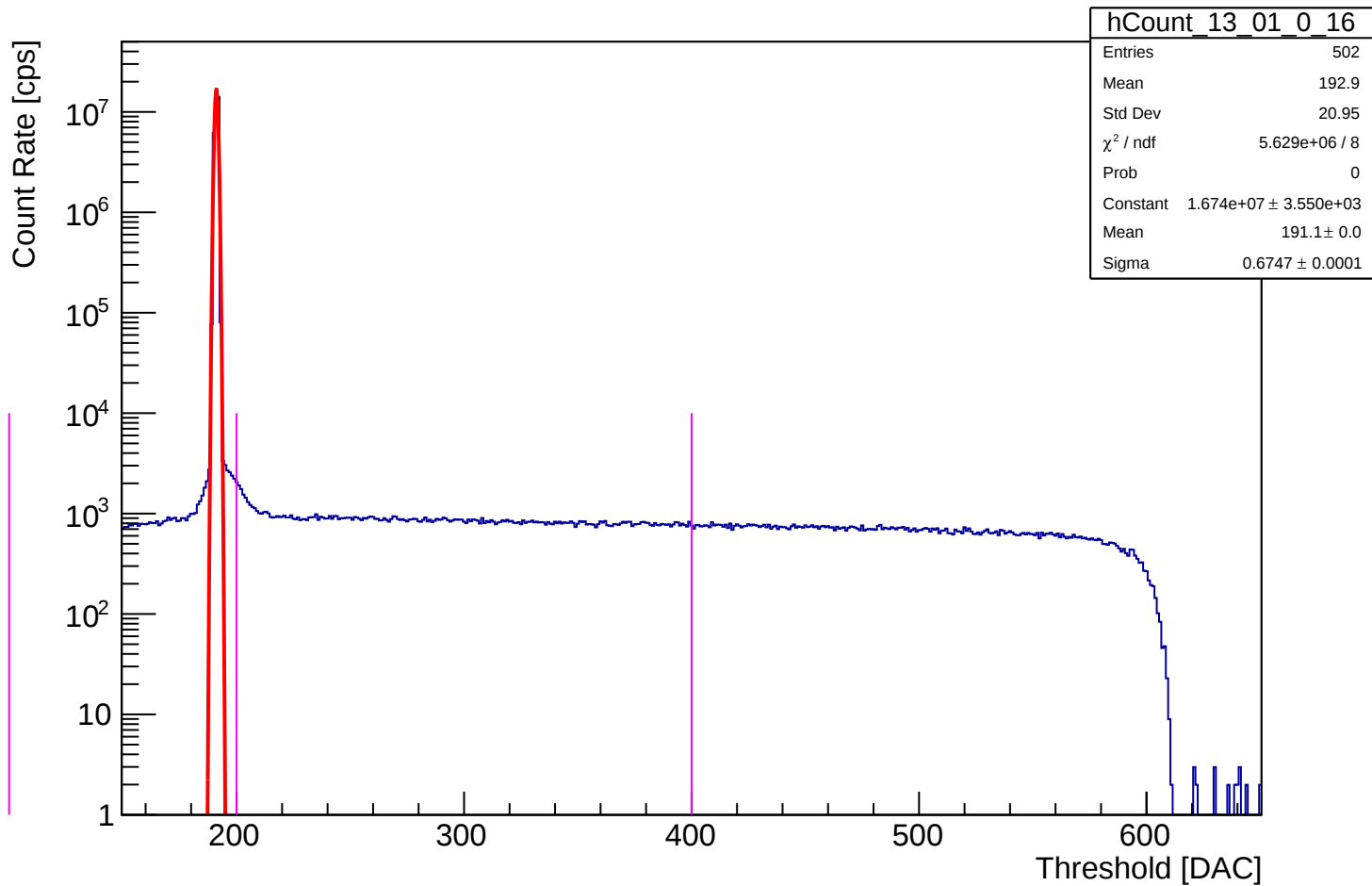
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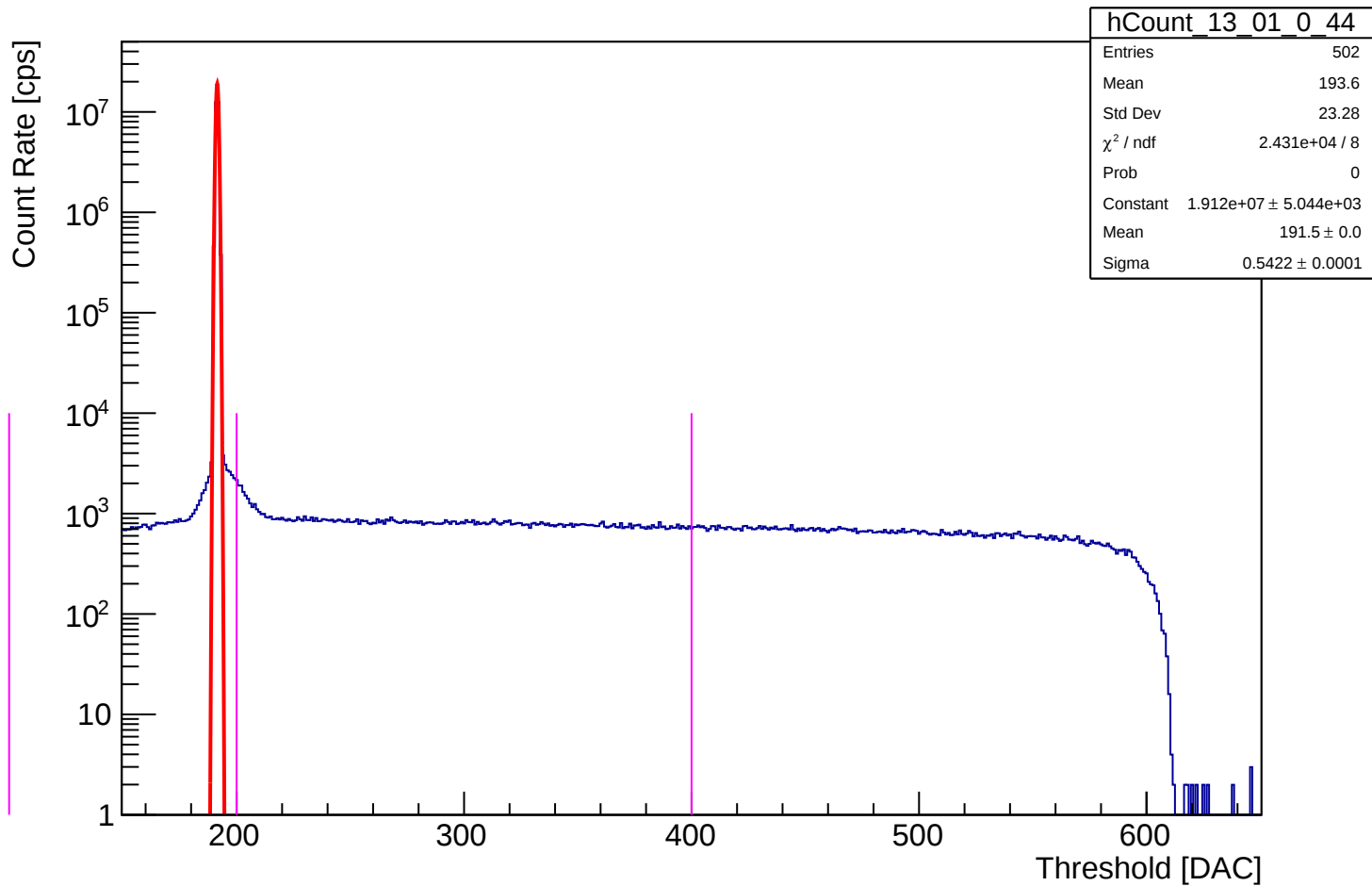
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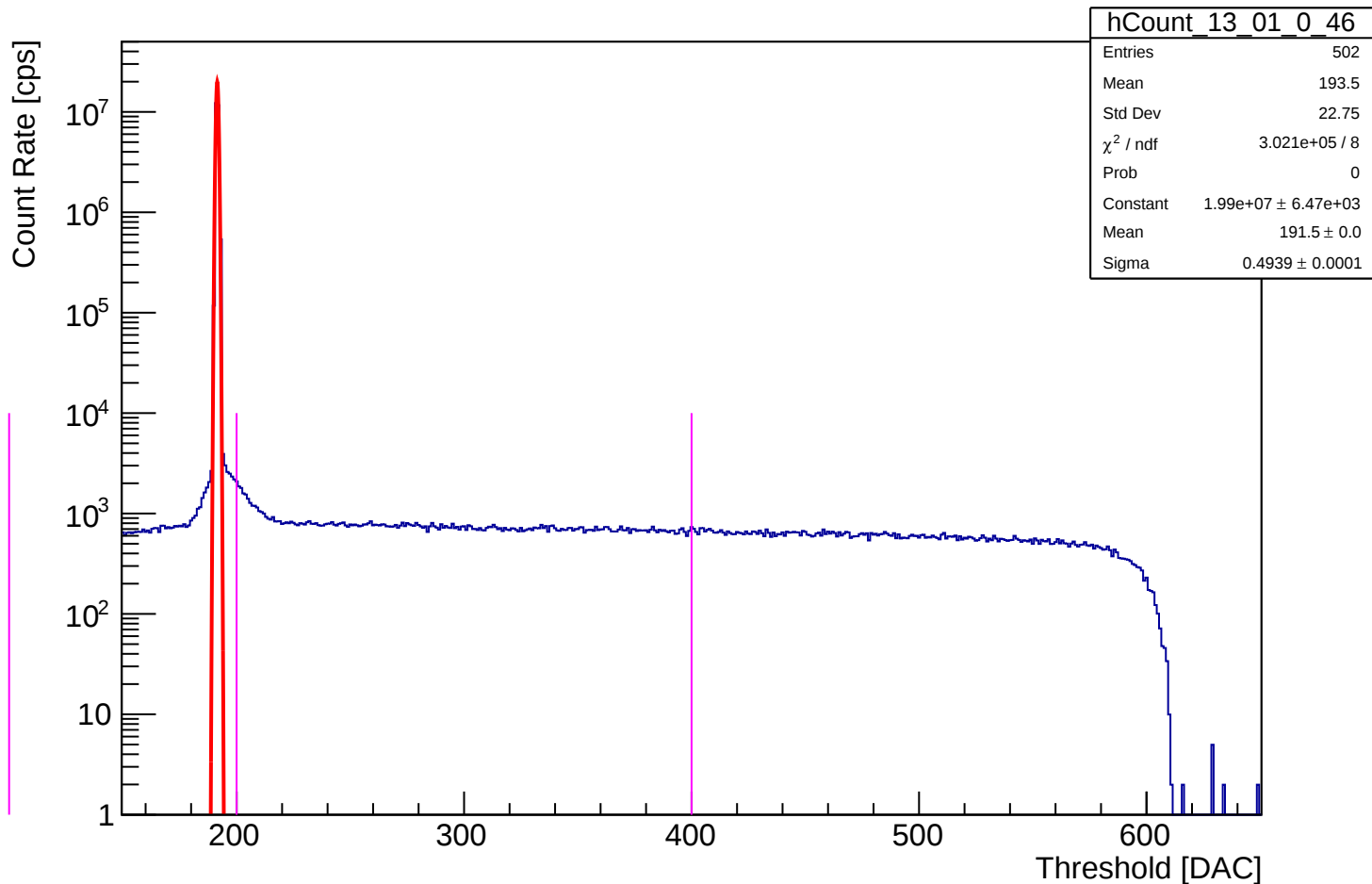
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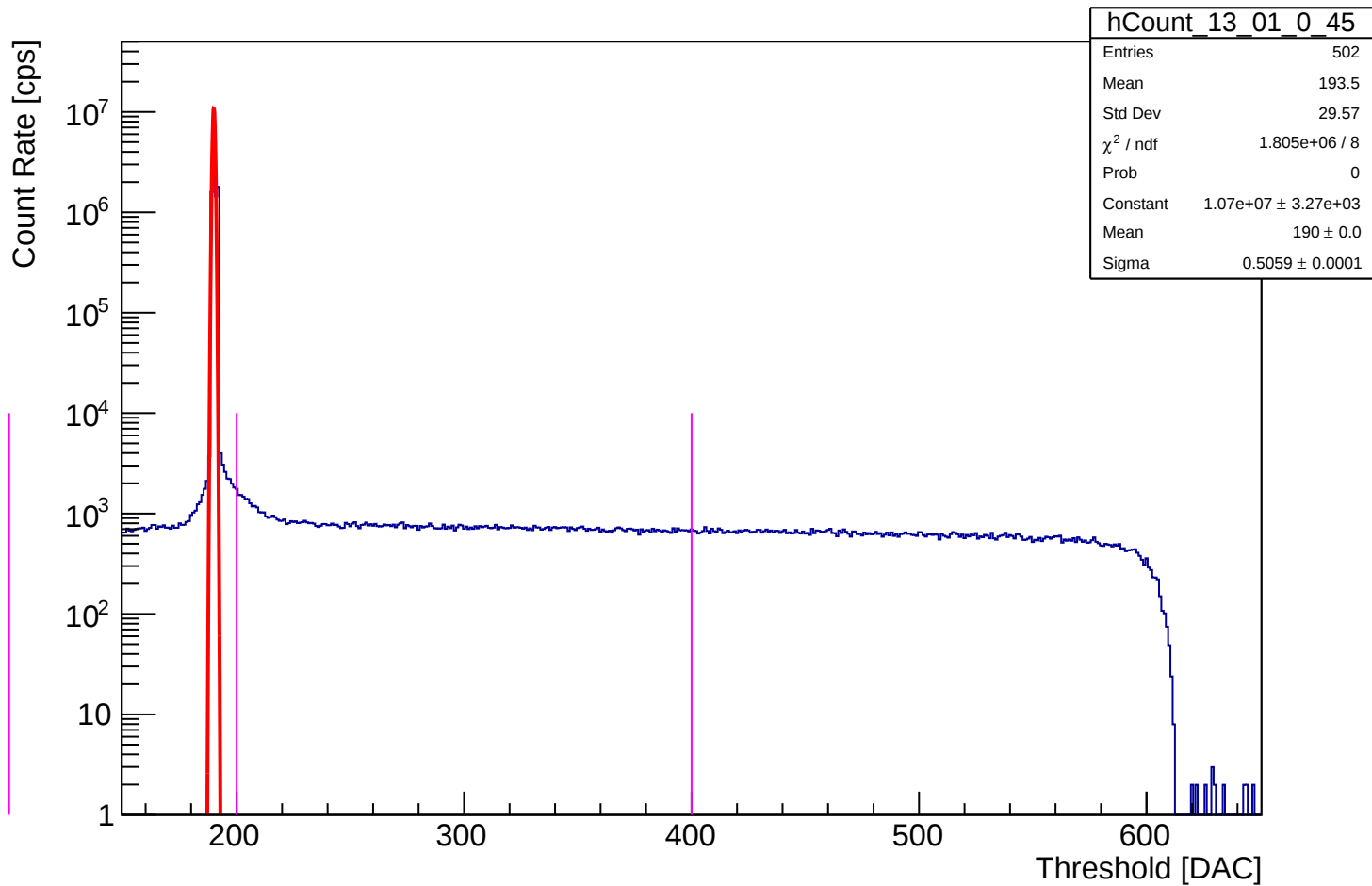
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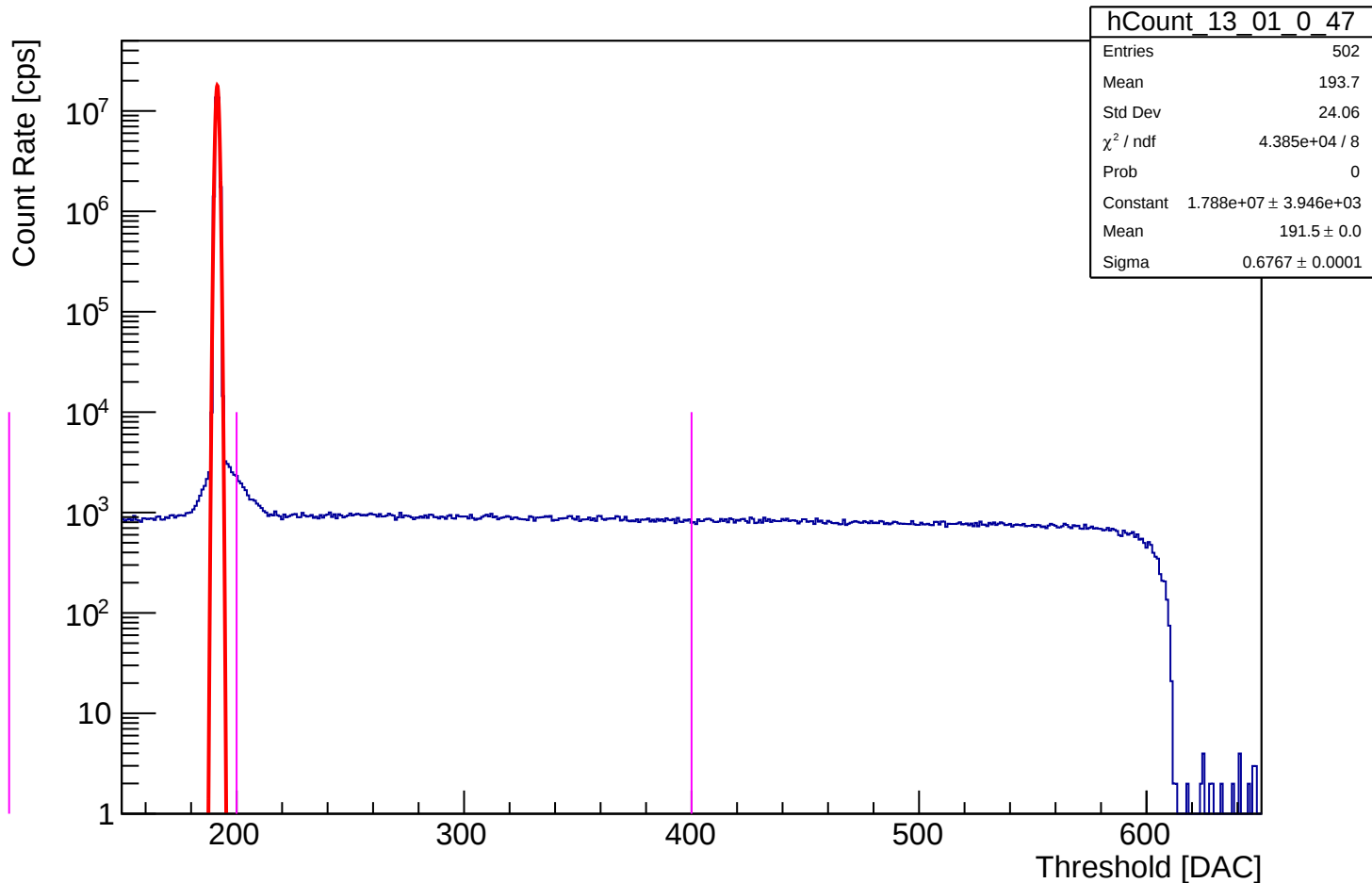
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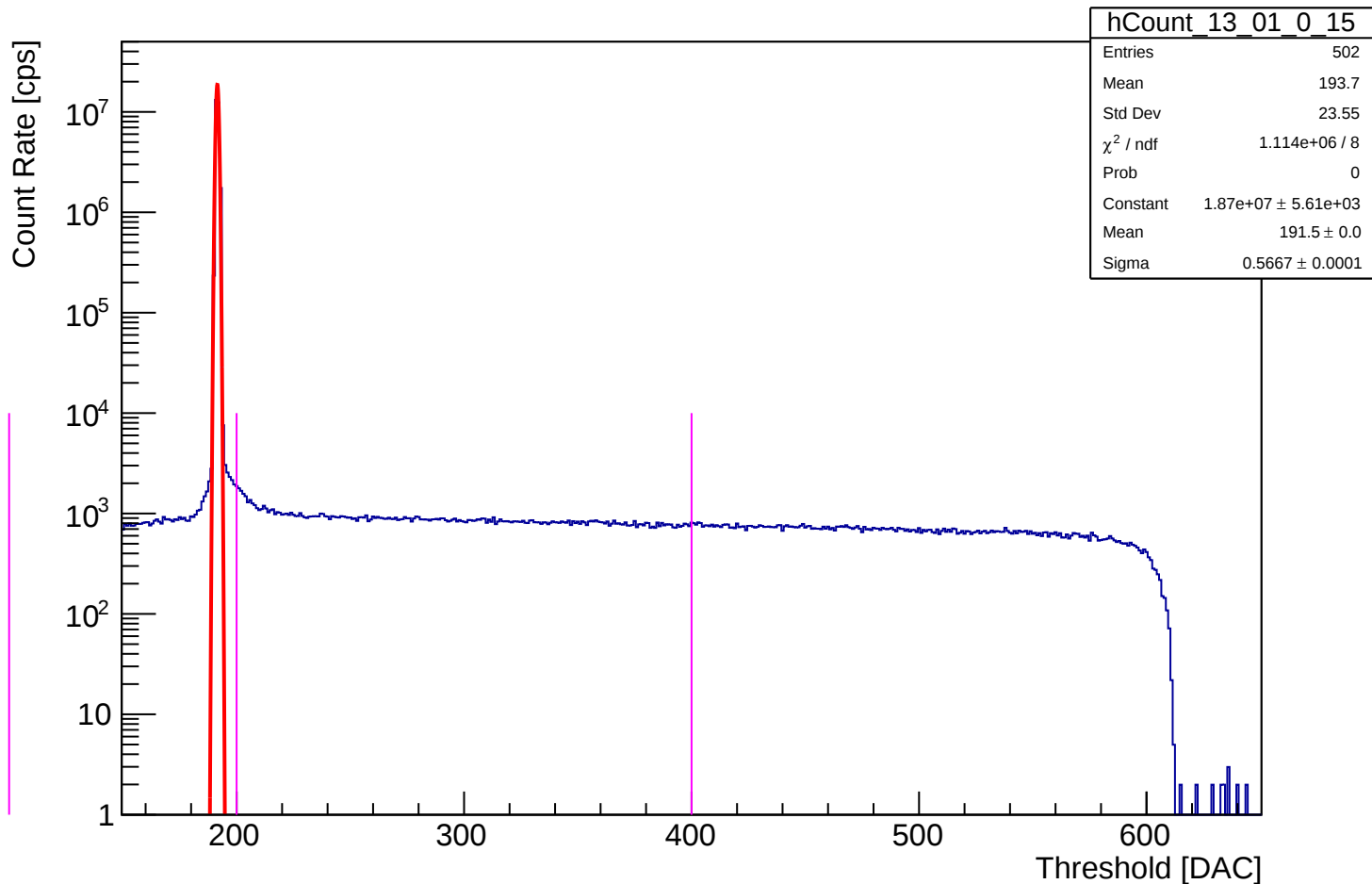
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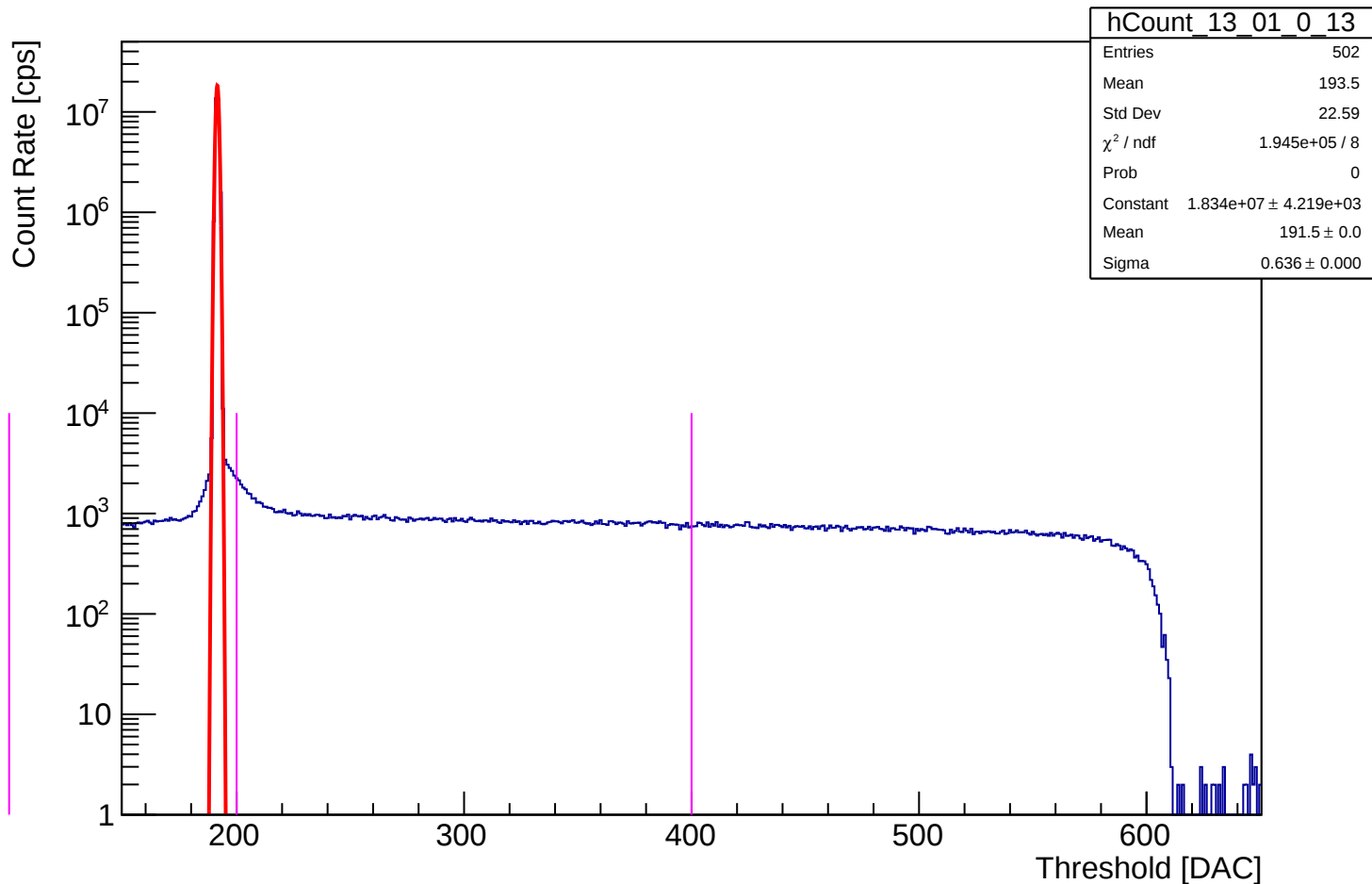
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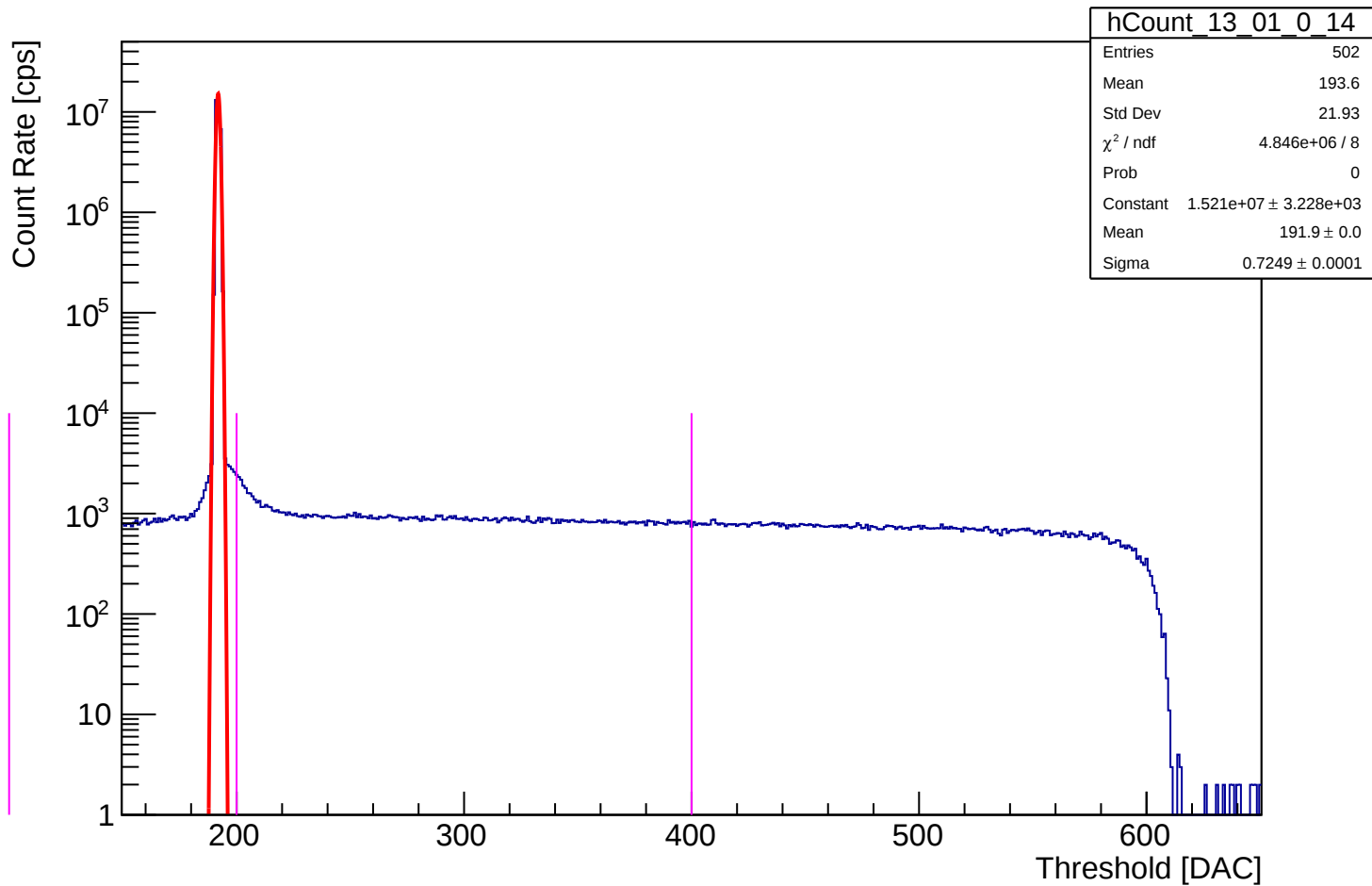
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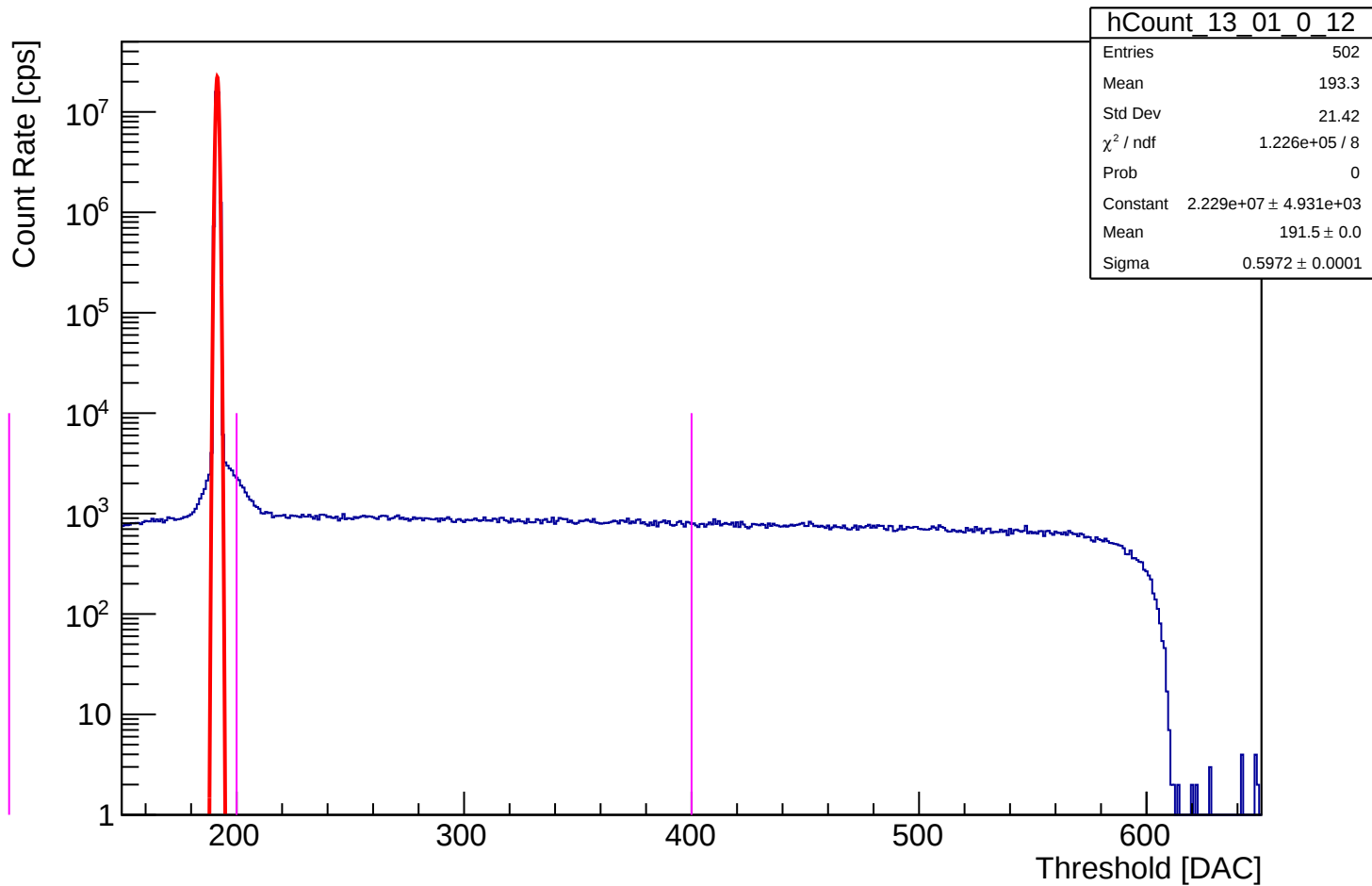
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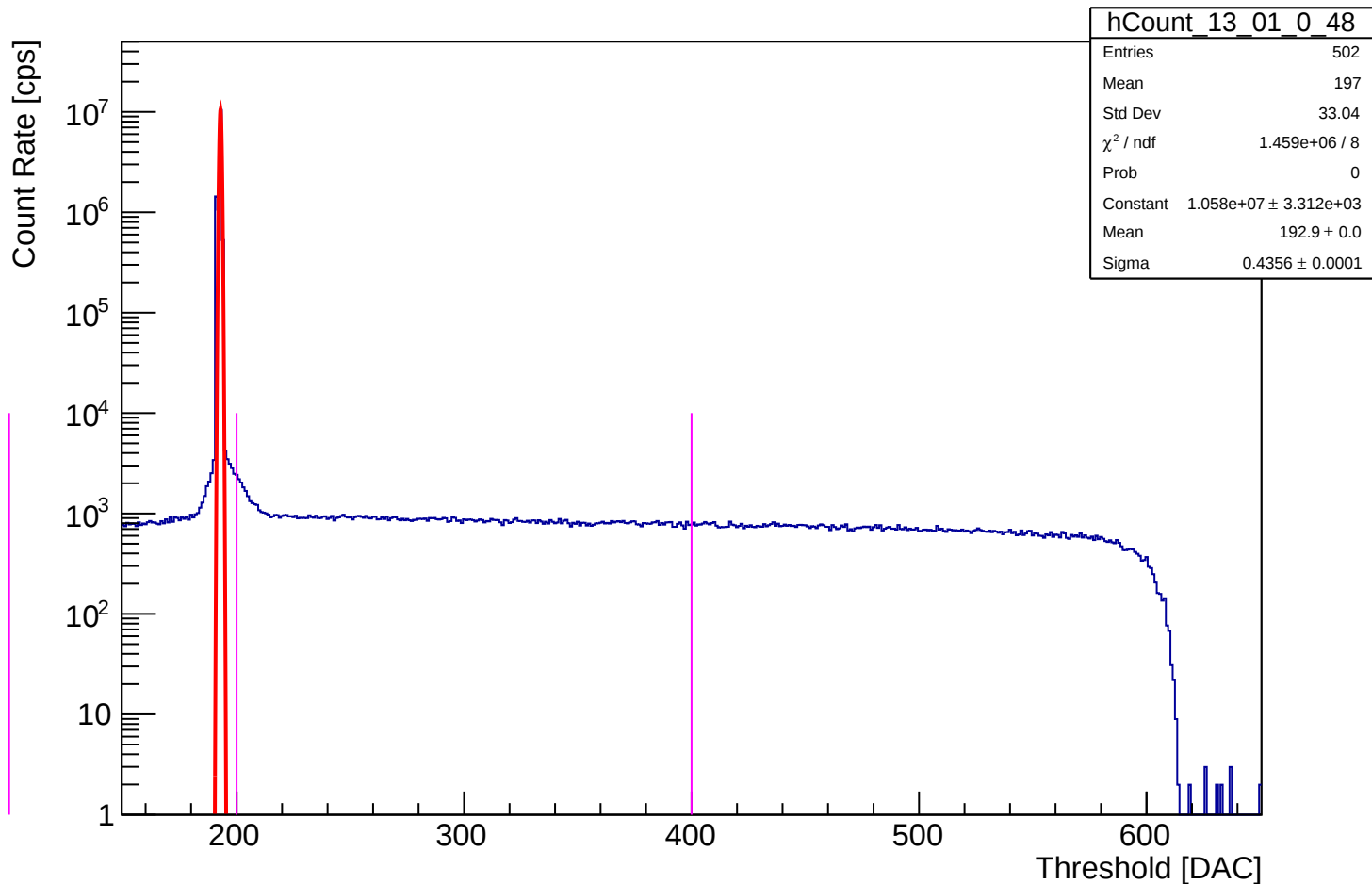
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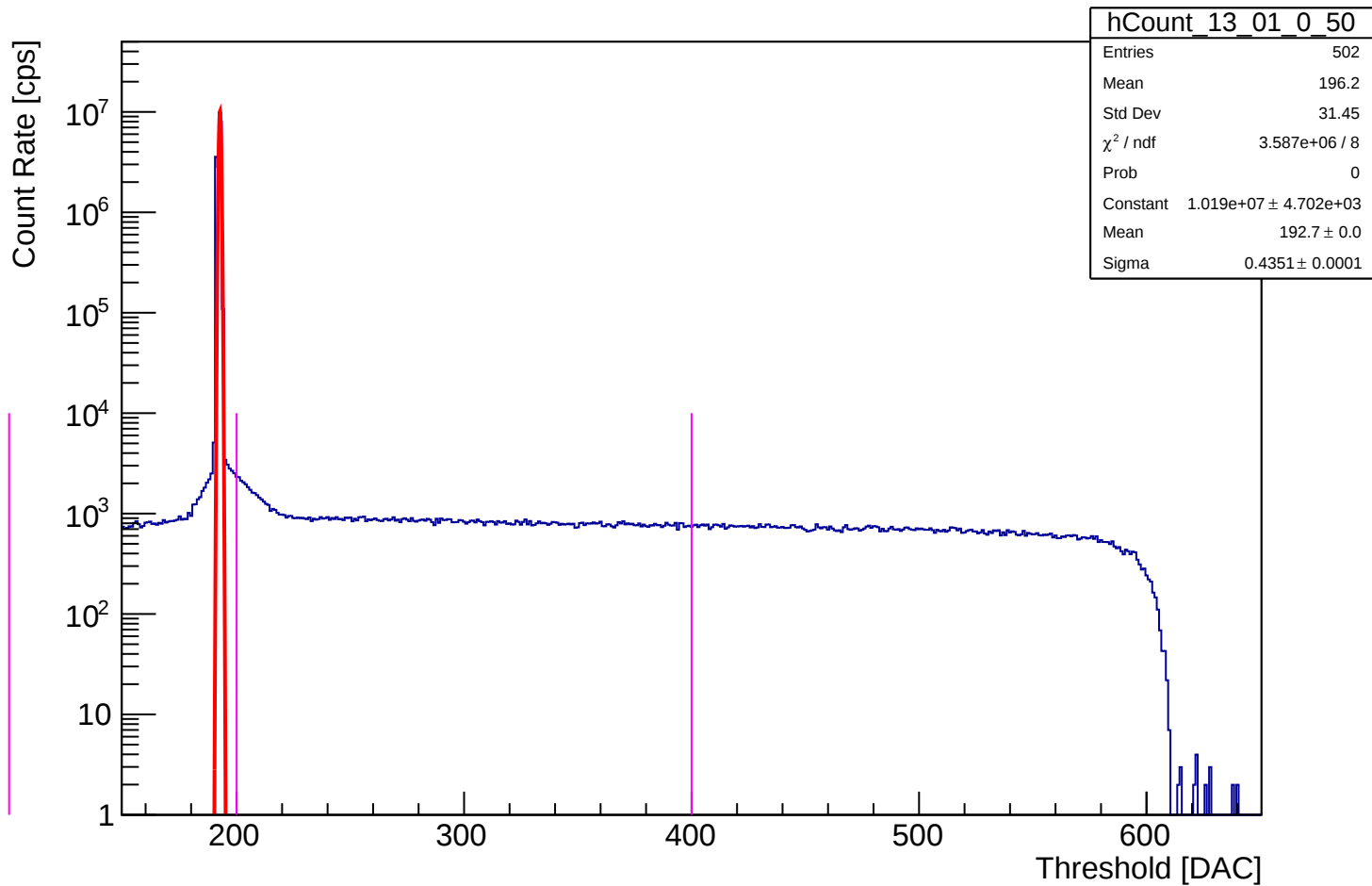
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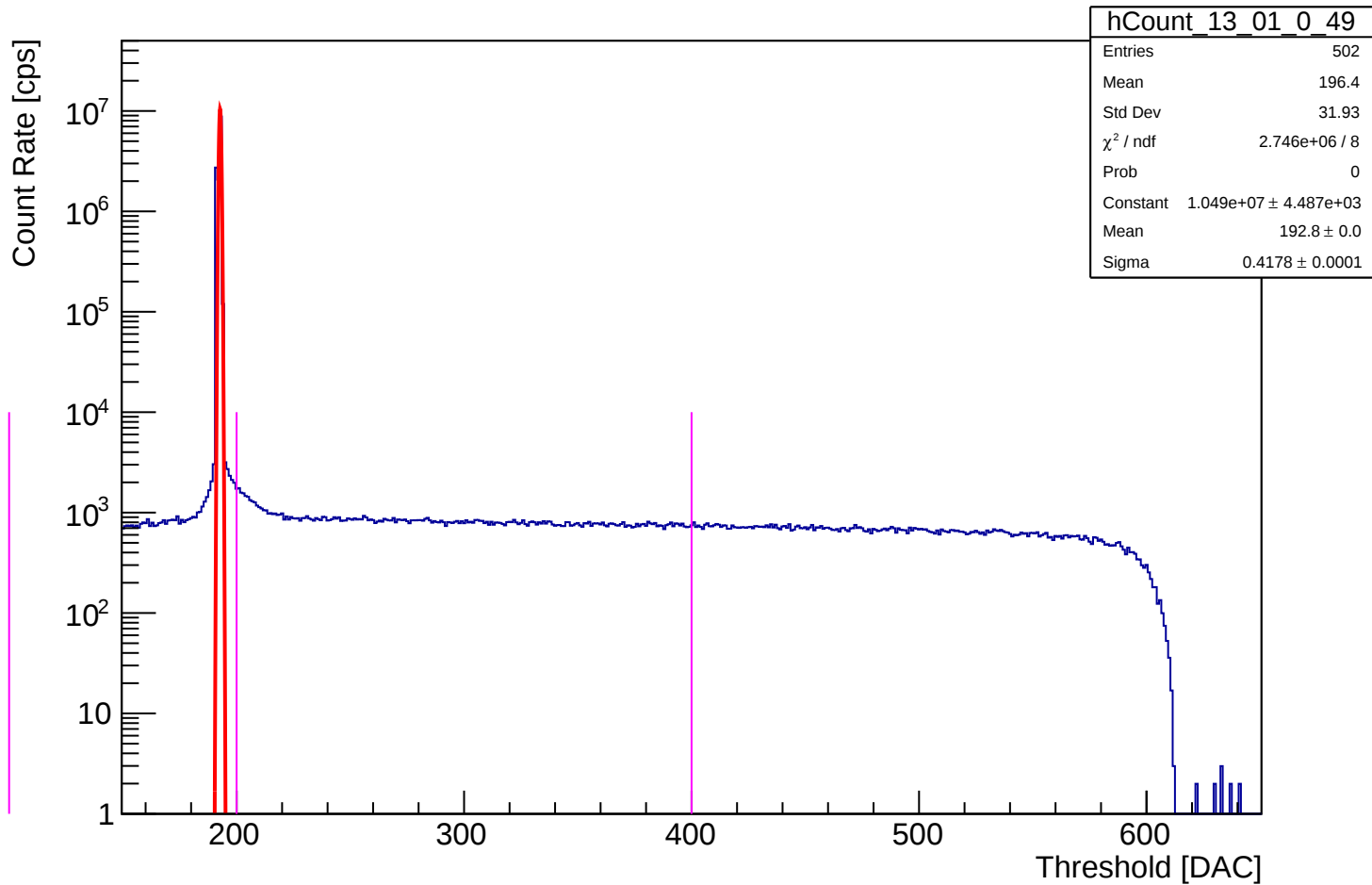
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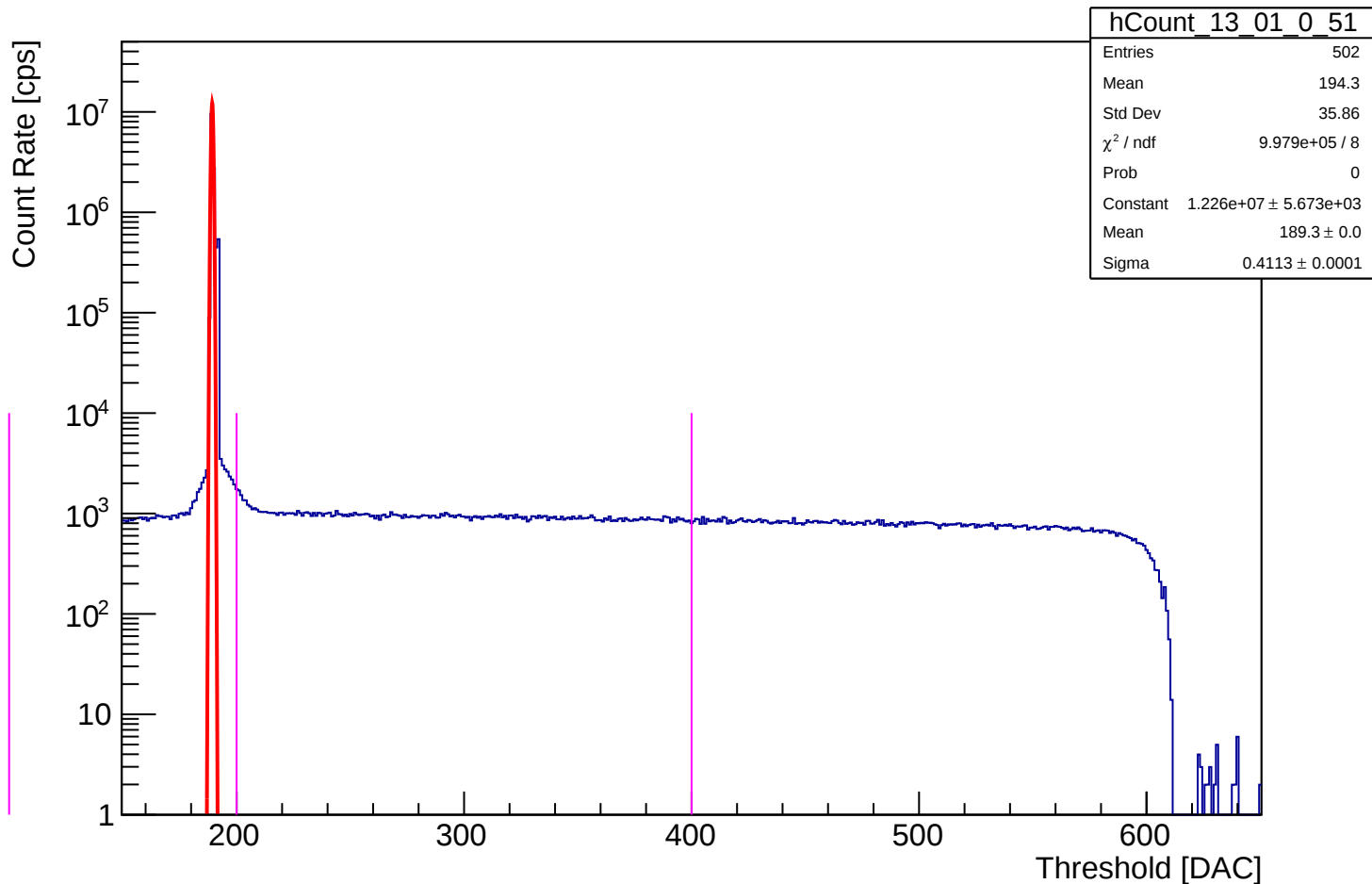
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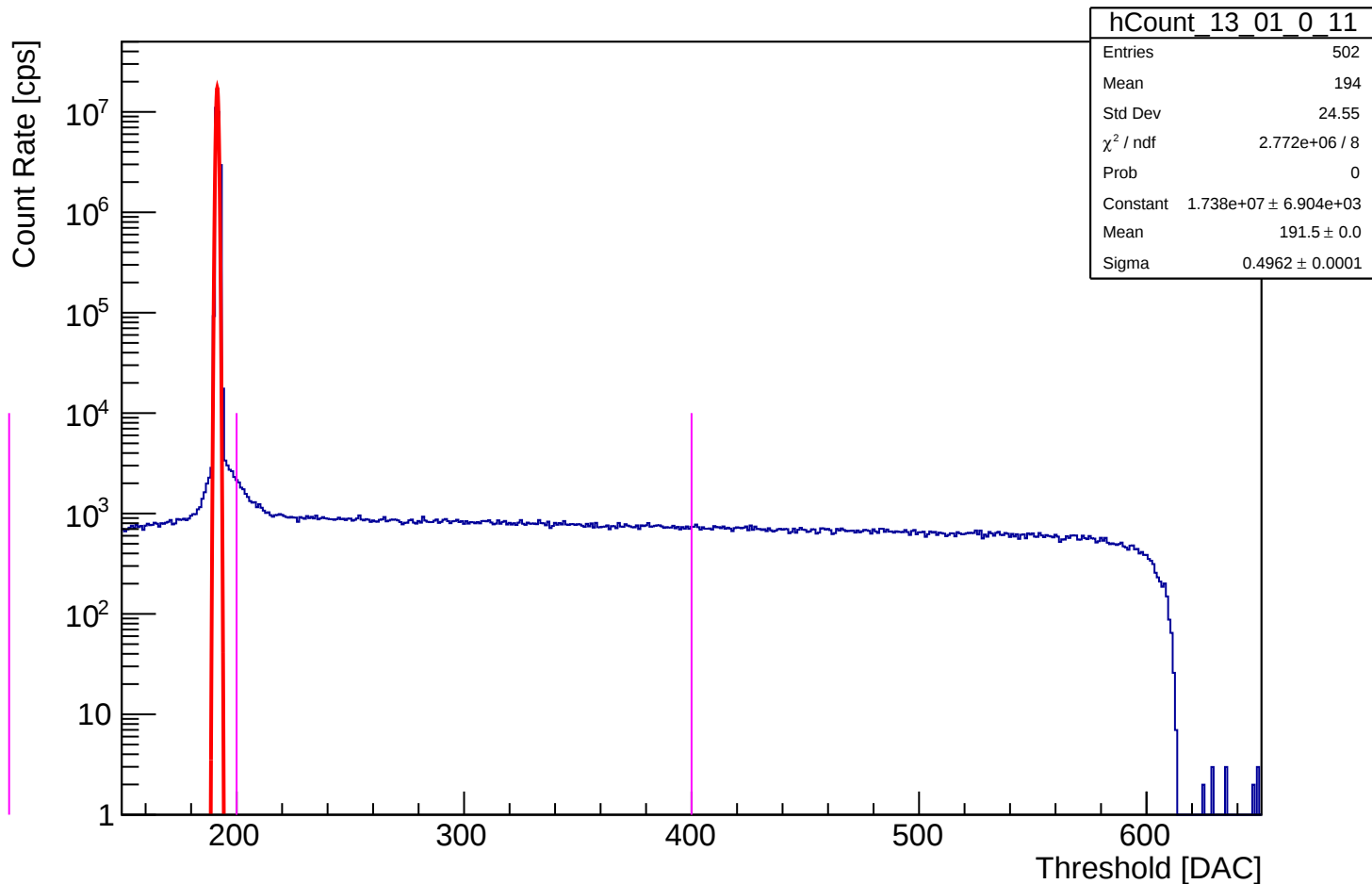
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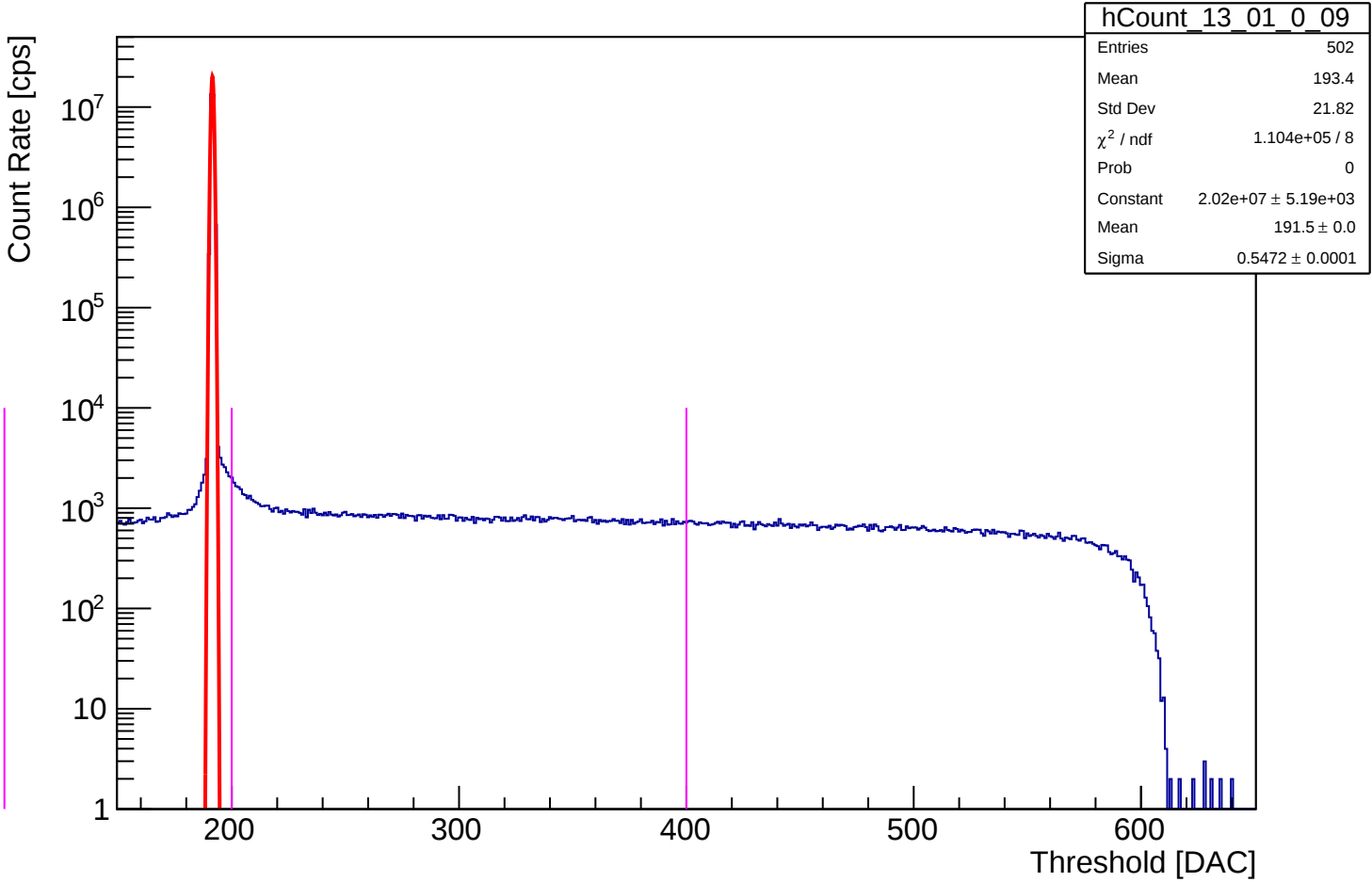


Row 1 Tile 1 Pmt 4 Pixel 40 Slot 13 Fiber 1 Asic 0 Channel 51

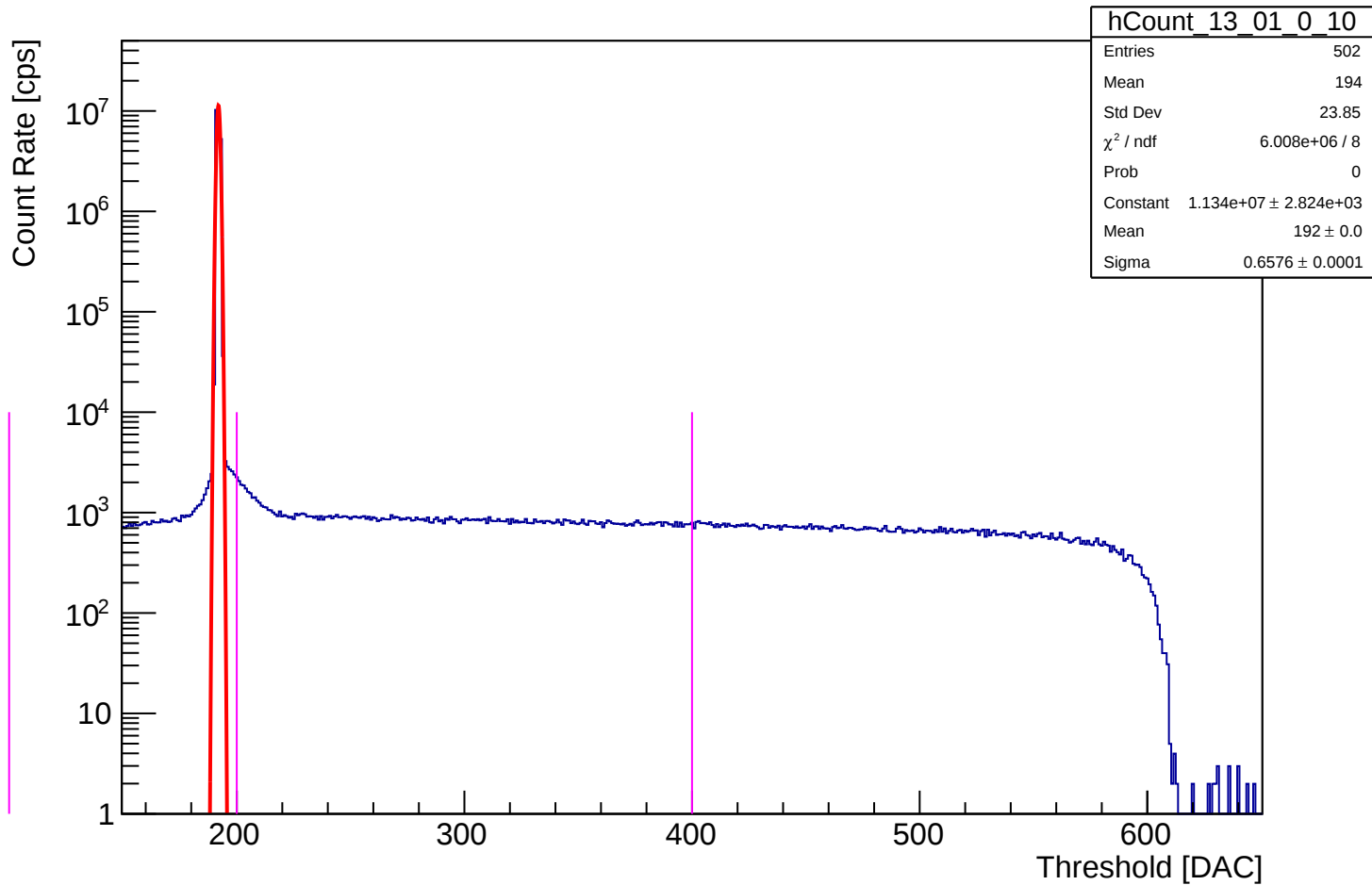


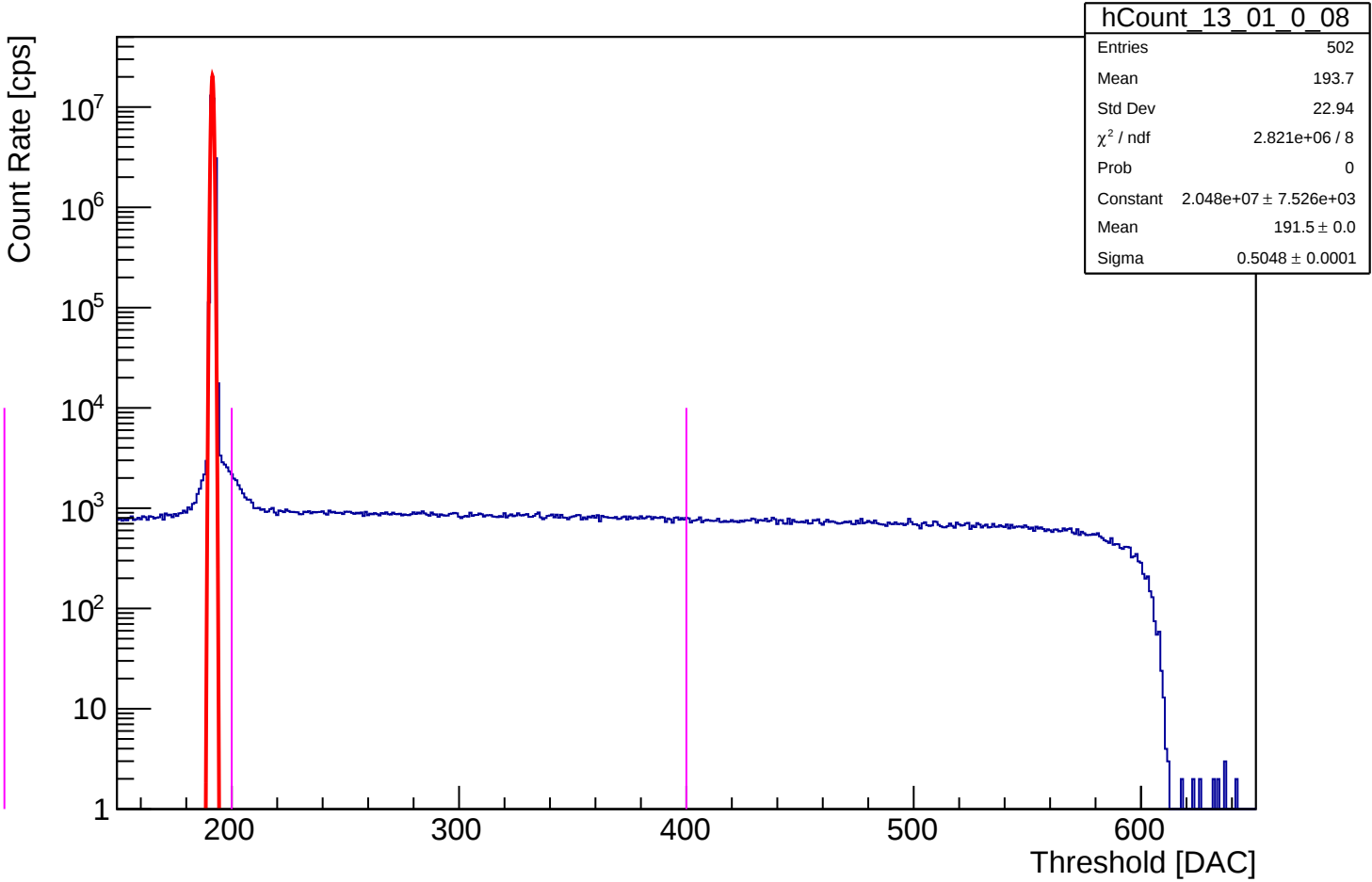
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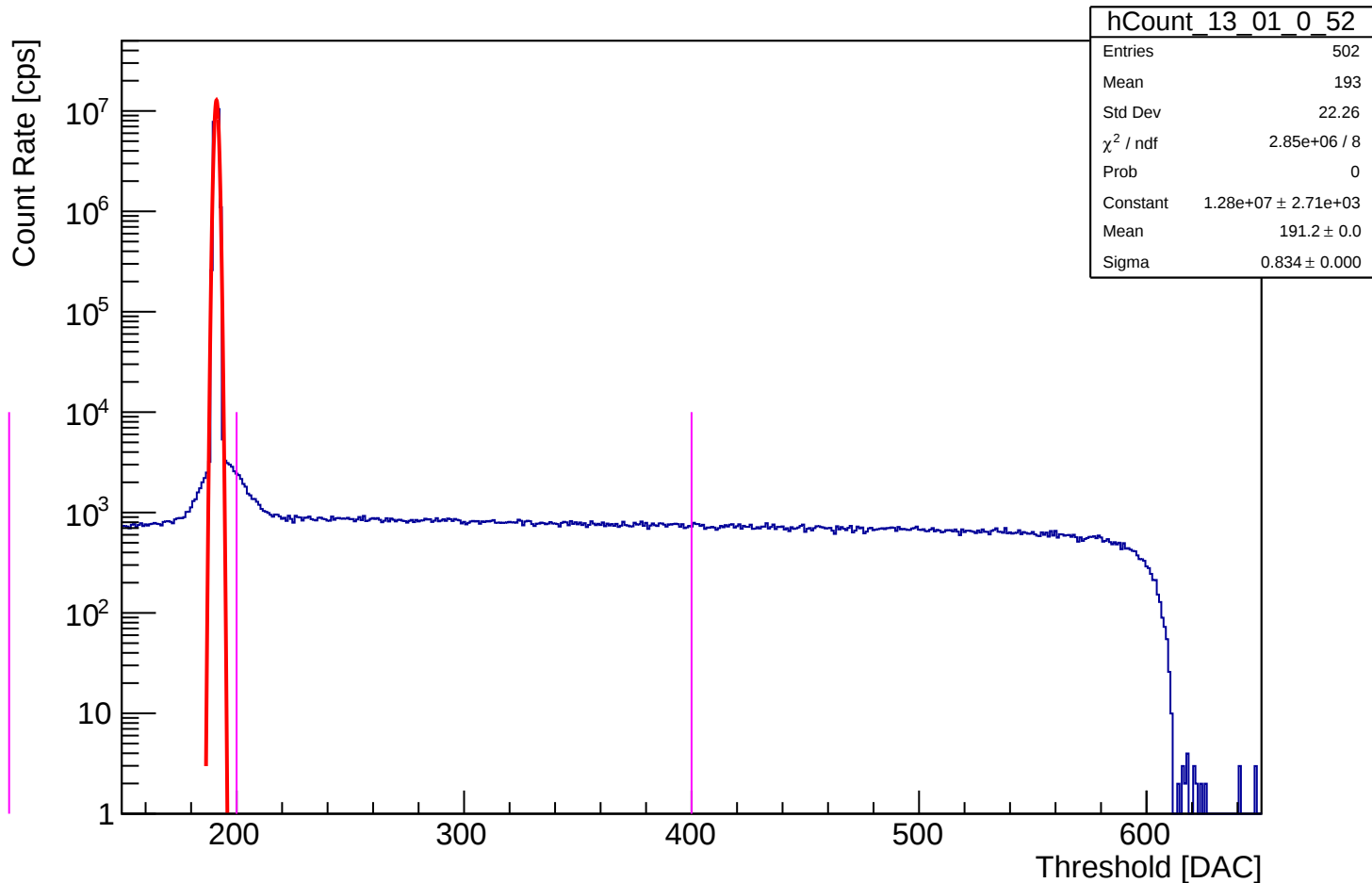


Row 1 Tile 1 Pmt 4 Pixel 43 Slot 13 Fiber 1 Asic 0 Channel 10

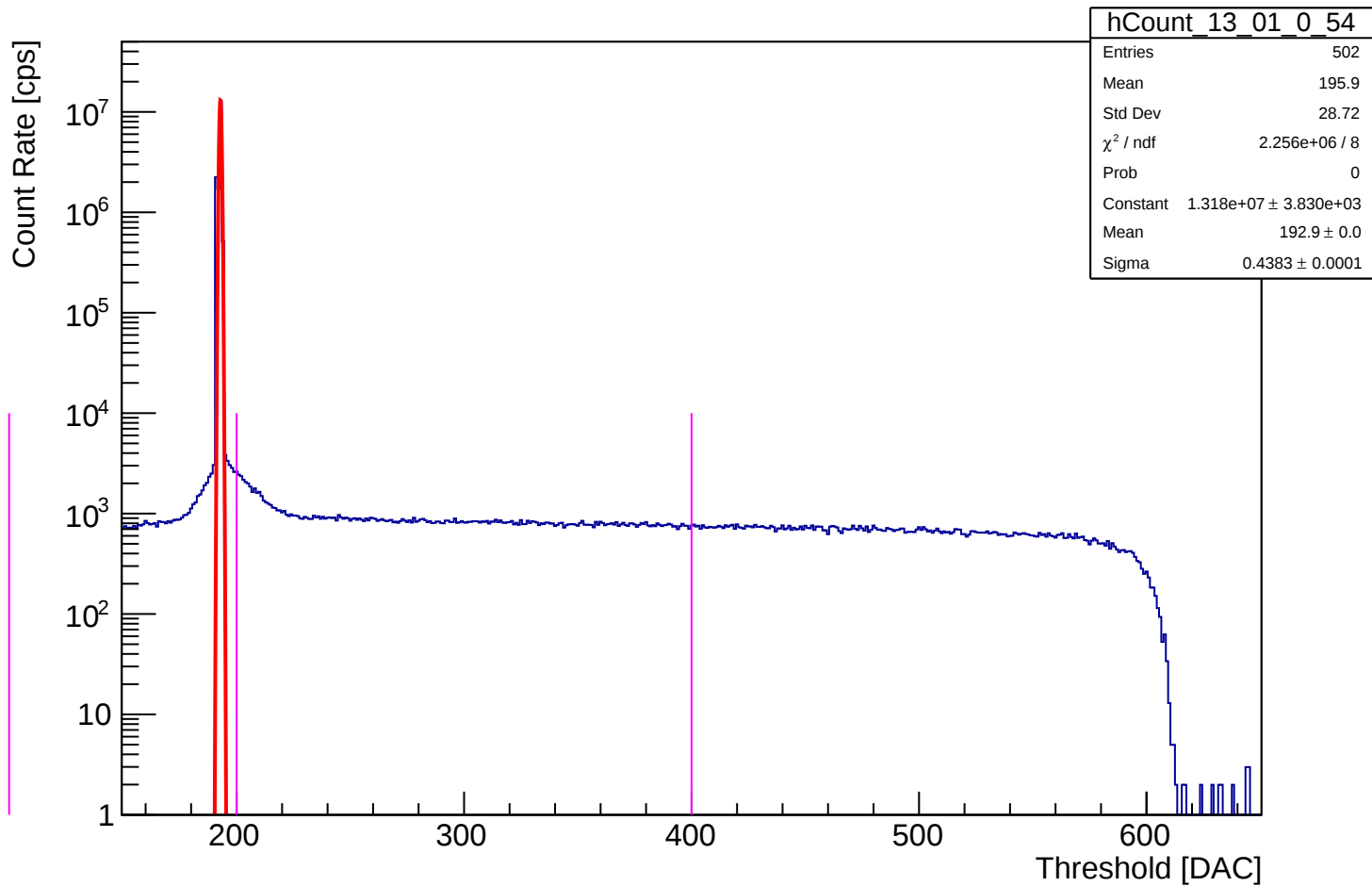




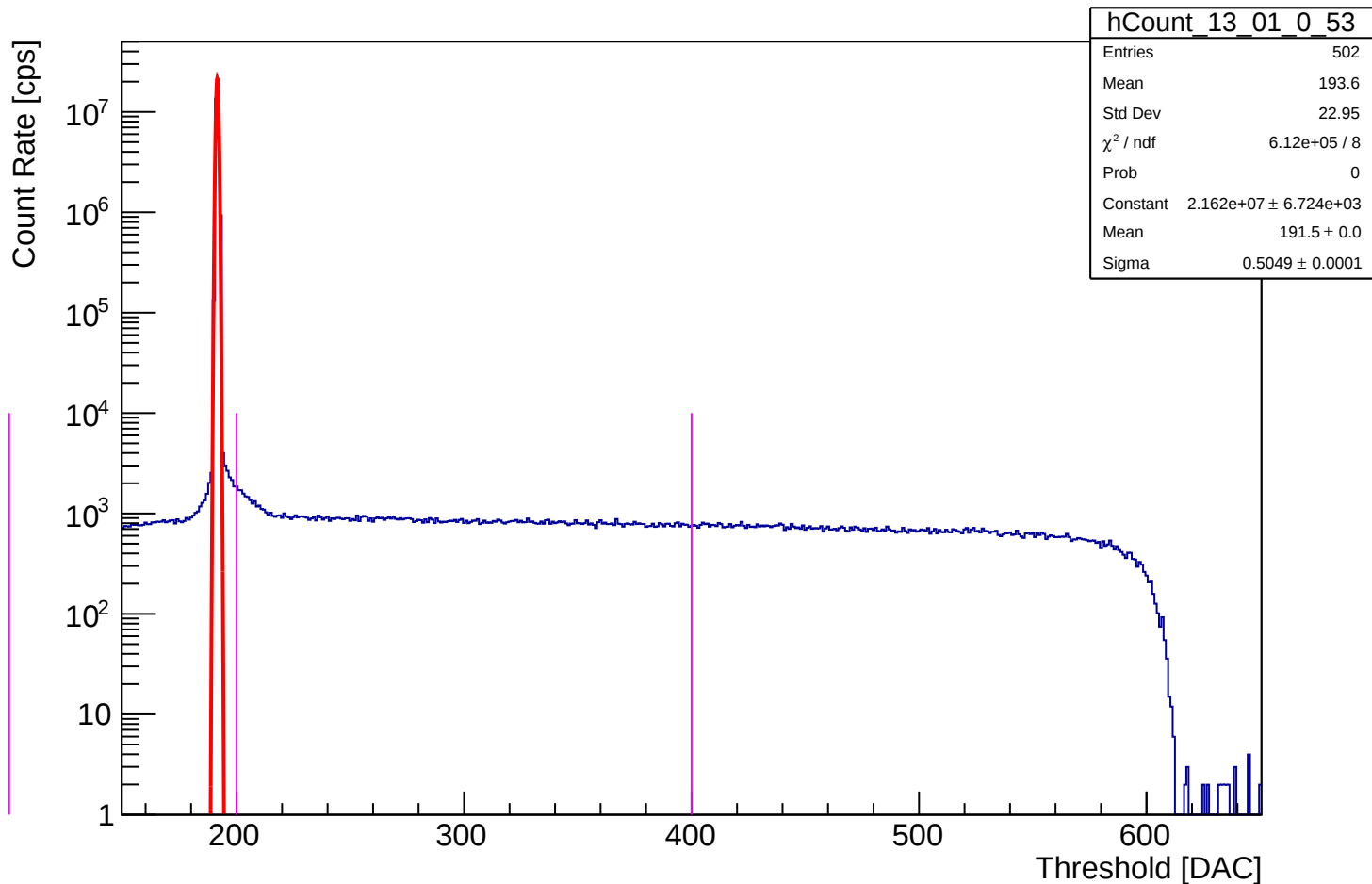
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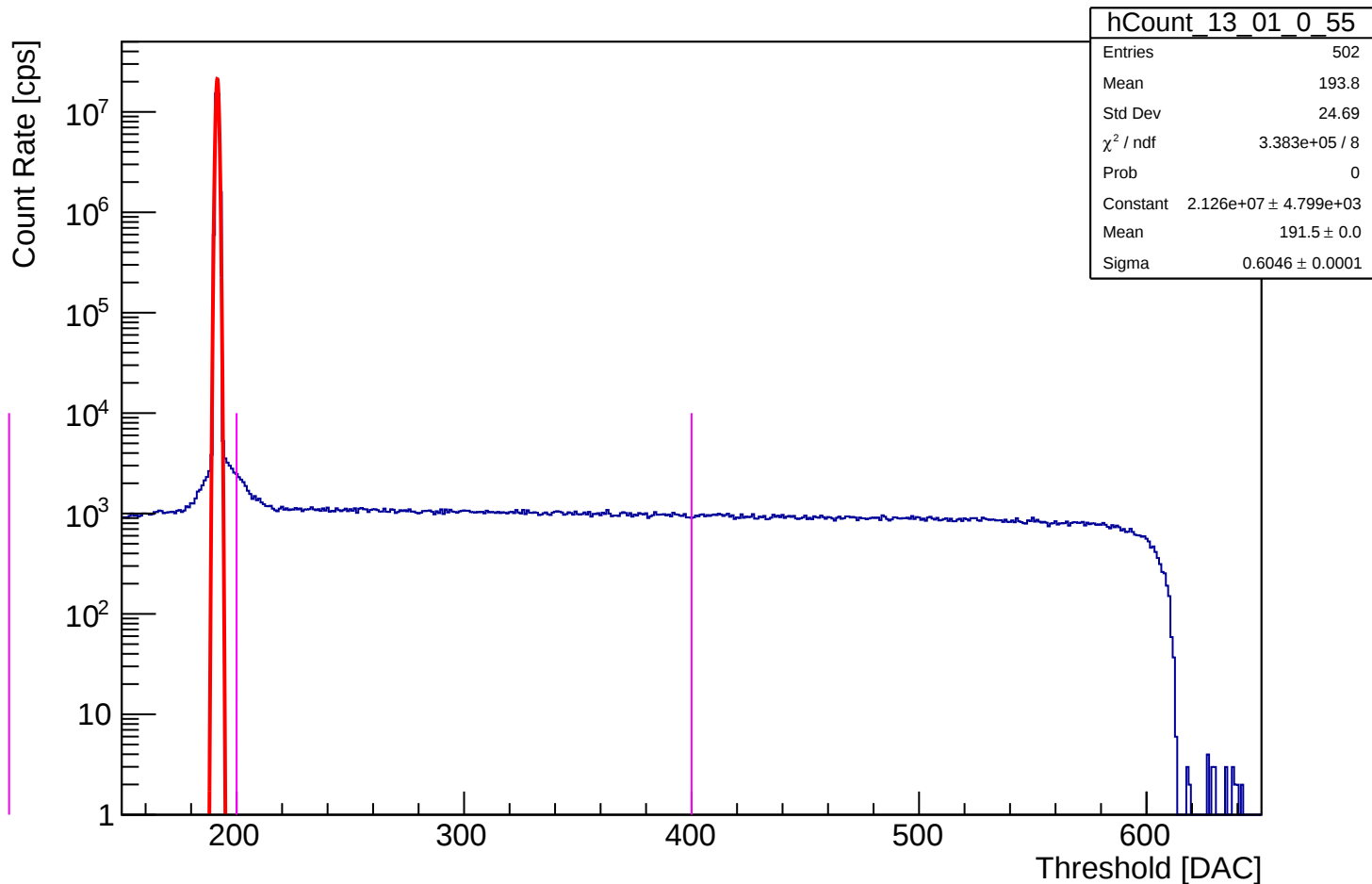
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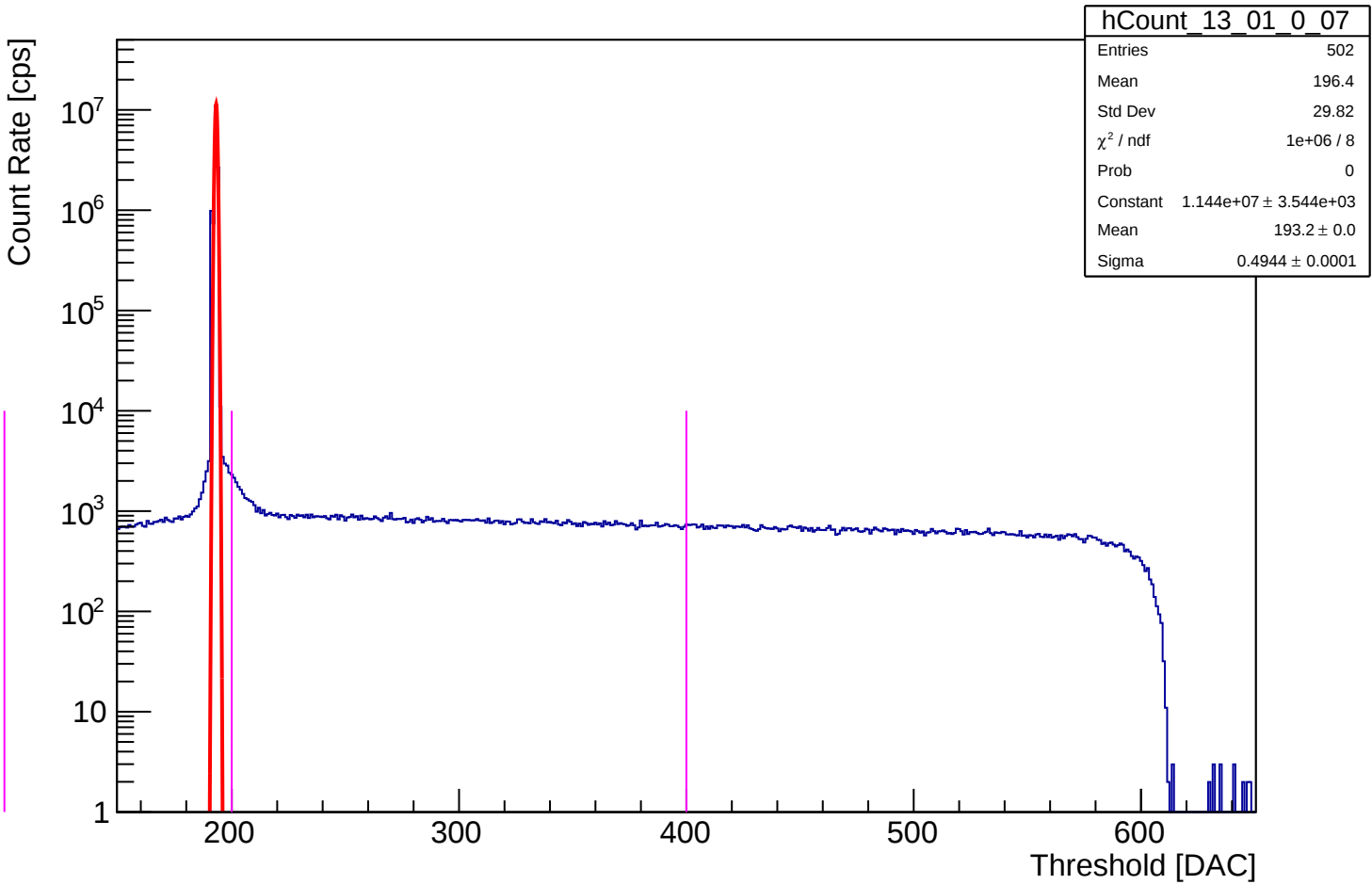


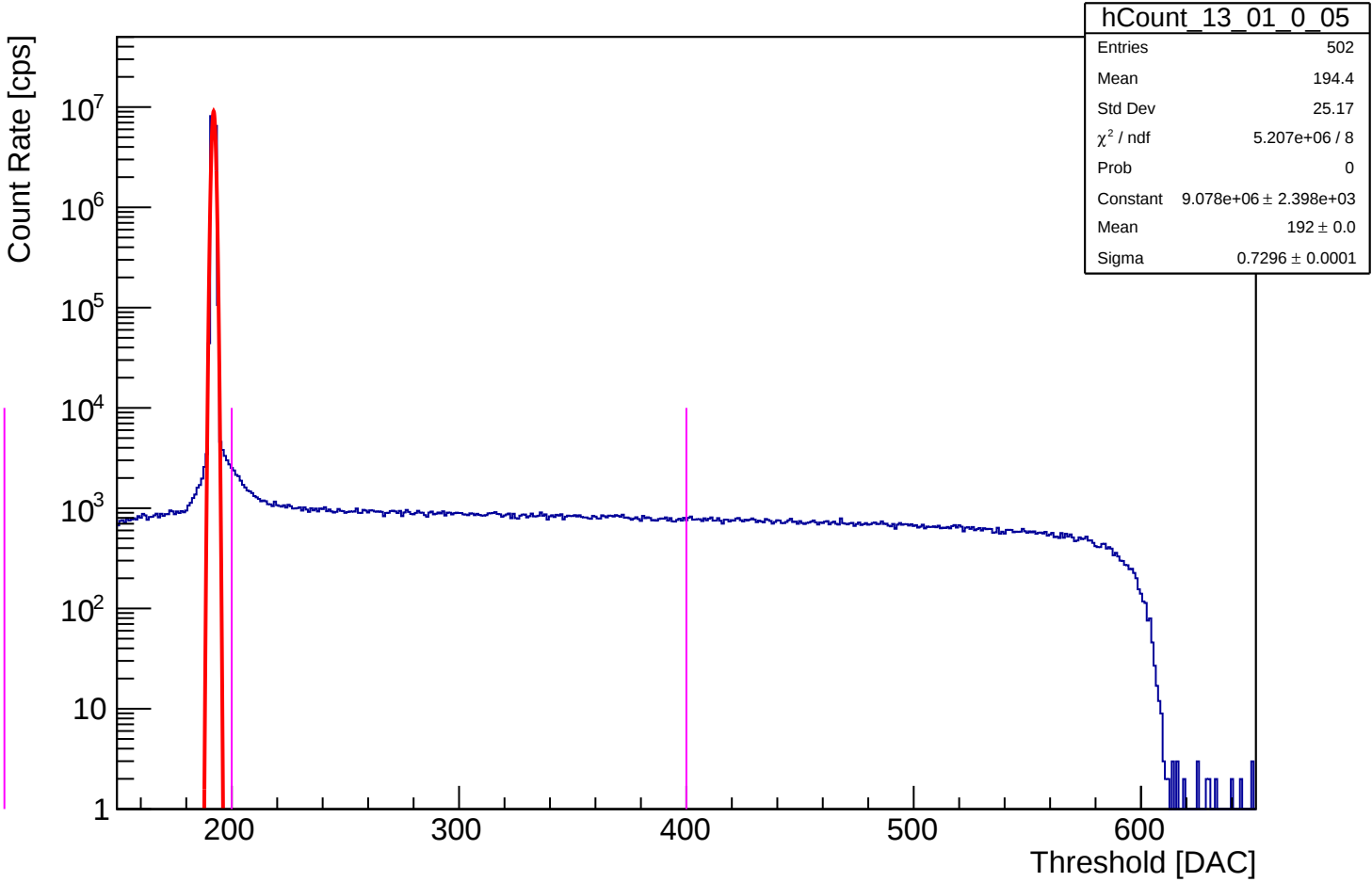
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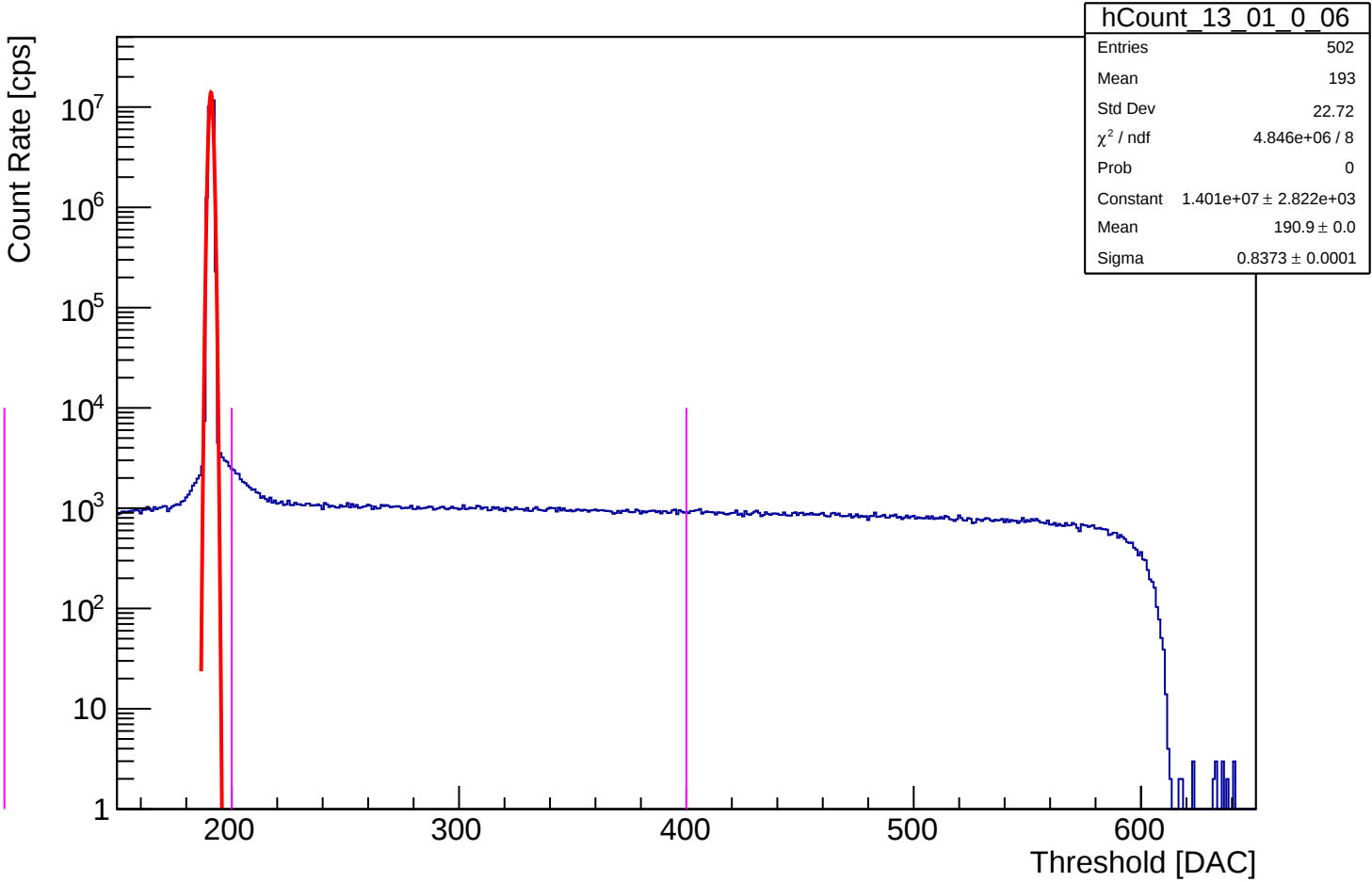


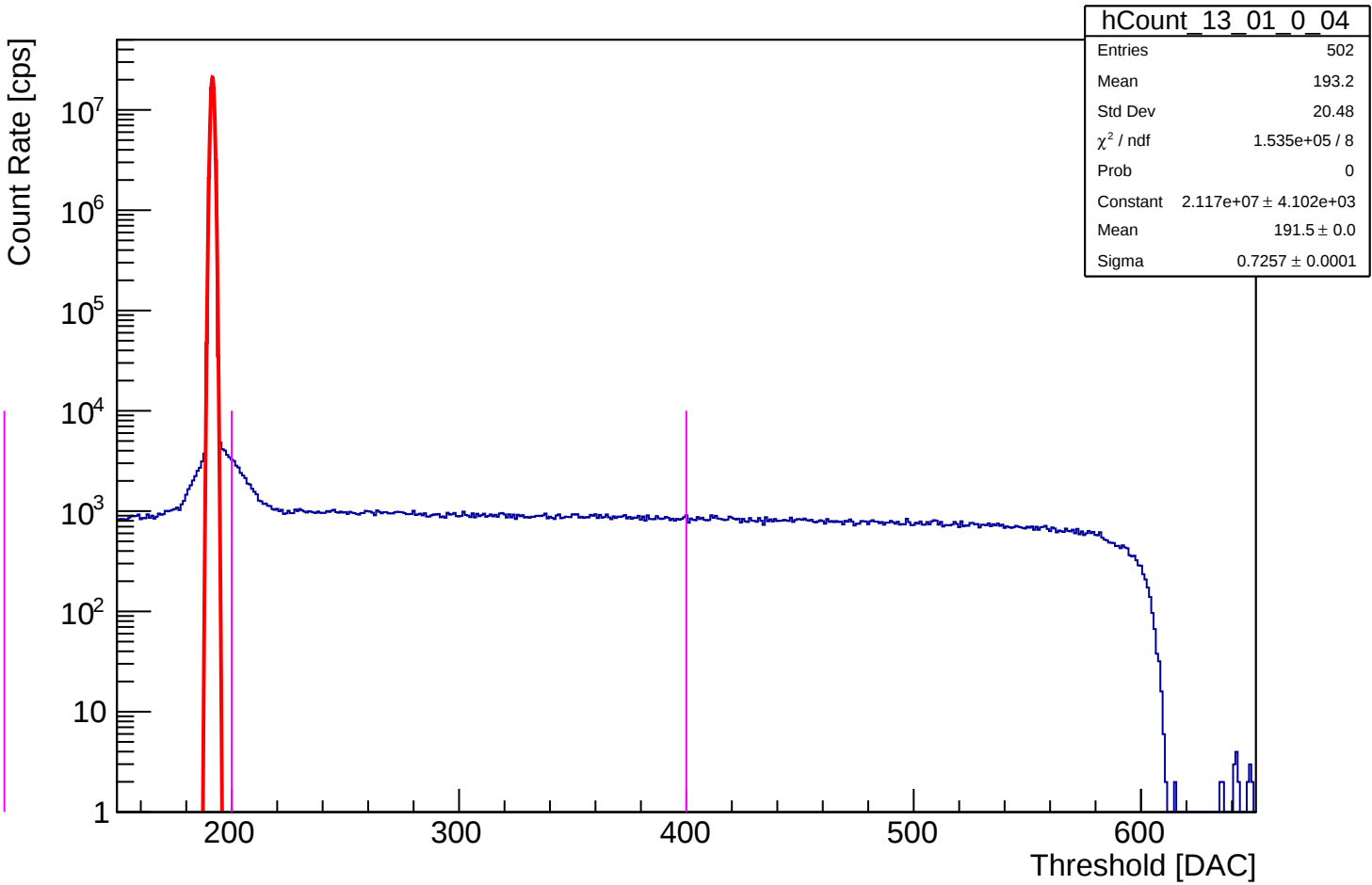
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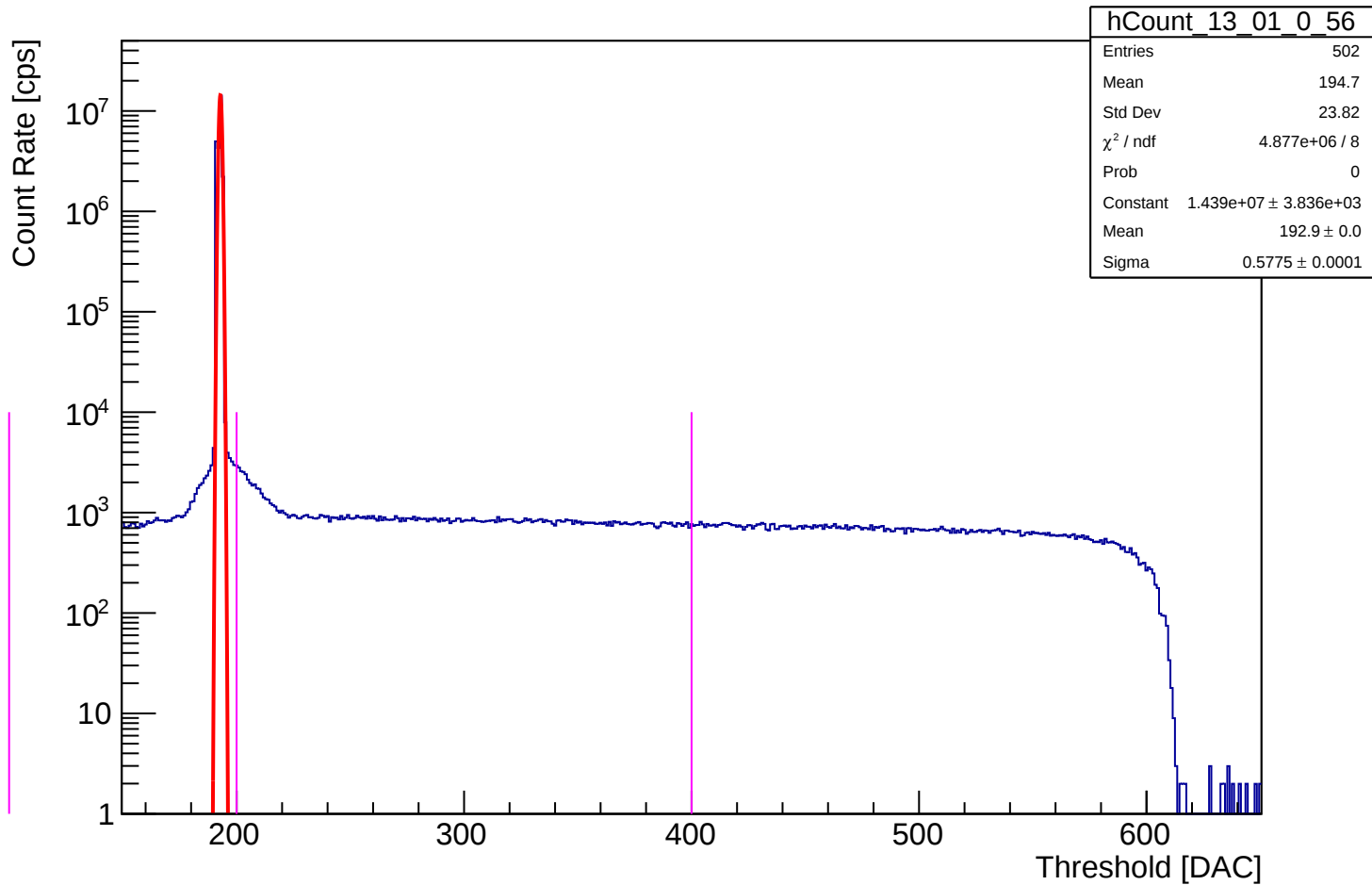




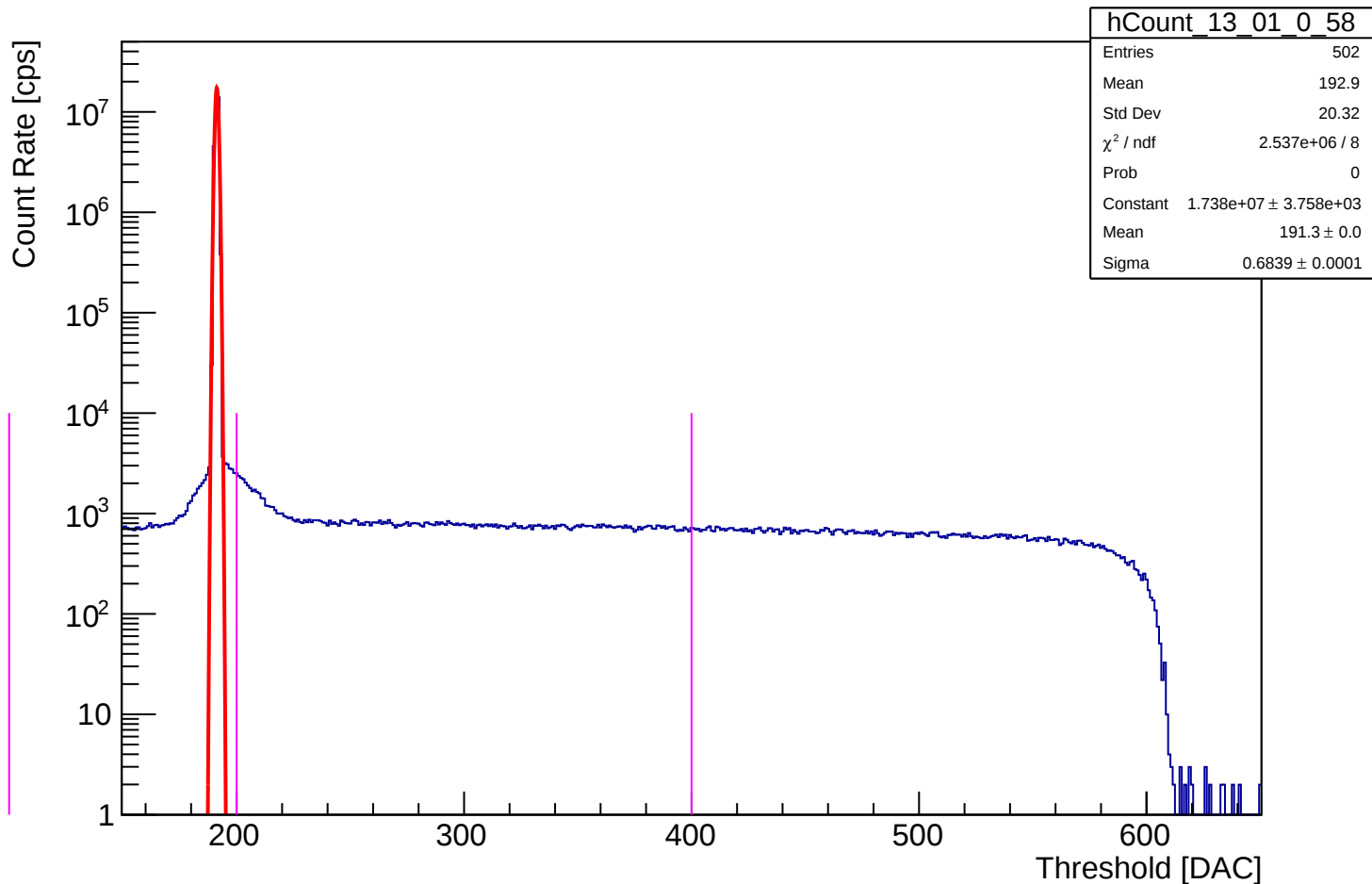




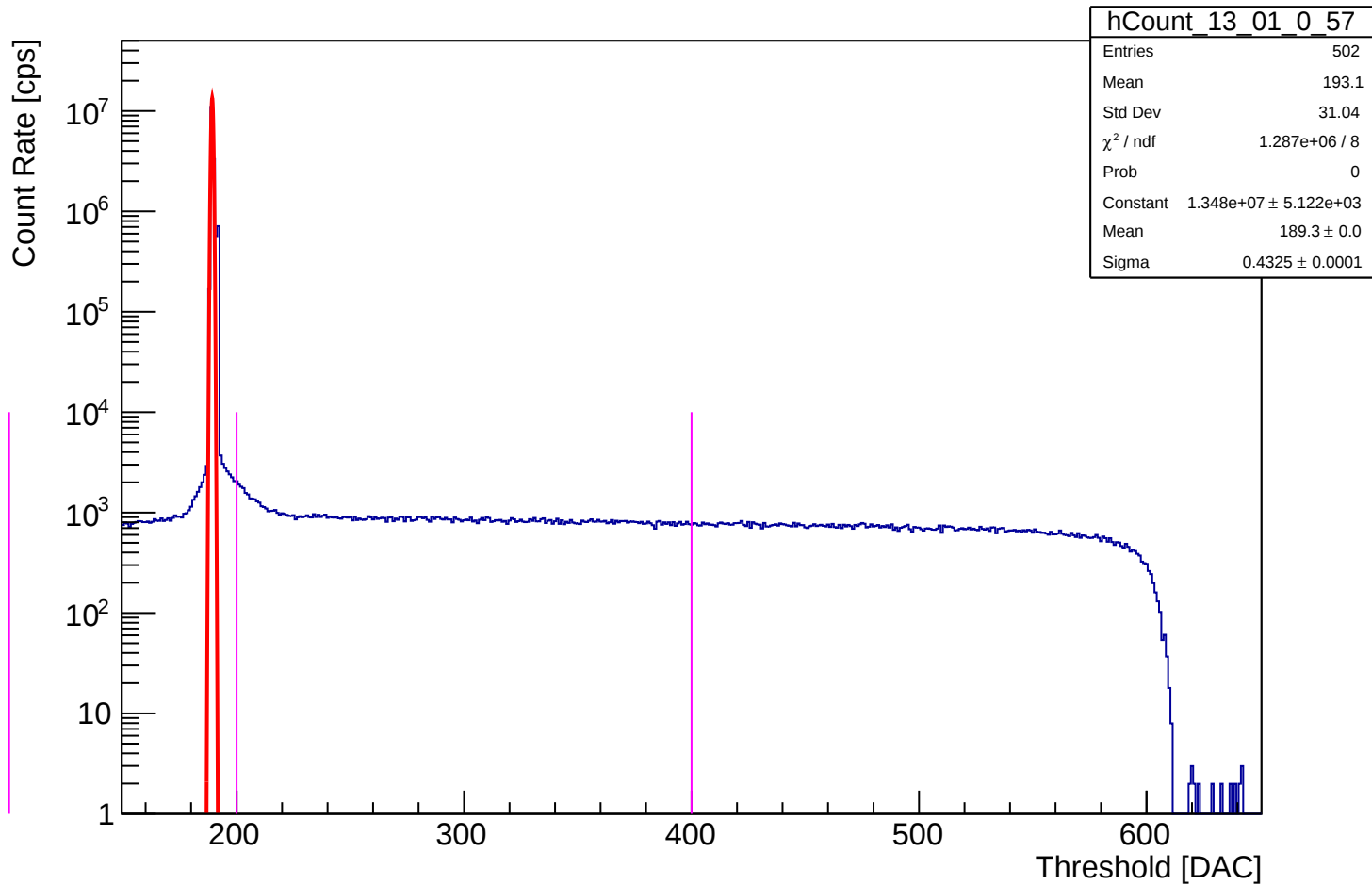
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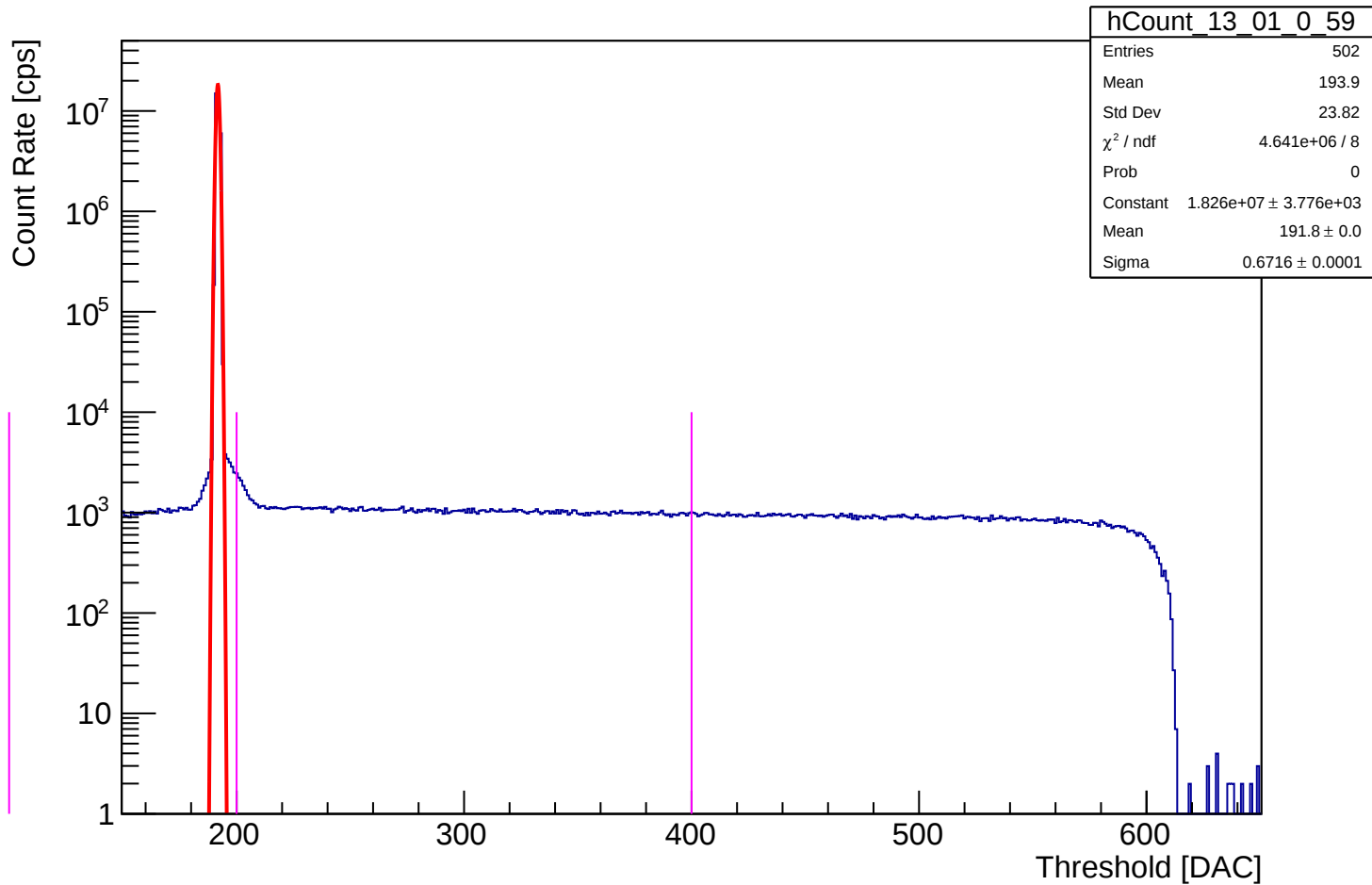
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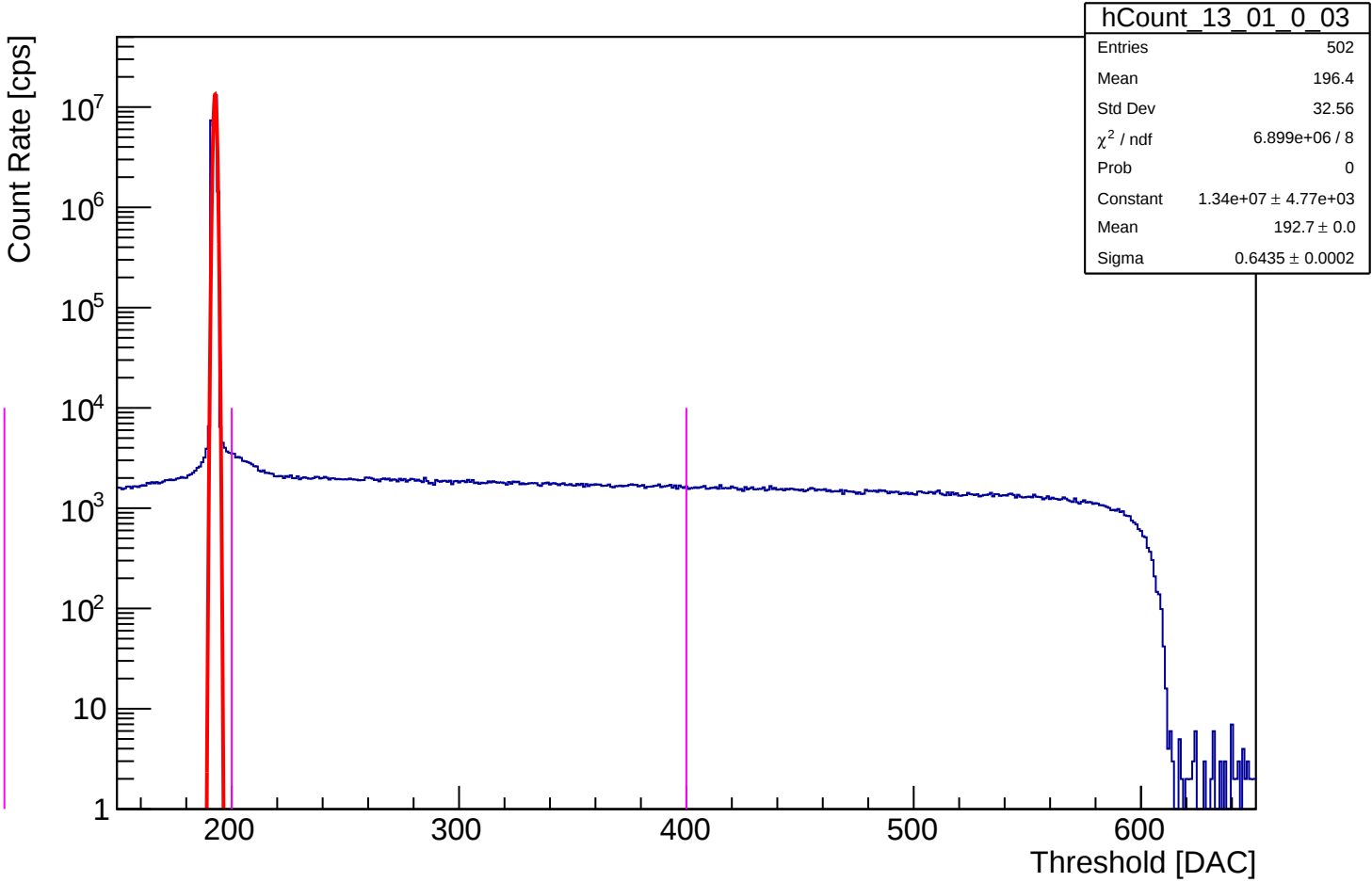


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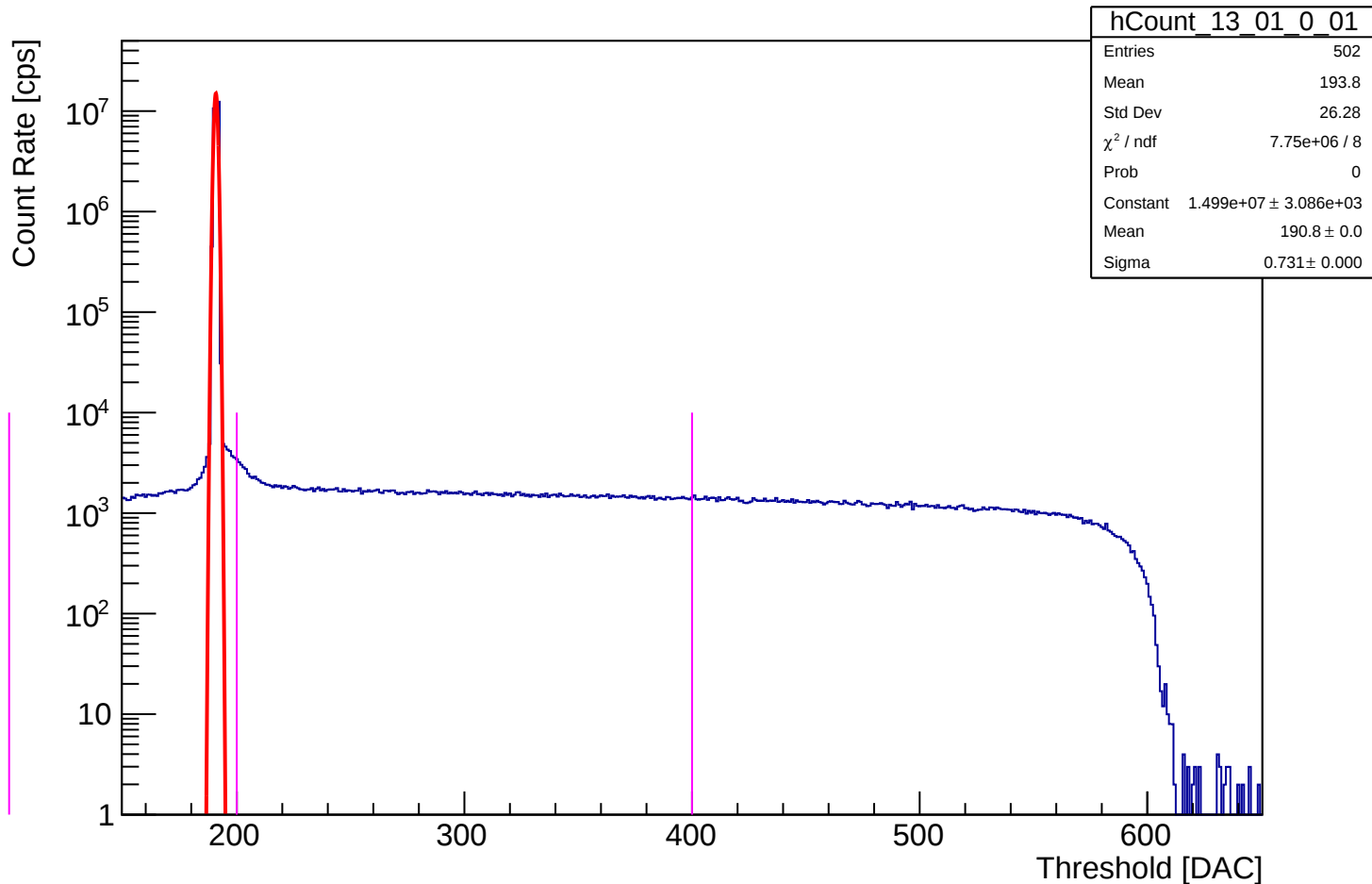


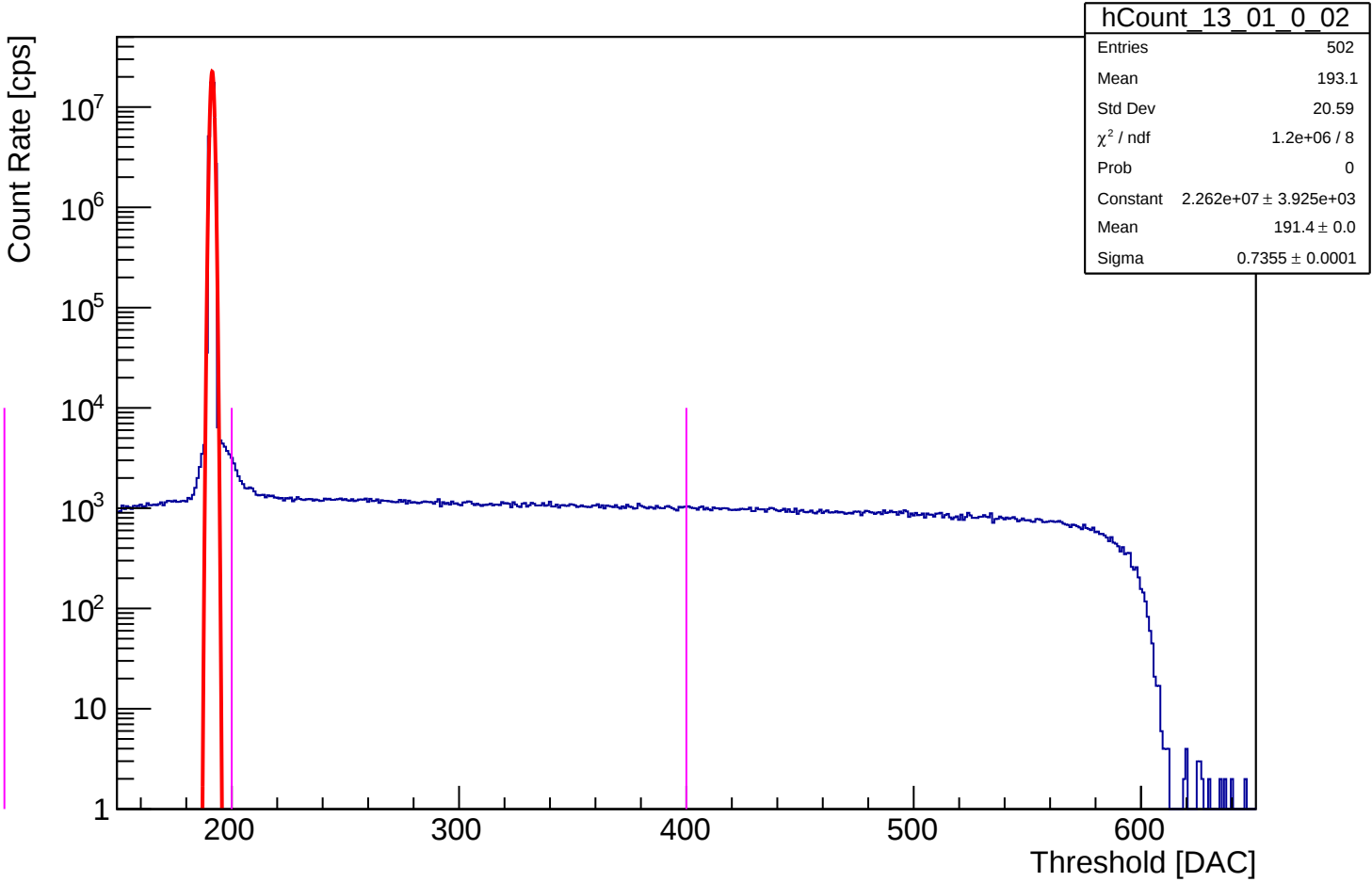
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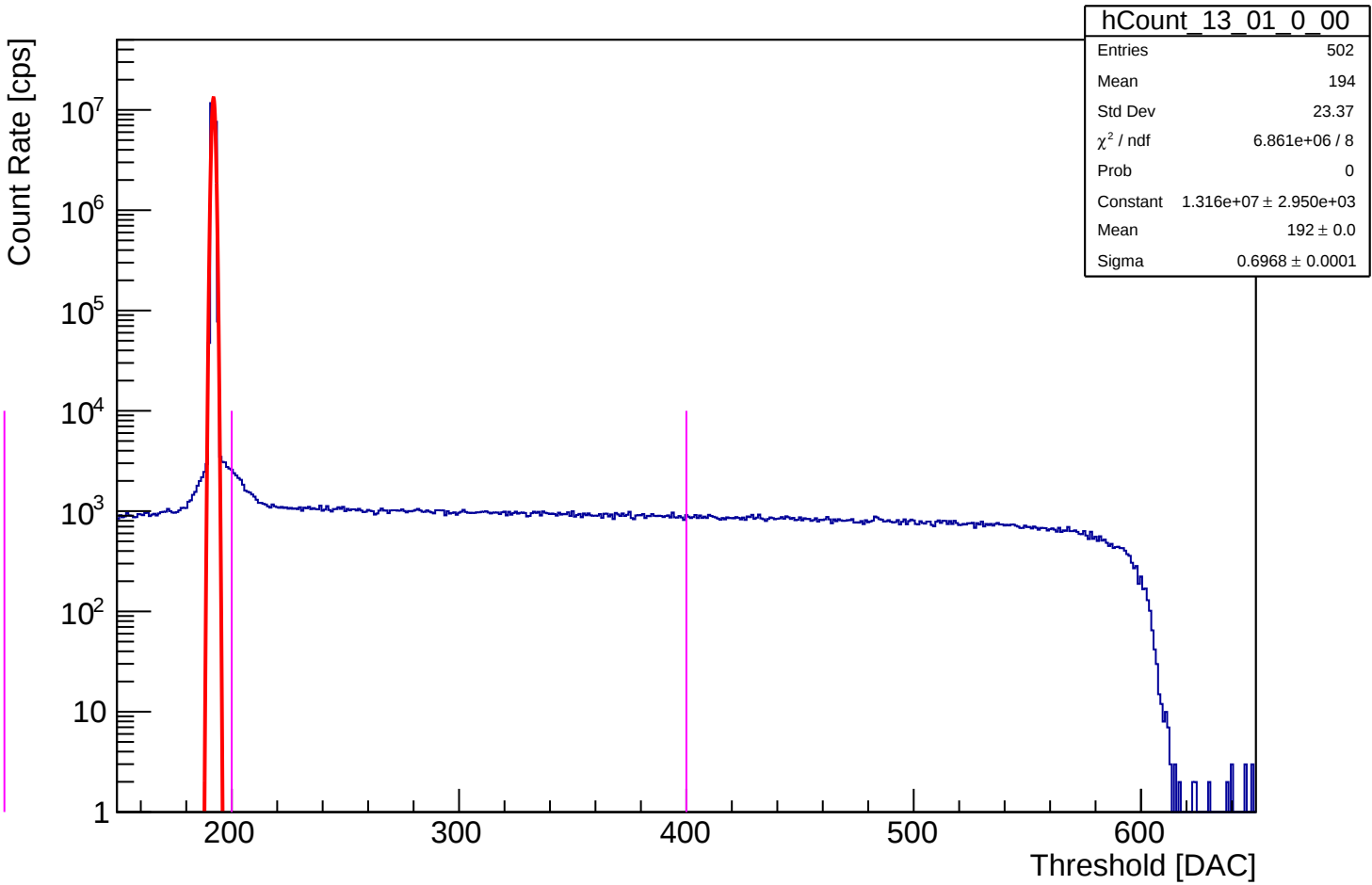




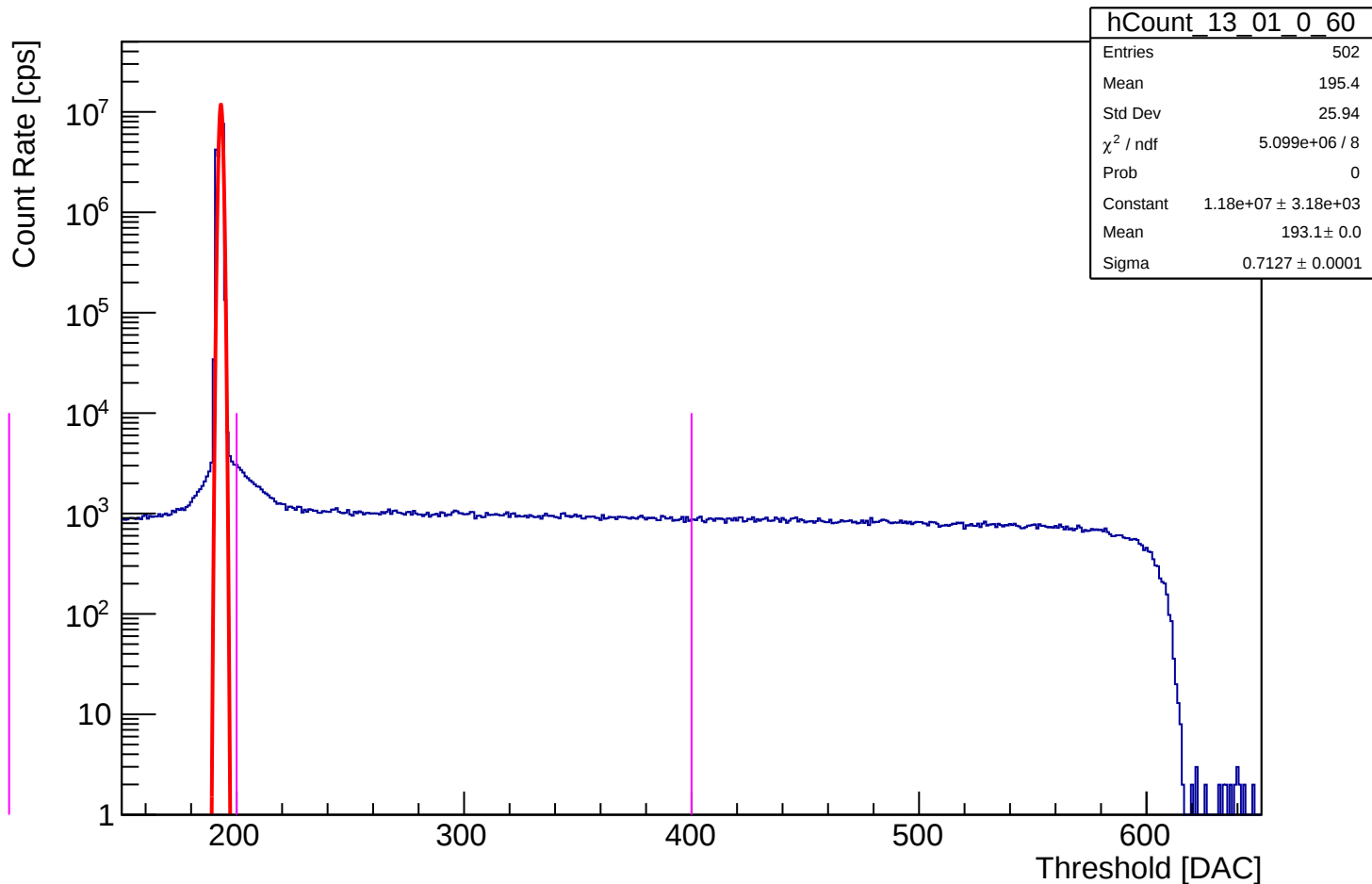
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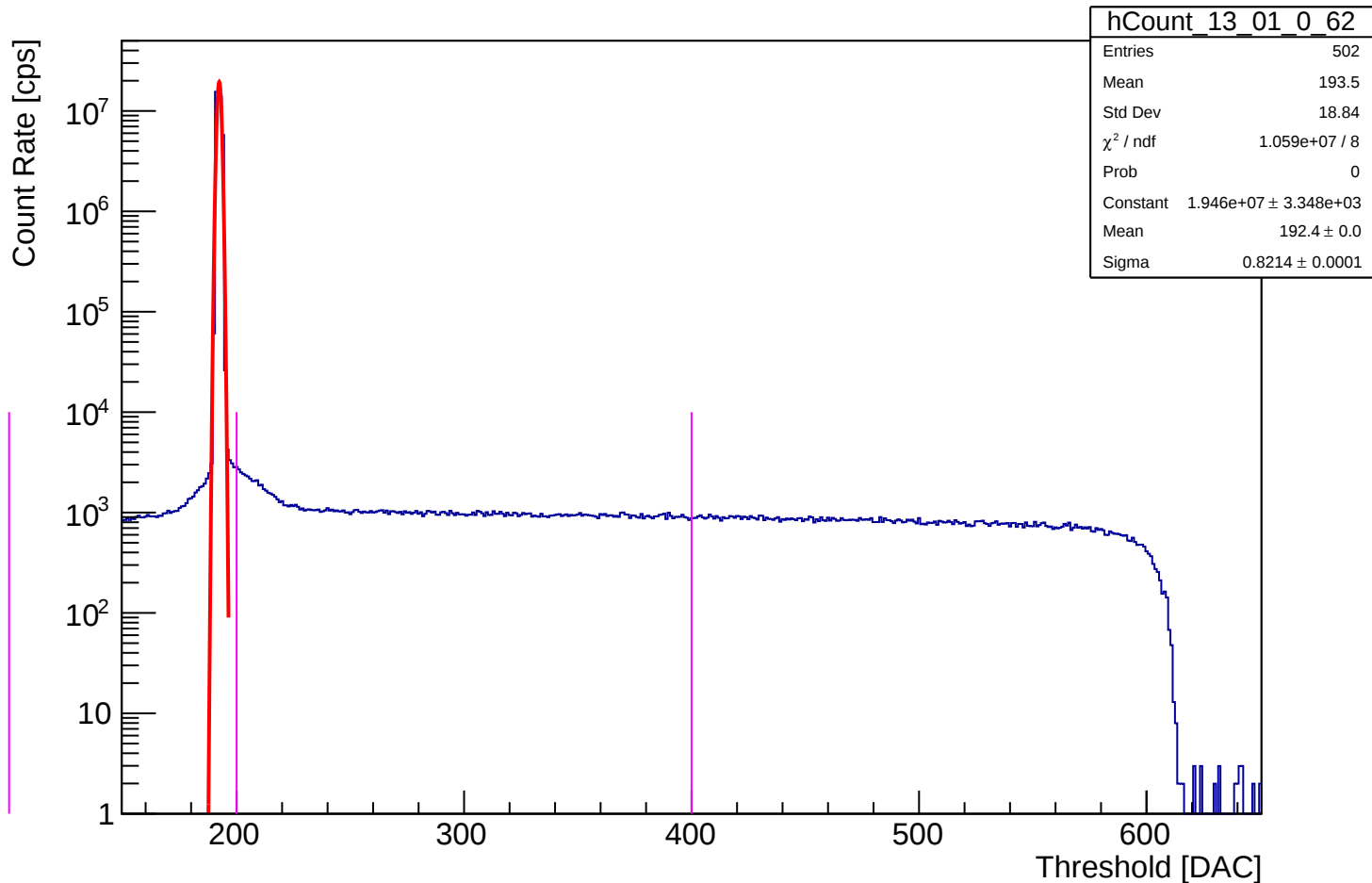




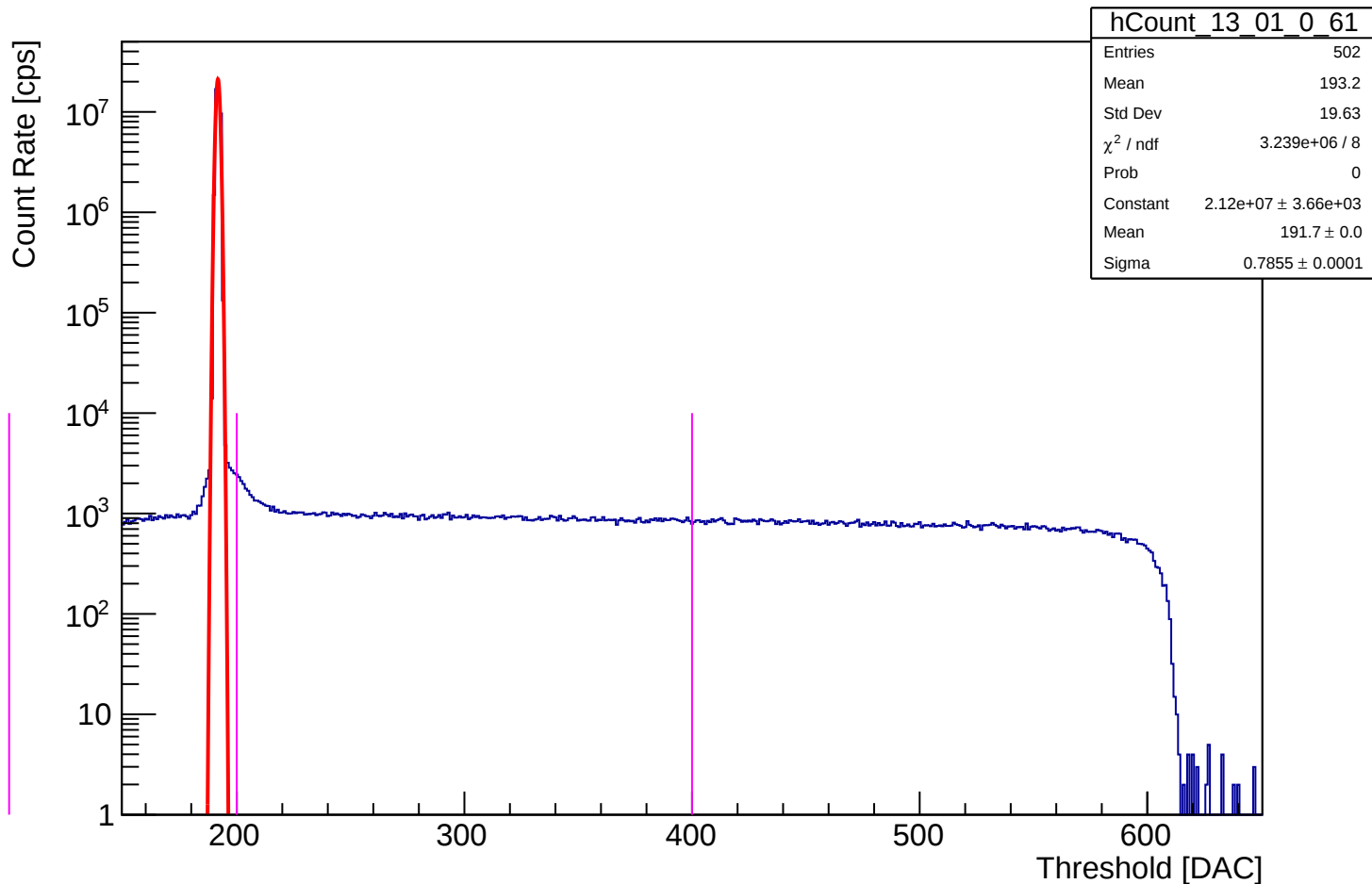
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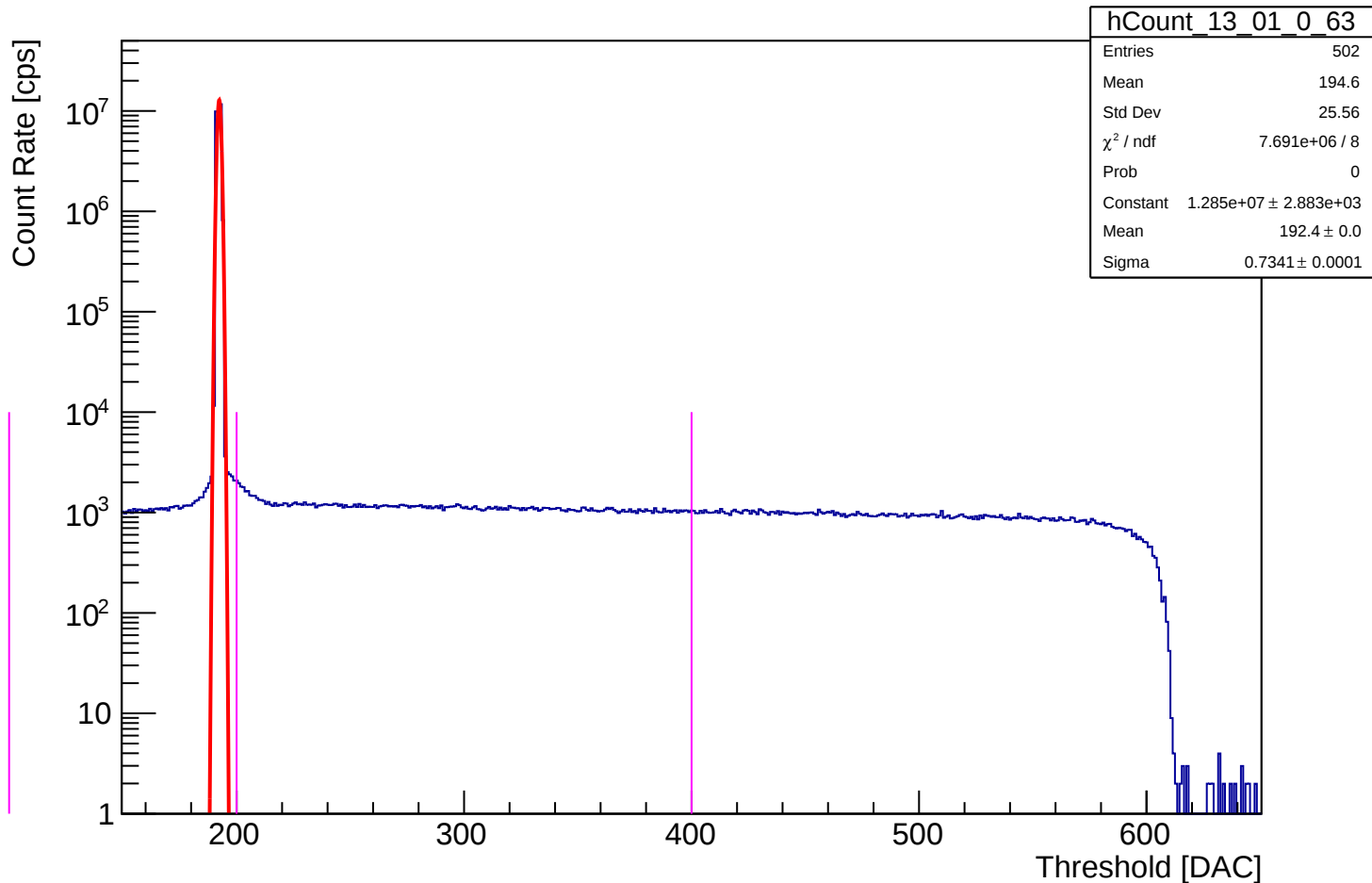
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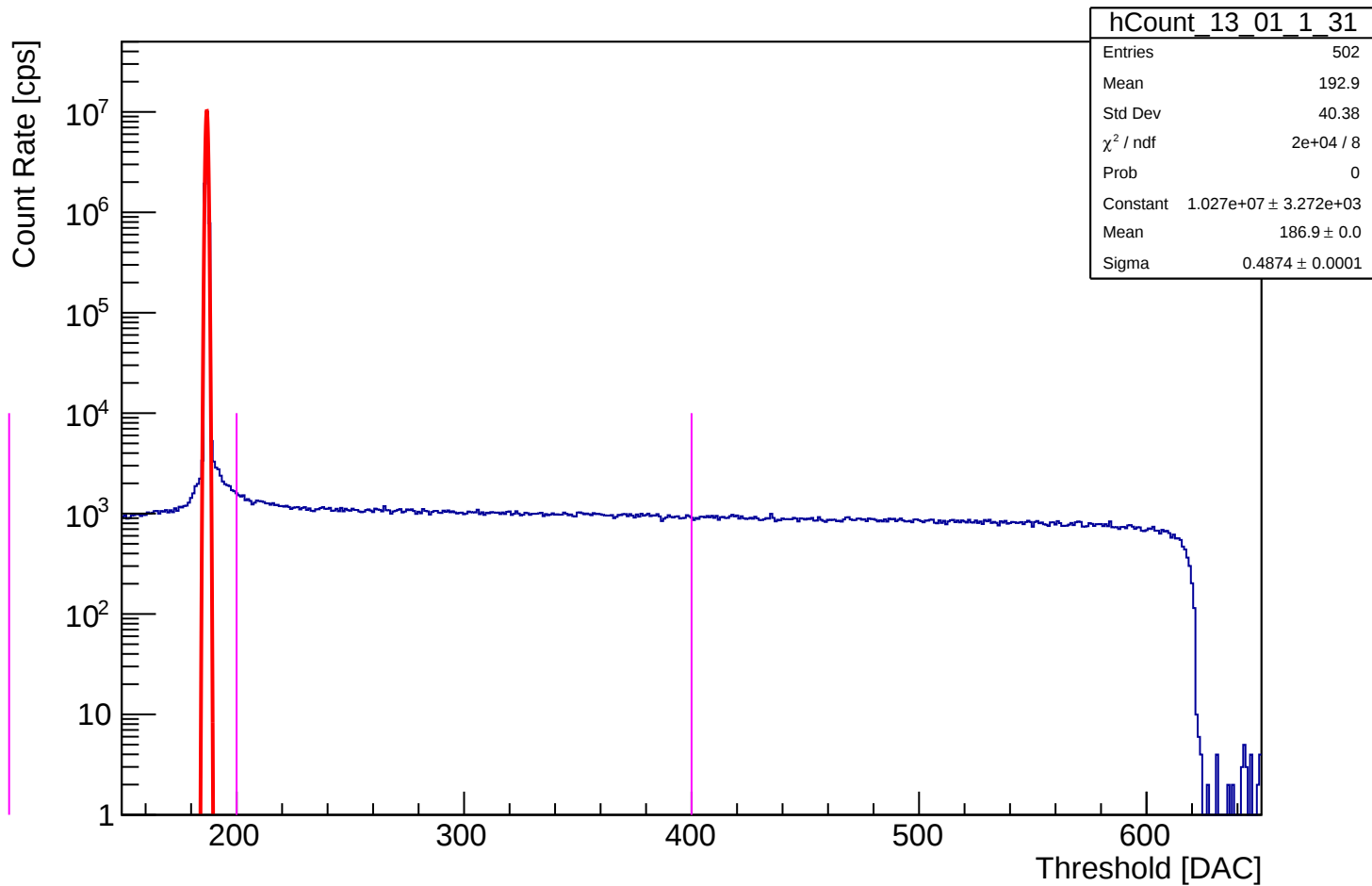
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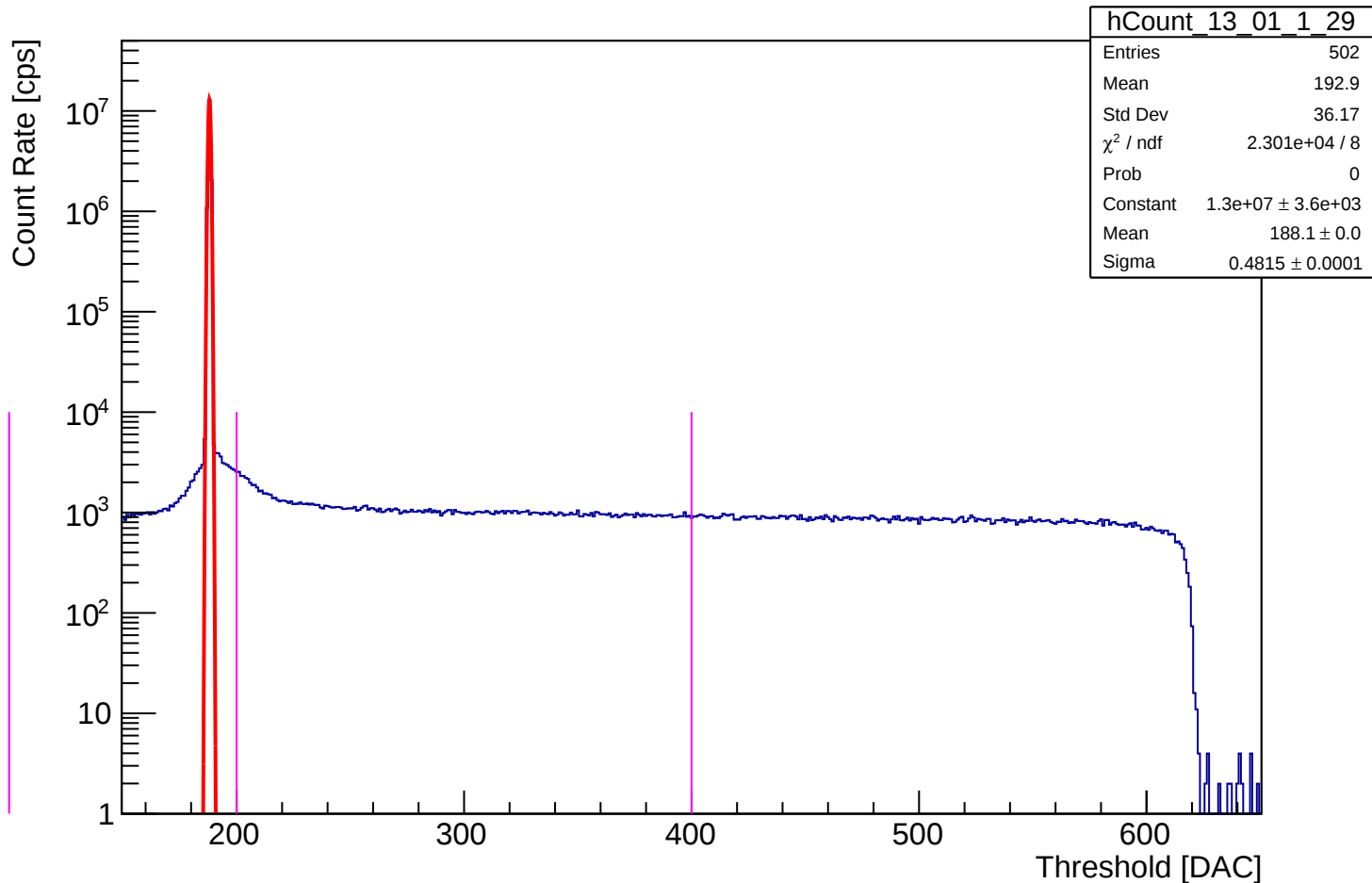
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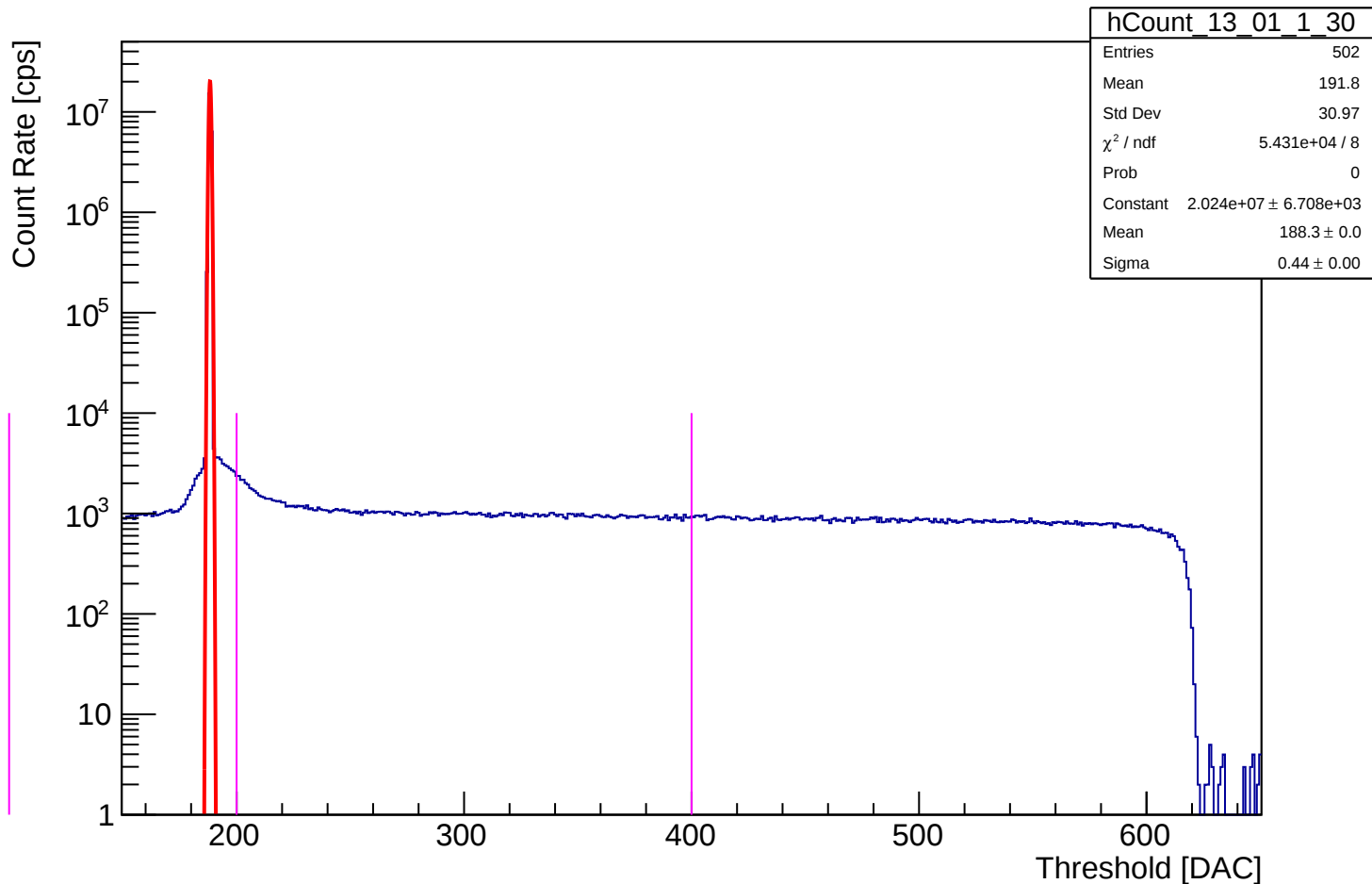
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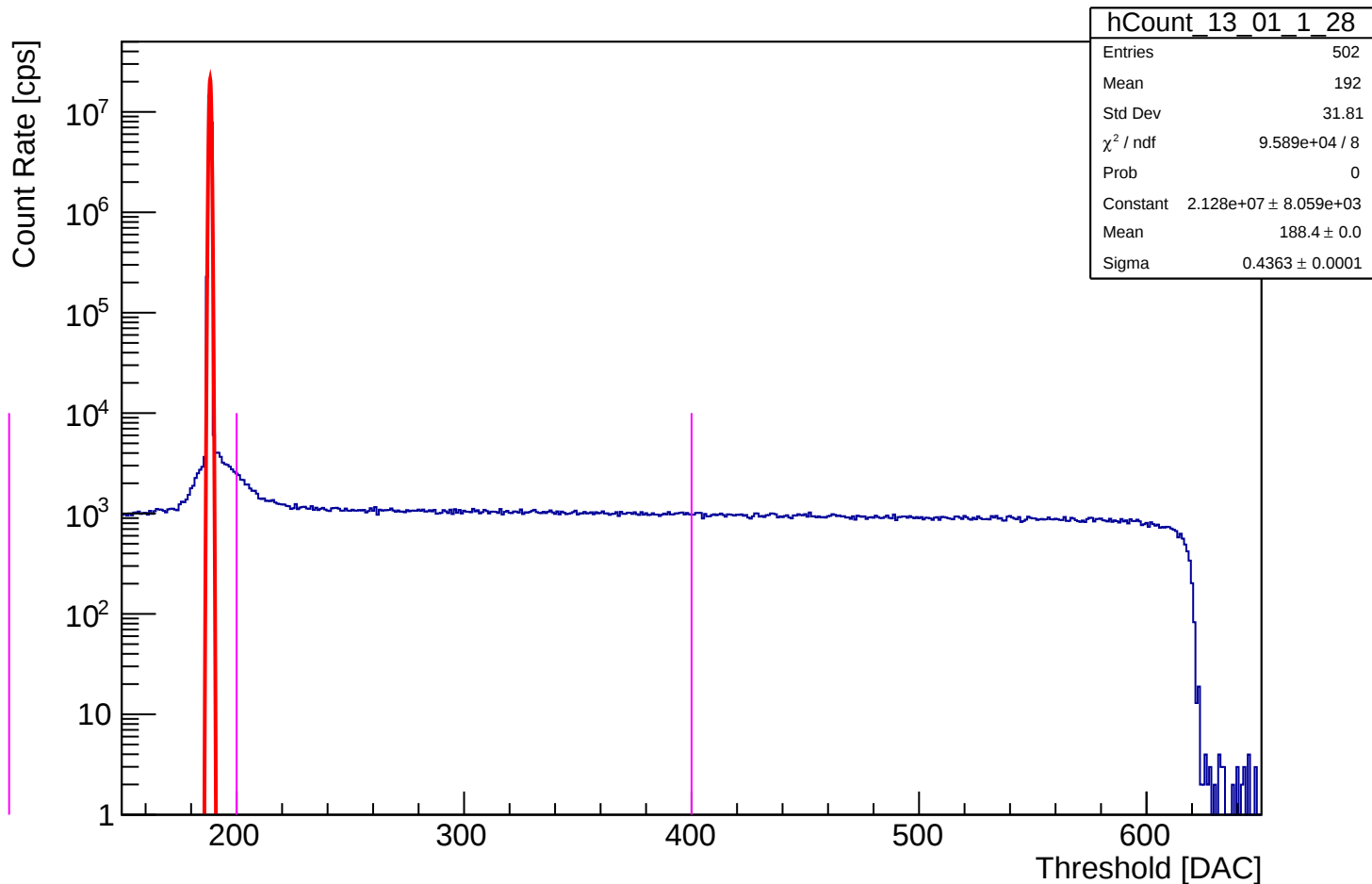
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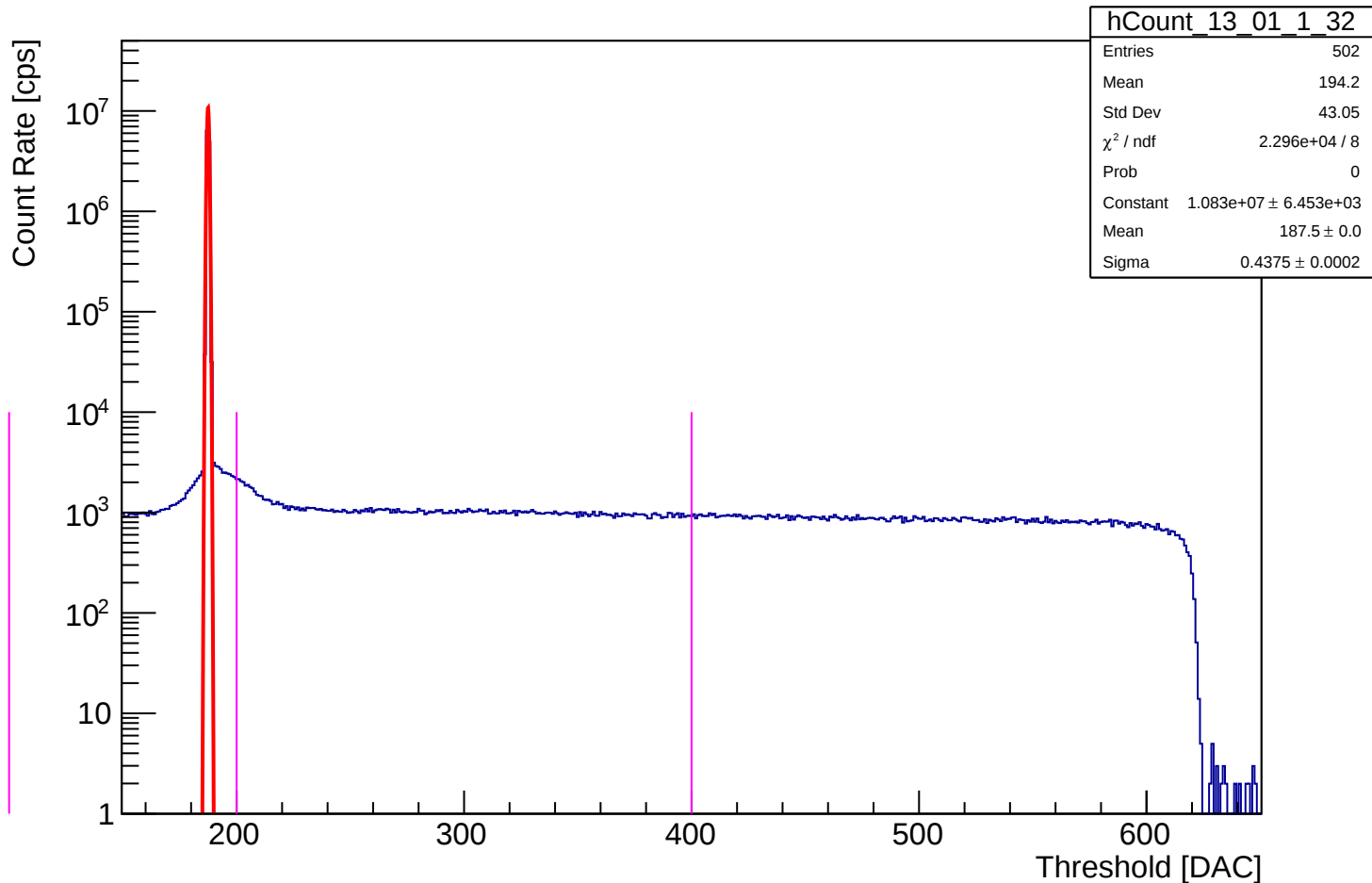
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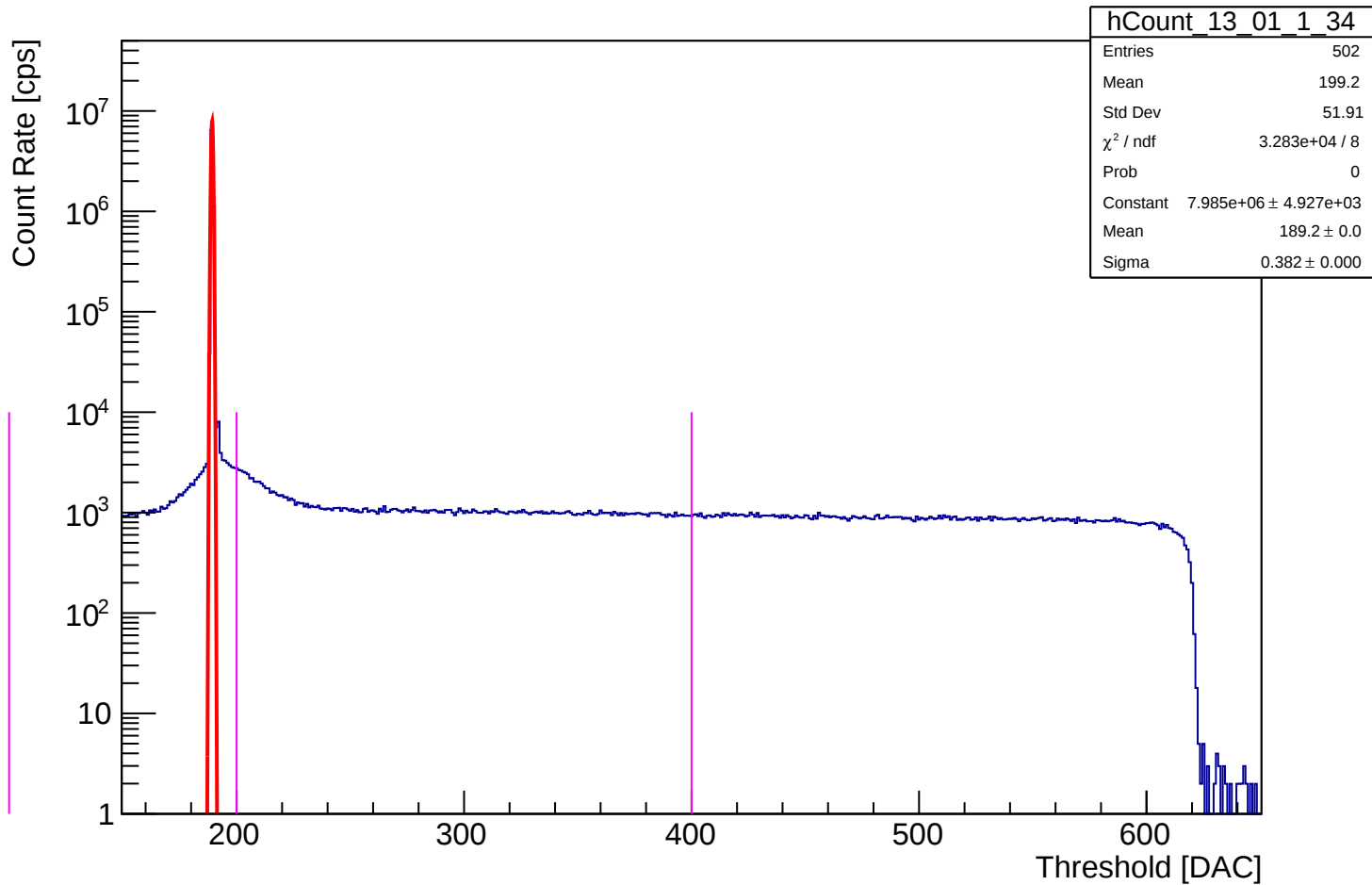
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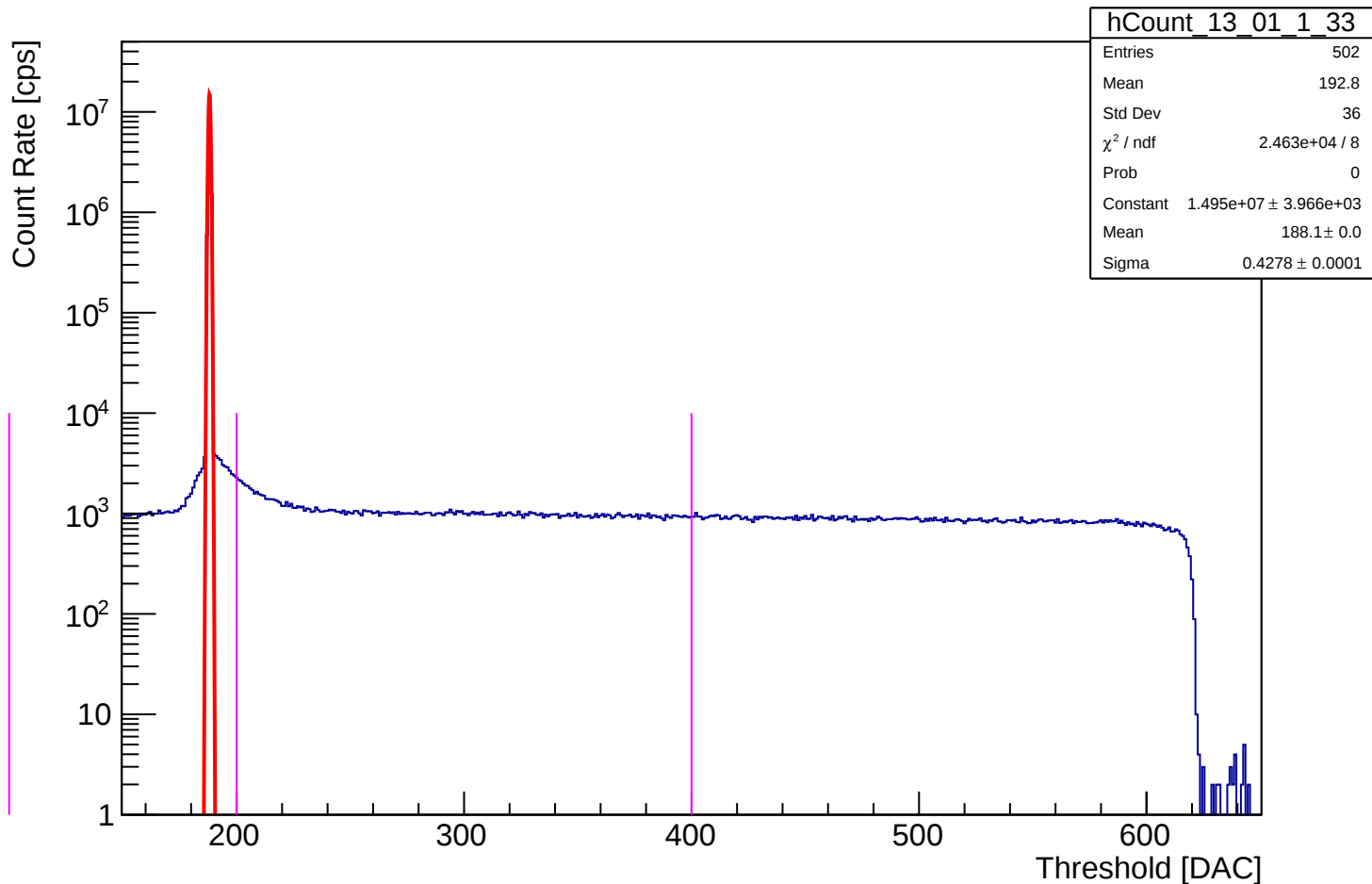
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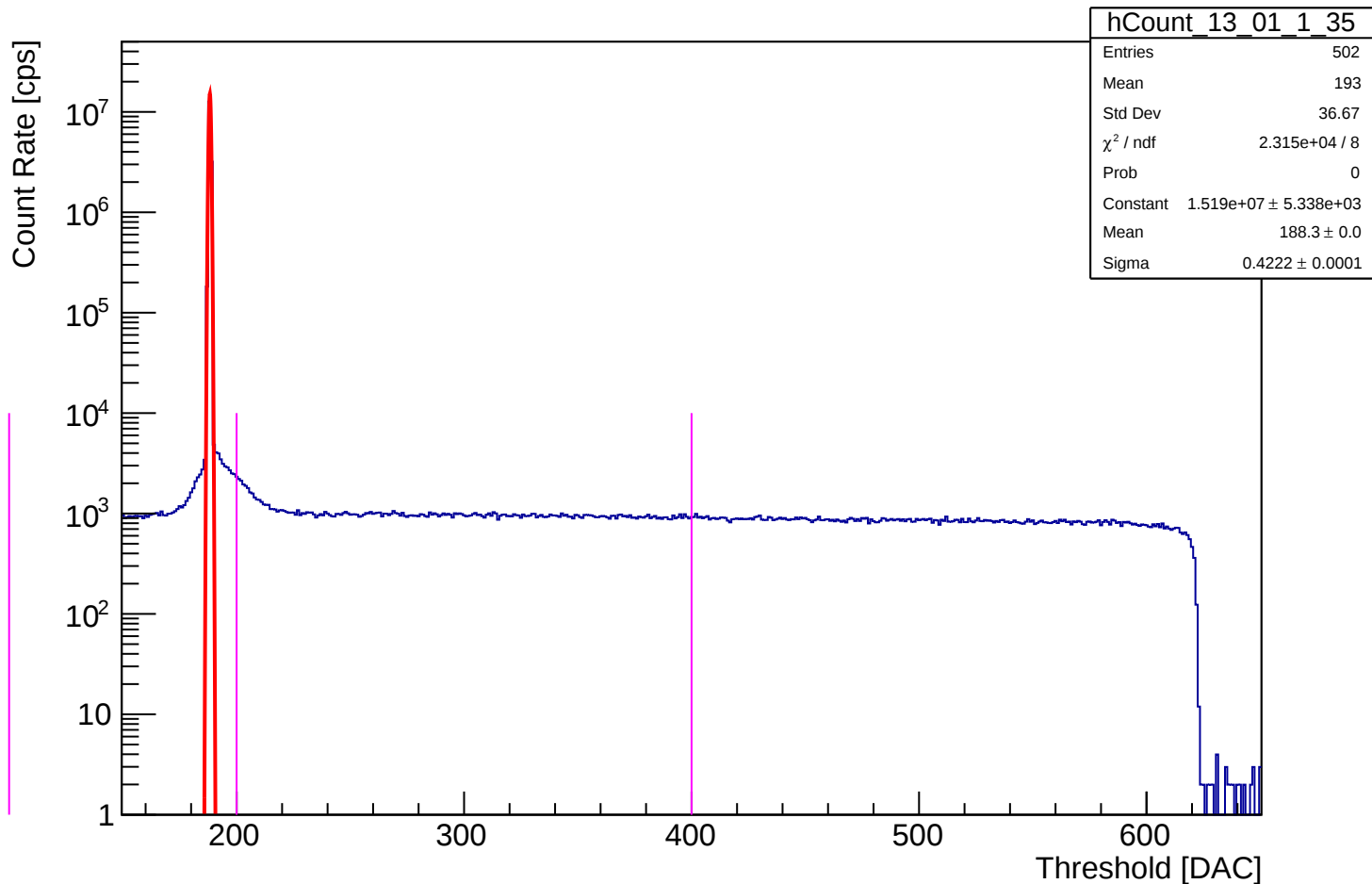
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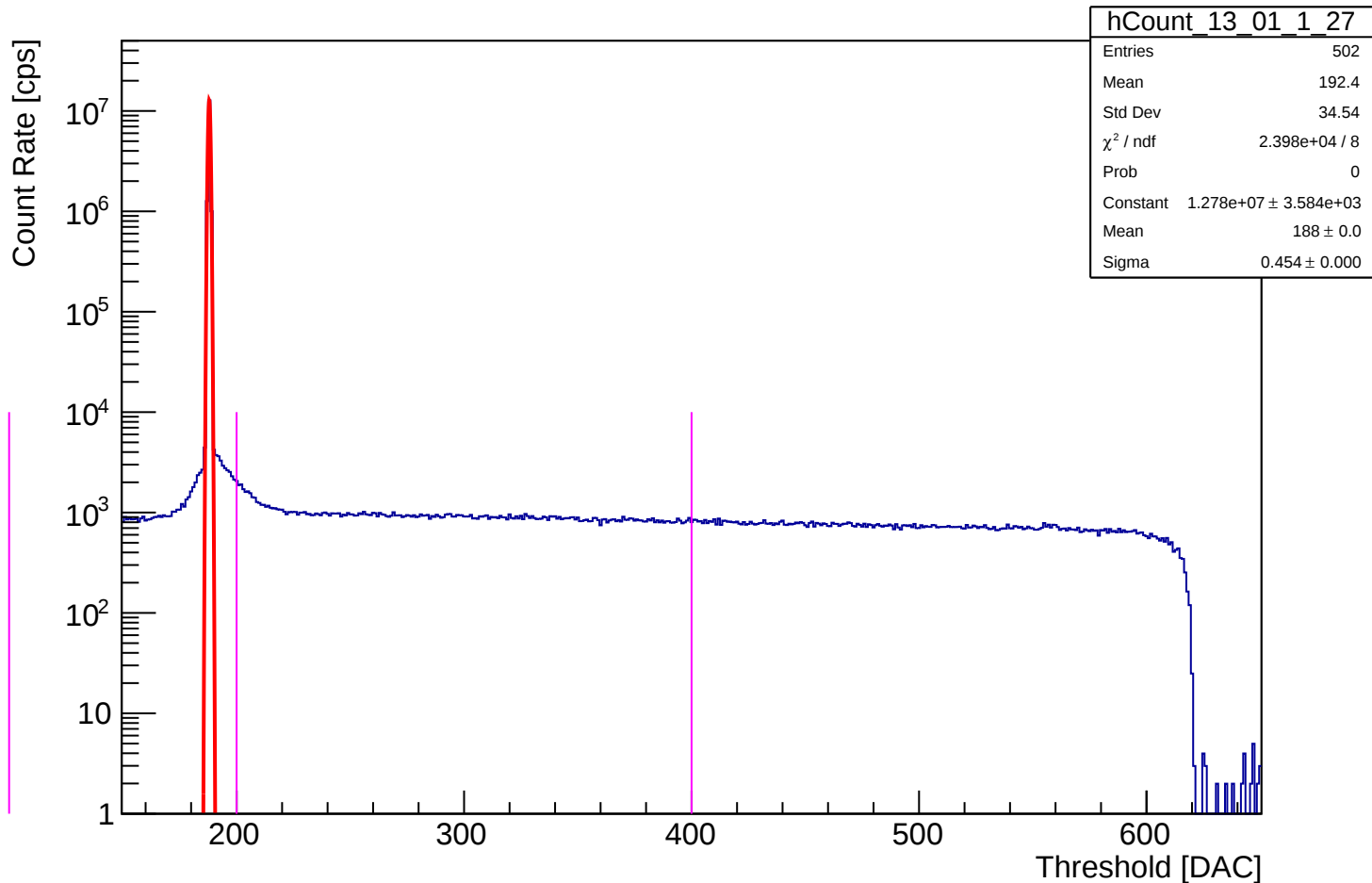
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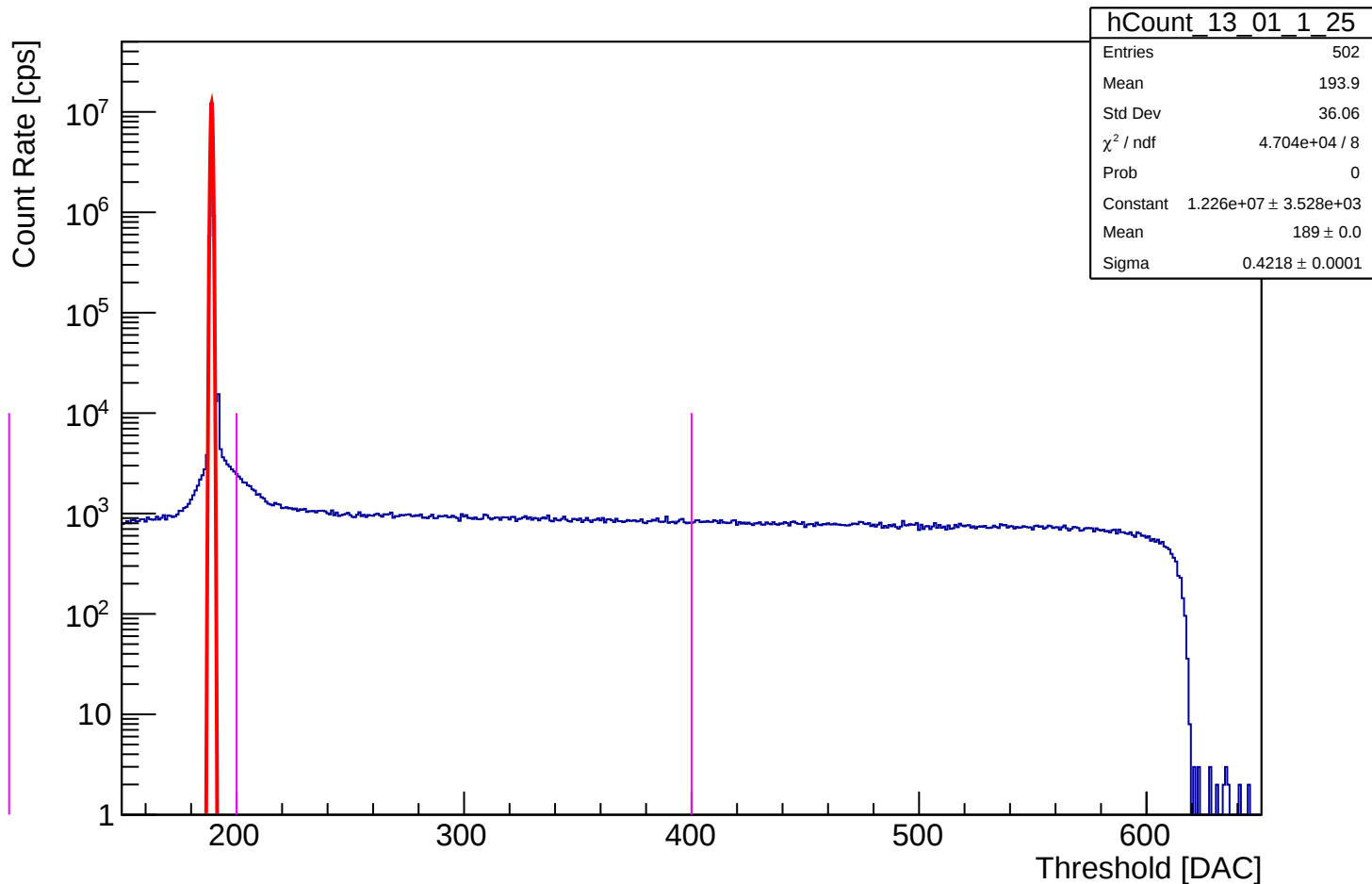
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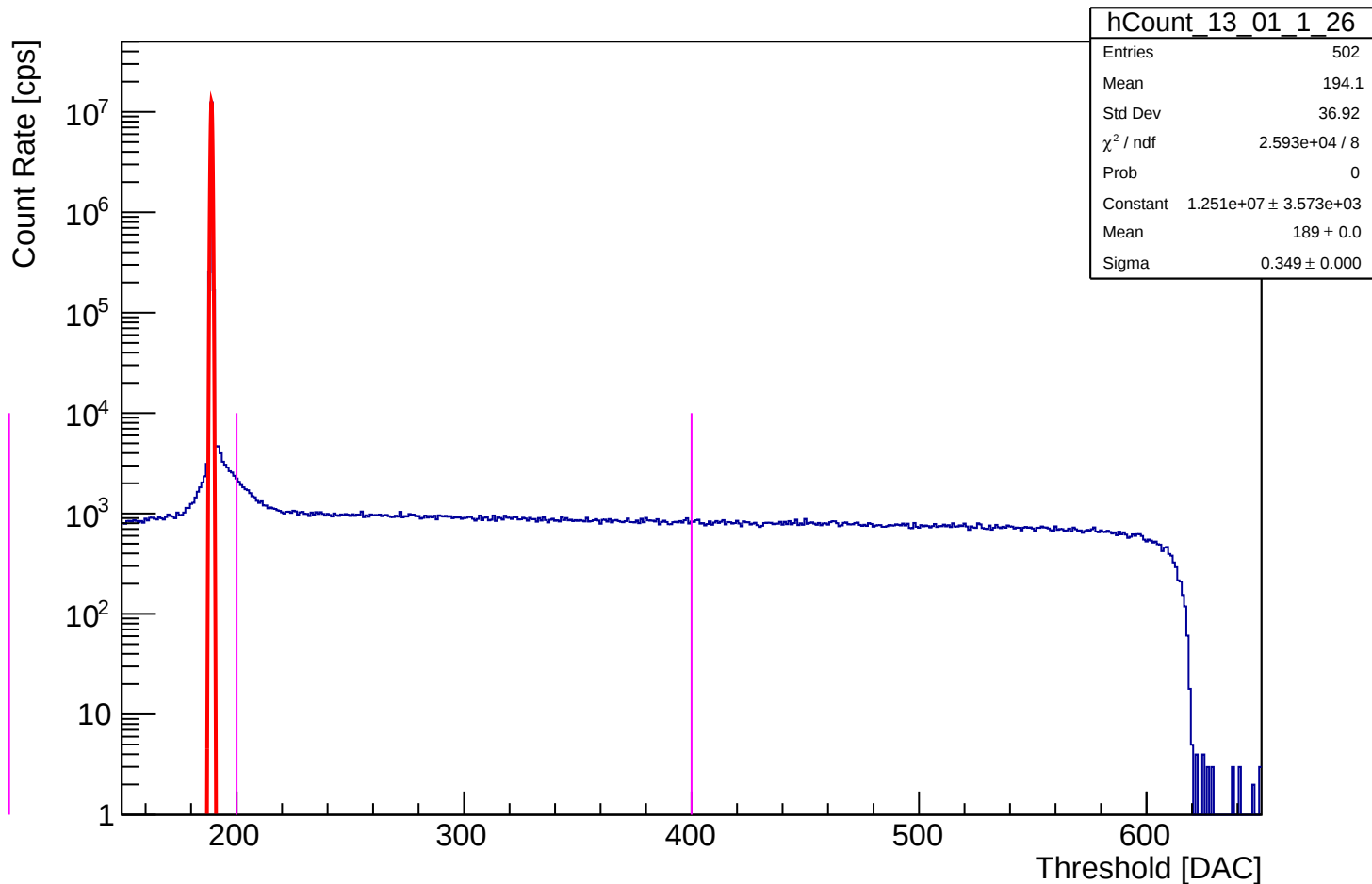
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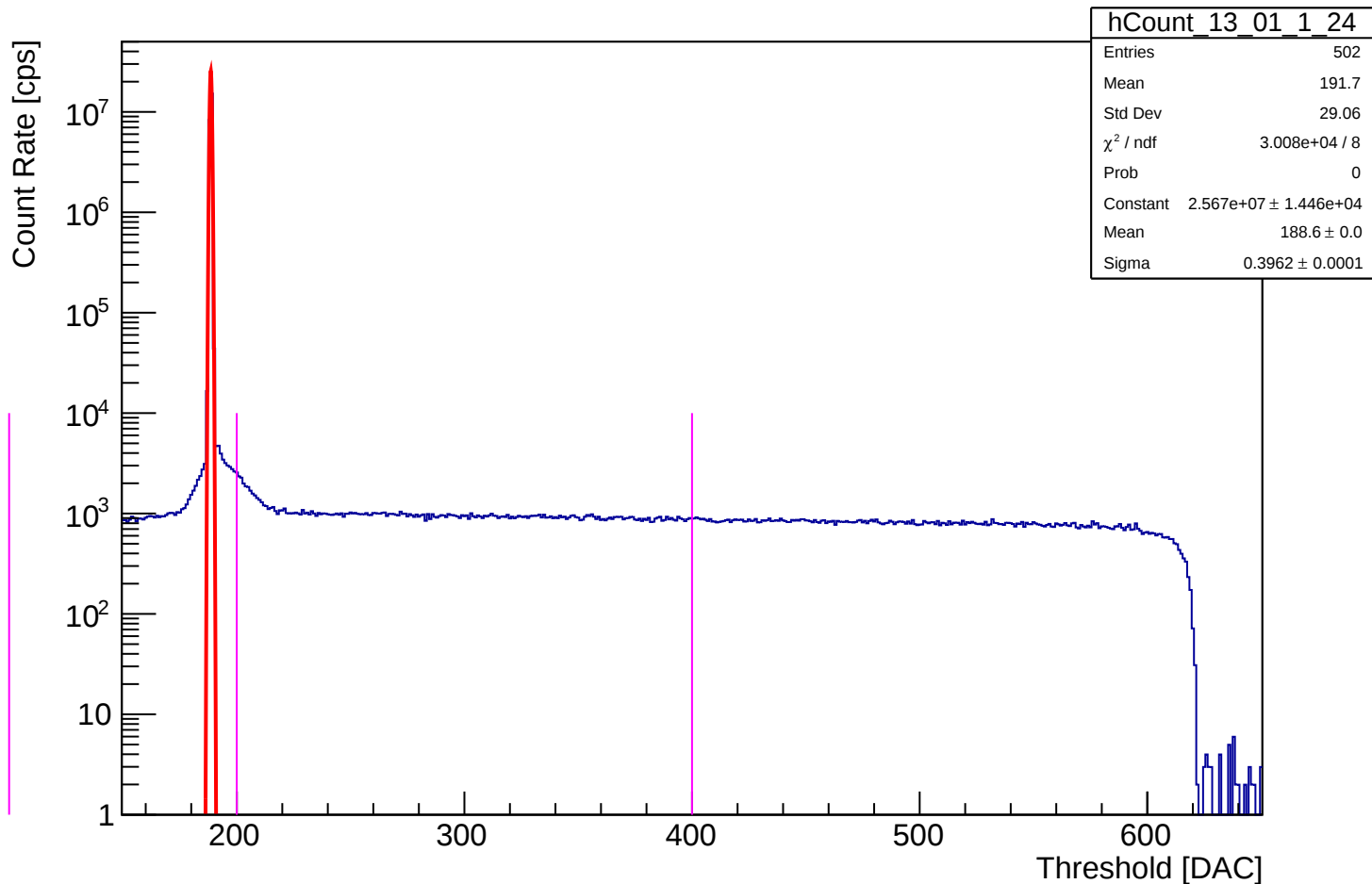
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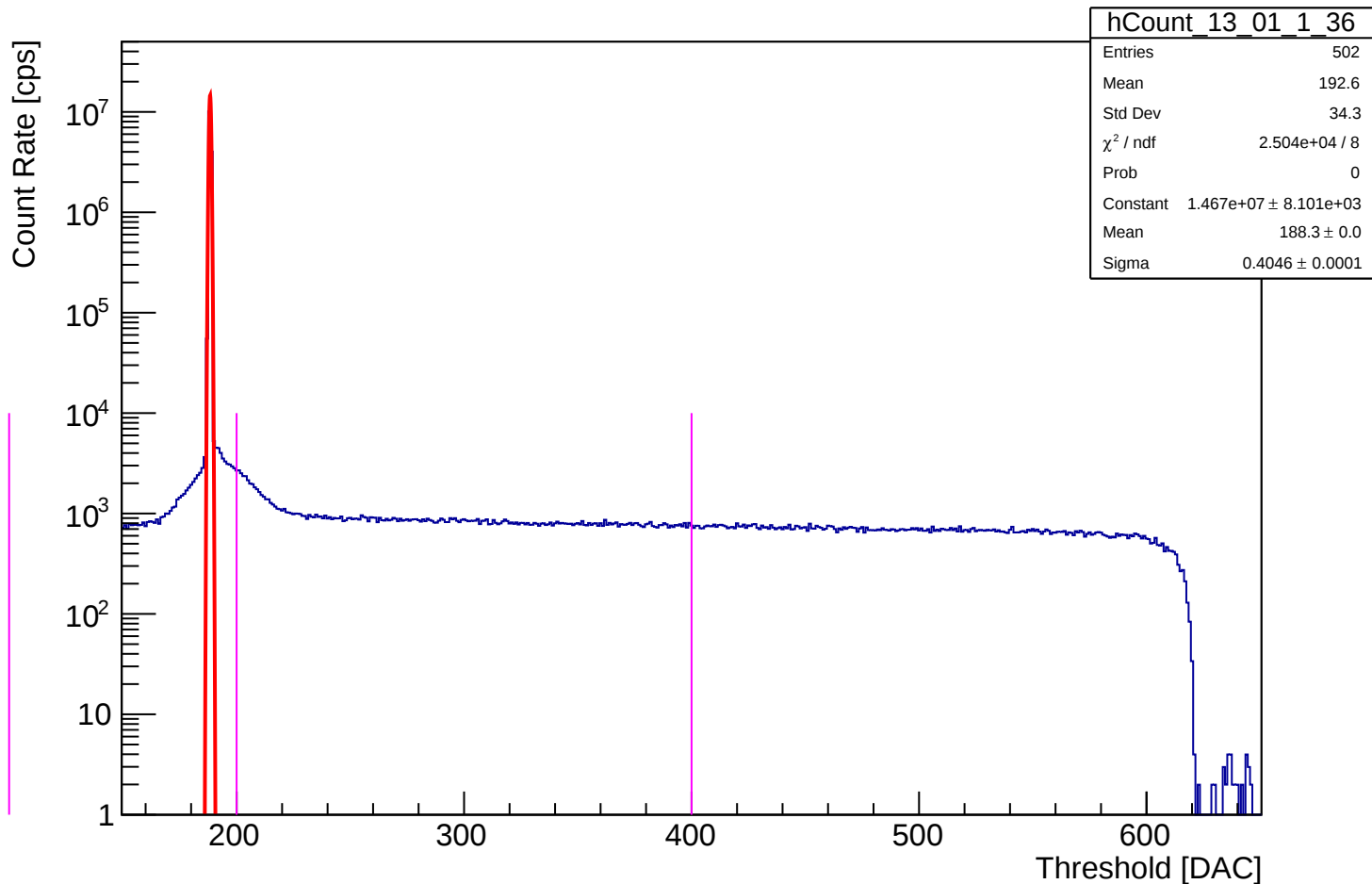
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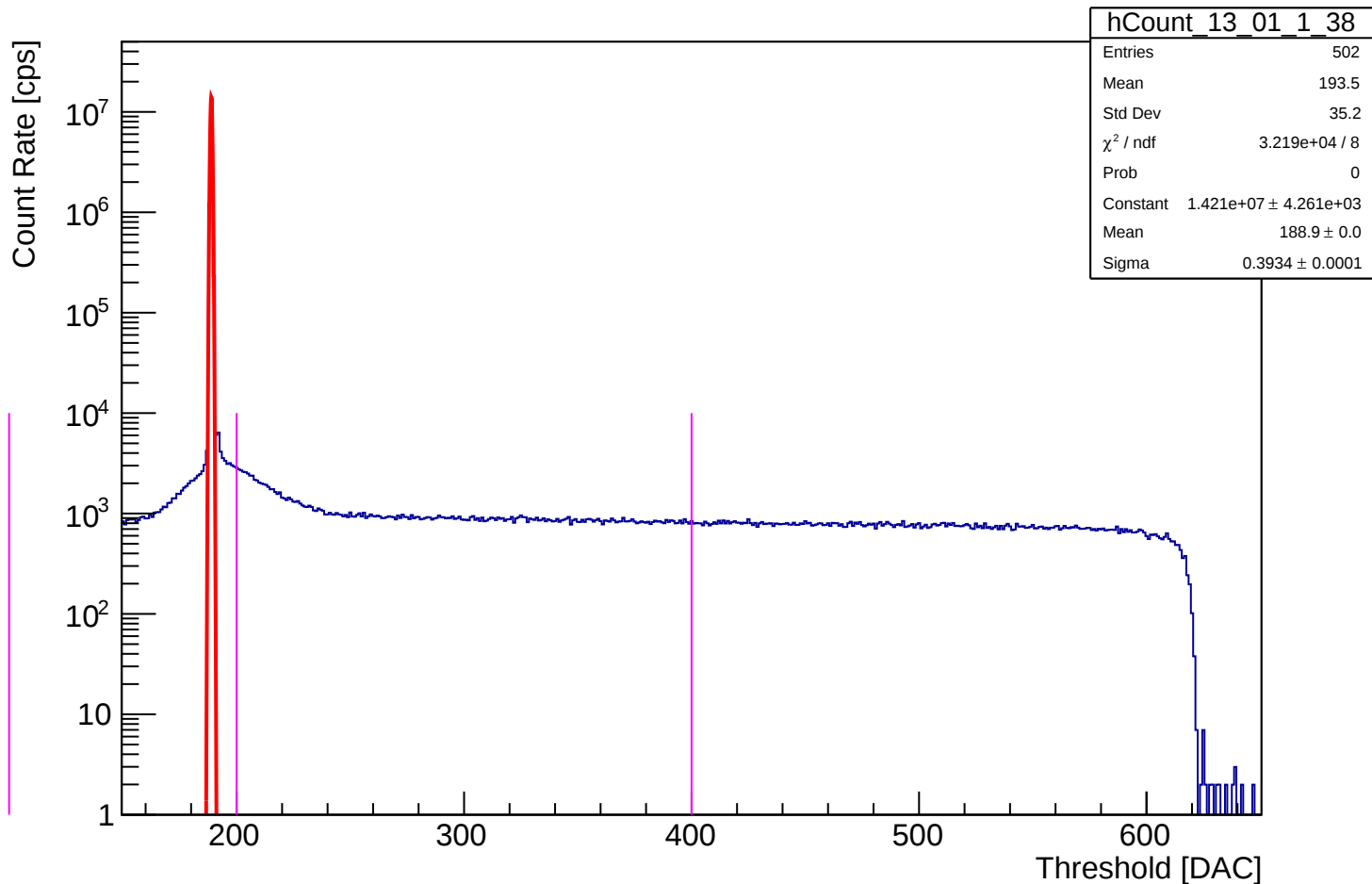
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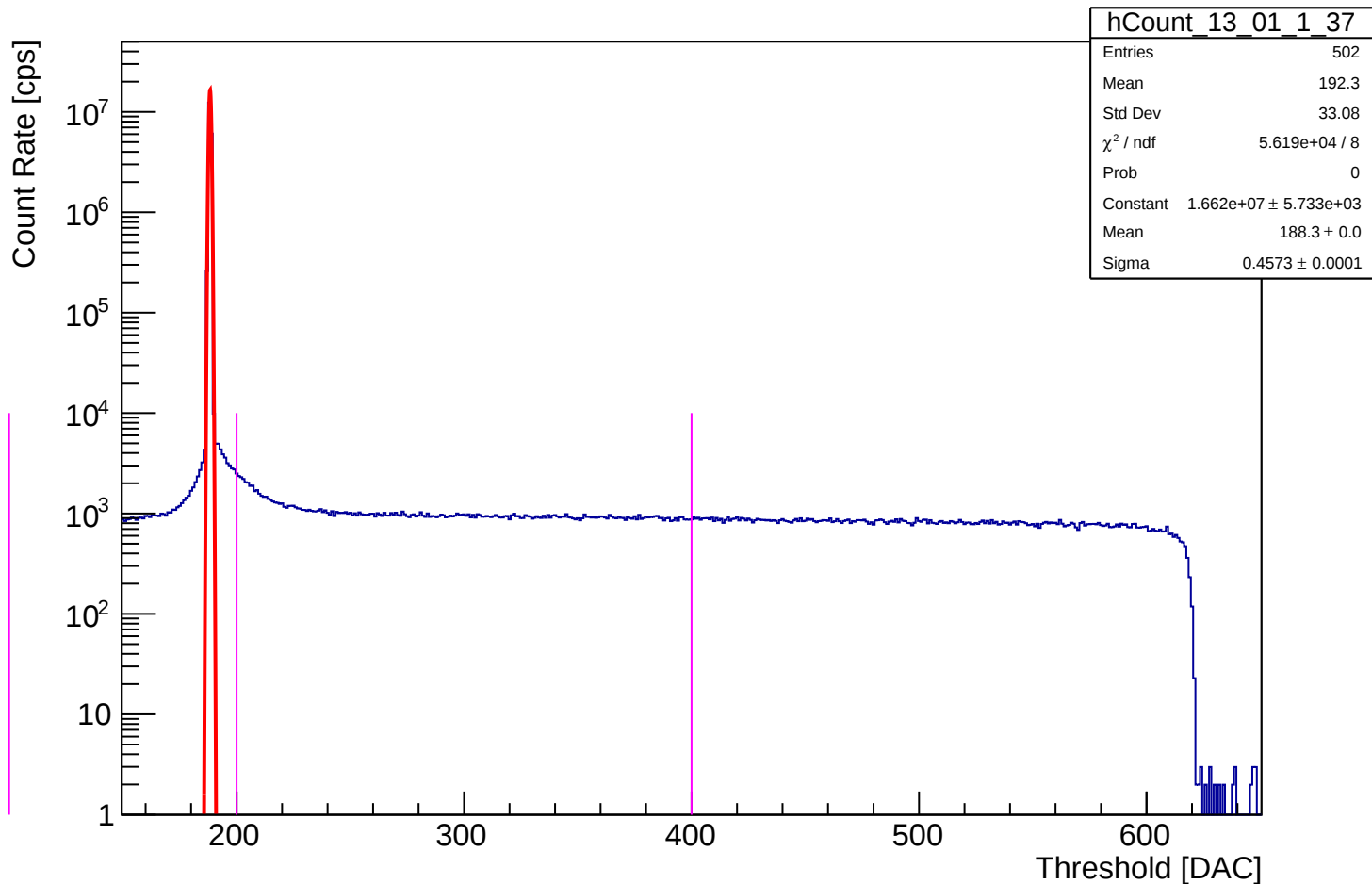
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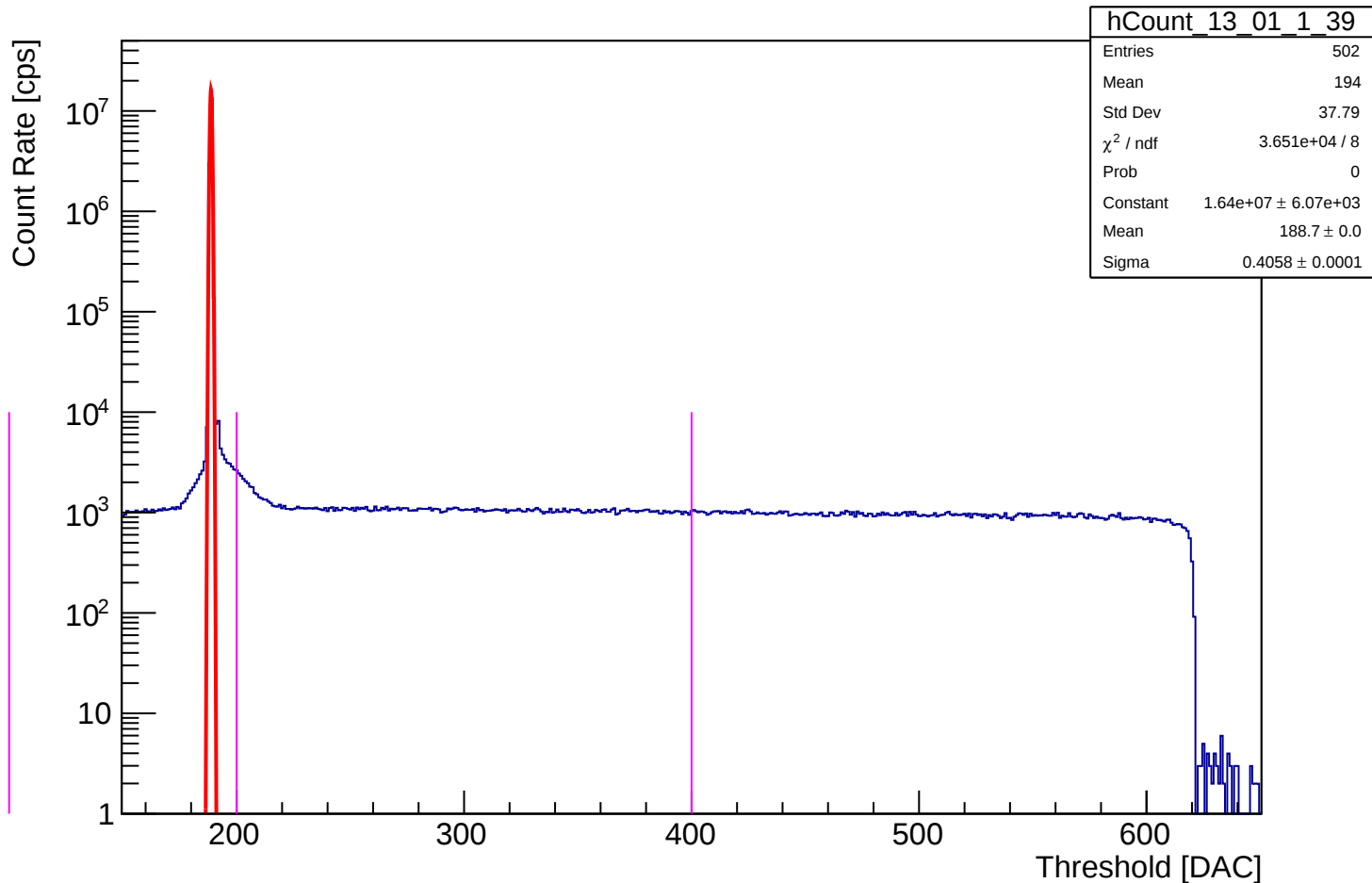
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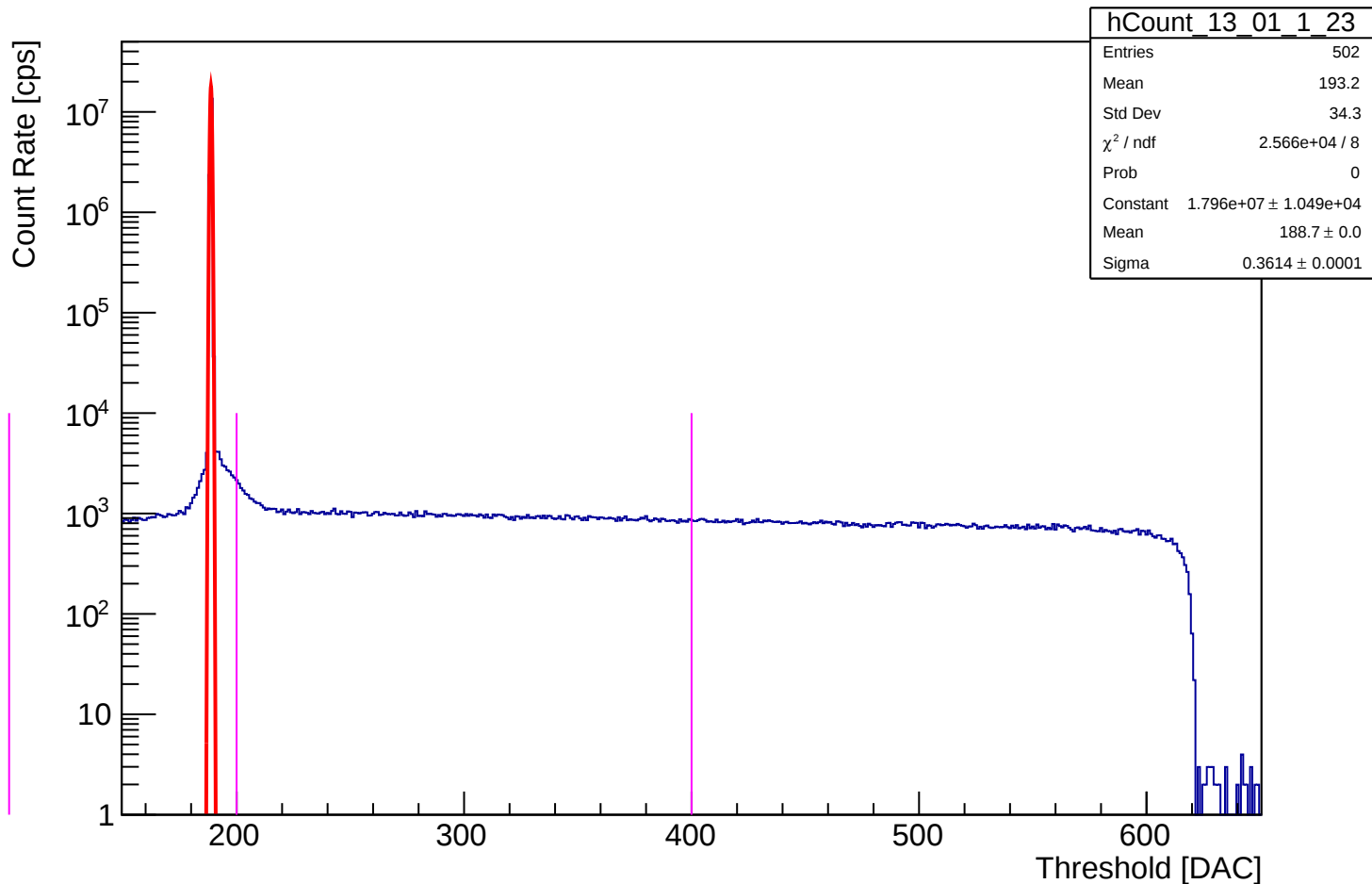
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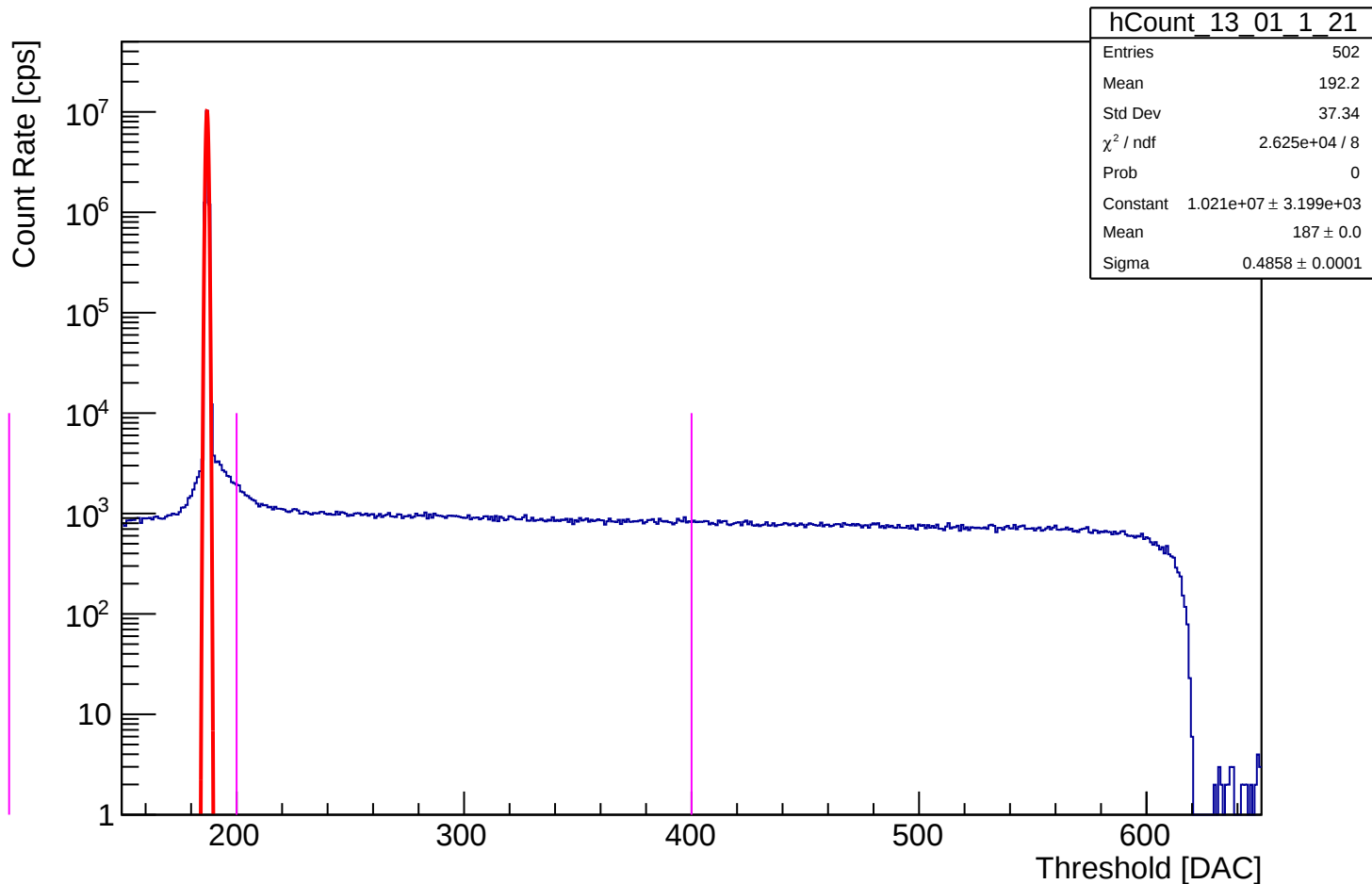
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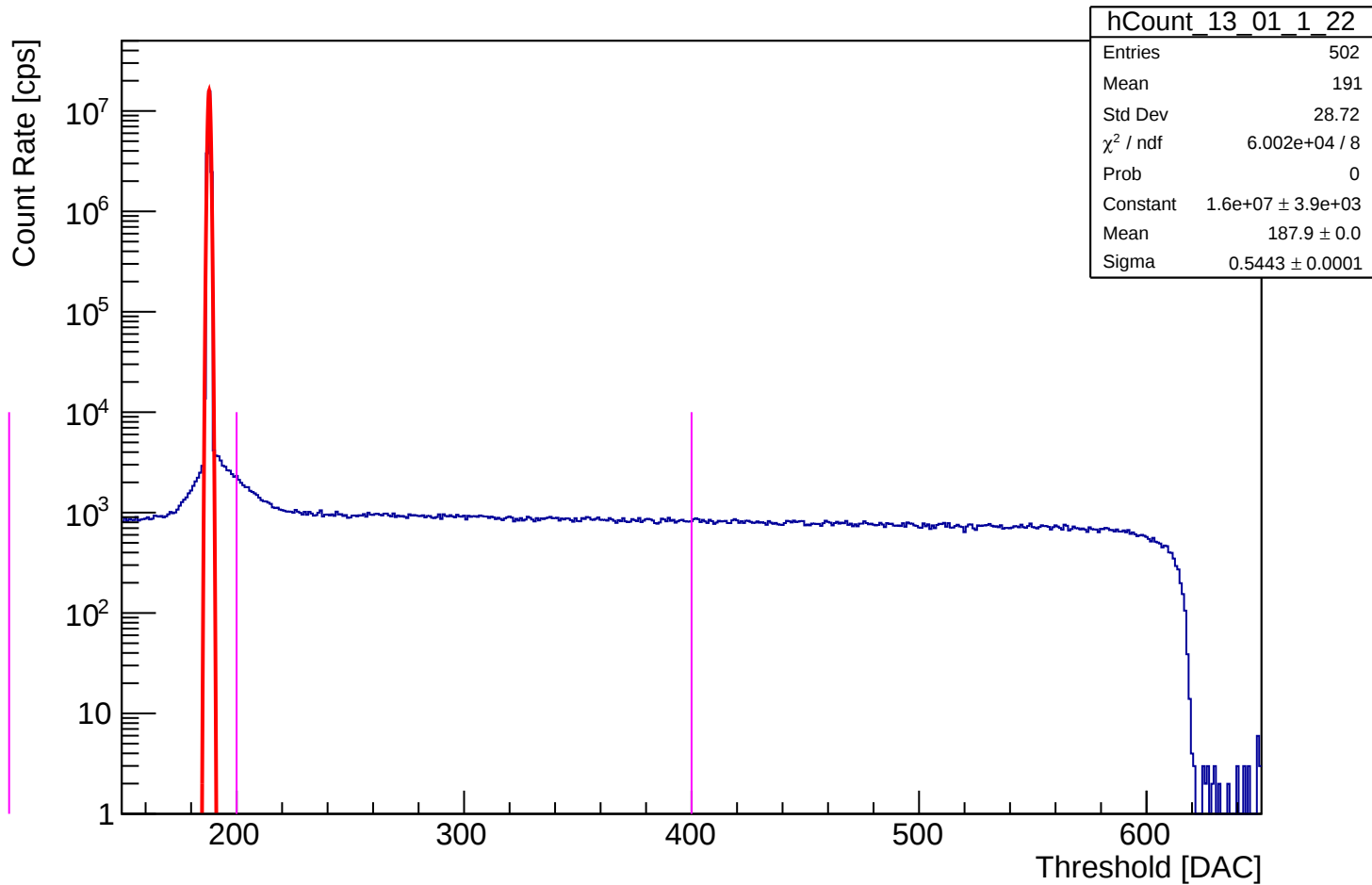
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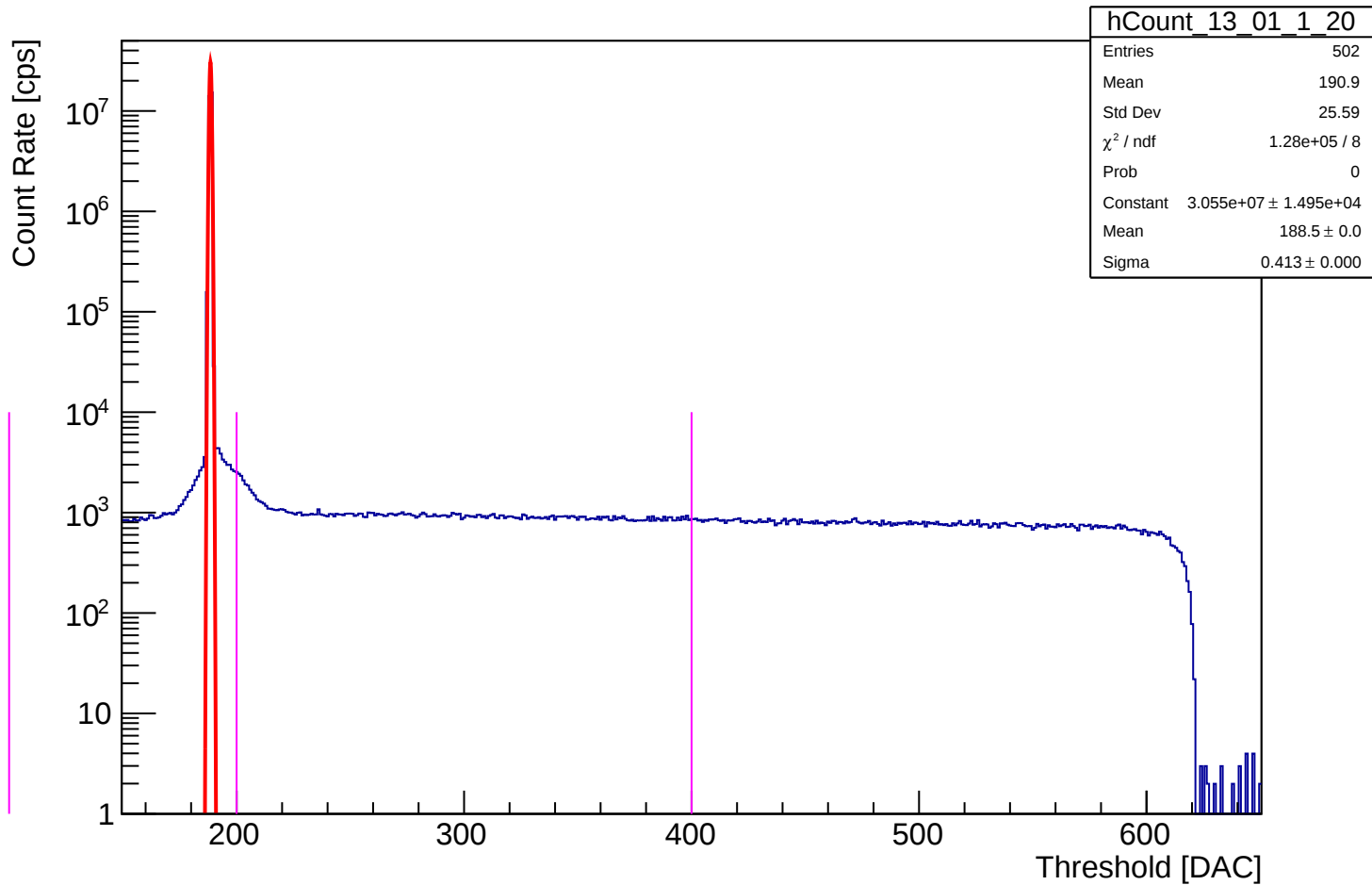
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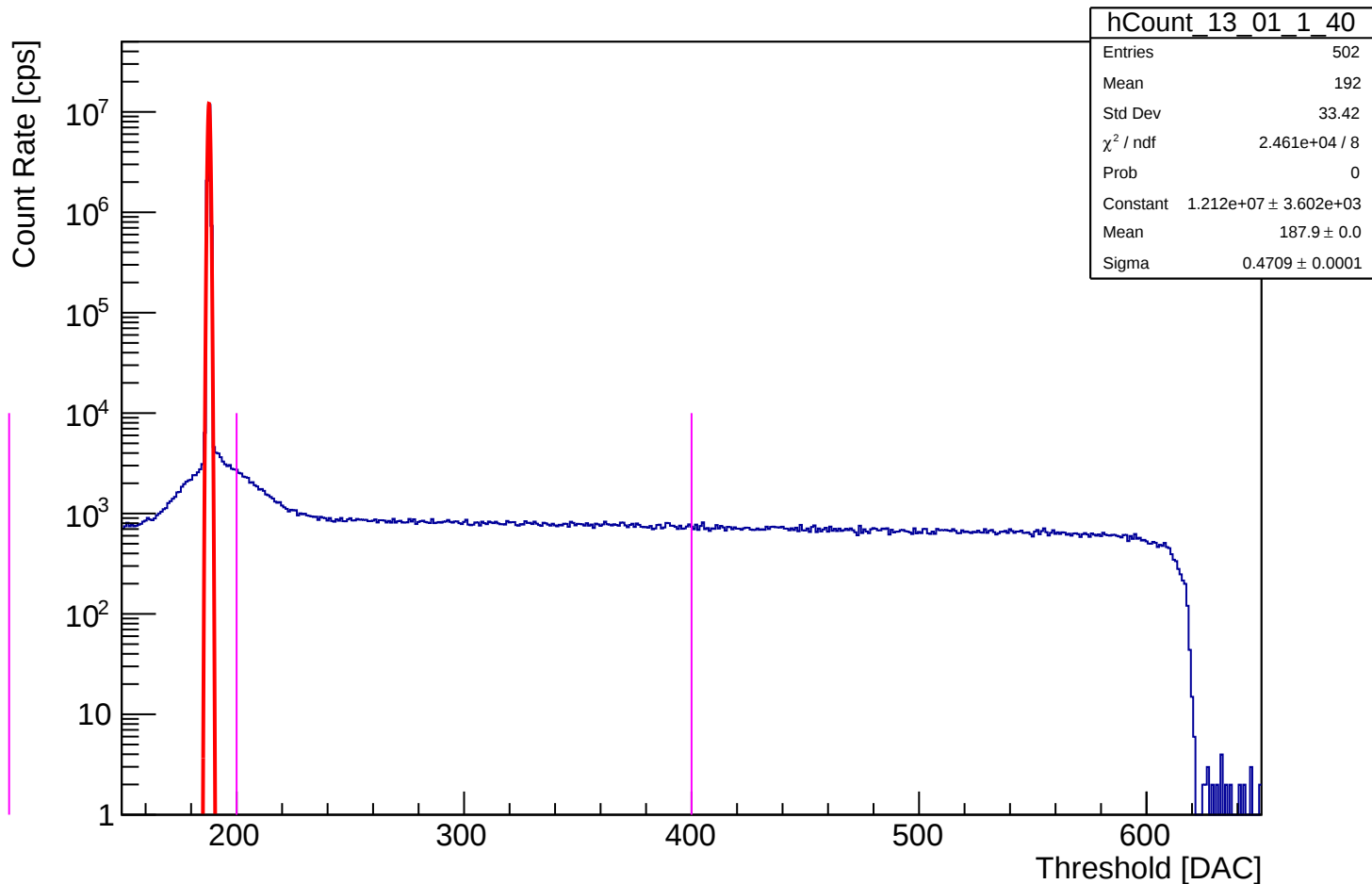
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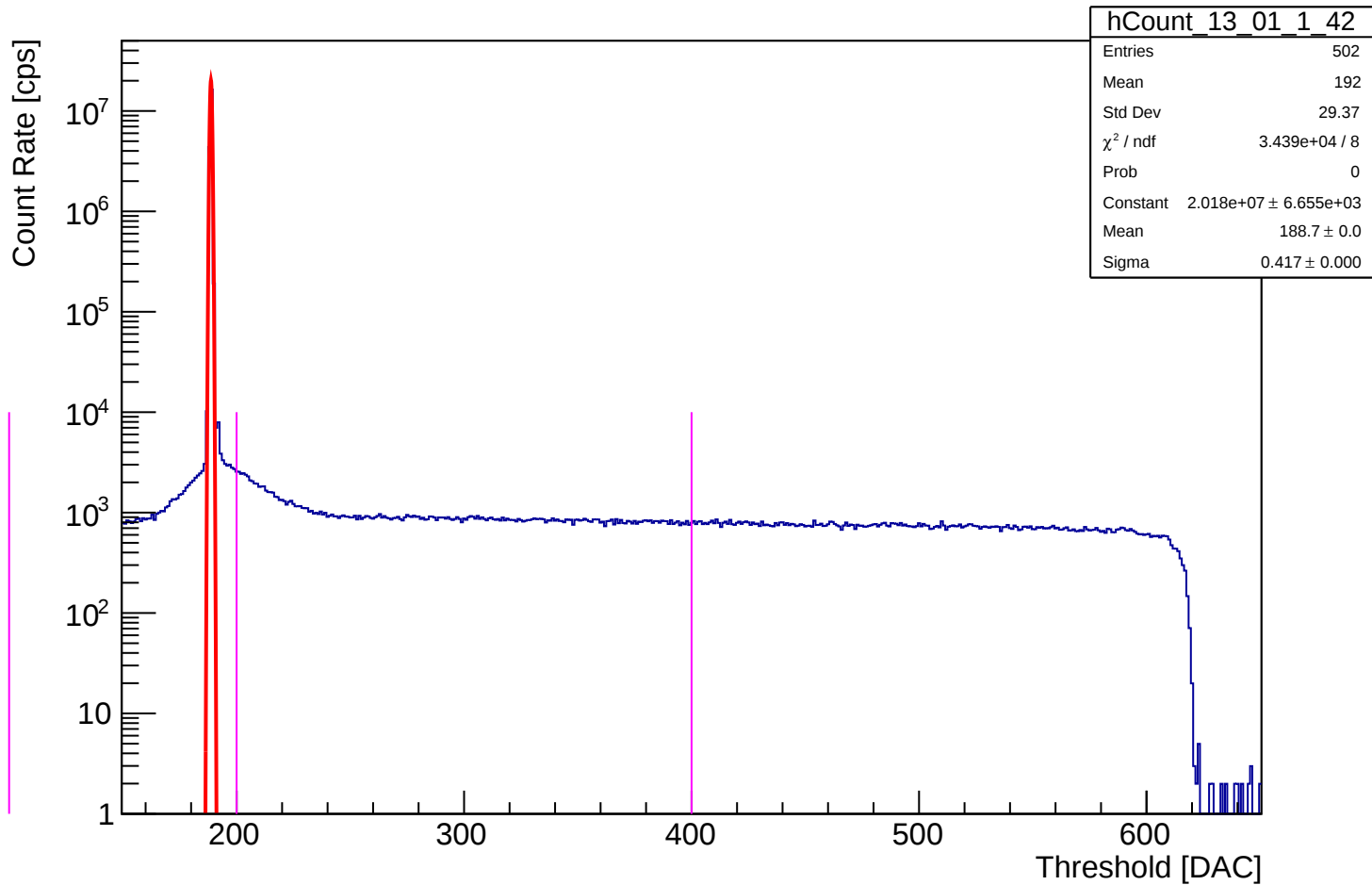
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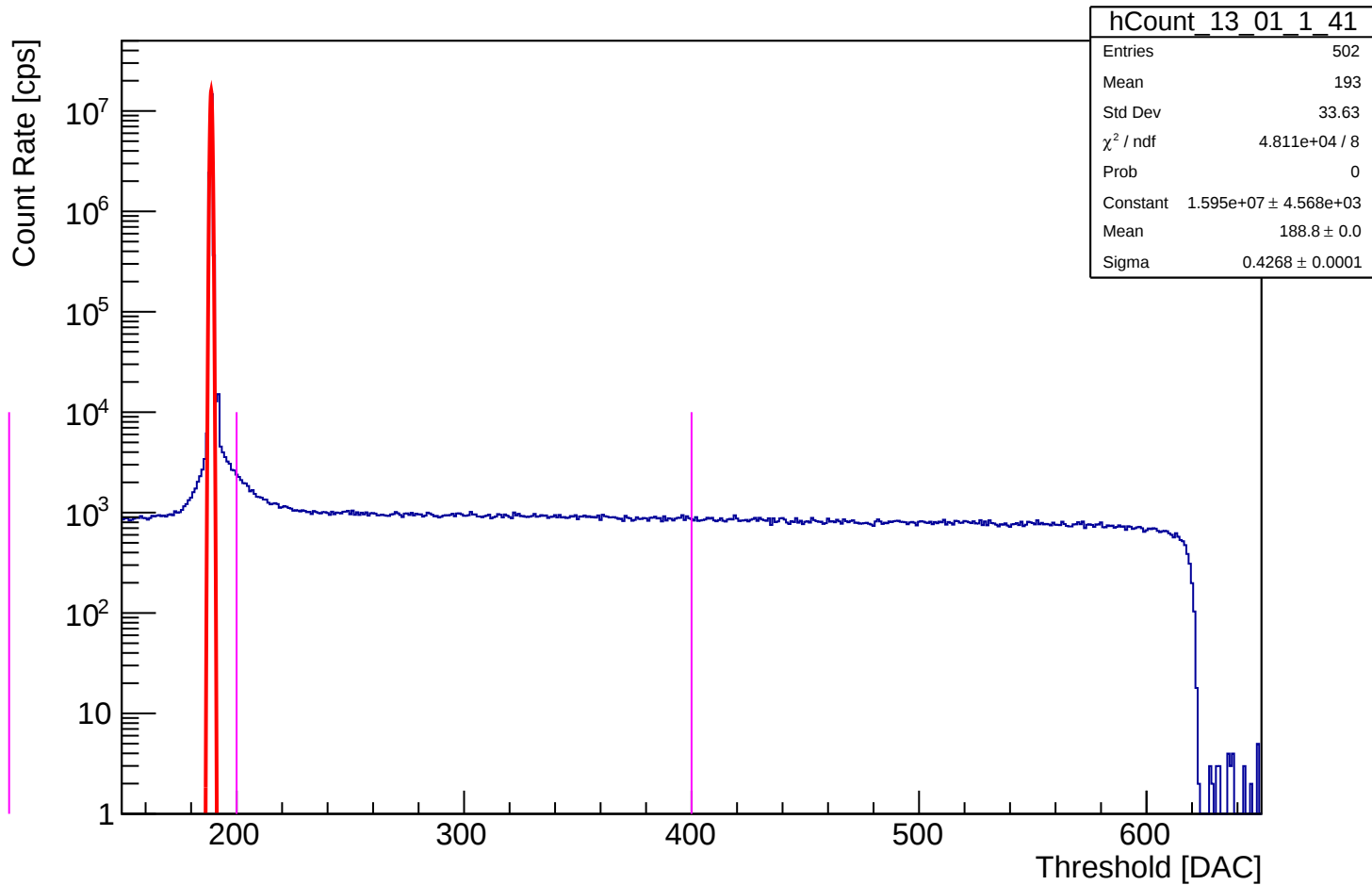
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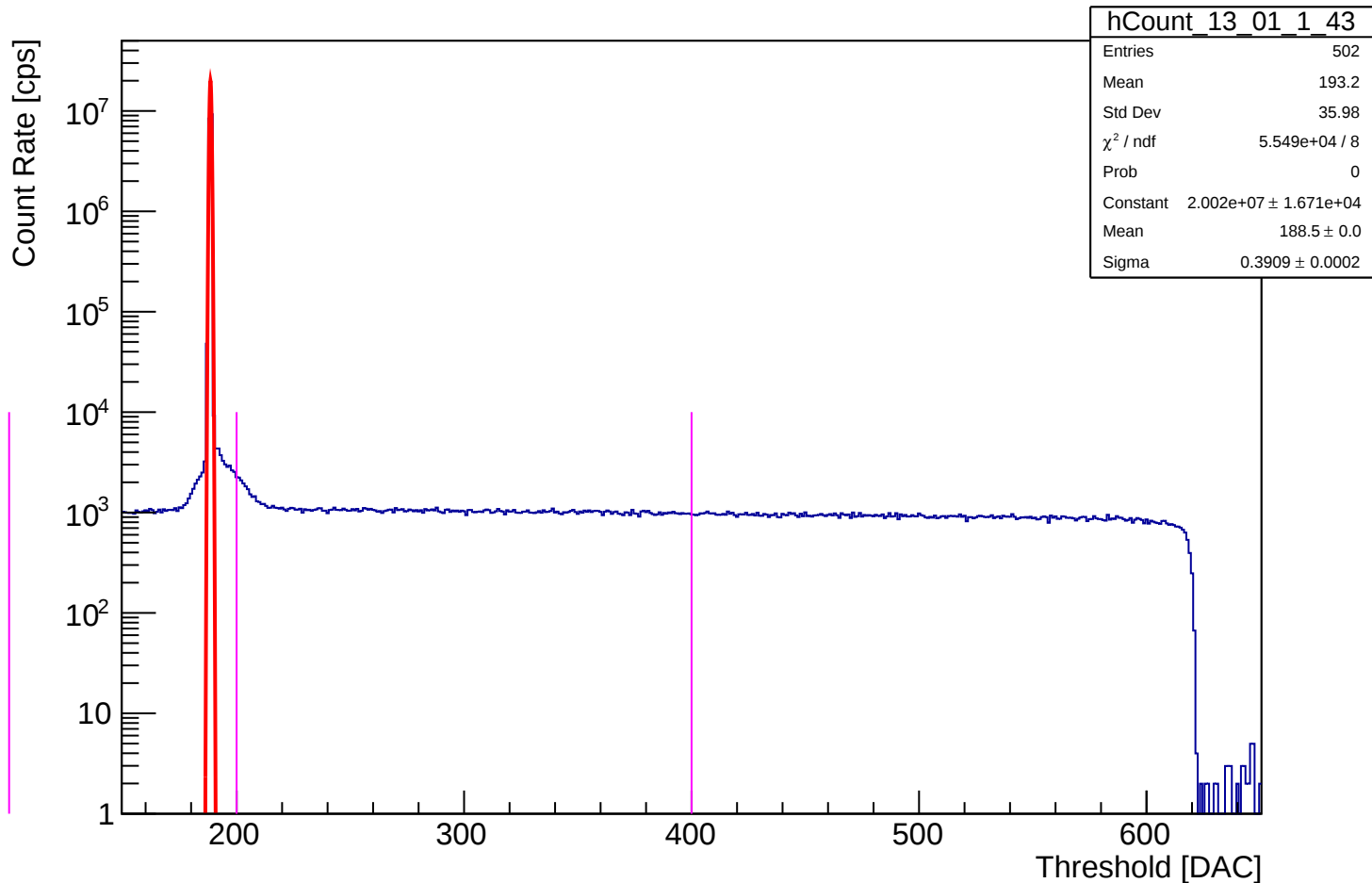
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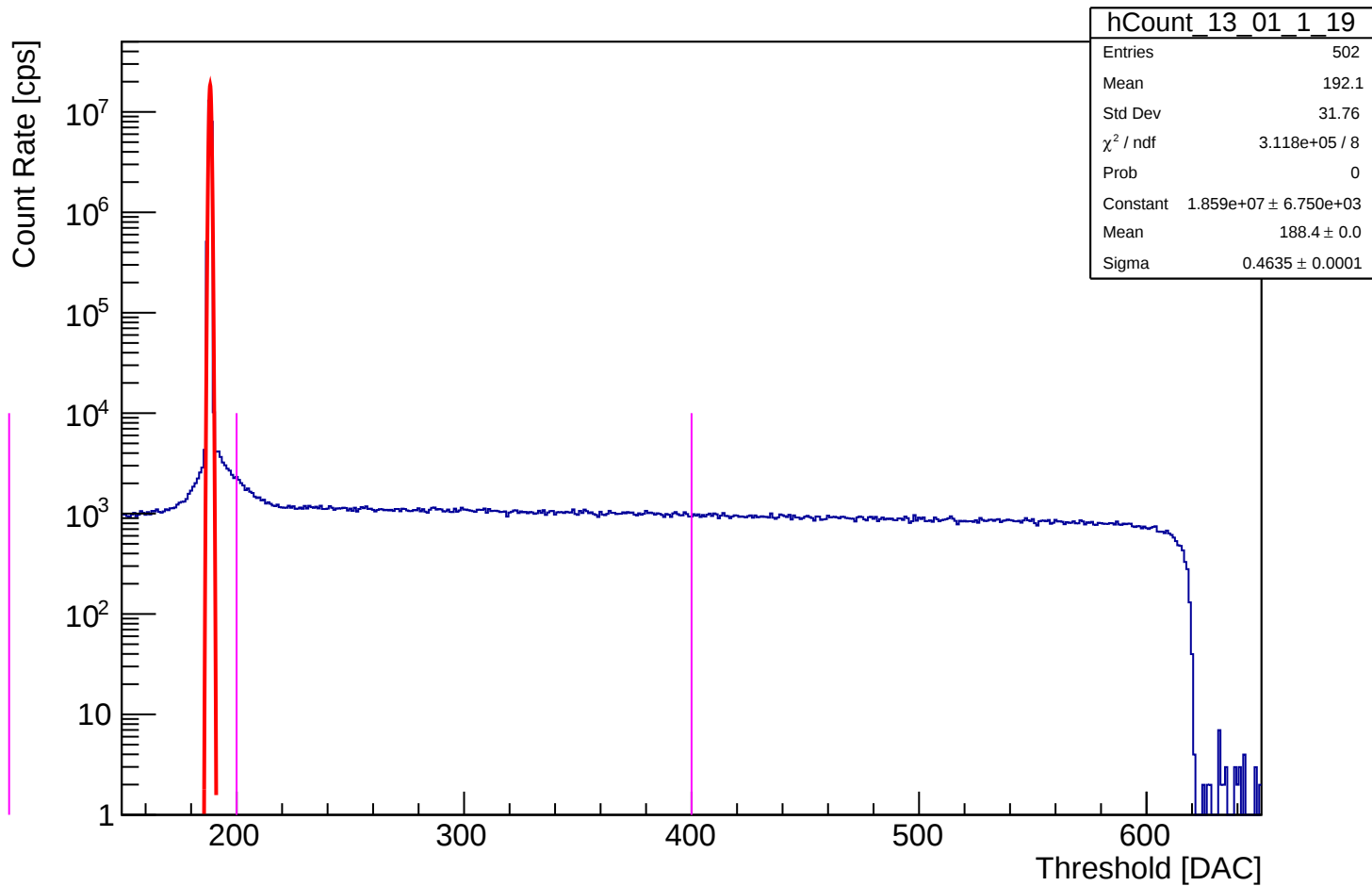
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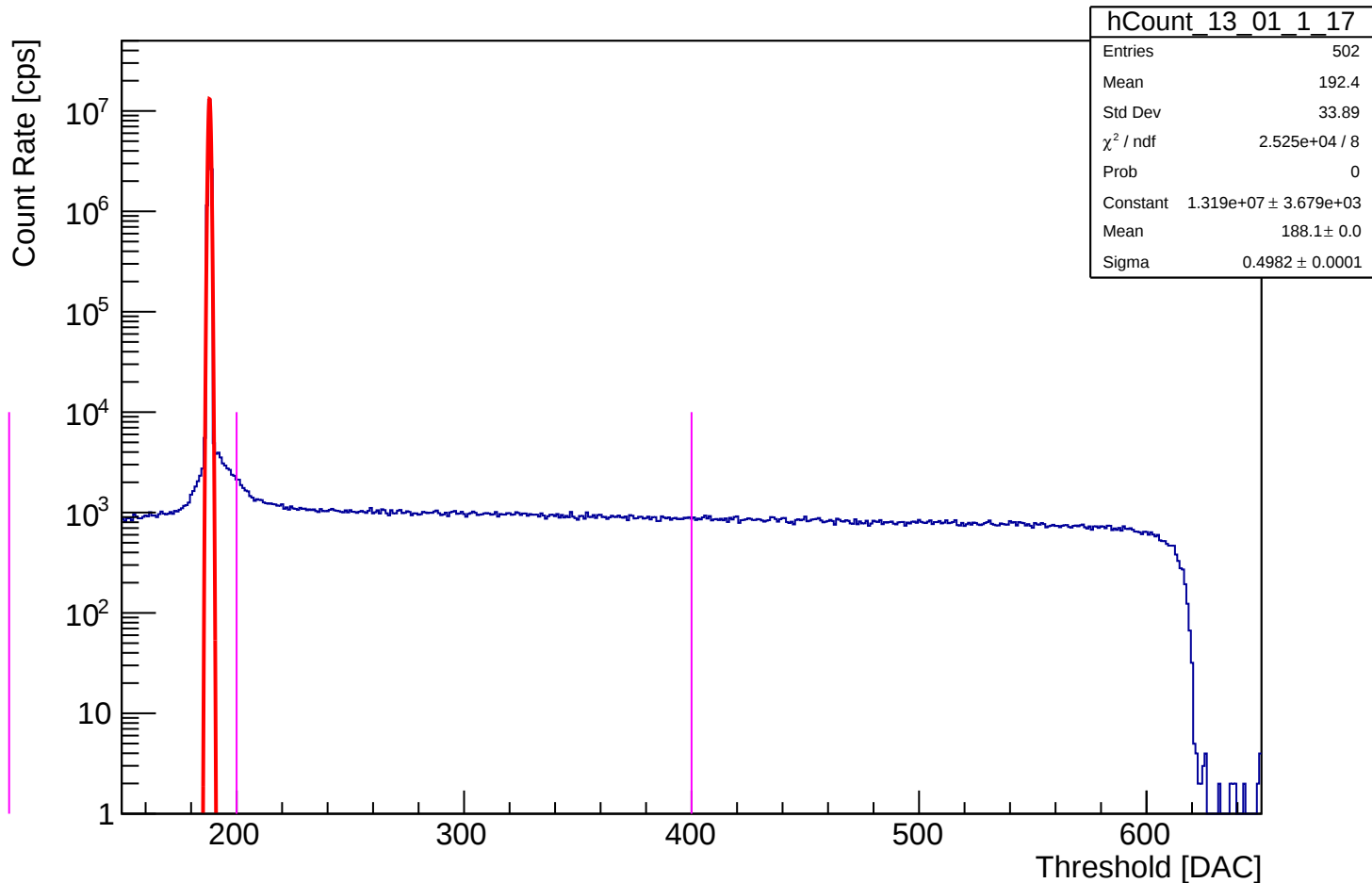
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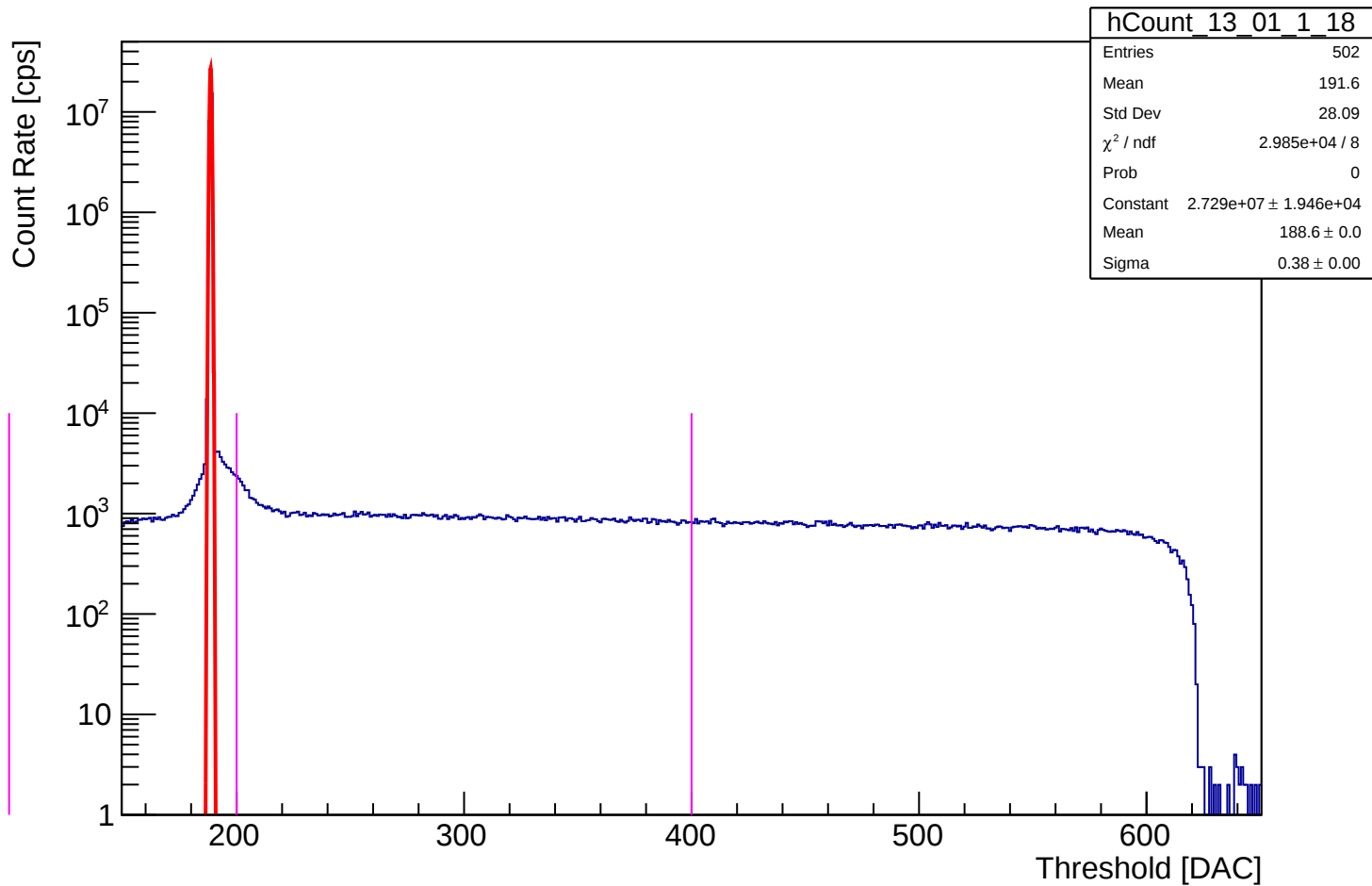
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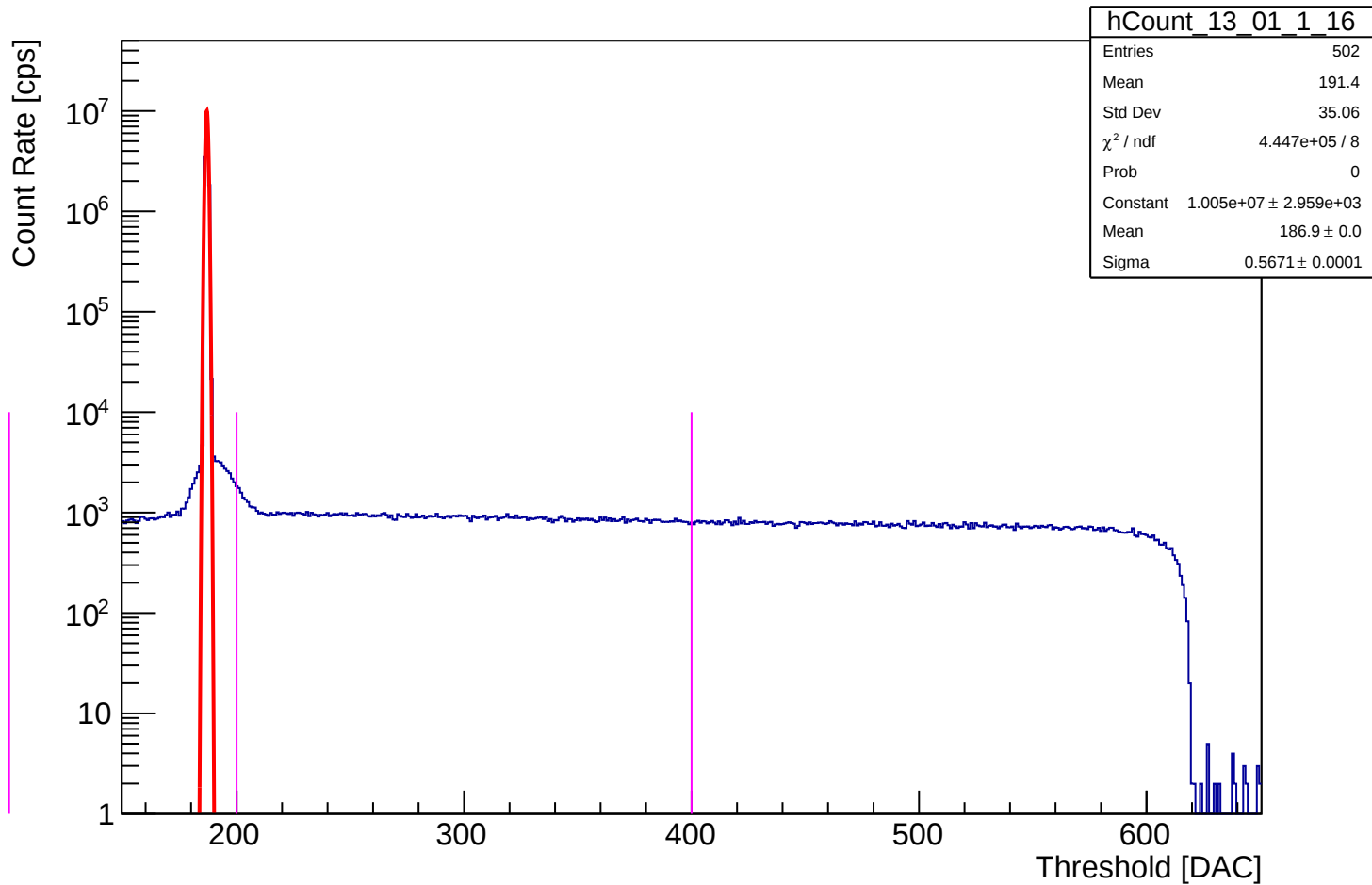
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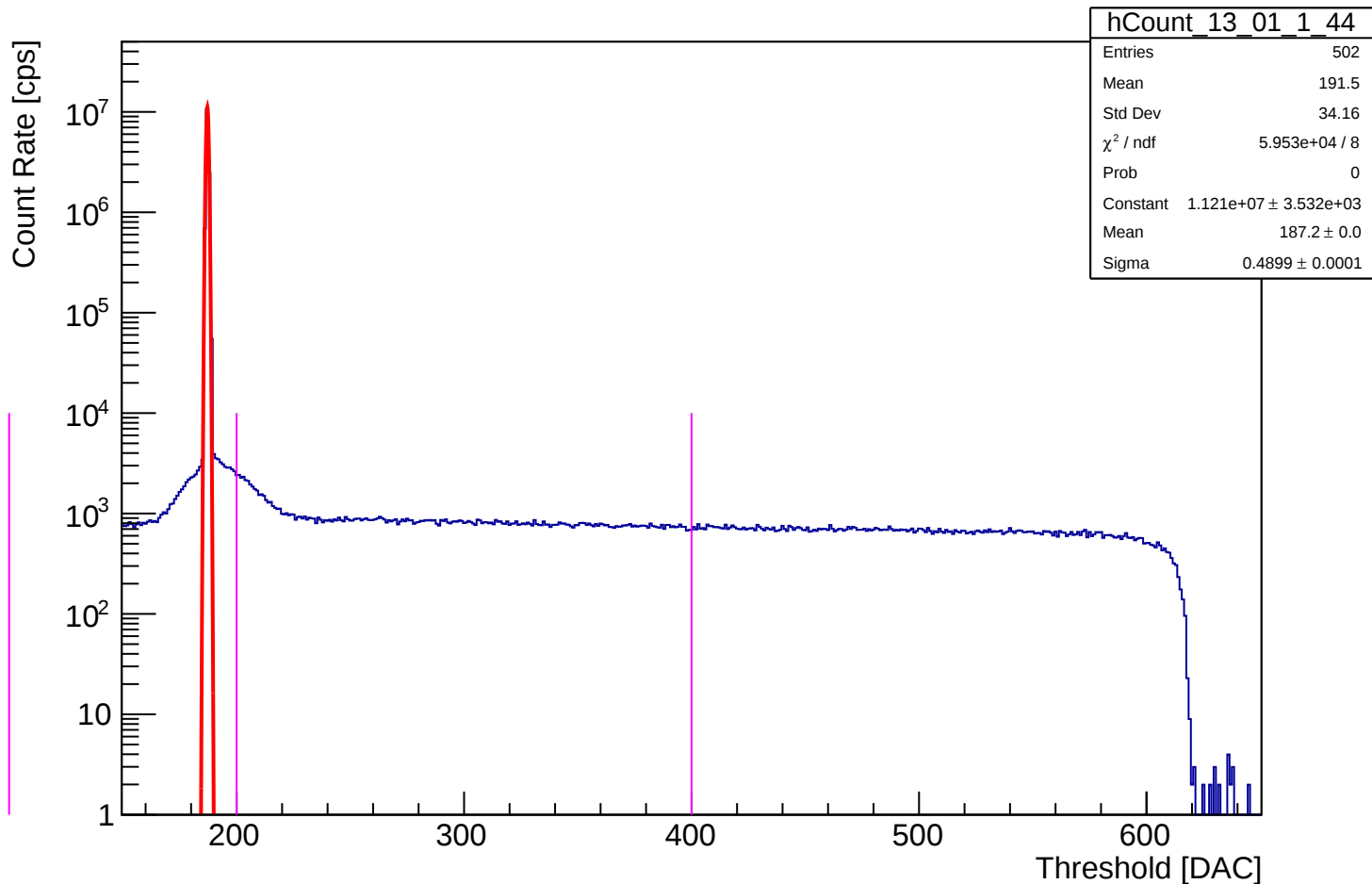
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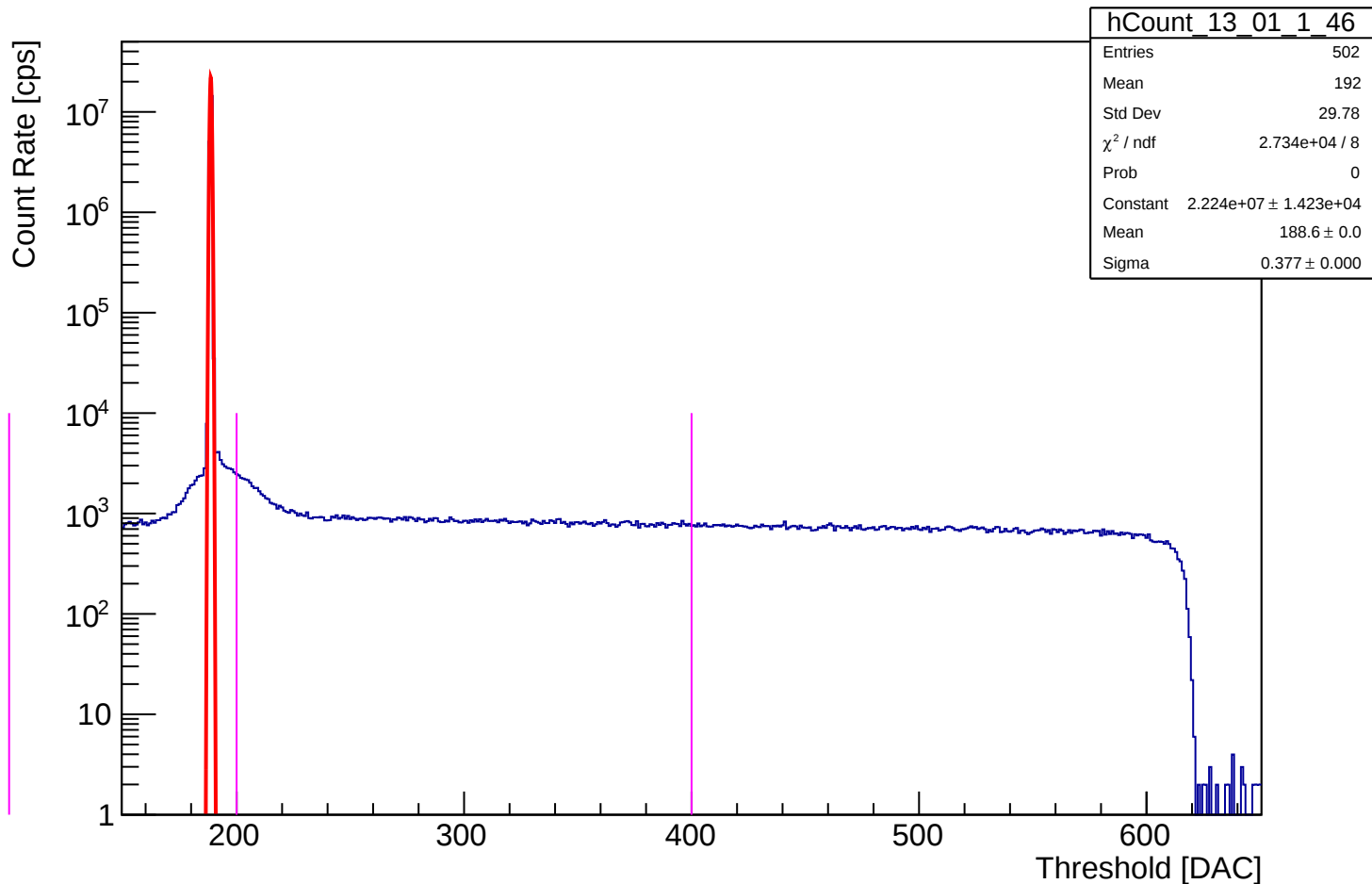
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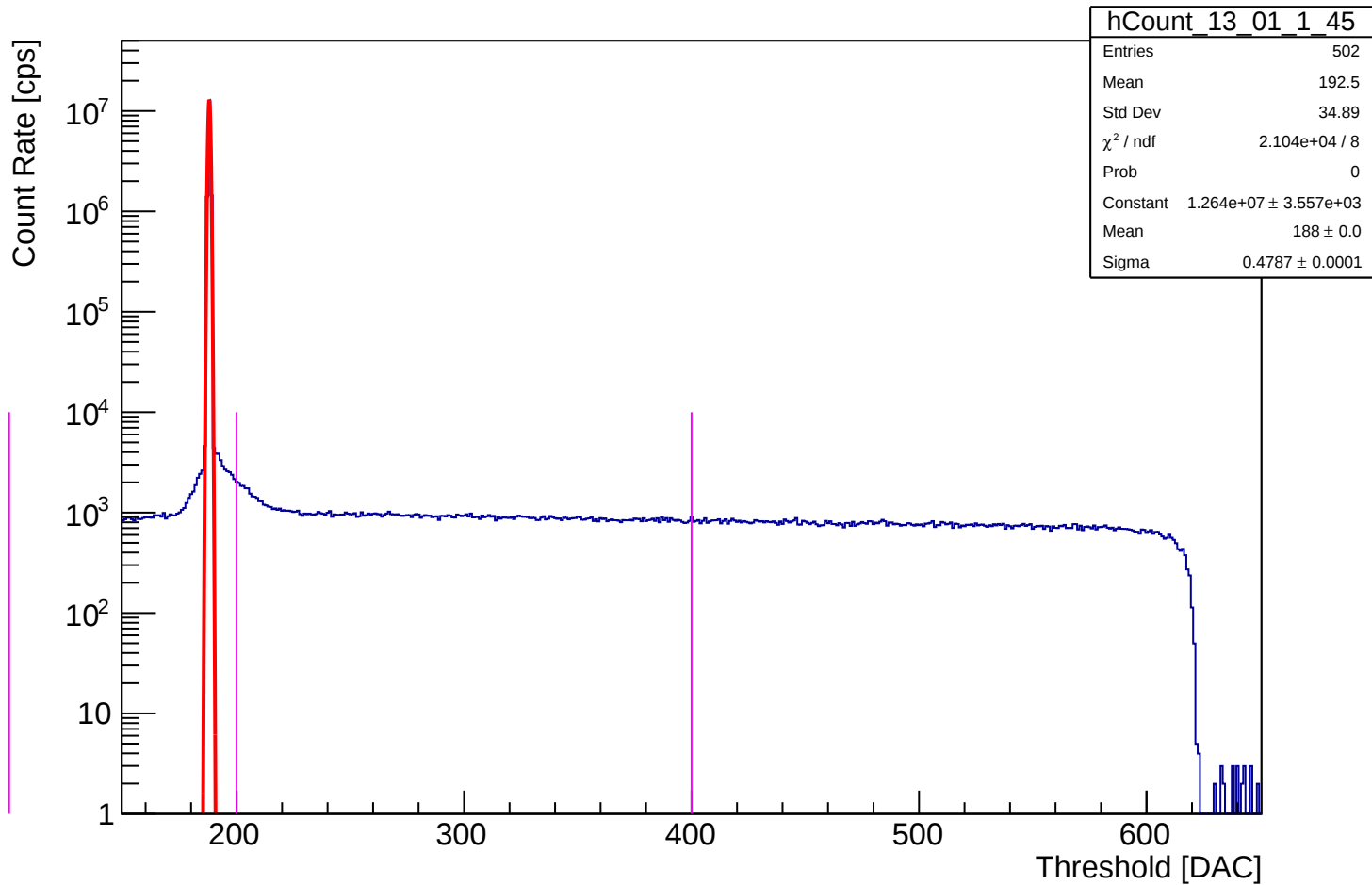
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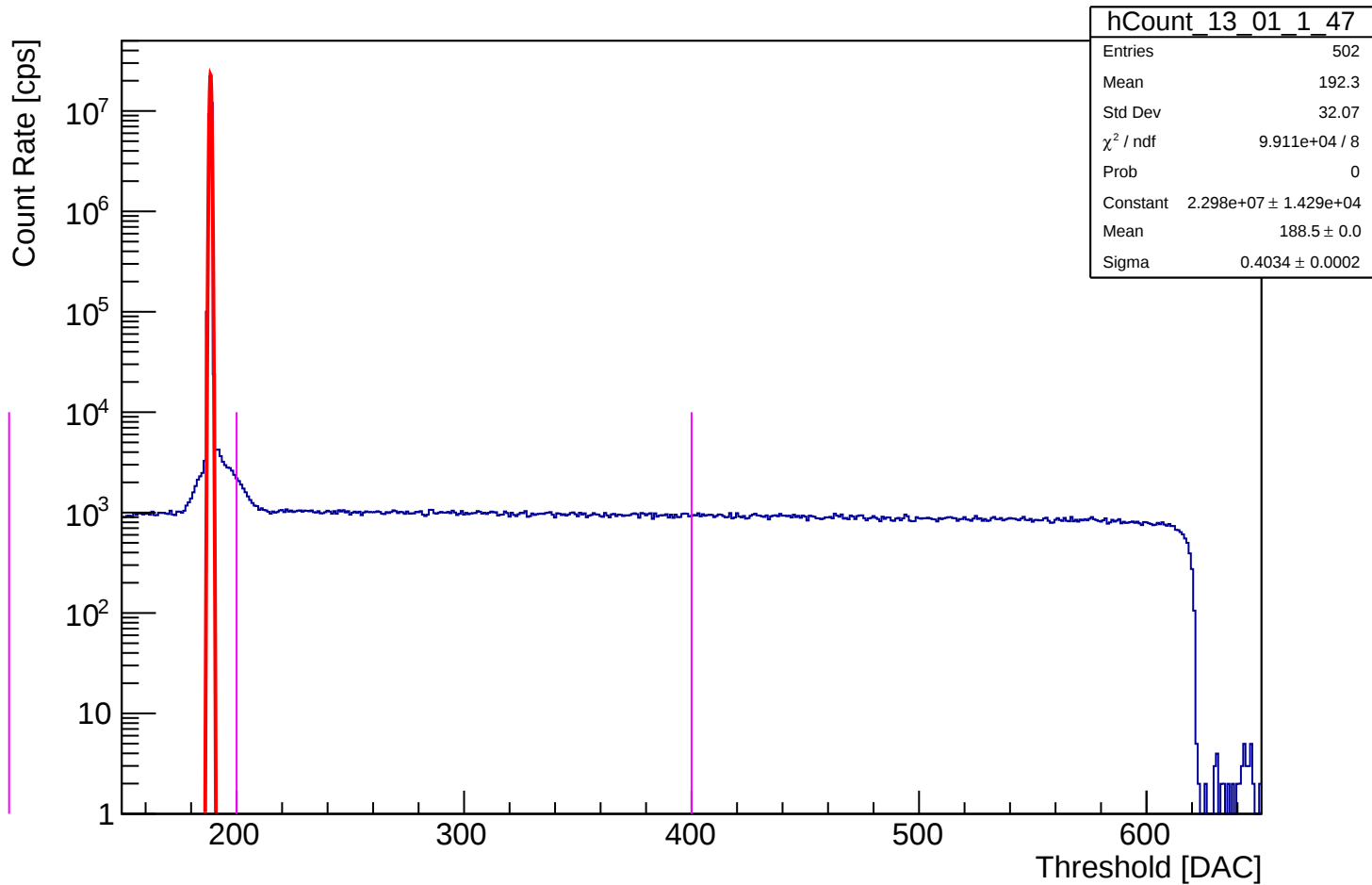
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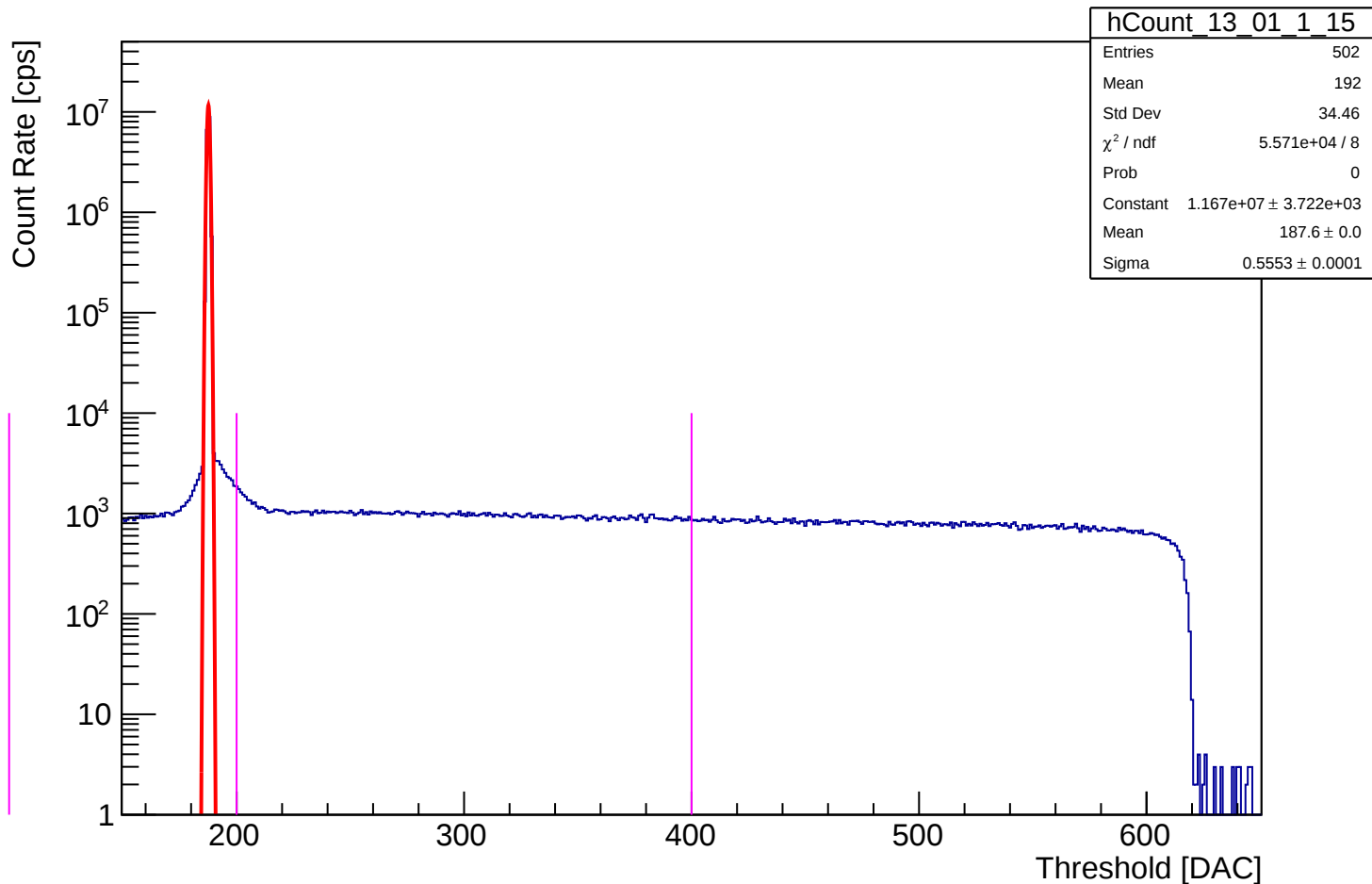
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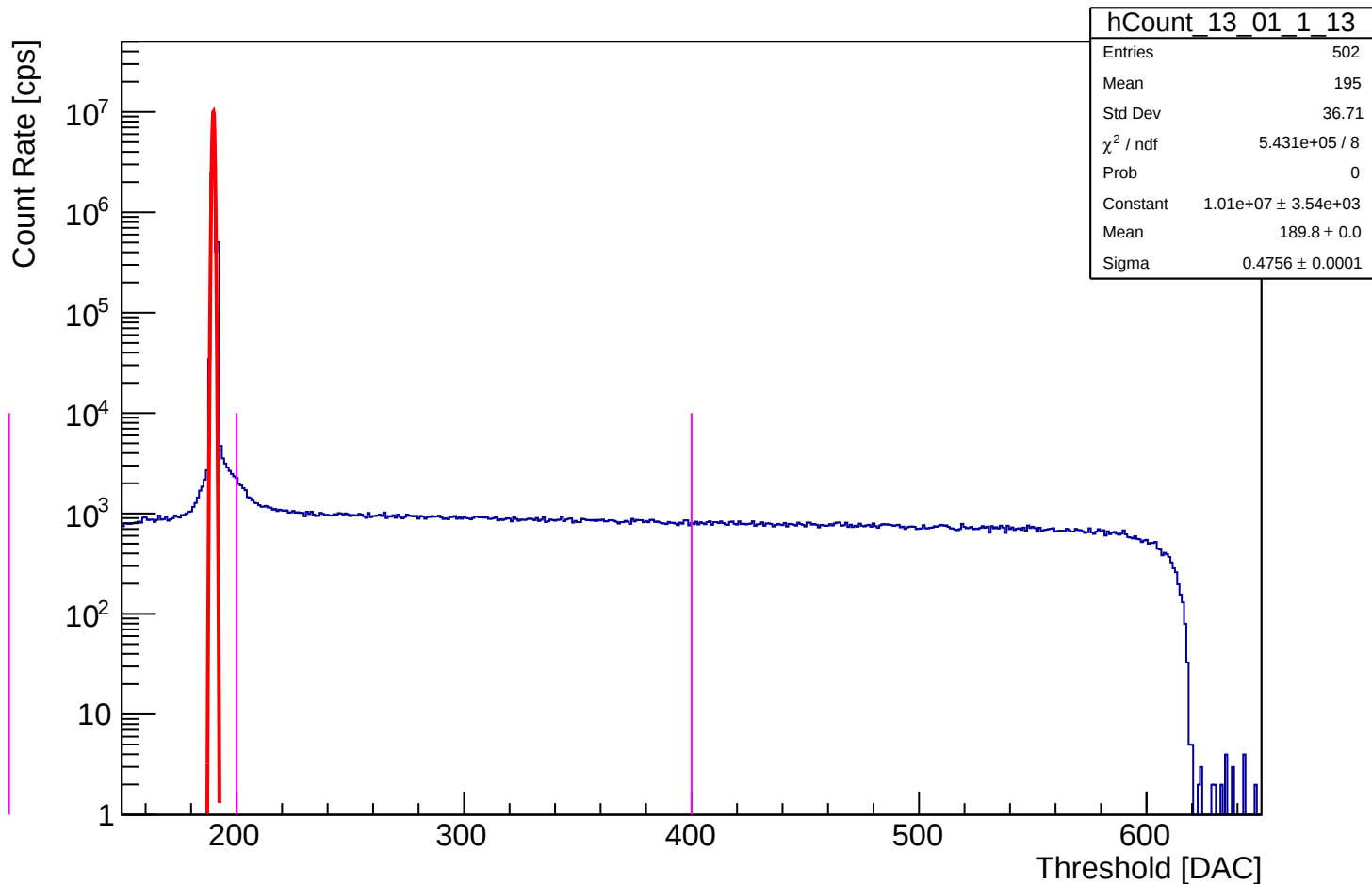
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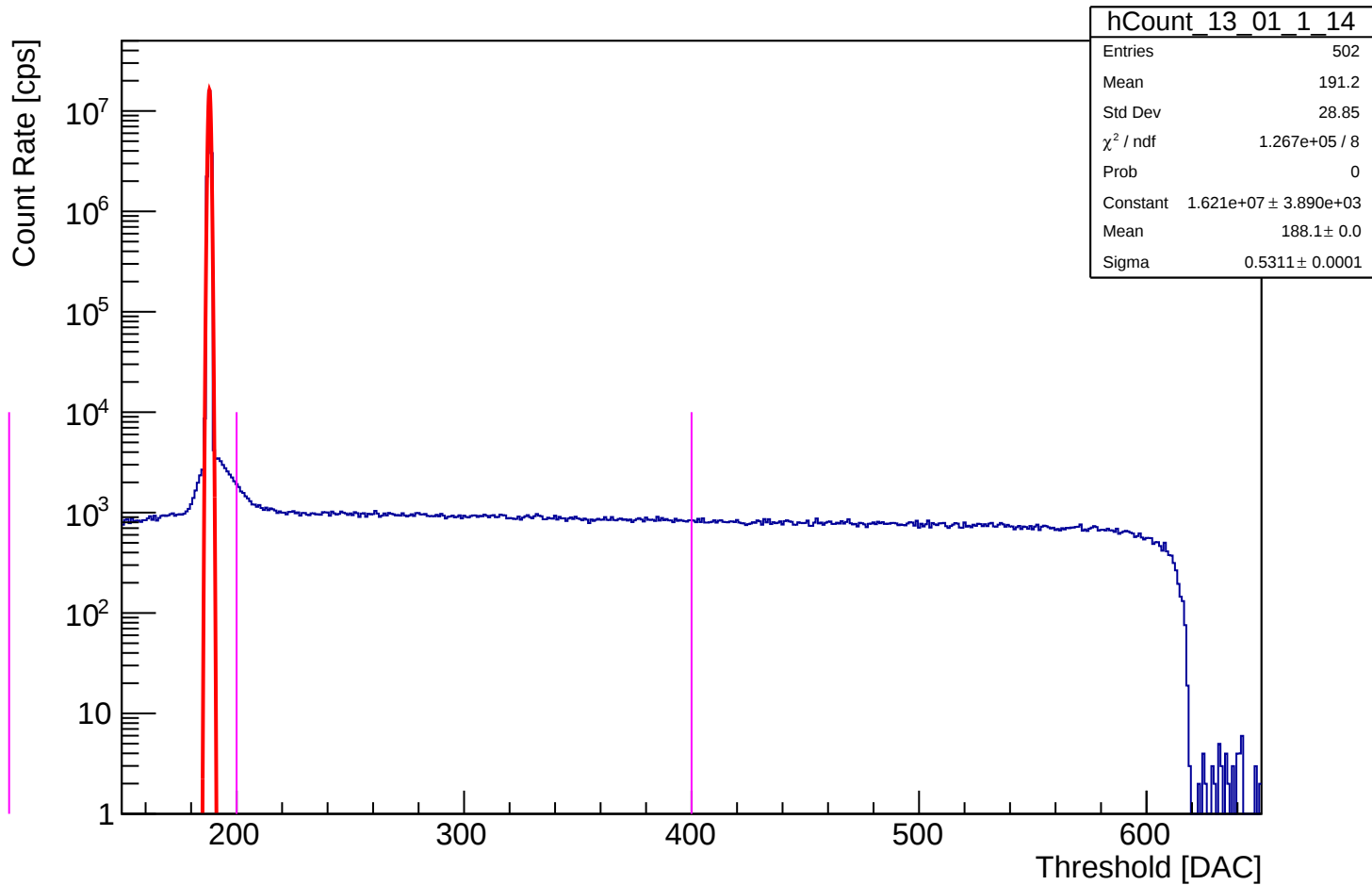
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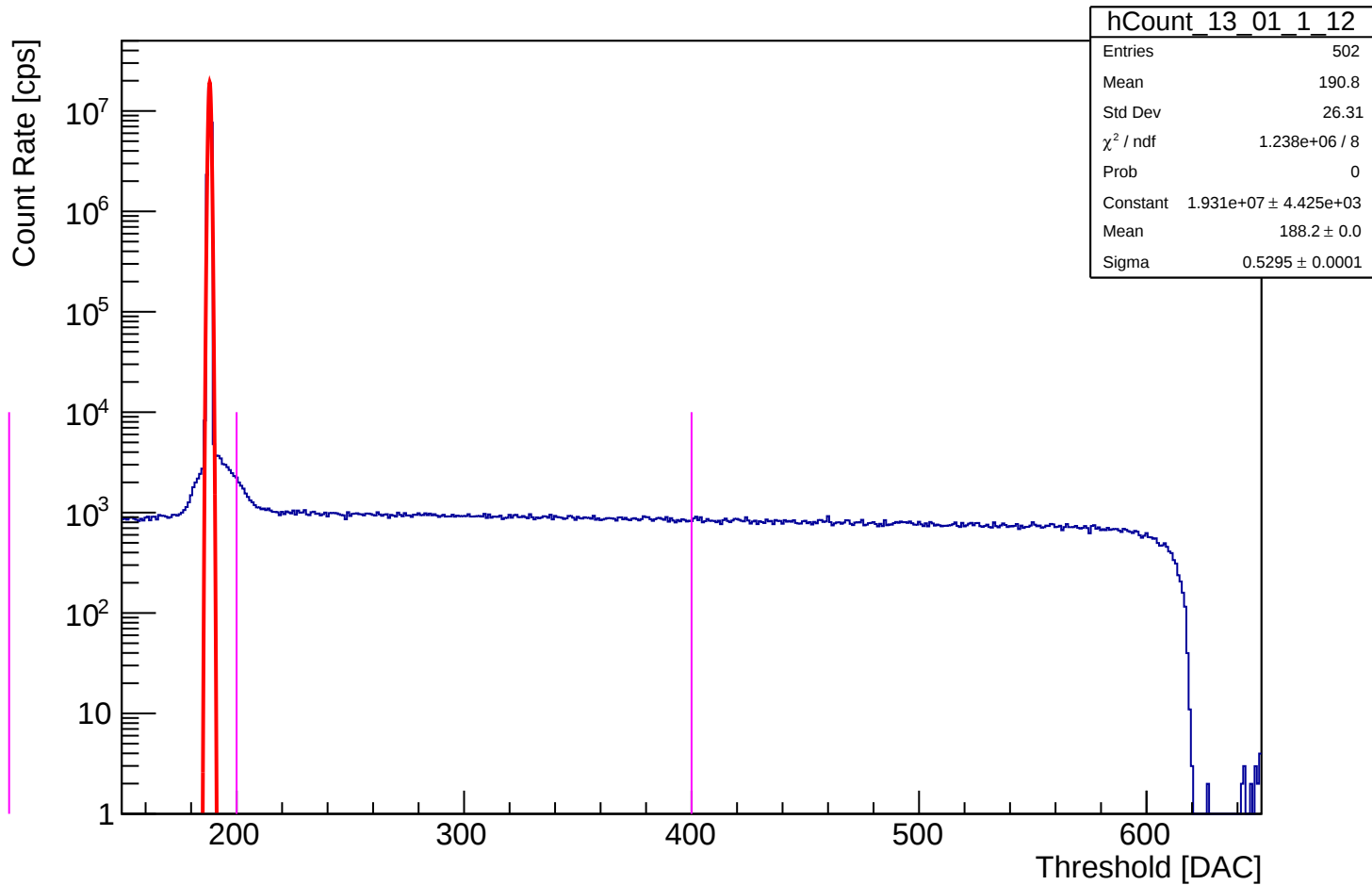
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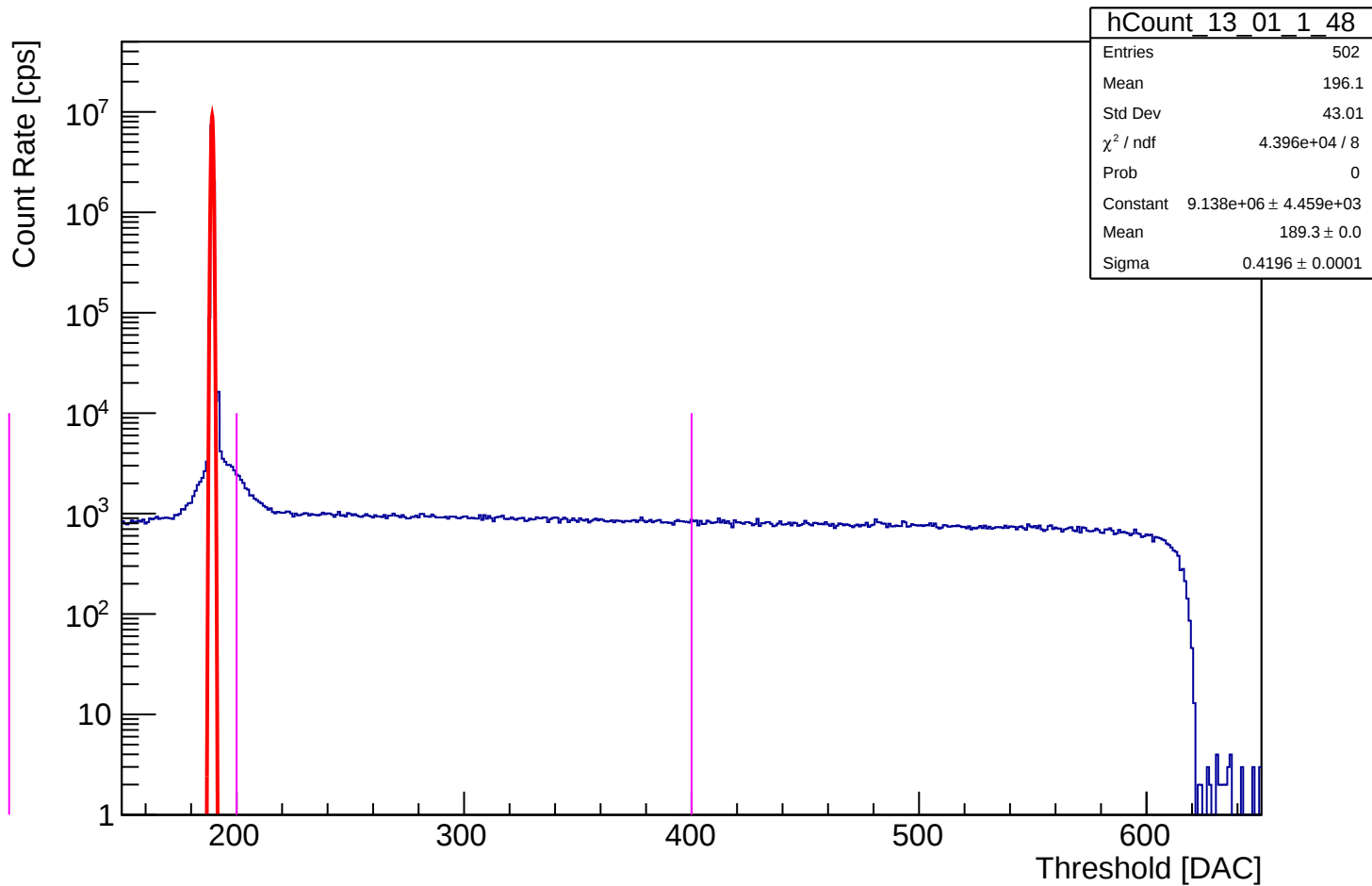
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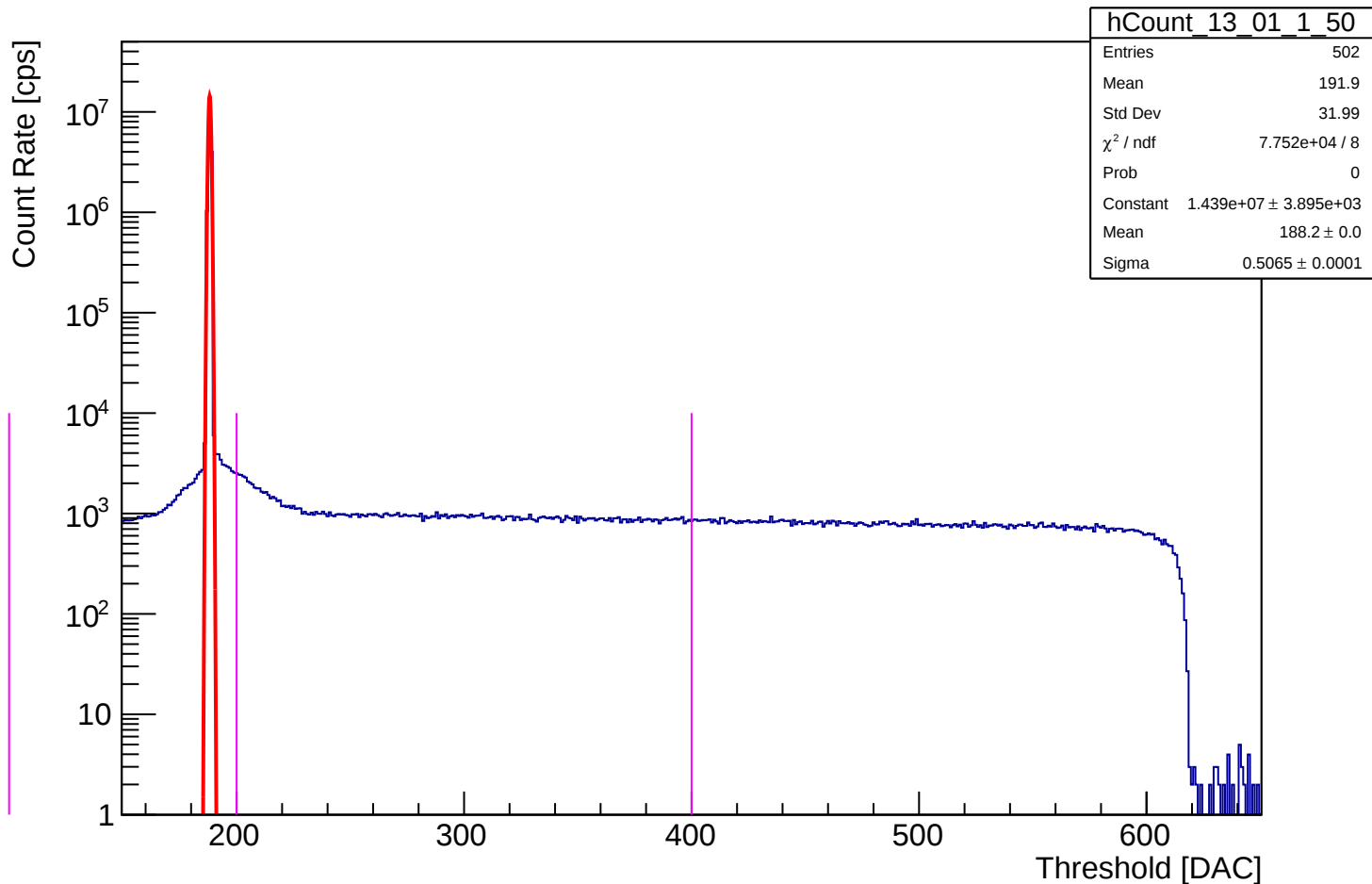
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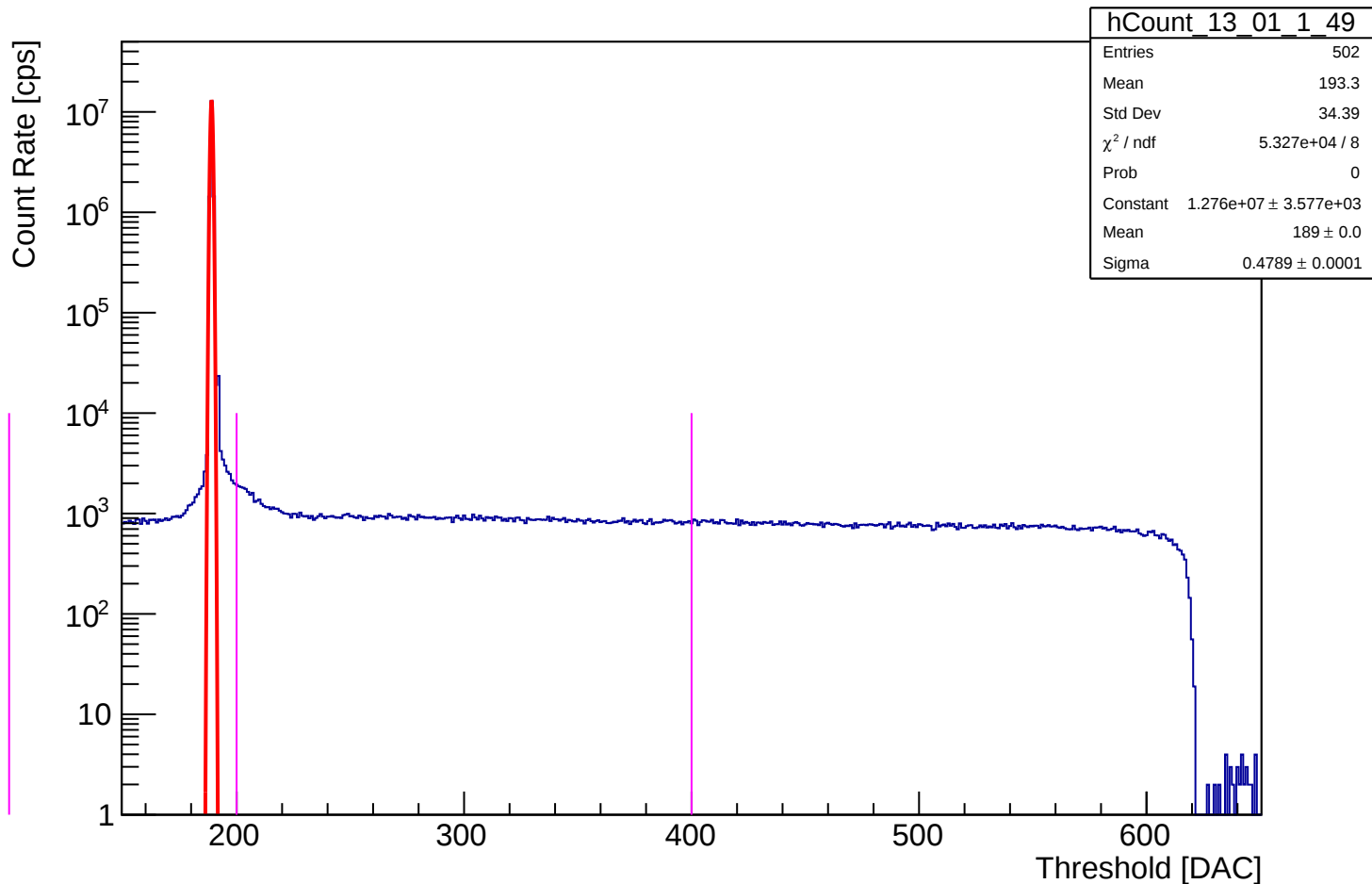
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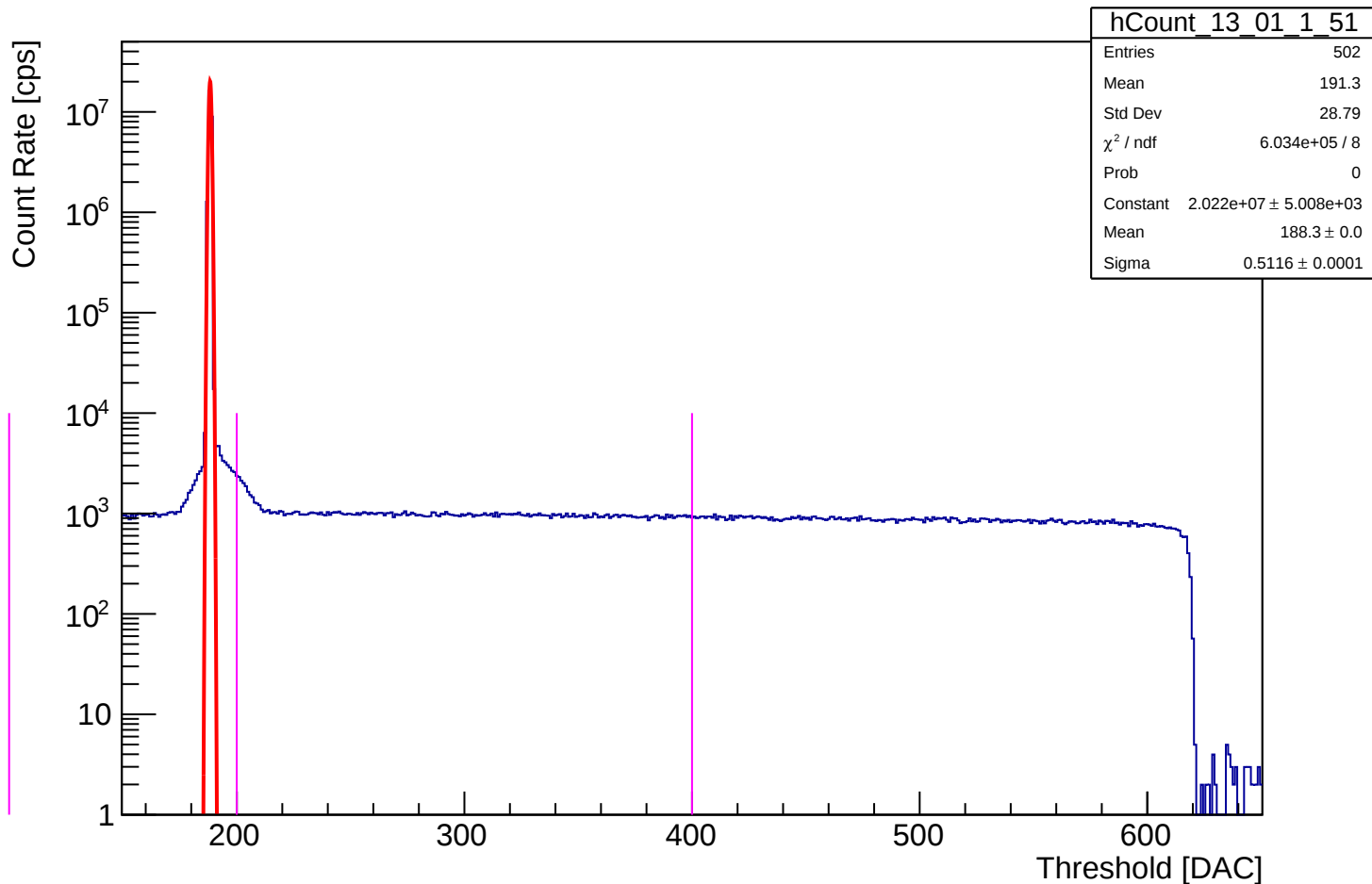
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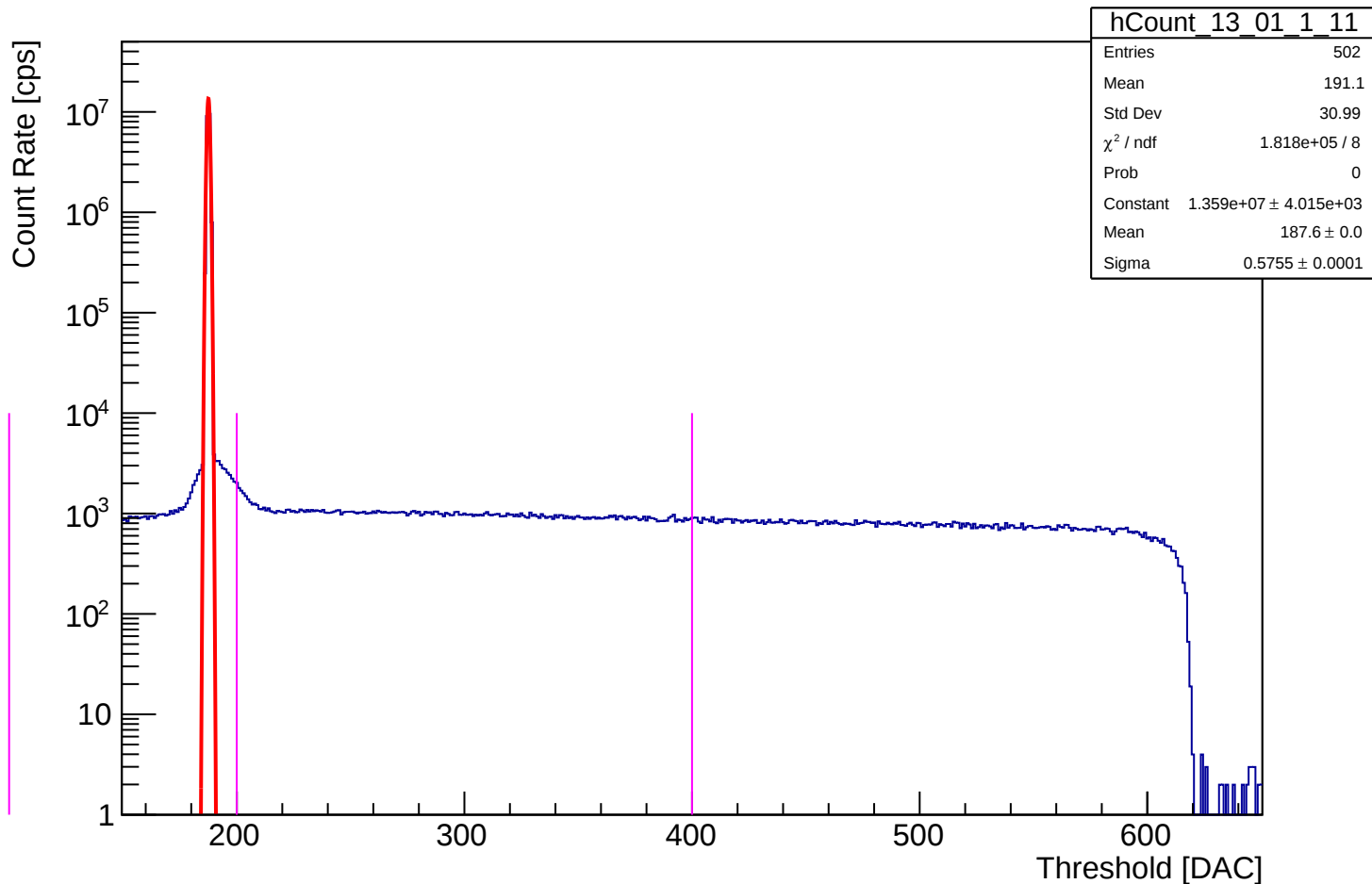
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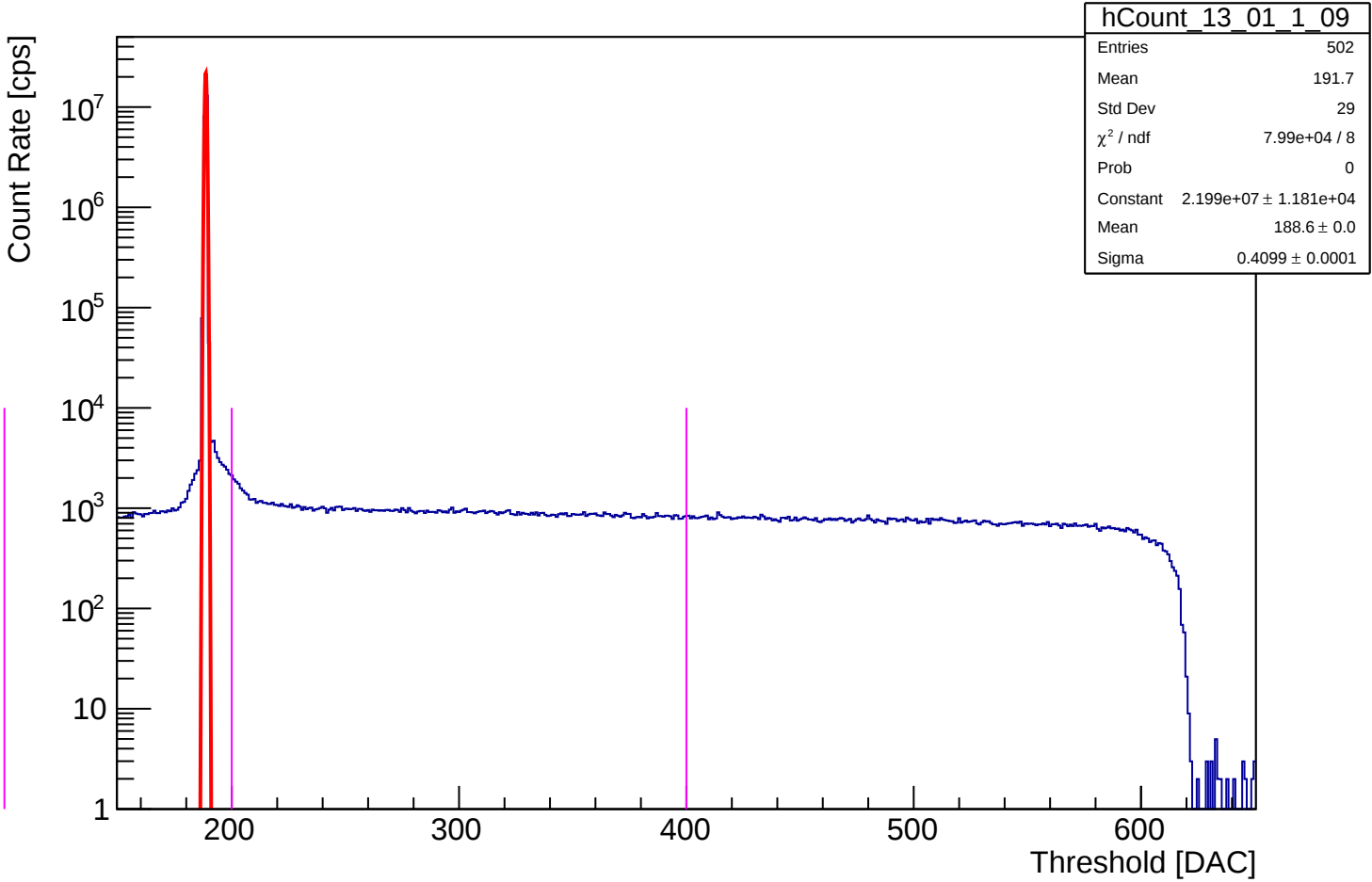


Row 1 Tile 1 Pmt 5 Pixel 40 Slot 13 Fiber 1 Asic 1 Channel 51

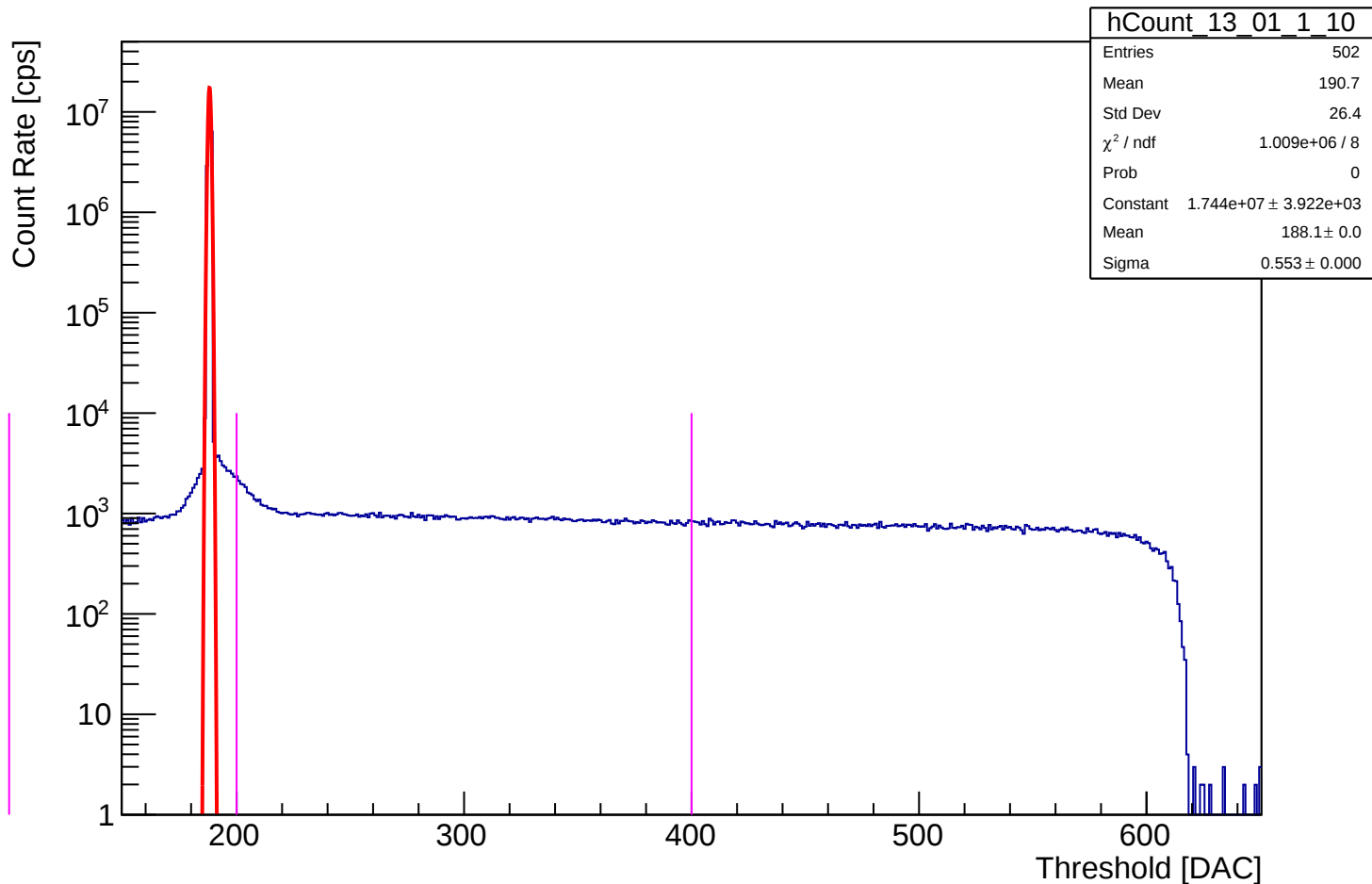


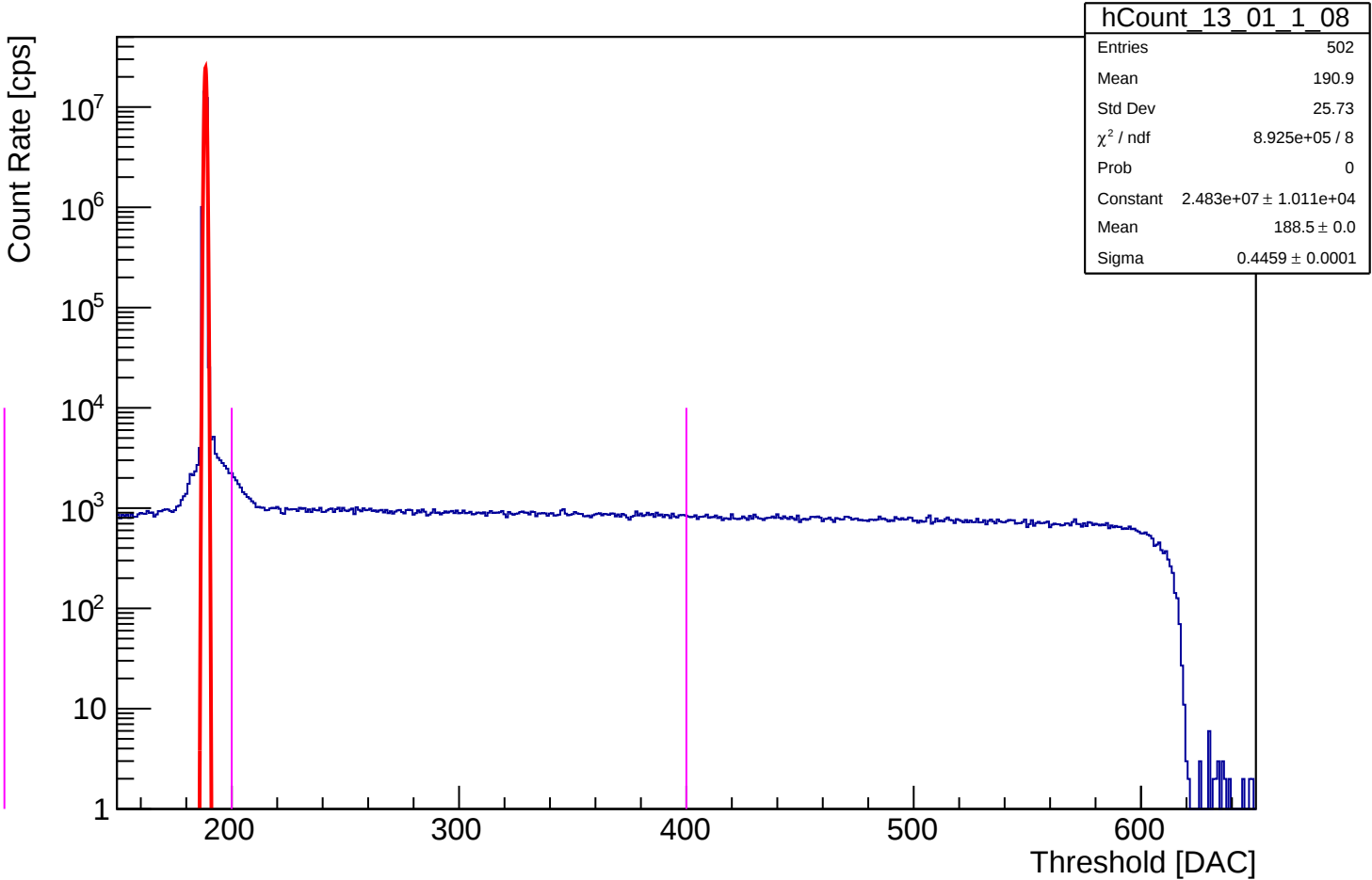
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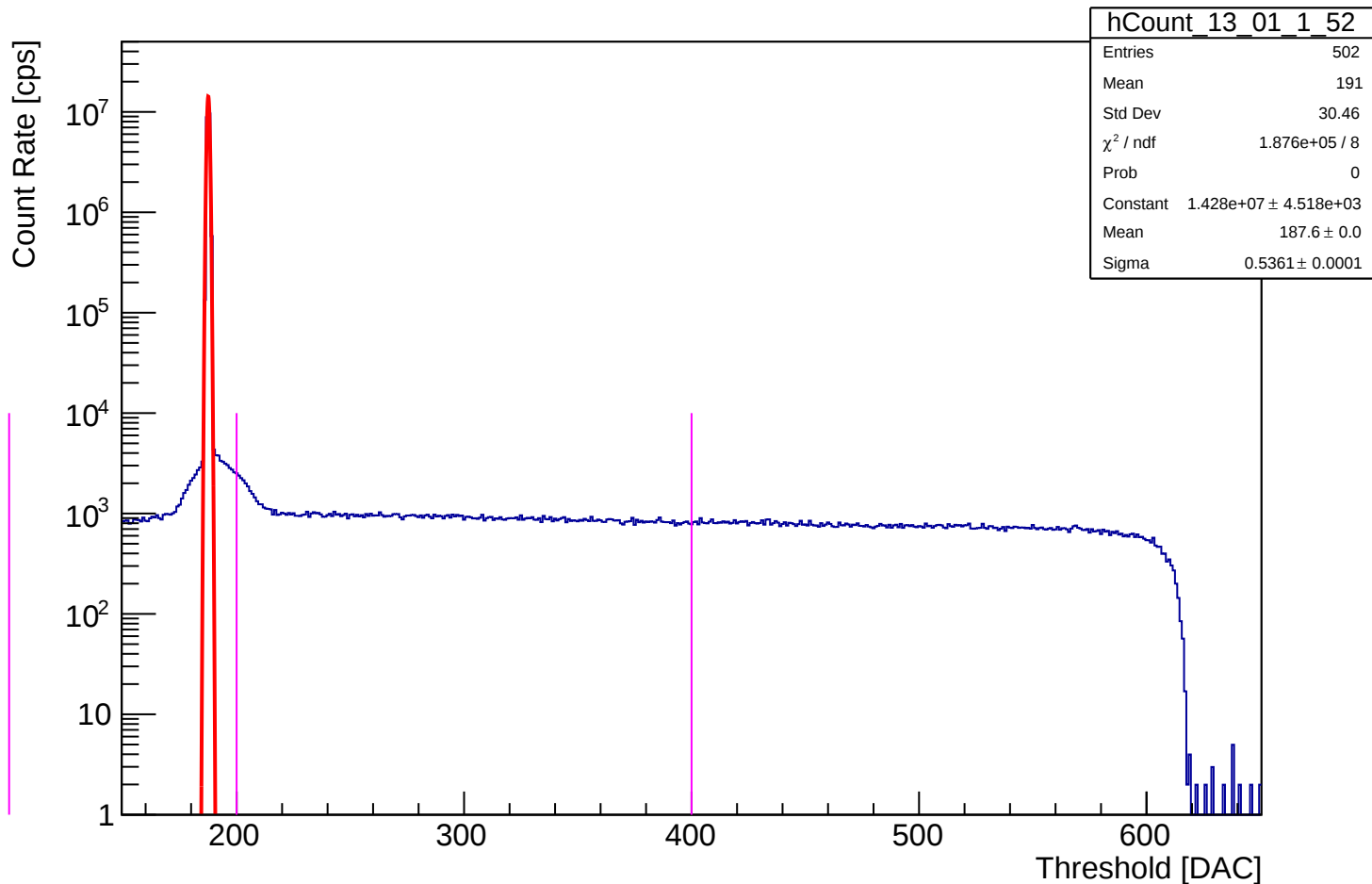


Row 1 Tile 1 Pmt 5 Pixel 43 Slot 13 Fiber 1 Asic 1 Channel 10

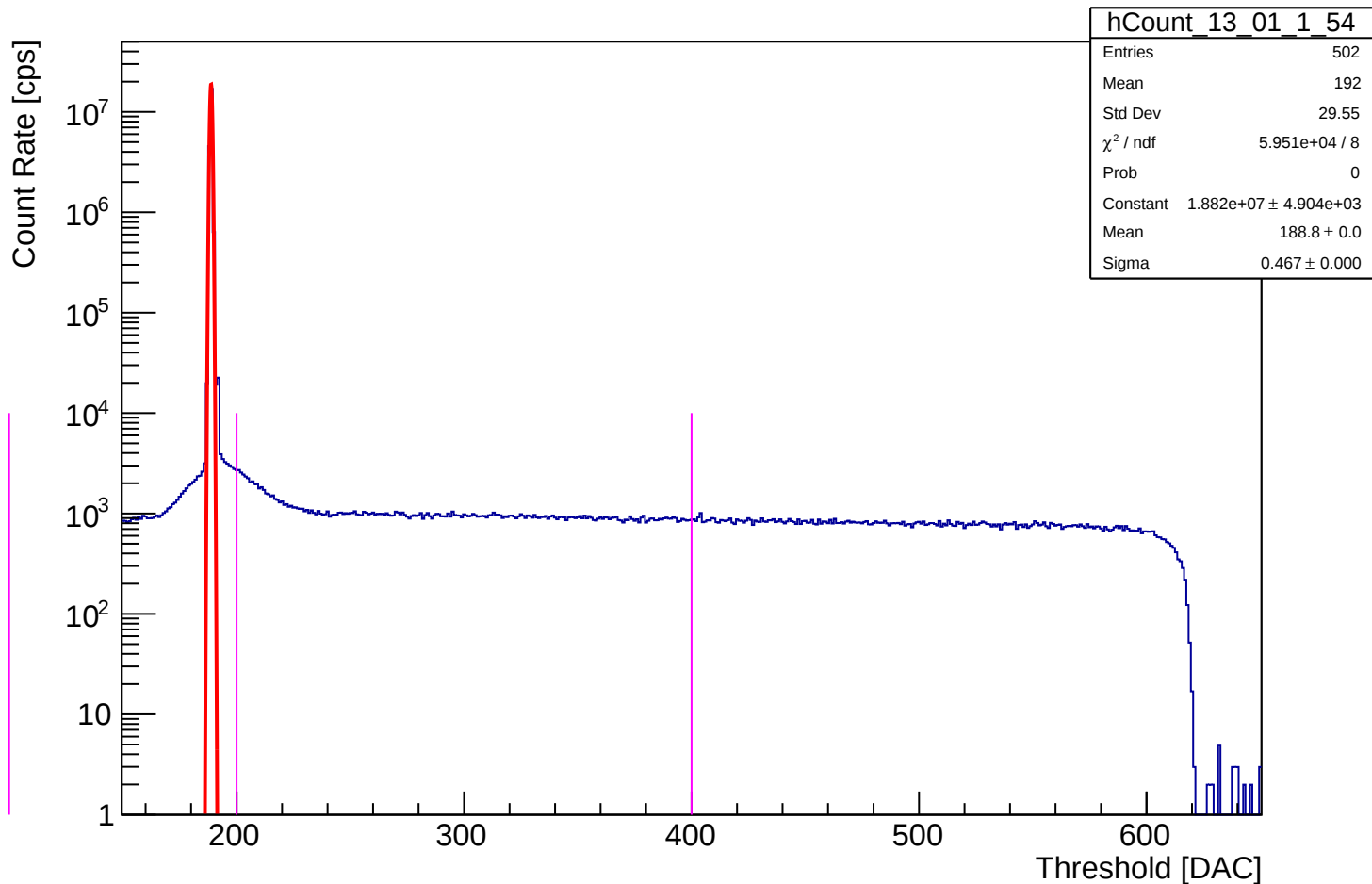




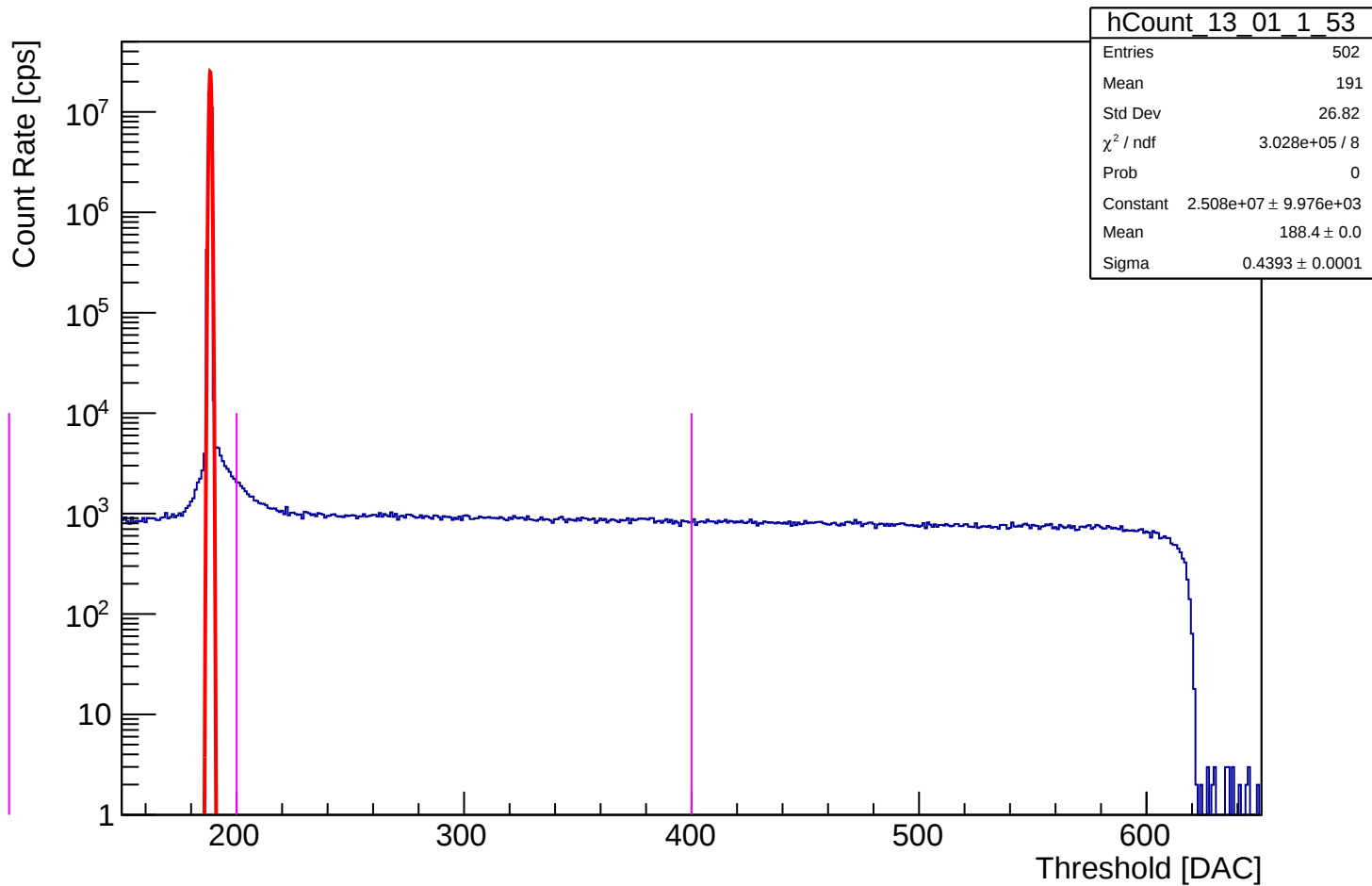
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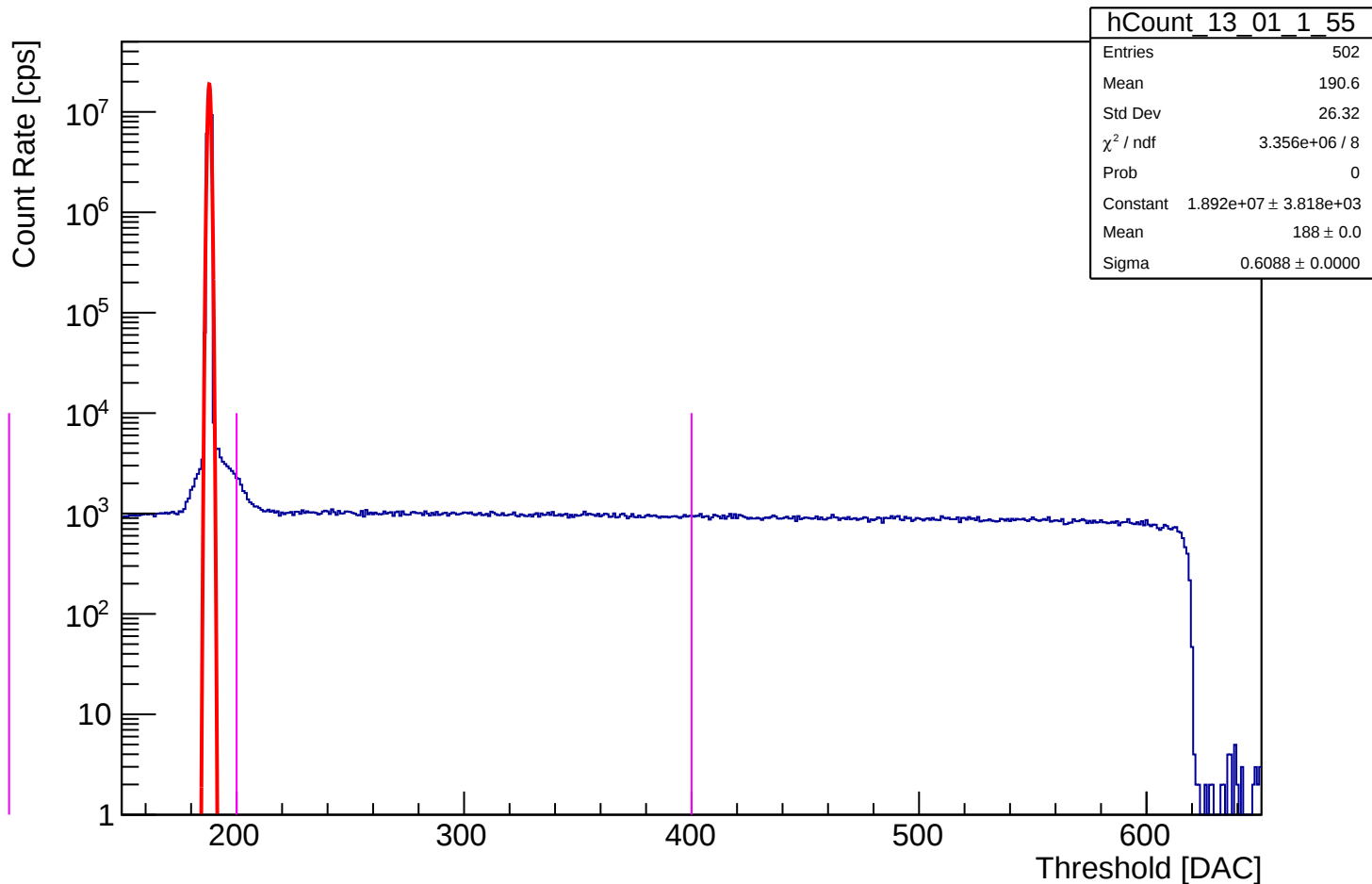
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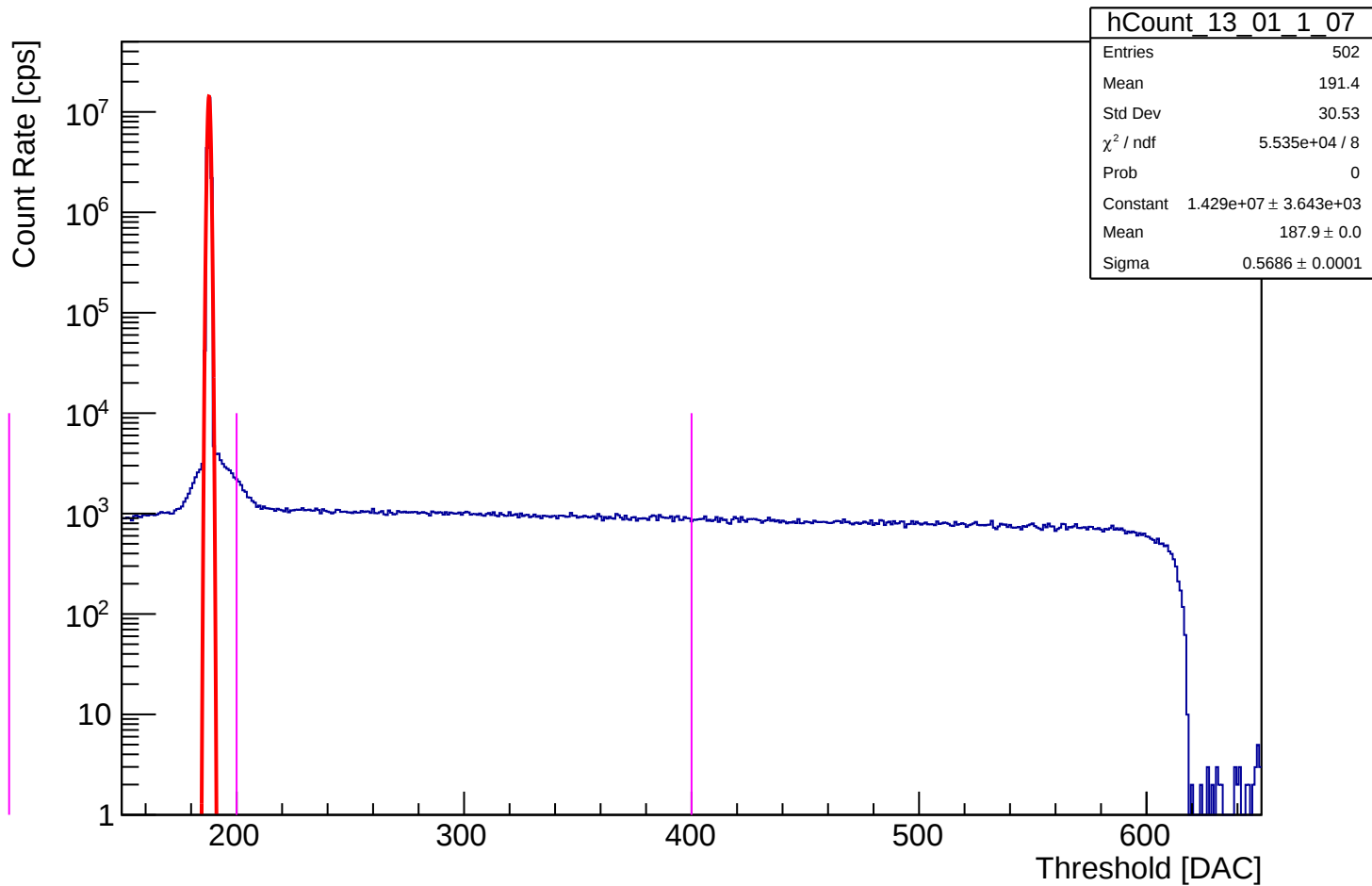
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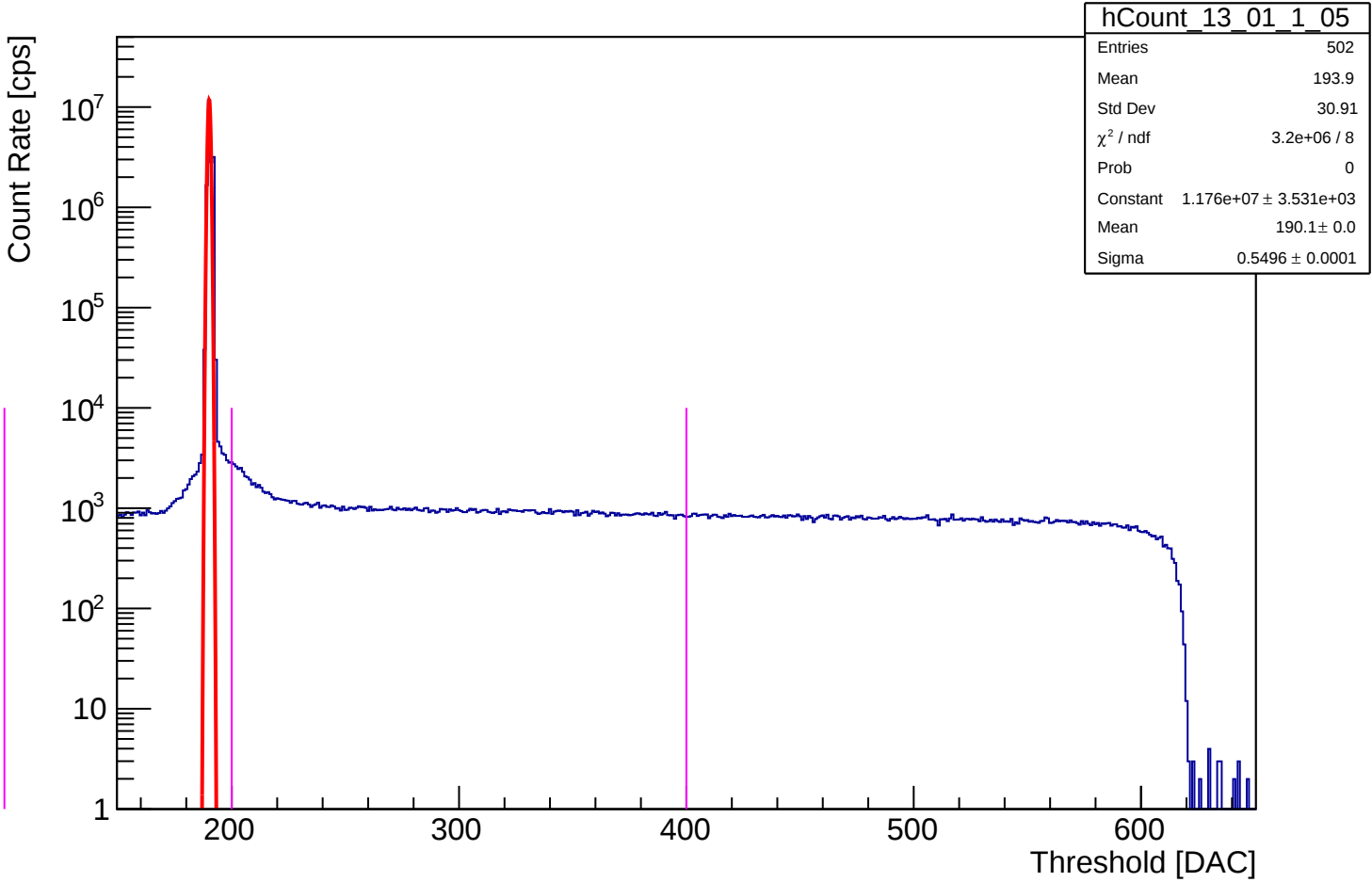


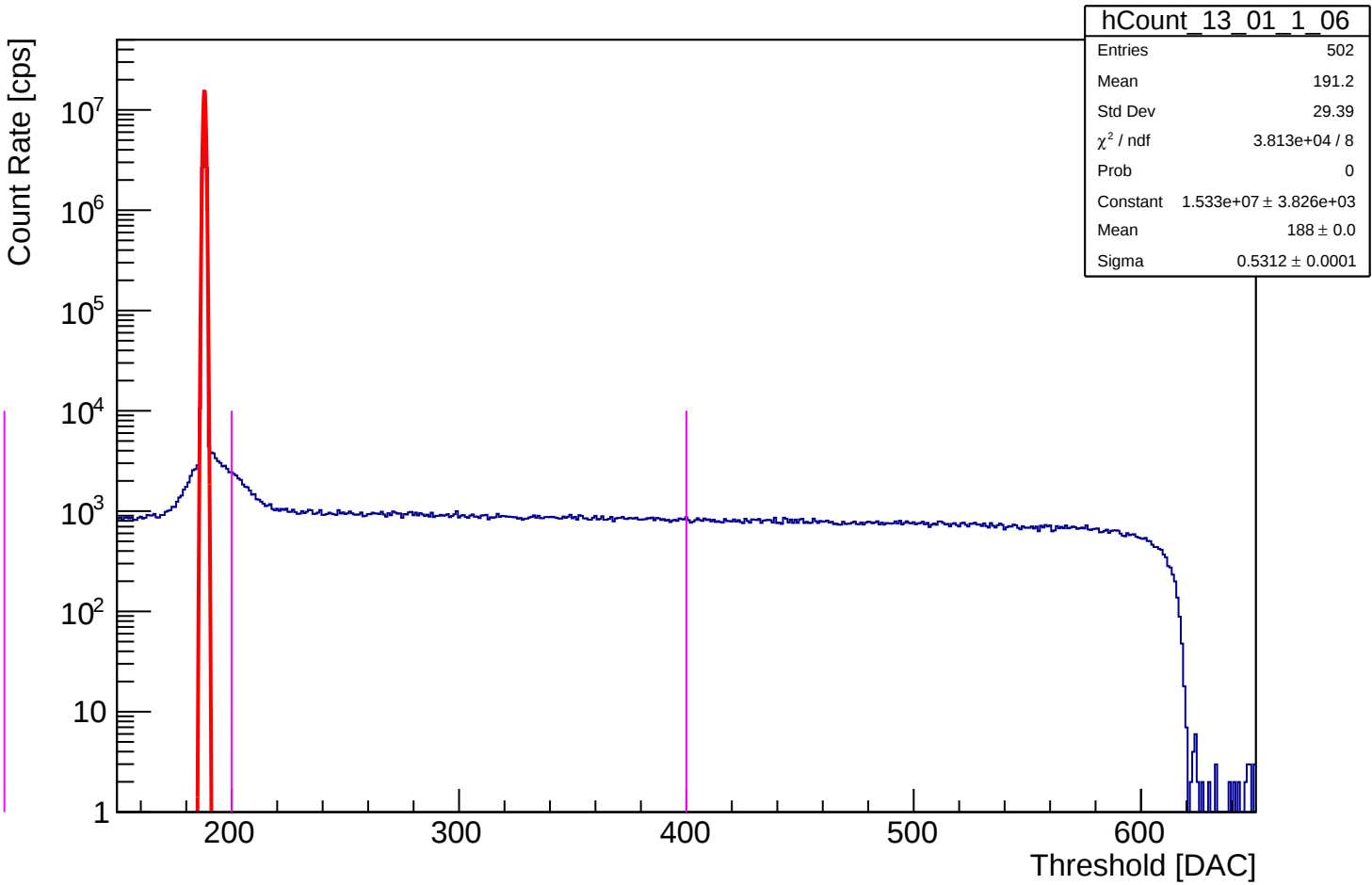
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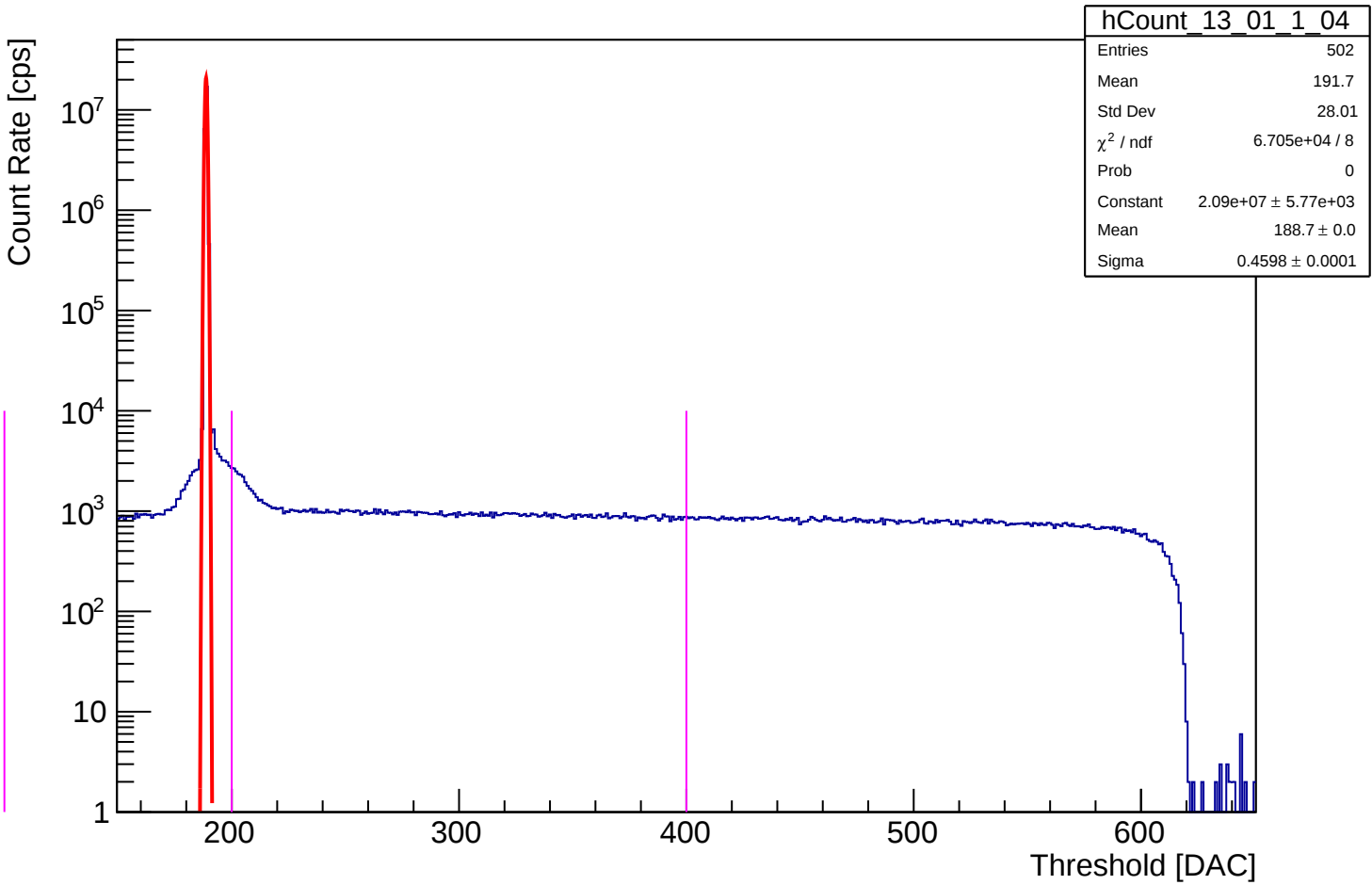


Row 1 Tile 1 Pmt 5 Pixel 49 Slot 13 Fiber 1 Asic 1 Channel 7

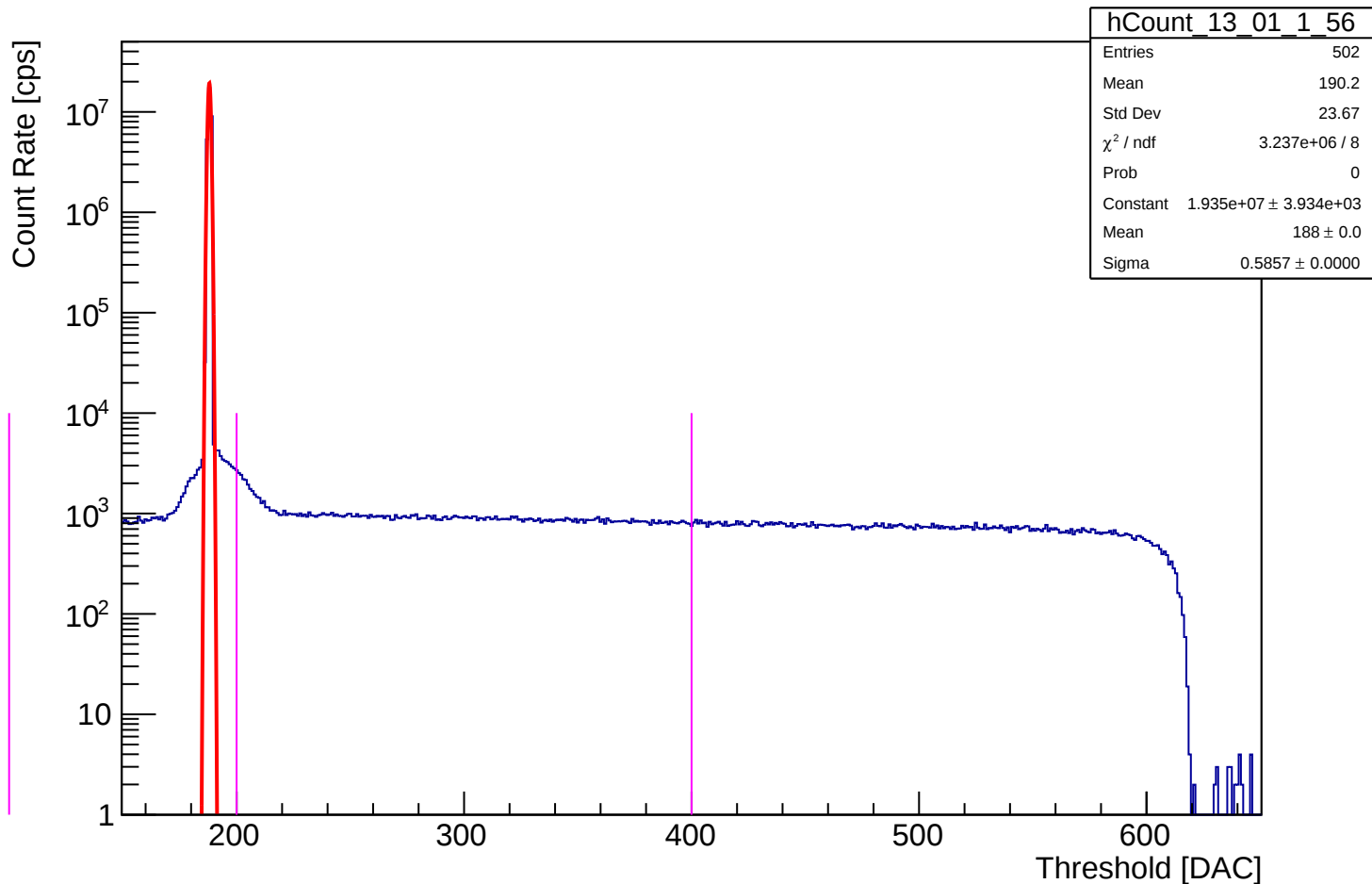




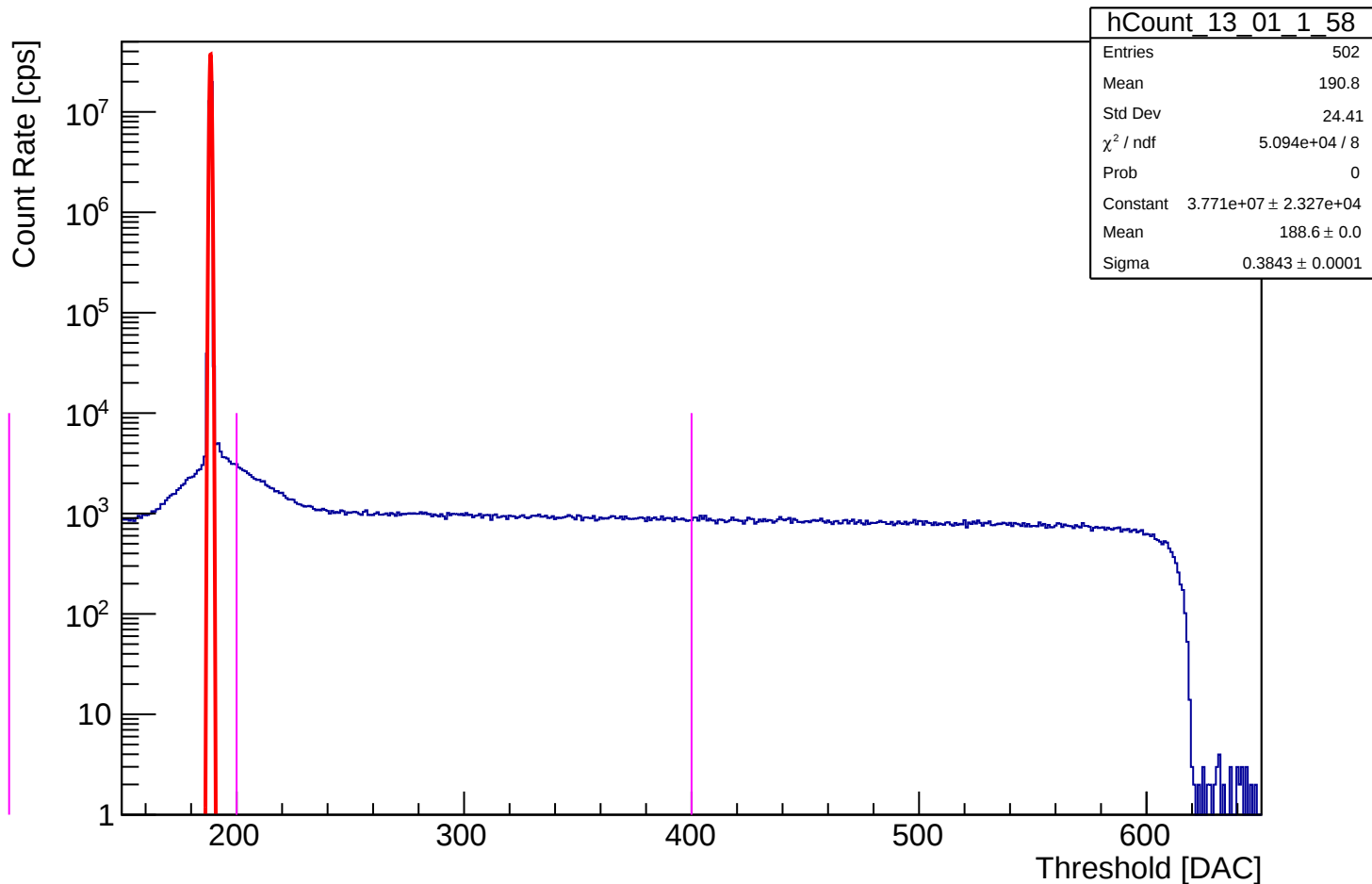




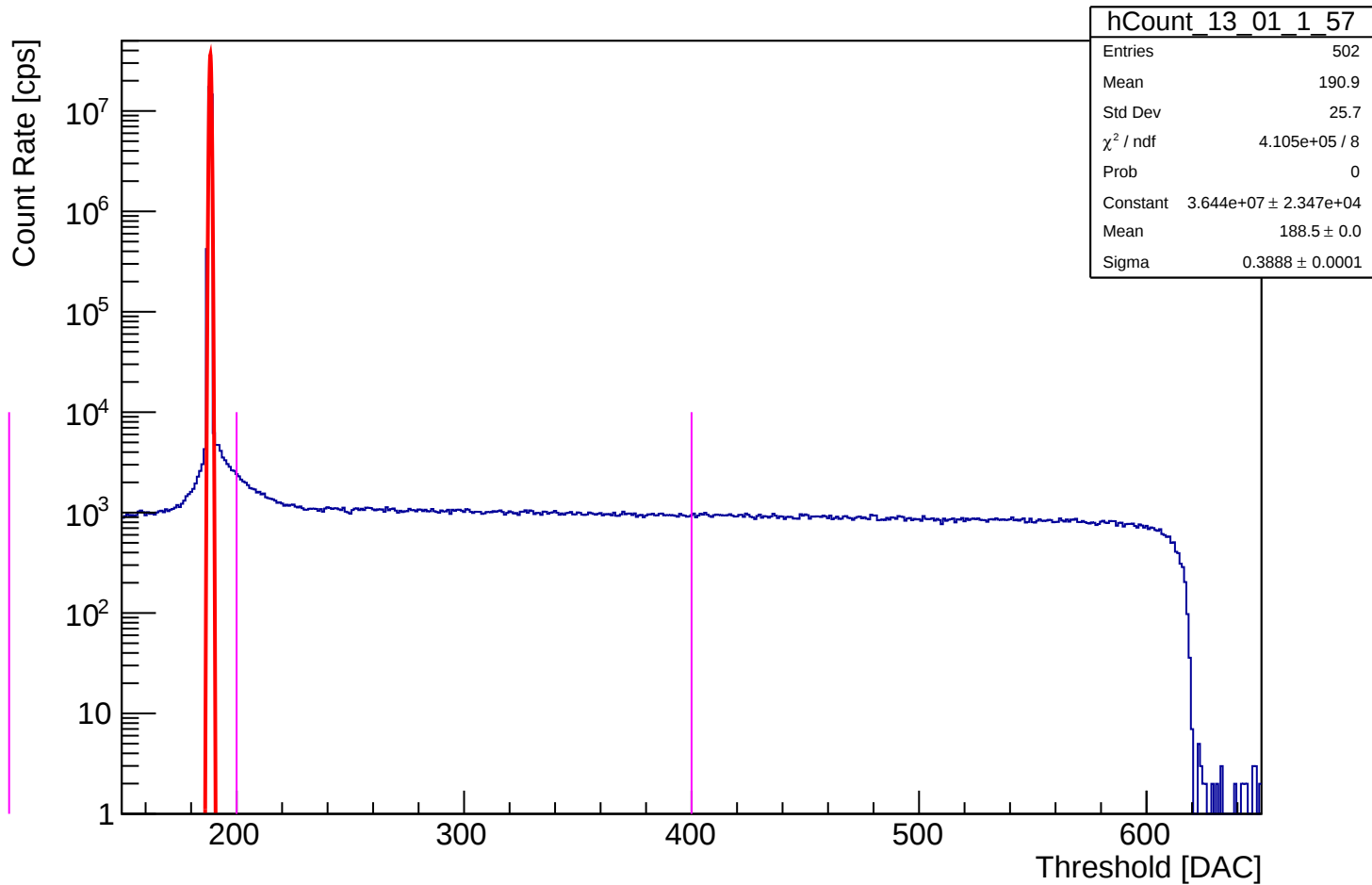
Row 1 Tile 1 Pmt 5 Pixel 53 Slot 13 Fiber 1 Asic 1 Channel 56



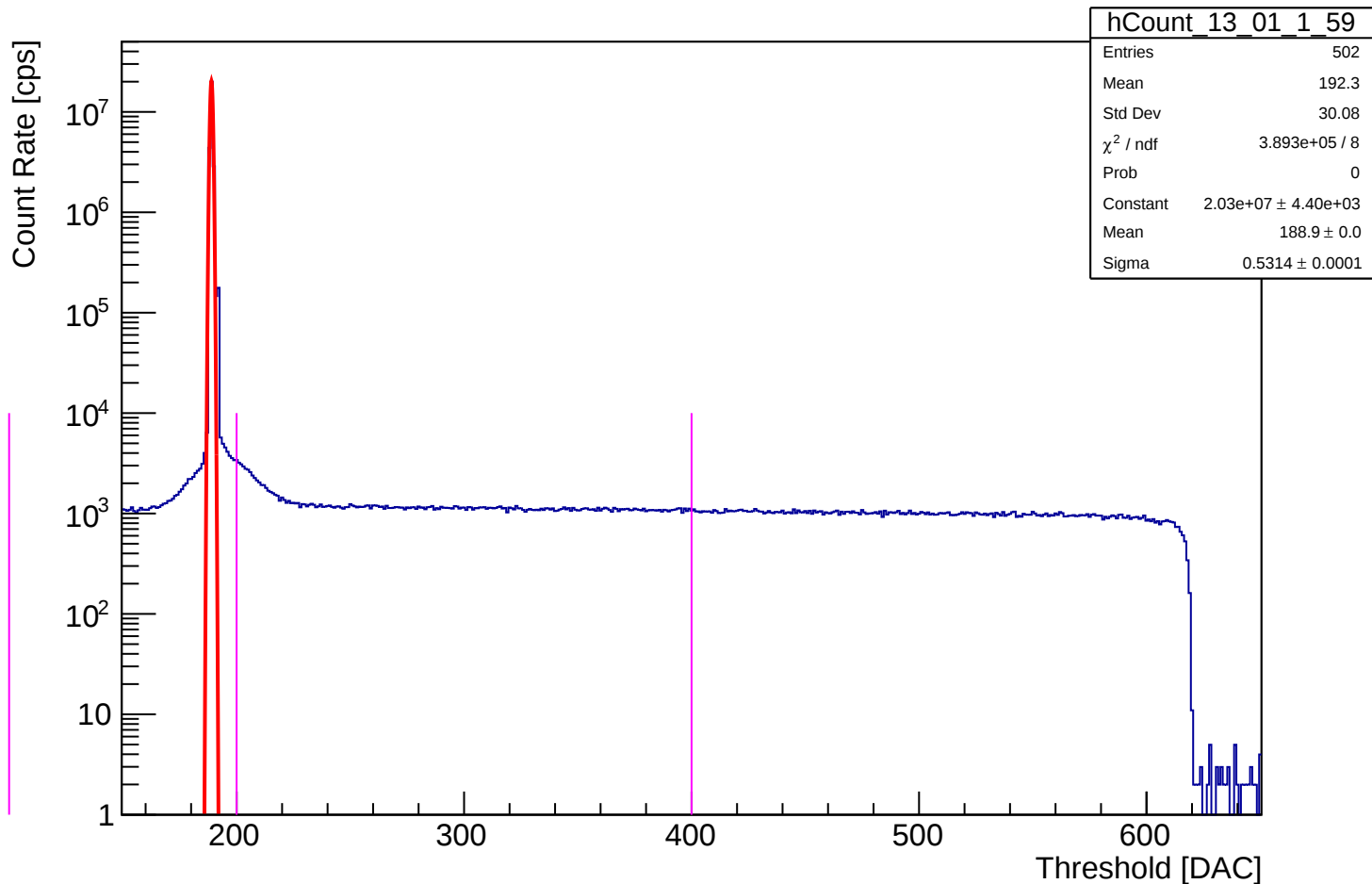
Row 1 Tile 1 Pmt 5 Pixel 54 Slot 13 Fiber 1 Asic 1 Channel 58



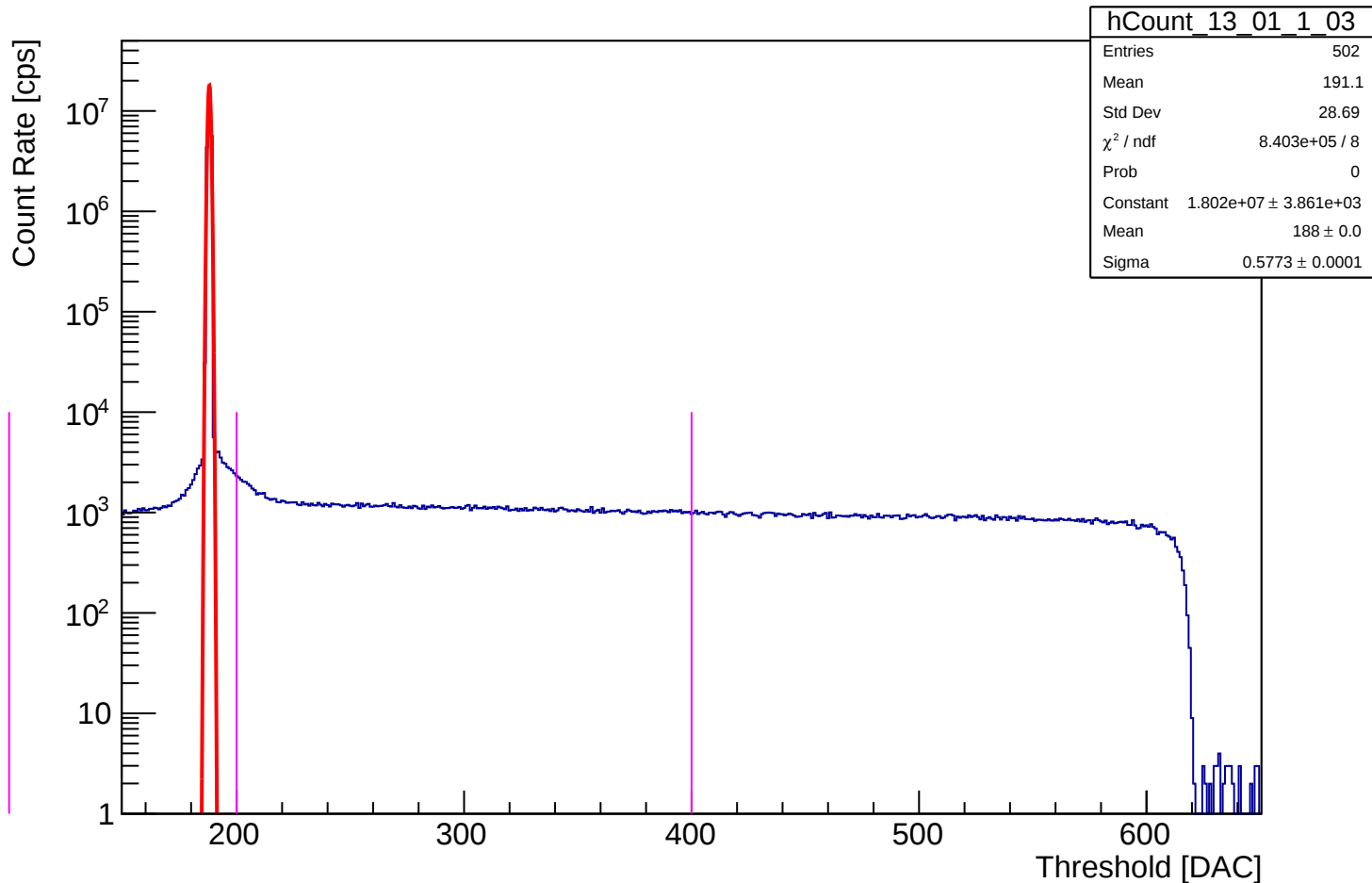
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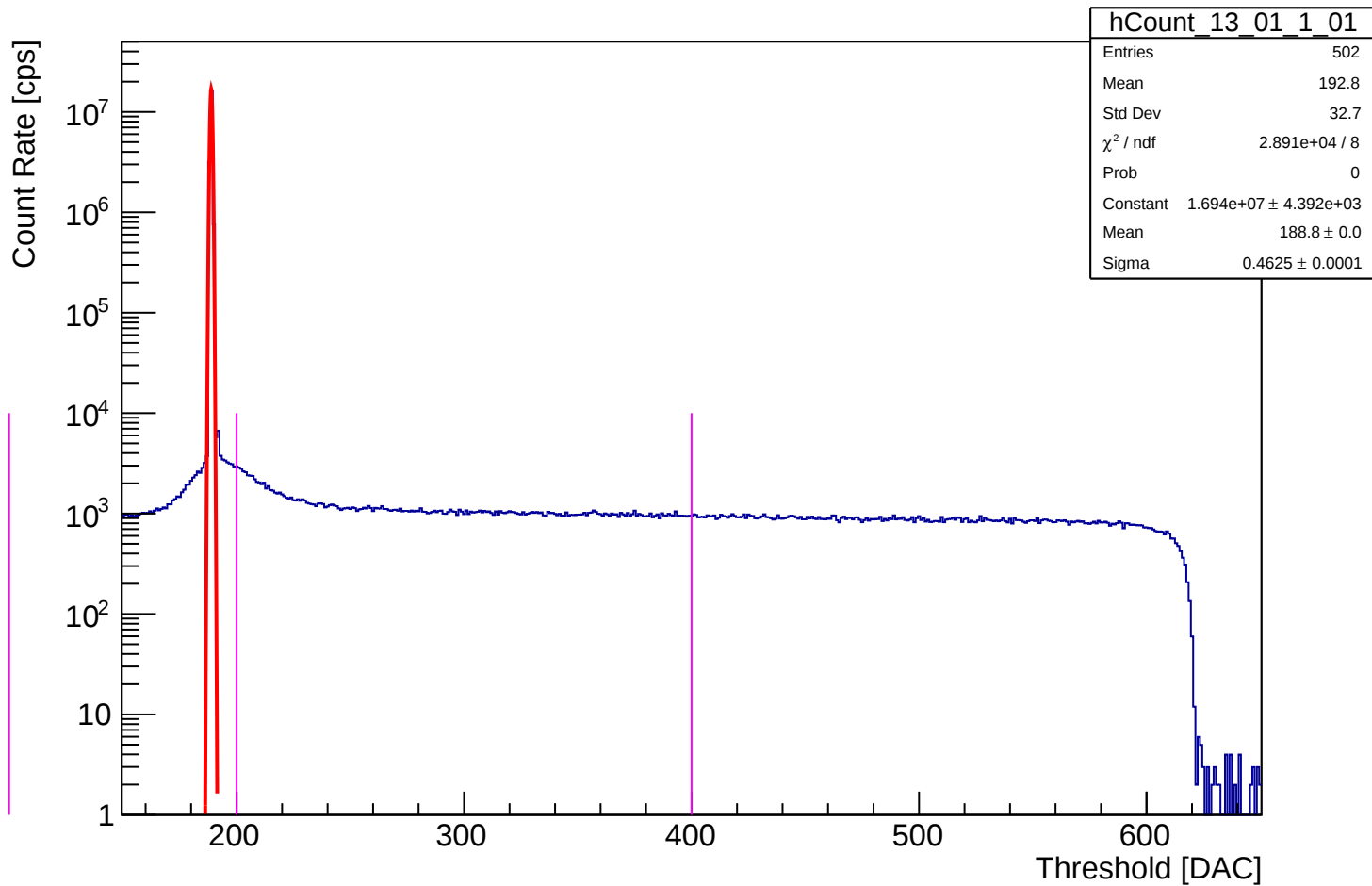
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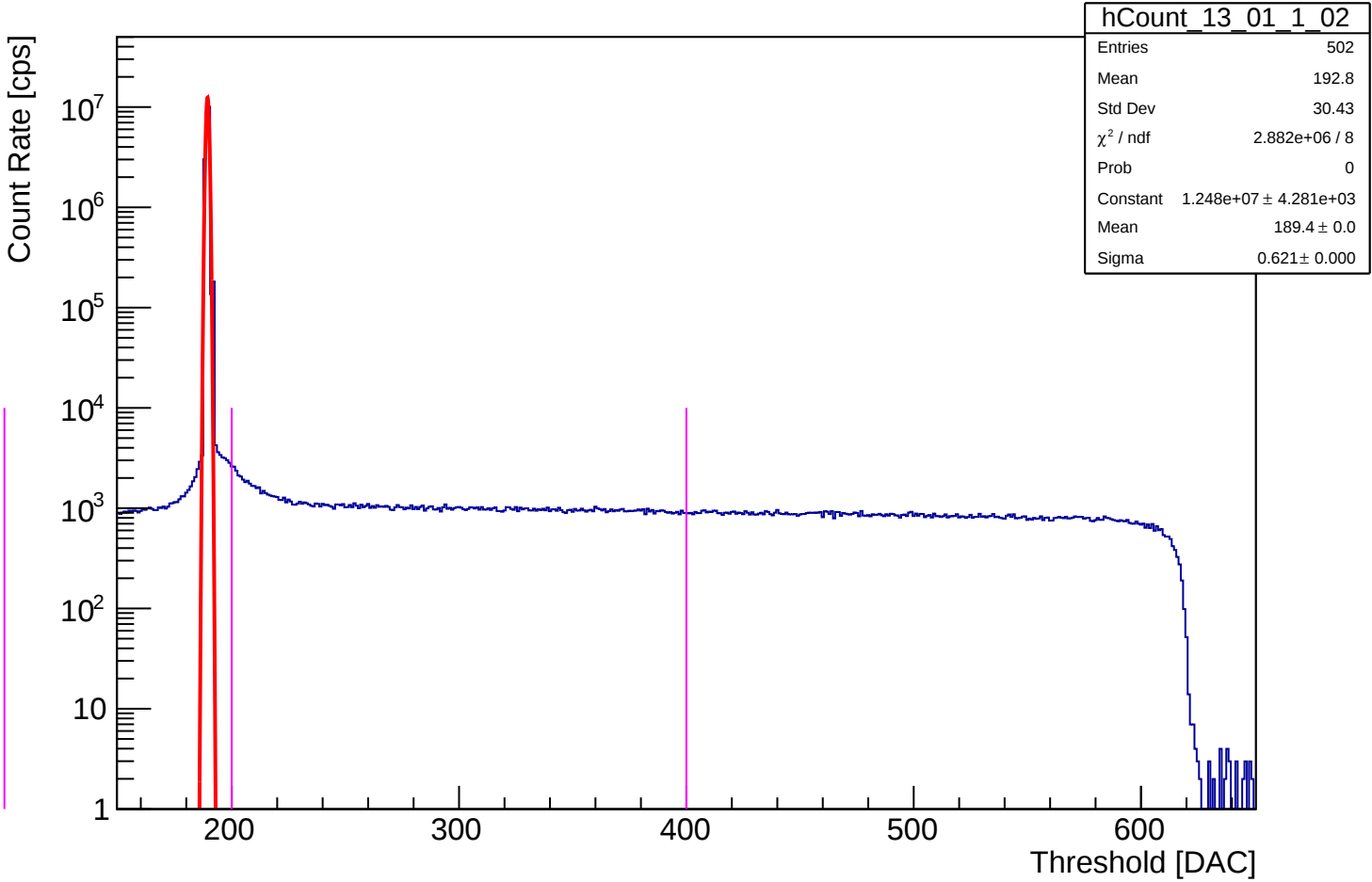


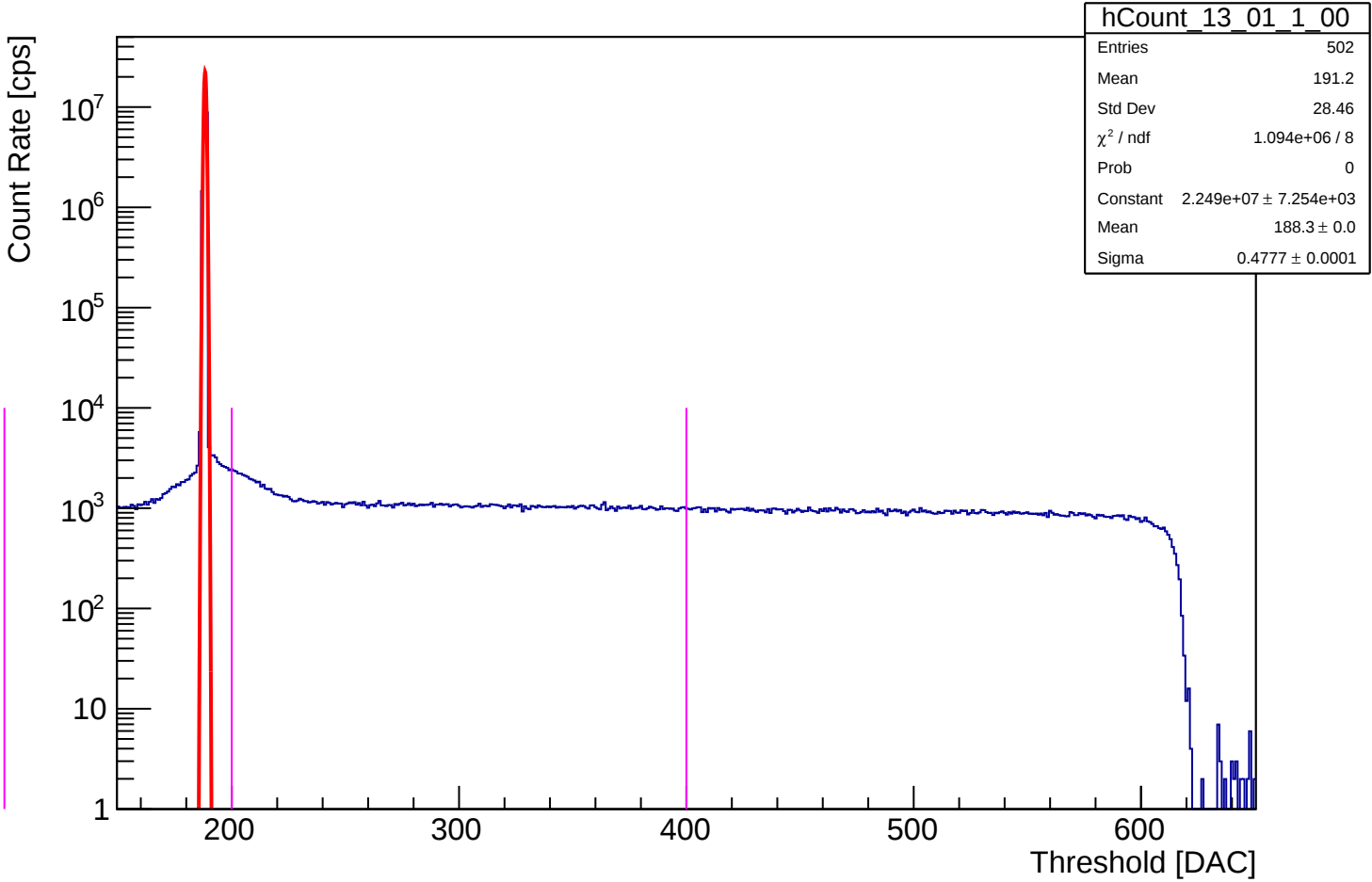
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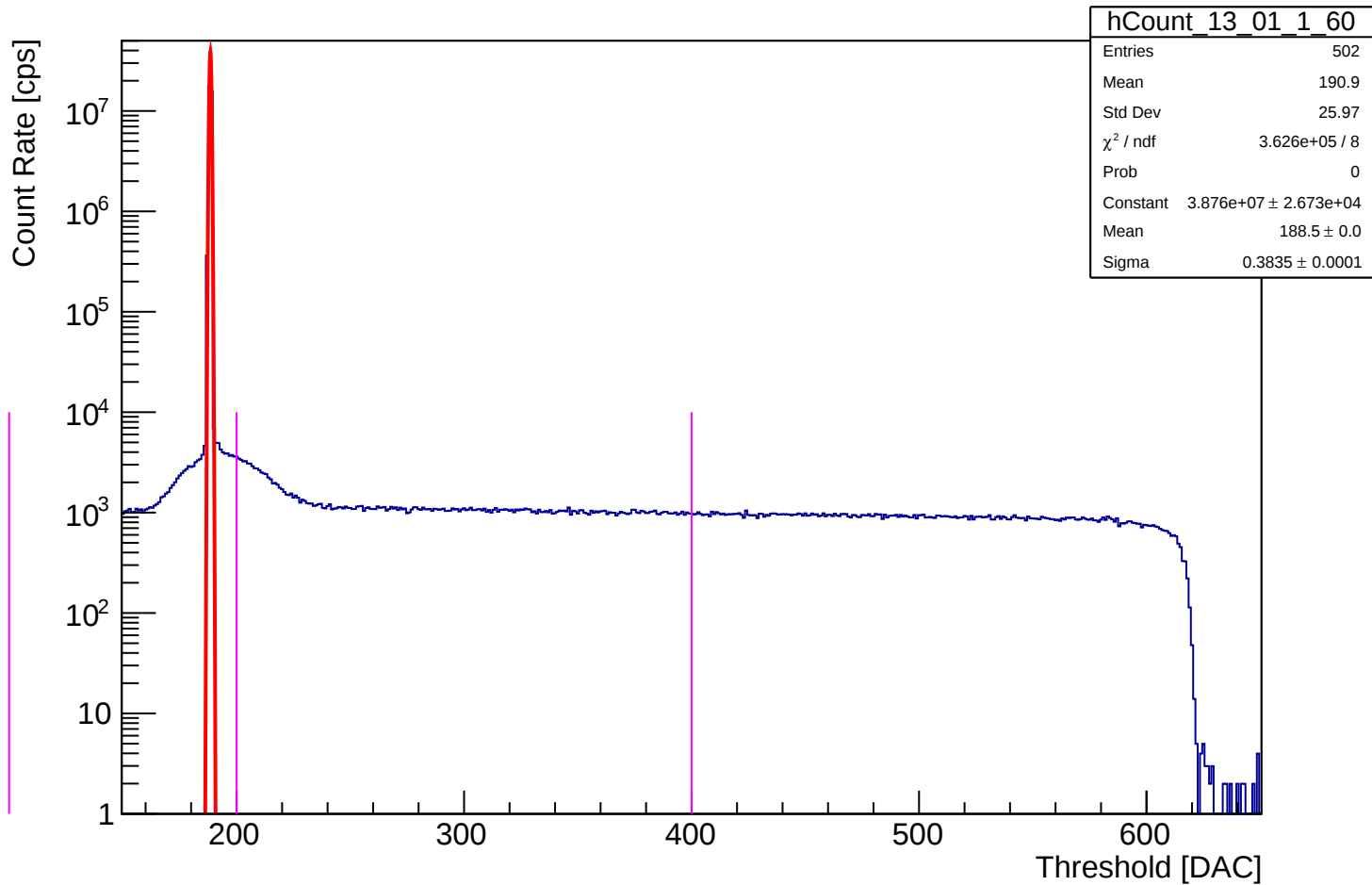
Row 1 Tile 1 Pmt 5 Pixel 58 Slot 13 Fiber 1 Asic 1 Channel 1



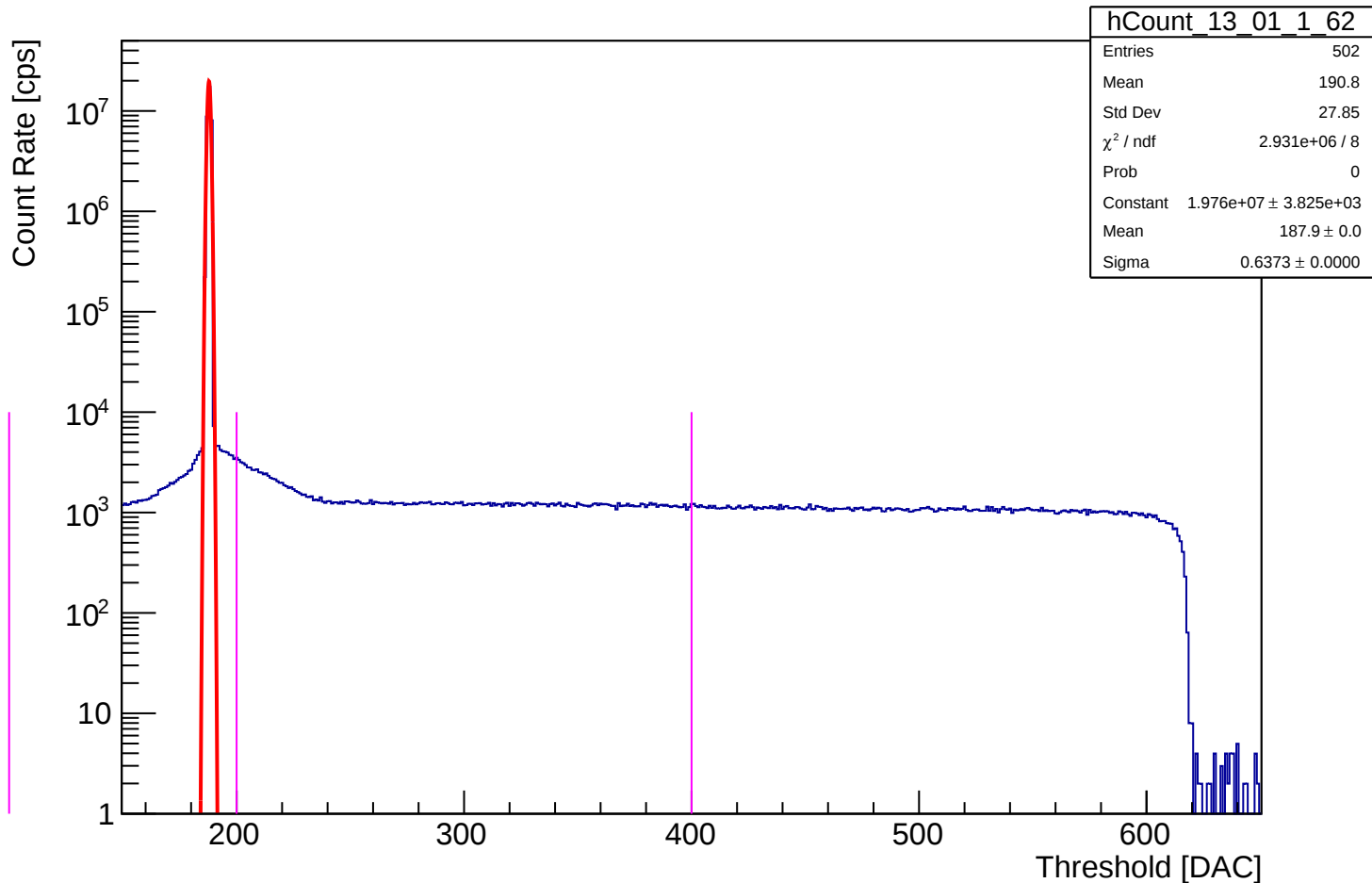




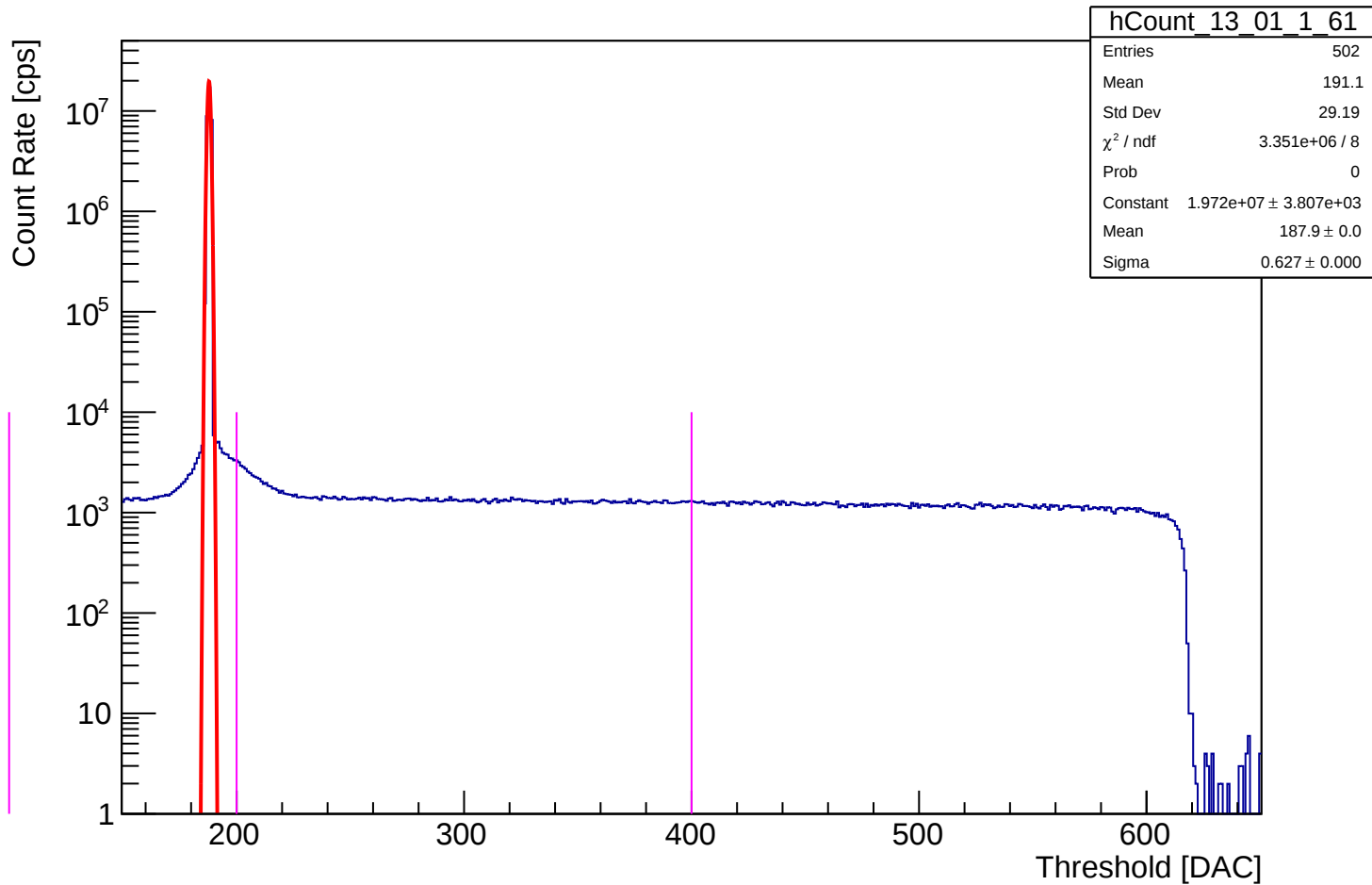
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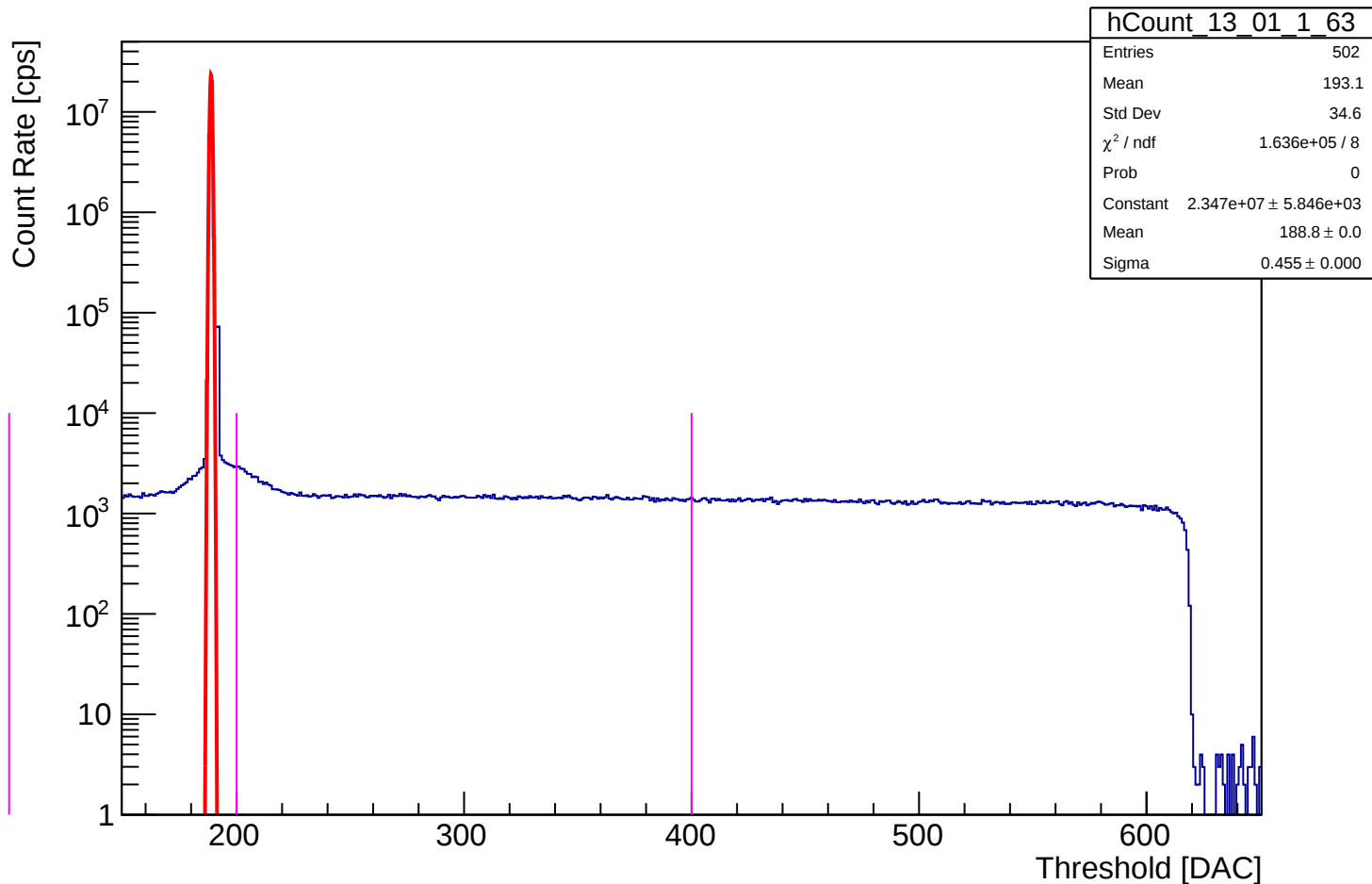
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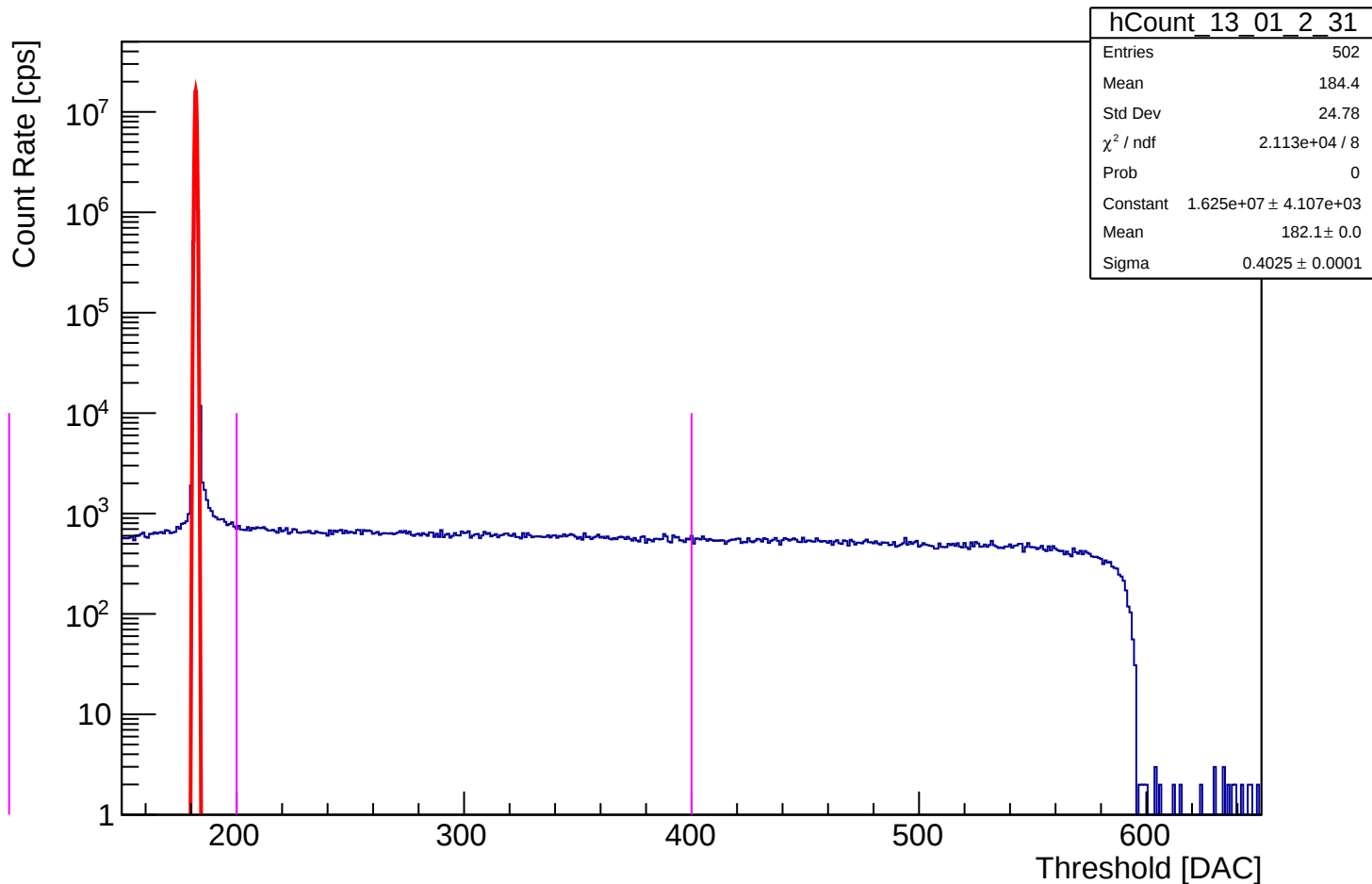
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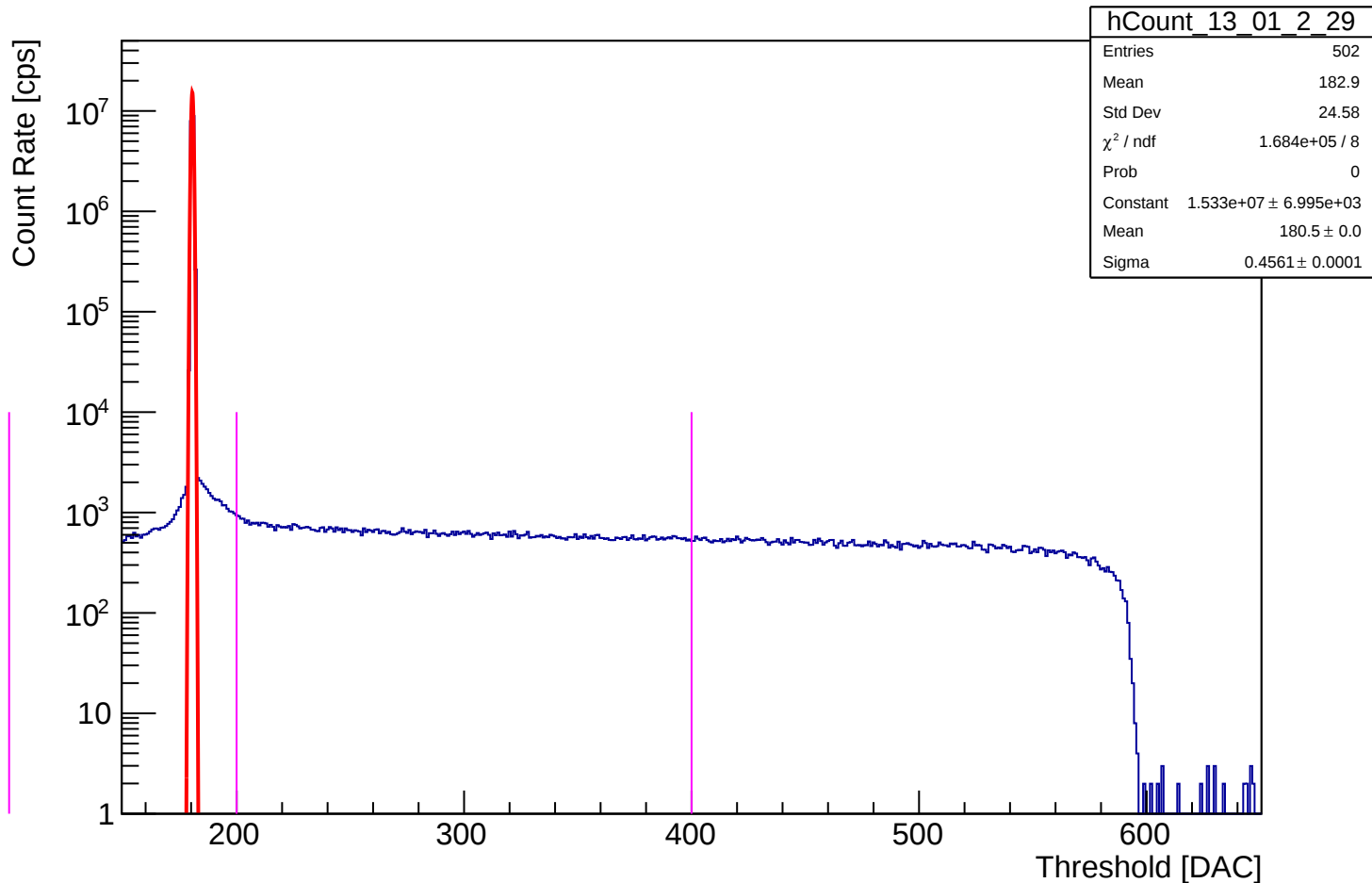
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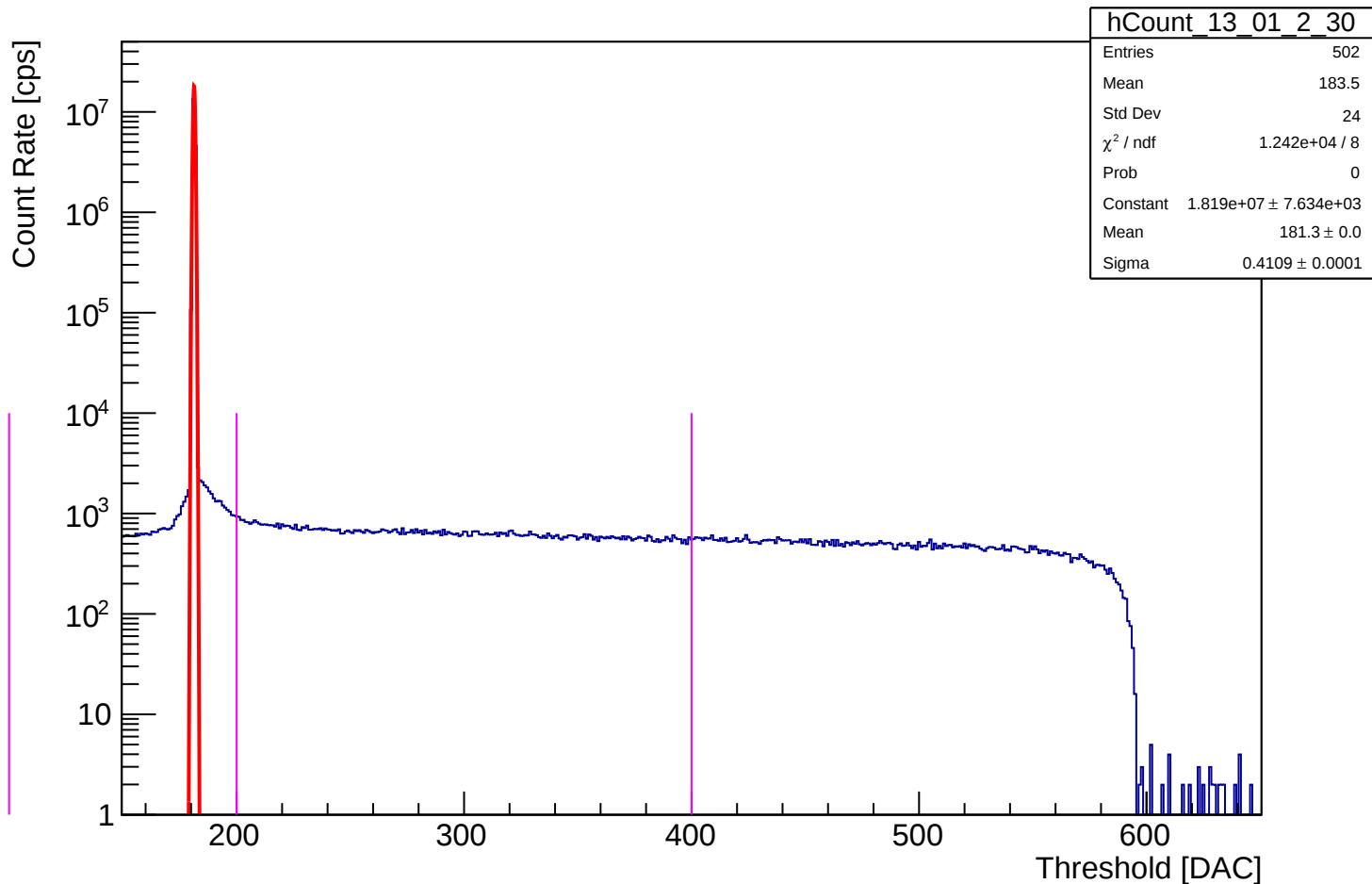
Row 1 Tile 1 Pmt 6 Pixel 1 Slot 13 Fiber 1 Asic 2 Channel 31



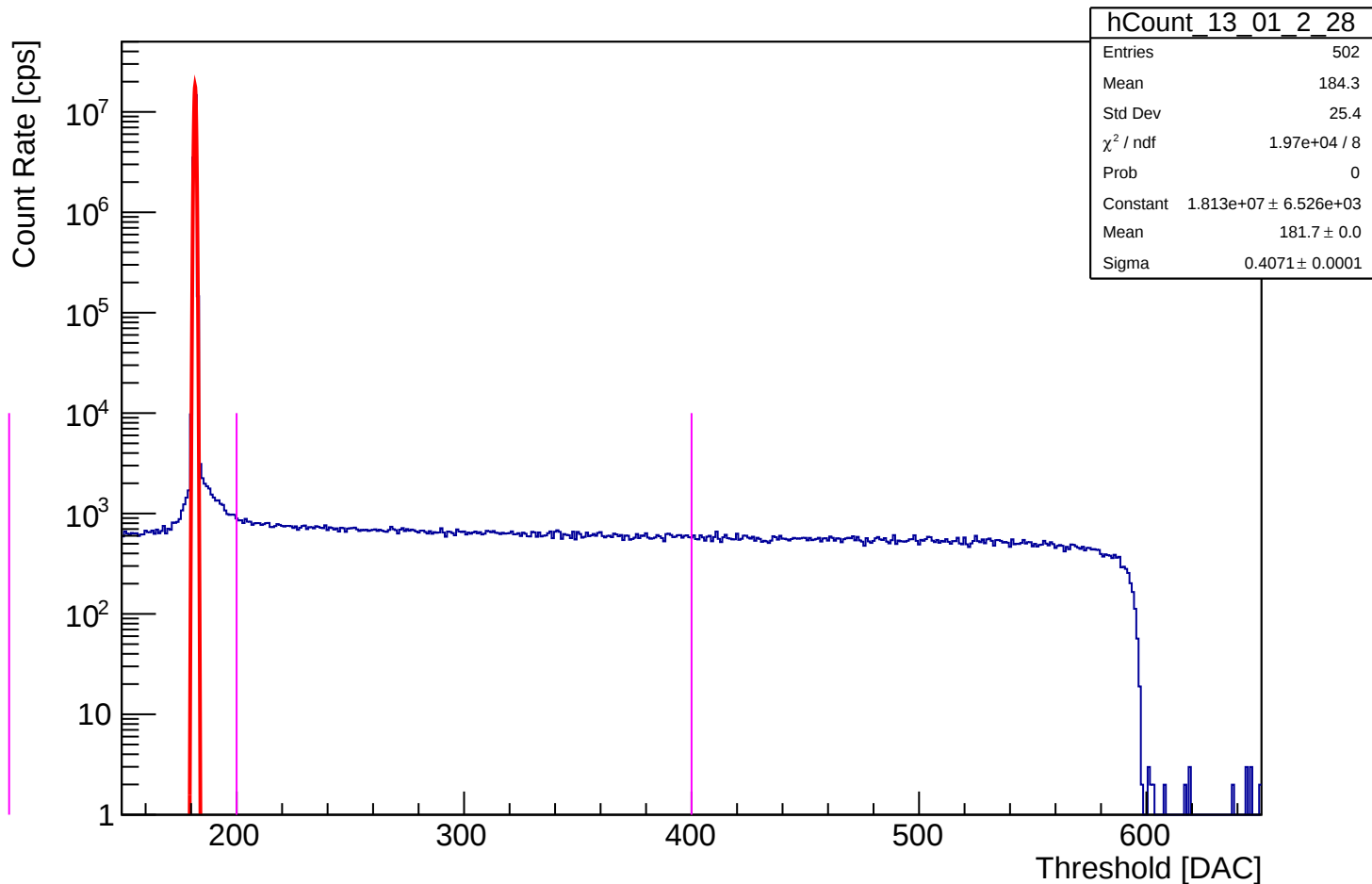
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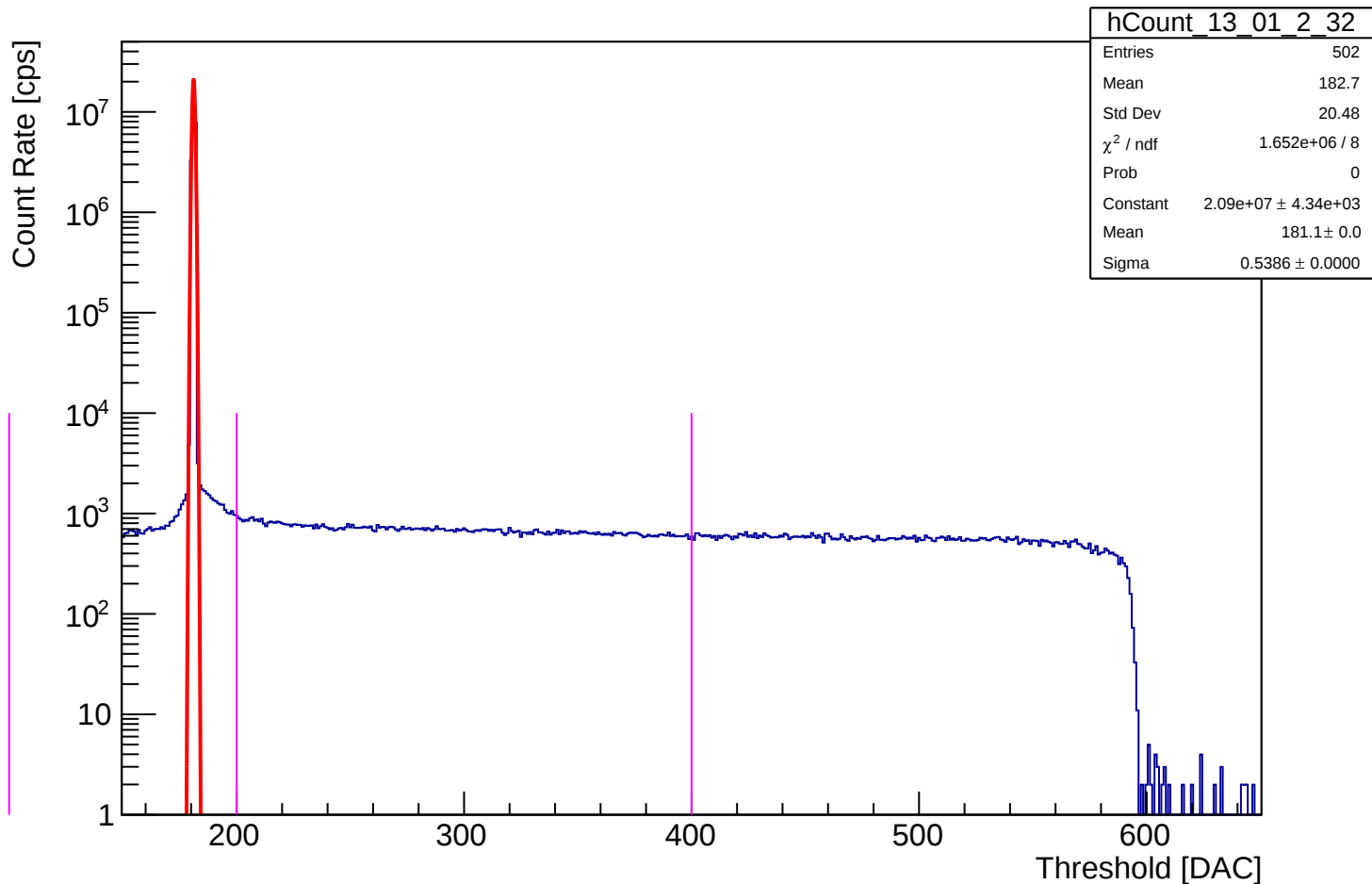
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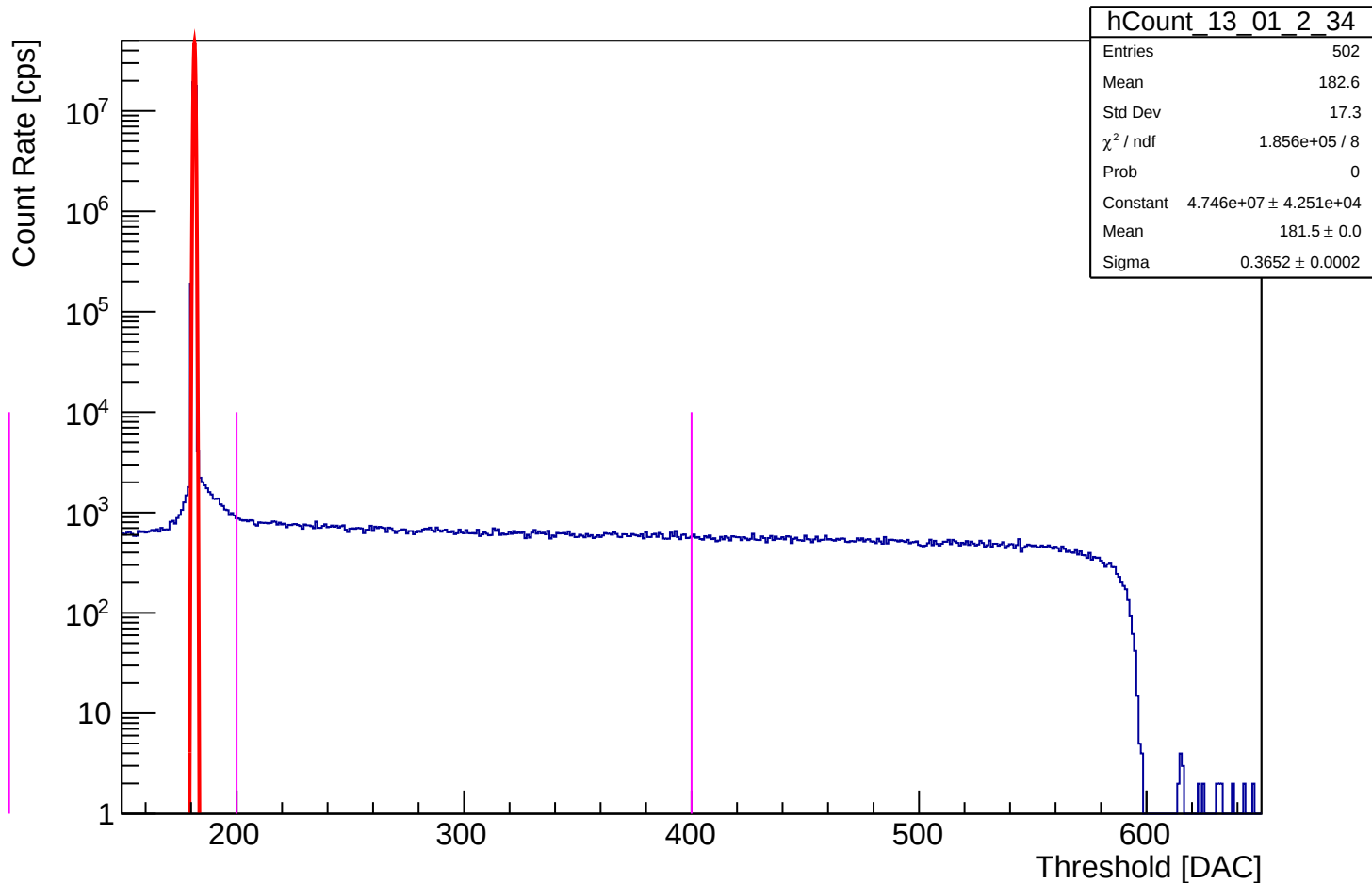
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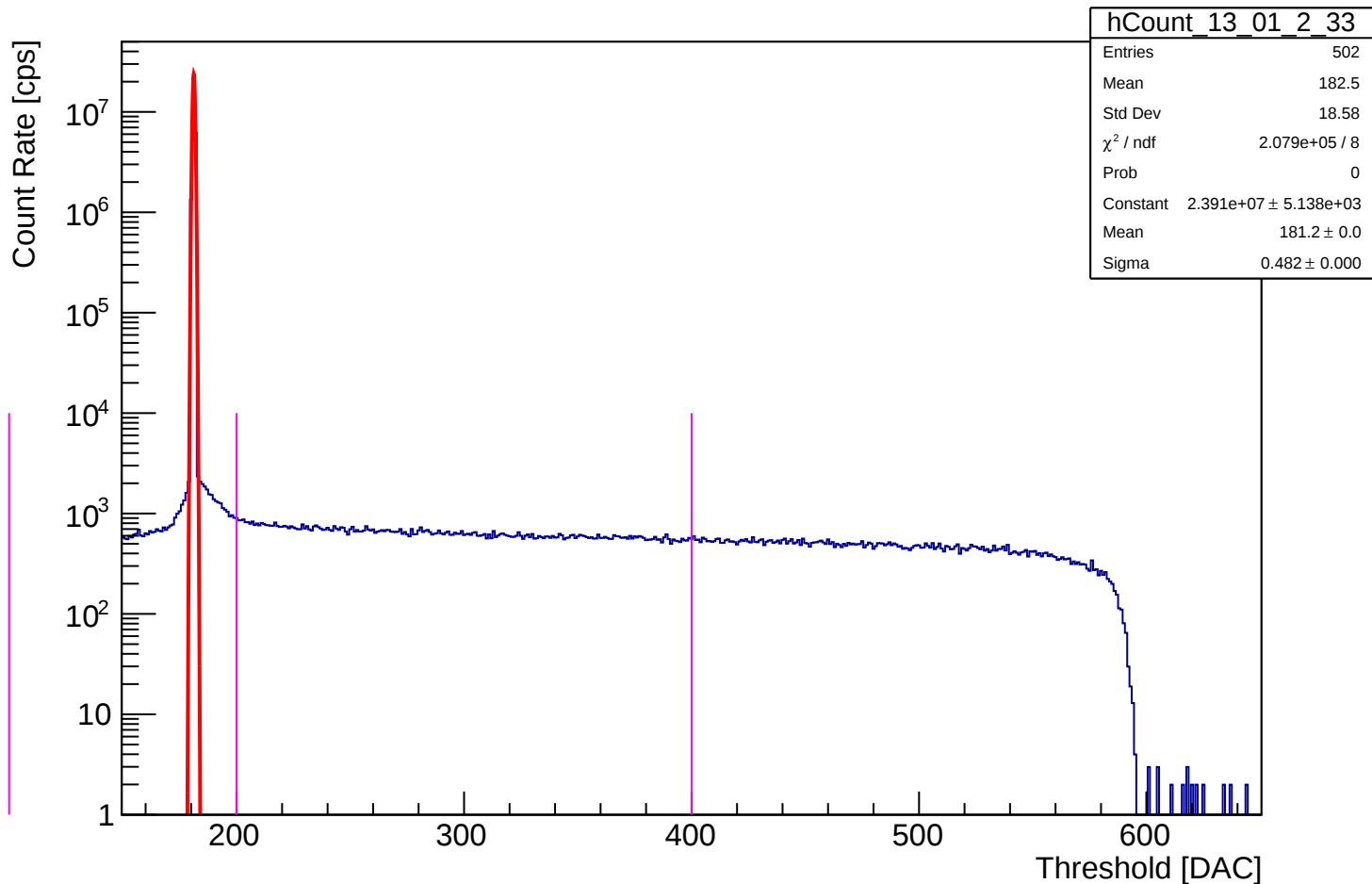
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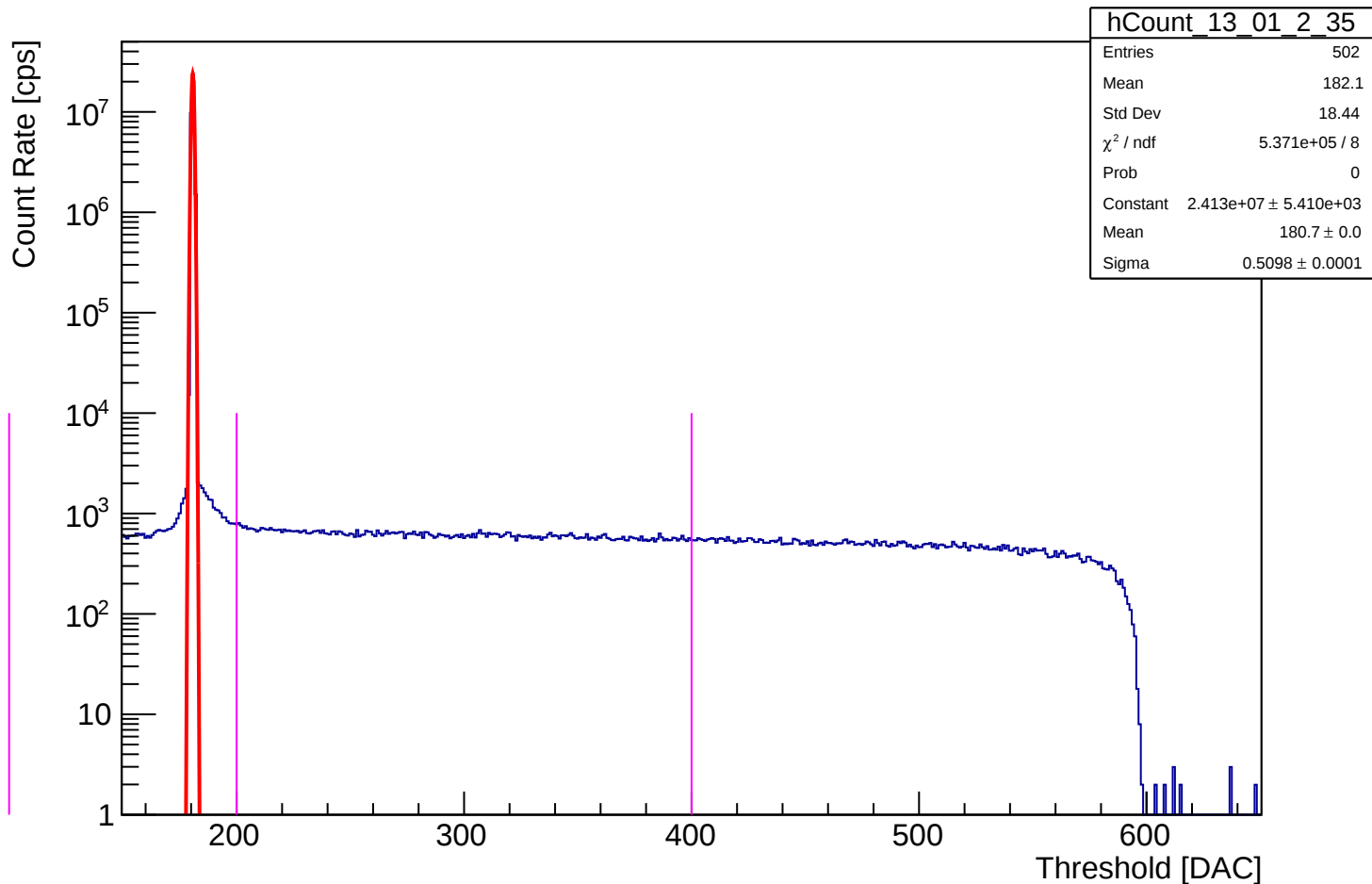
Row 1 Tile 1 Pmt 6 Pixel 6 Slot 13 Fiber 1 Asic 2 Channel 34



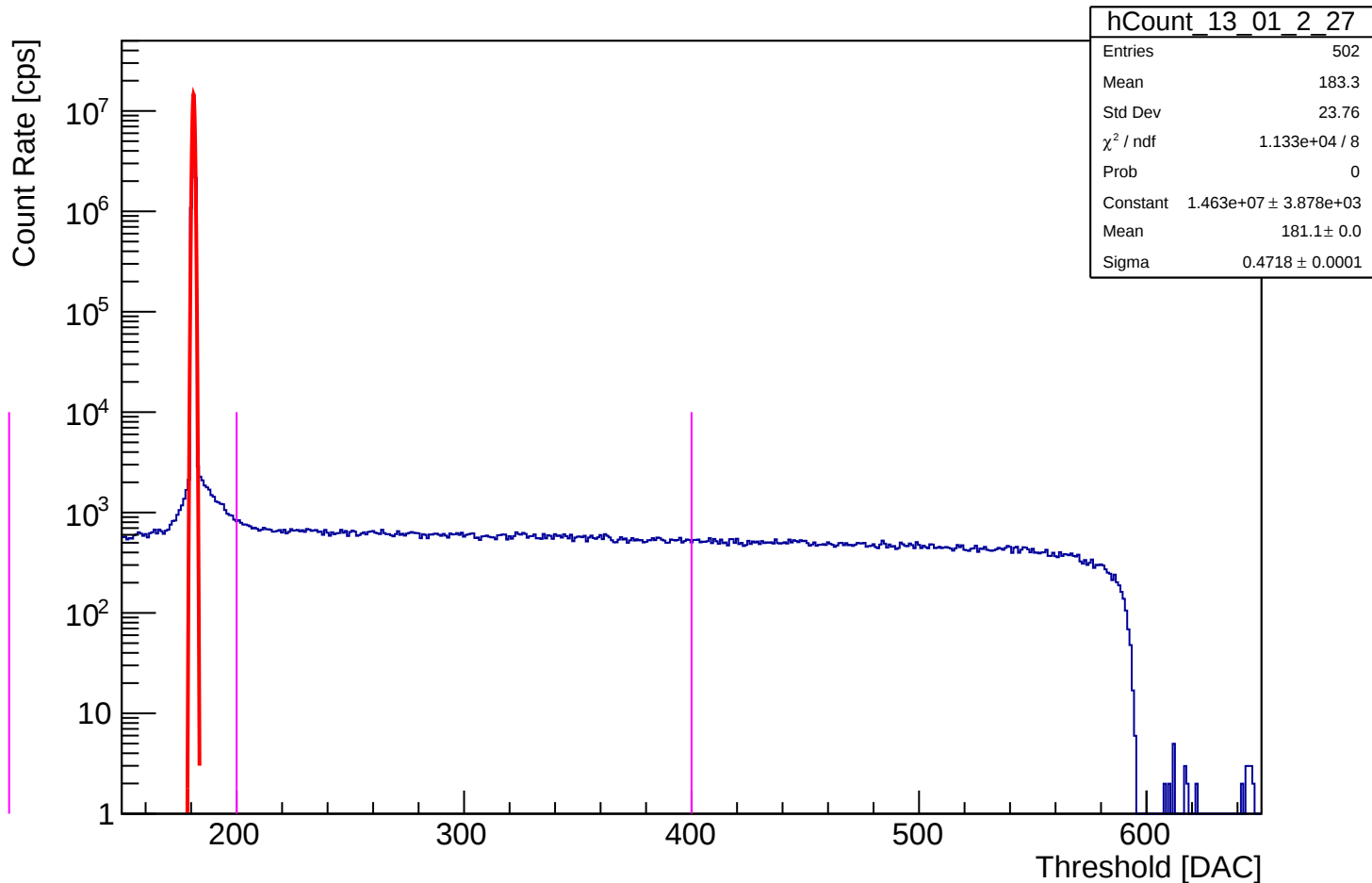
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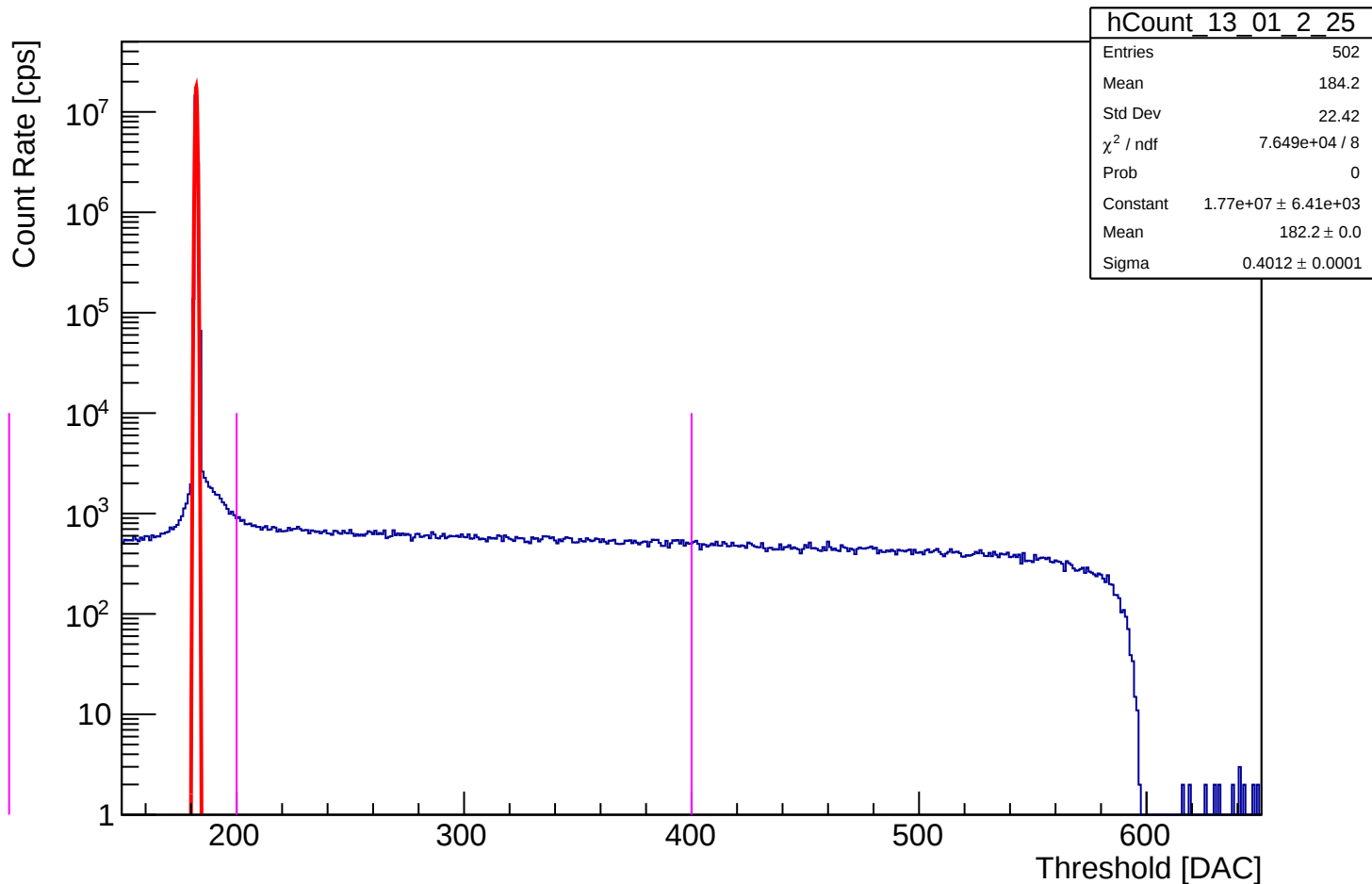
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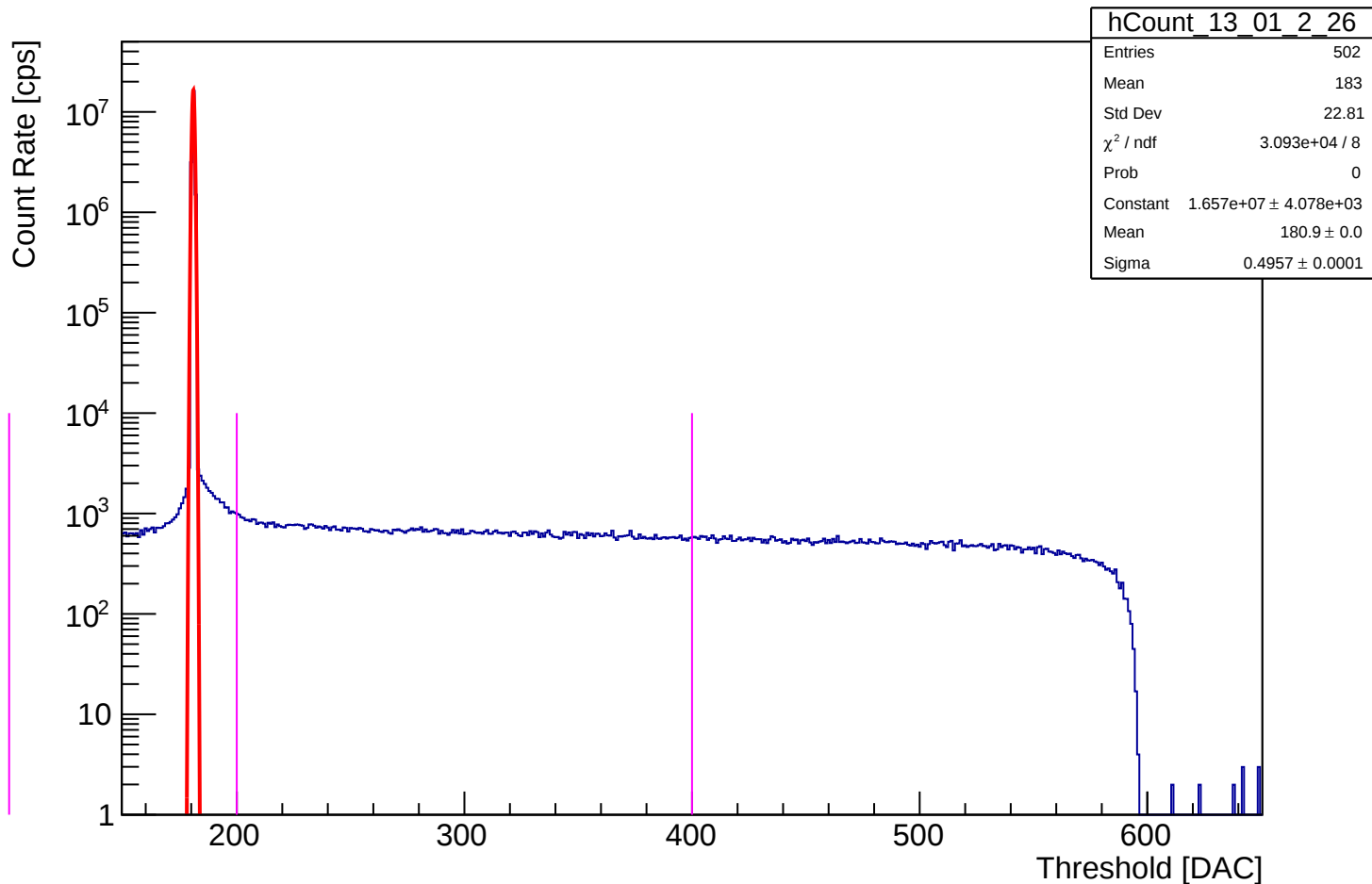
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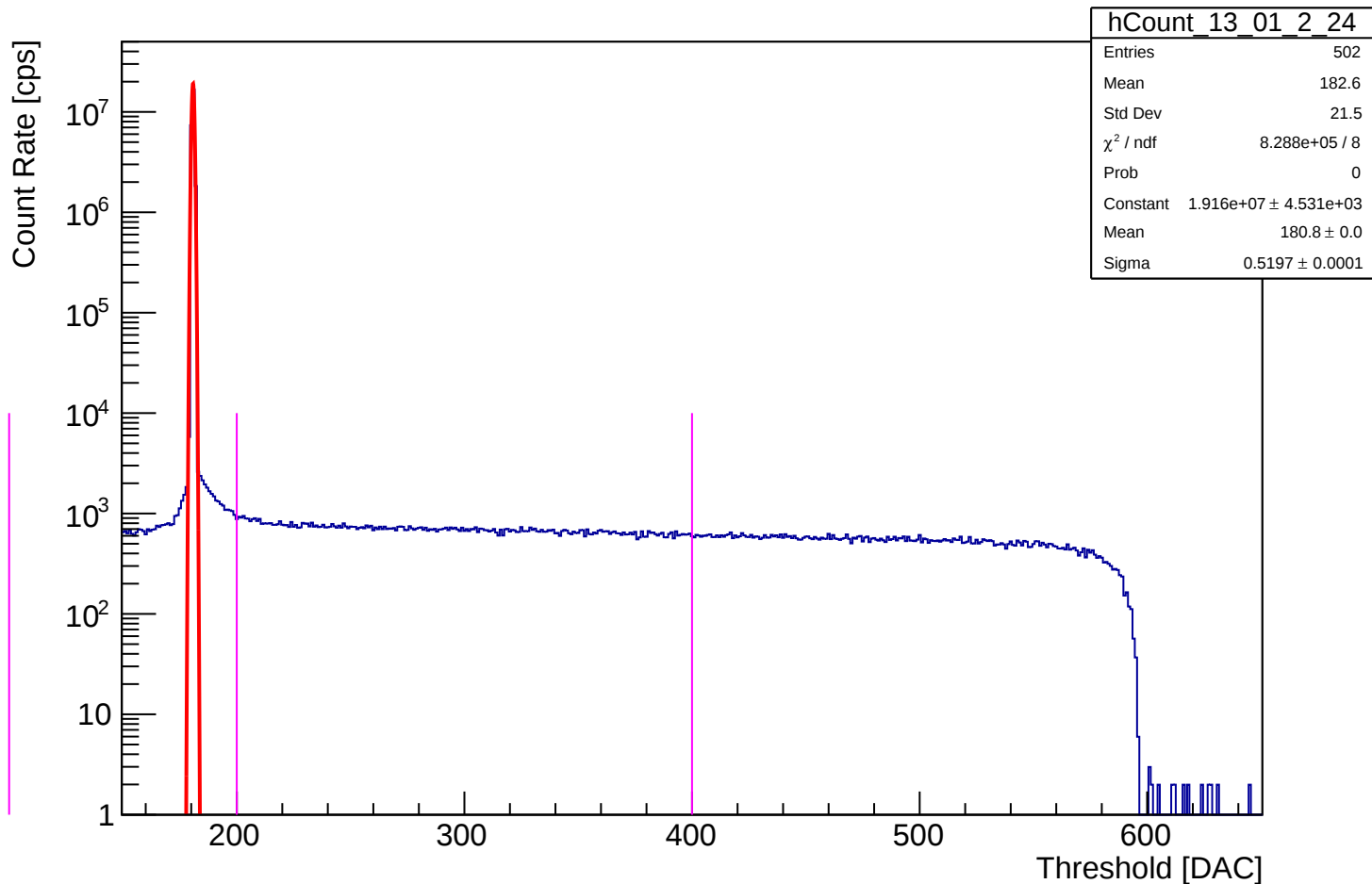
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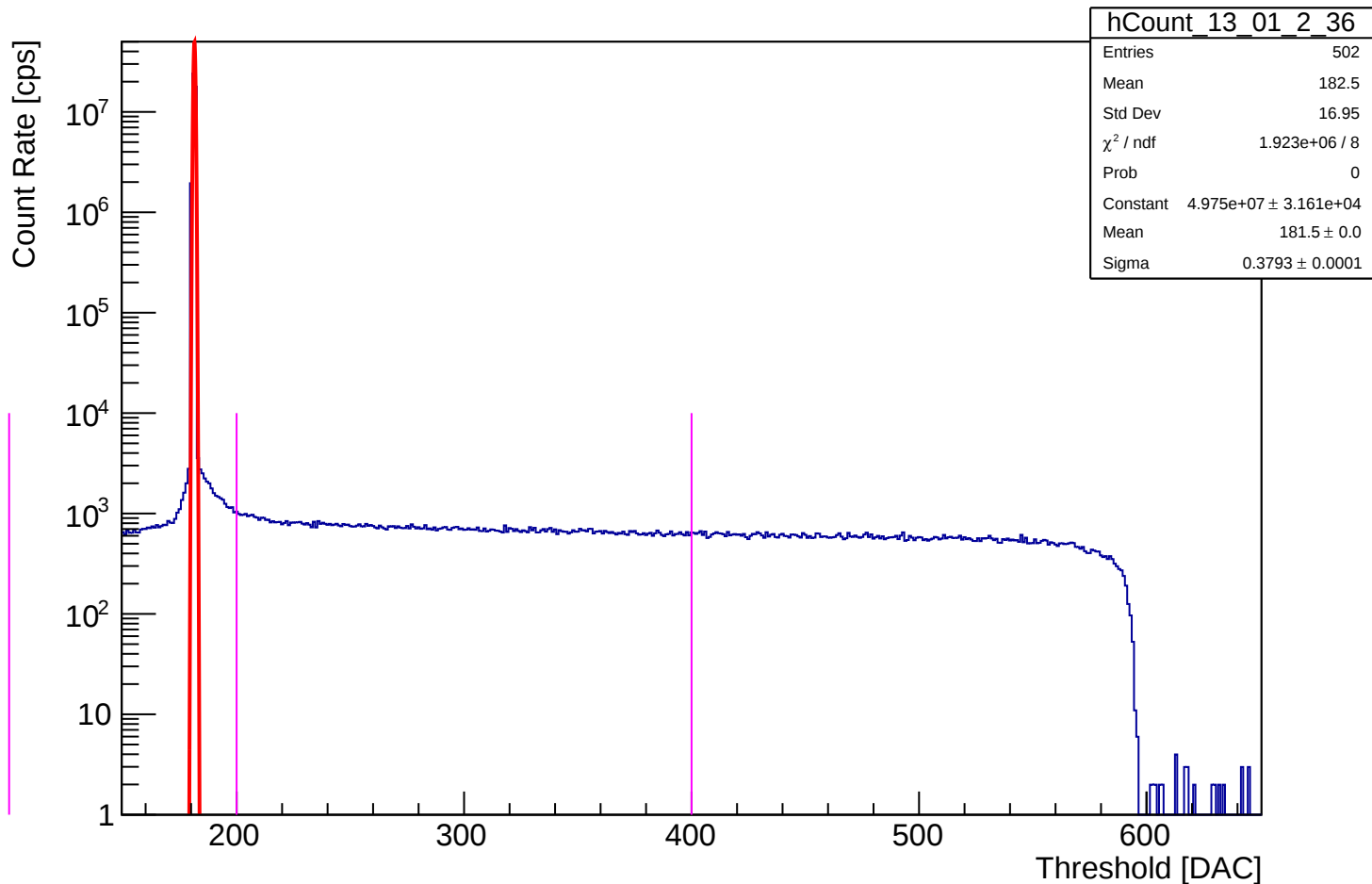
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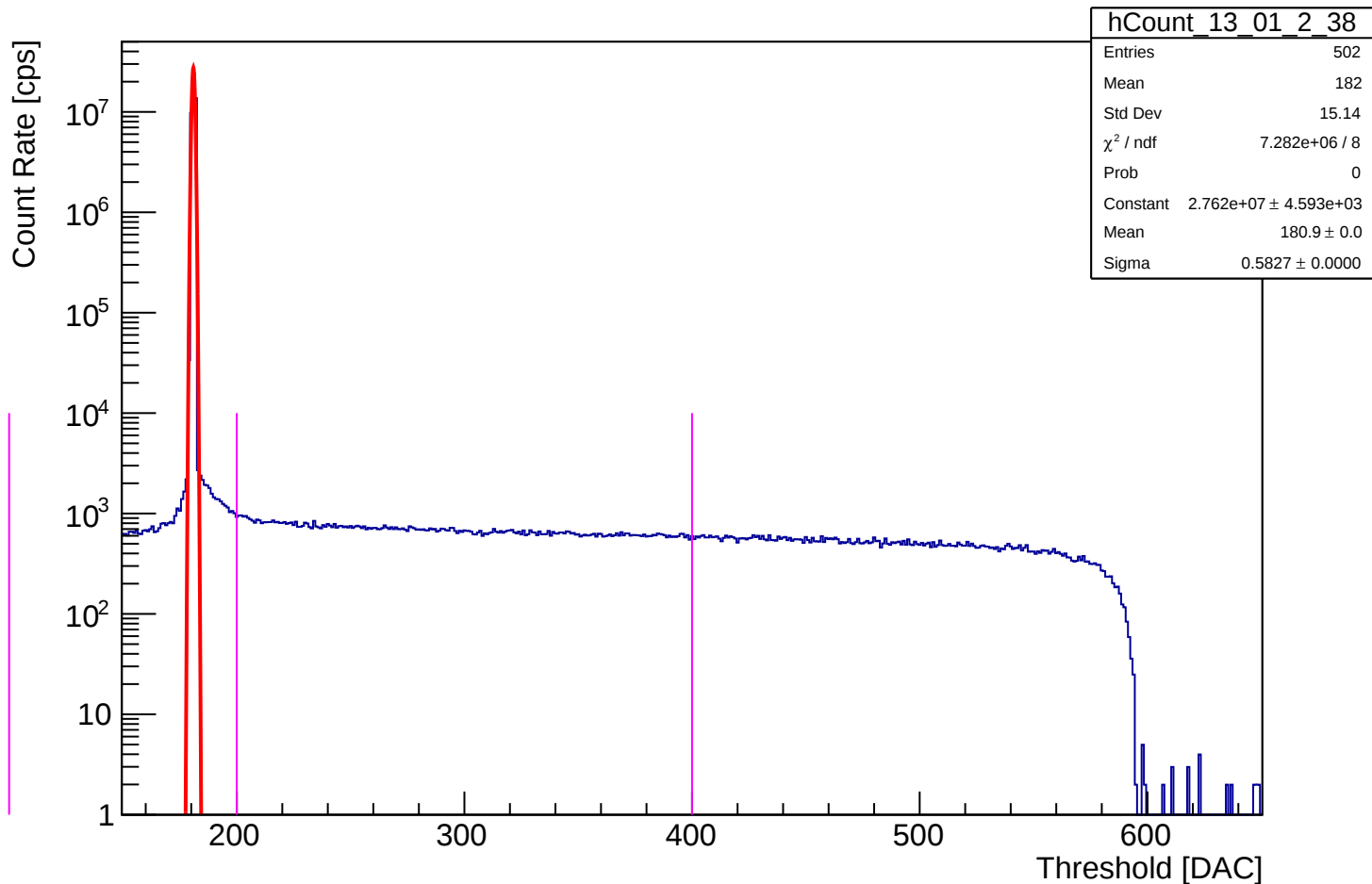
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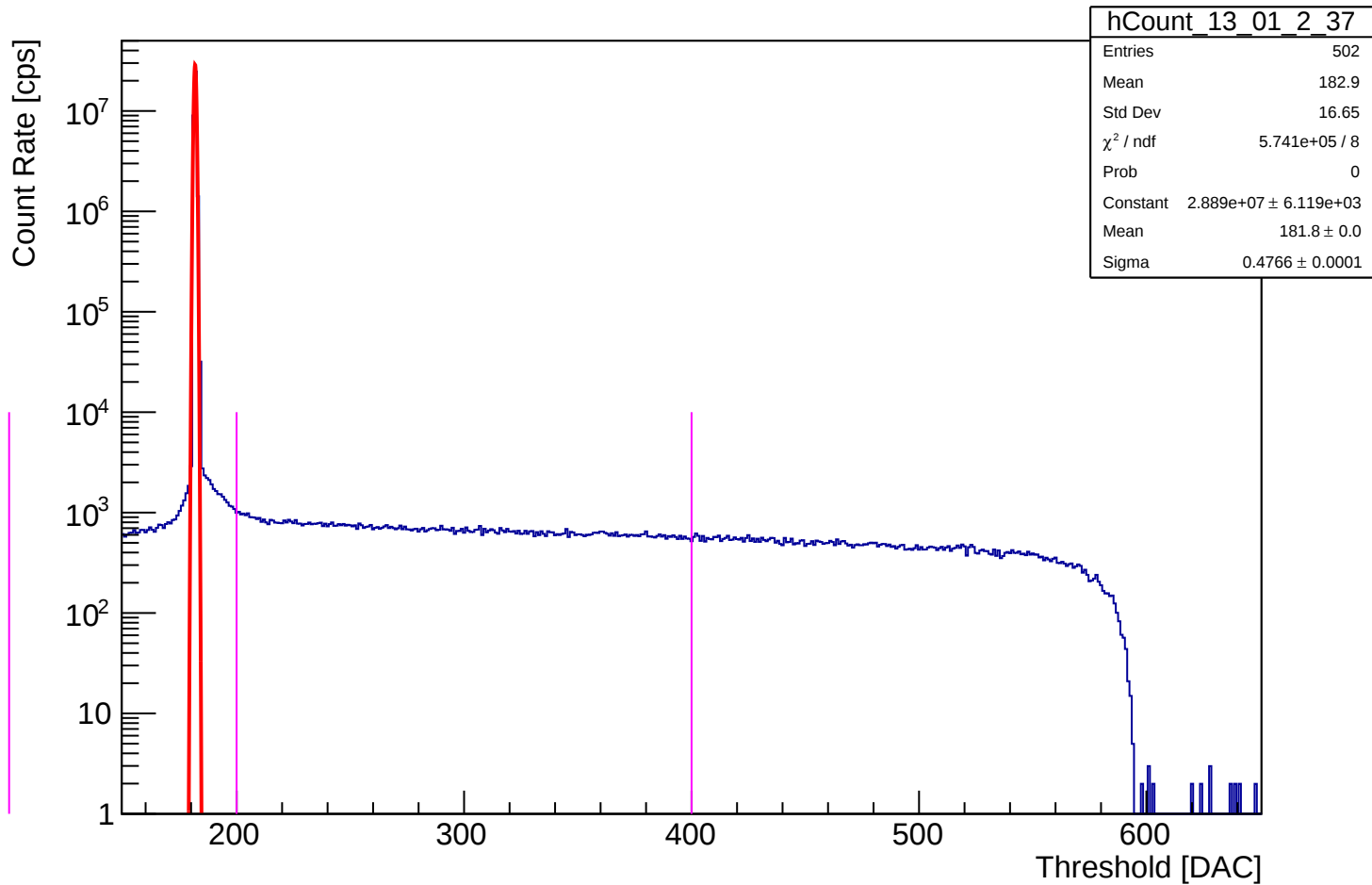
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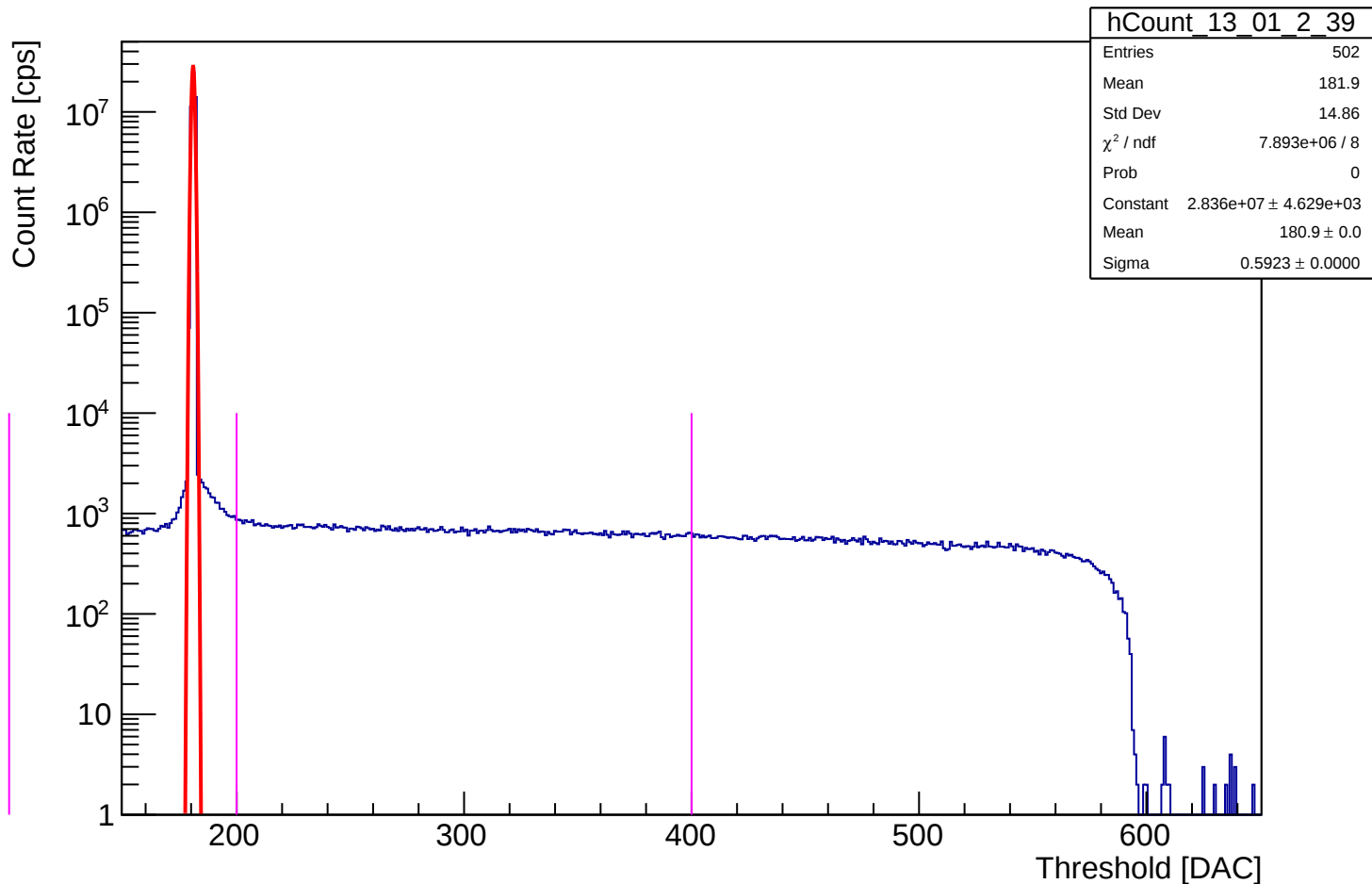
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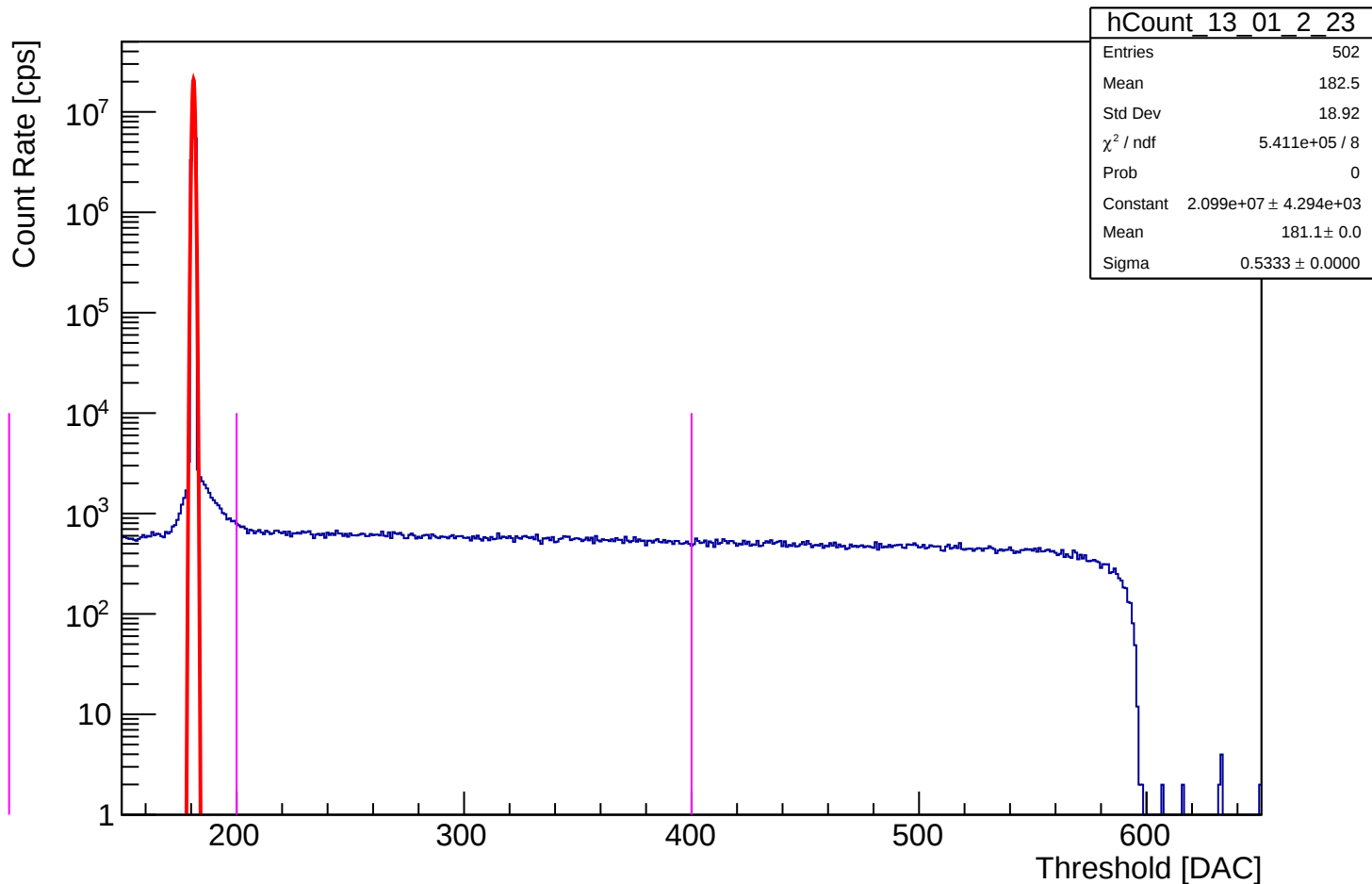
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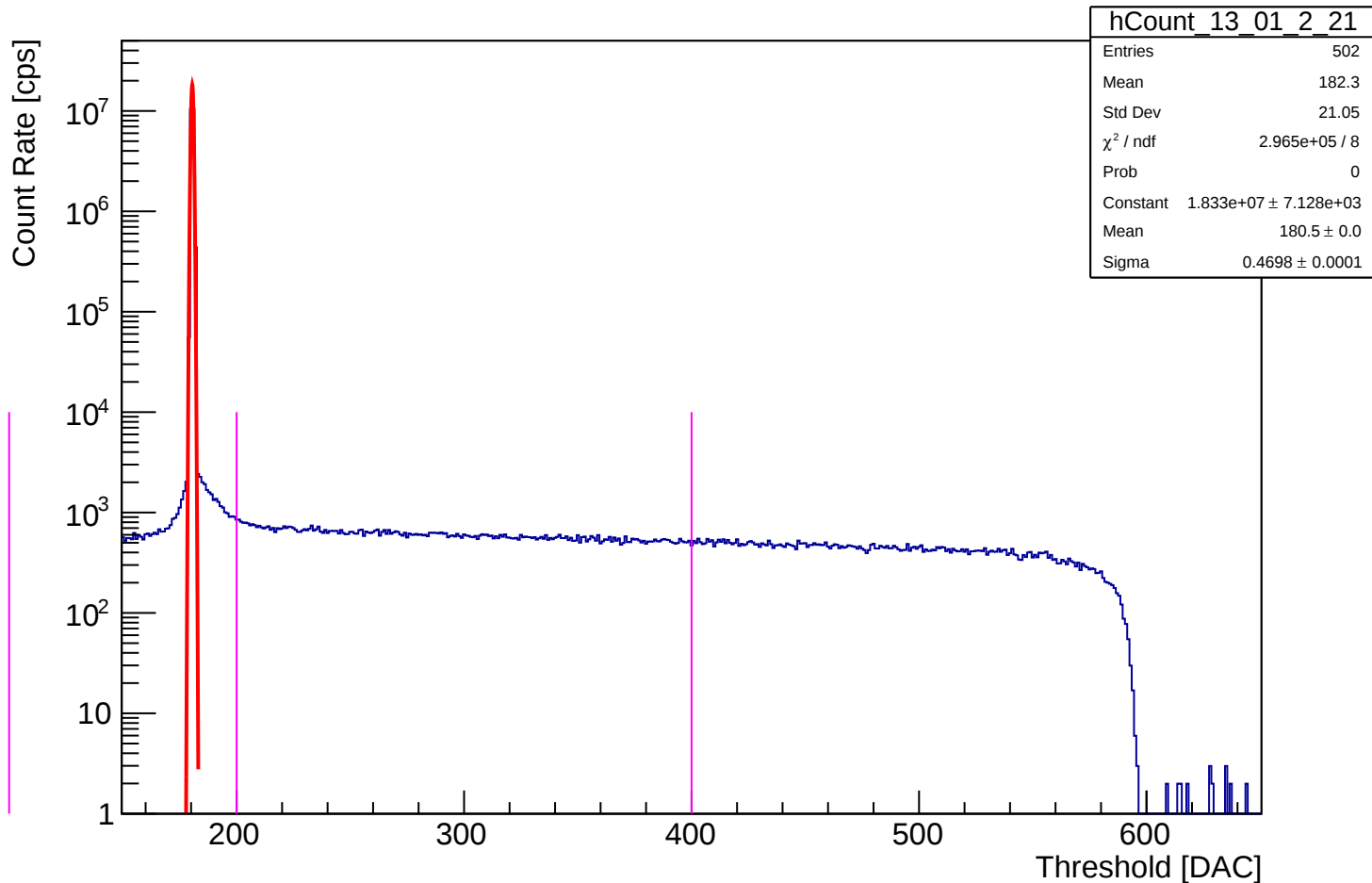
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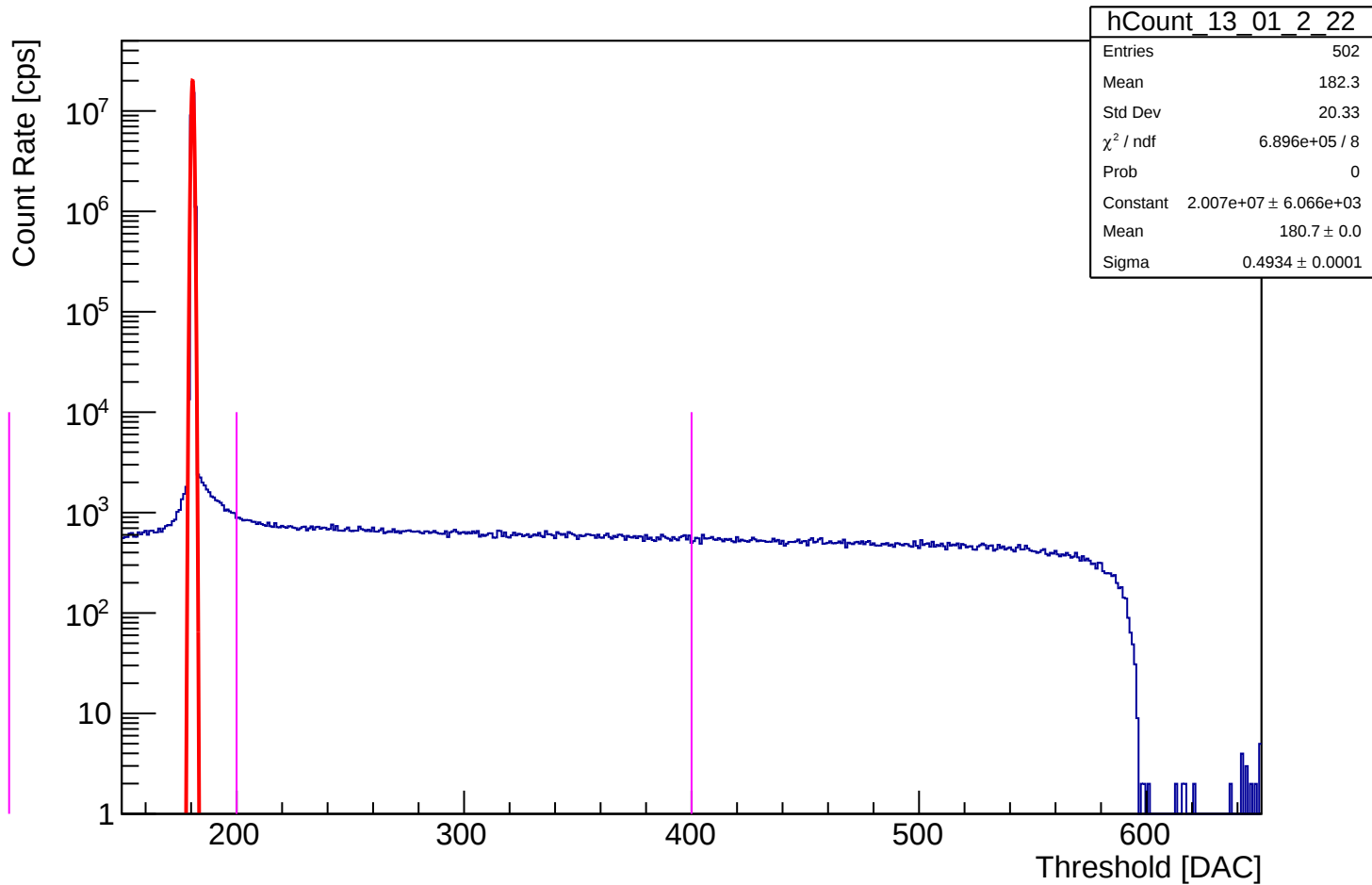
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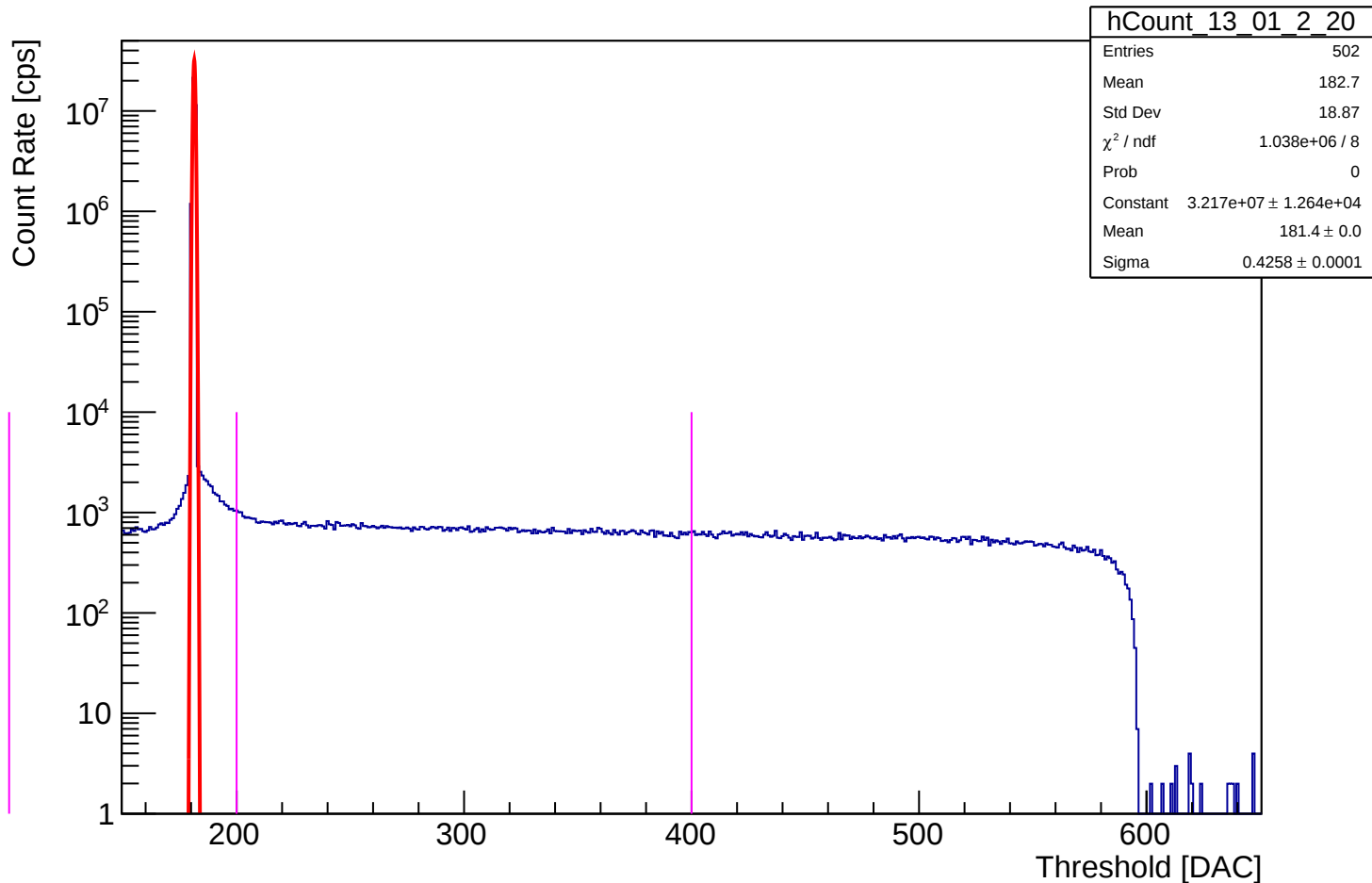
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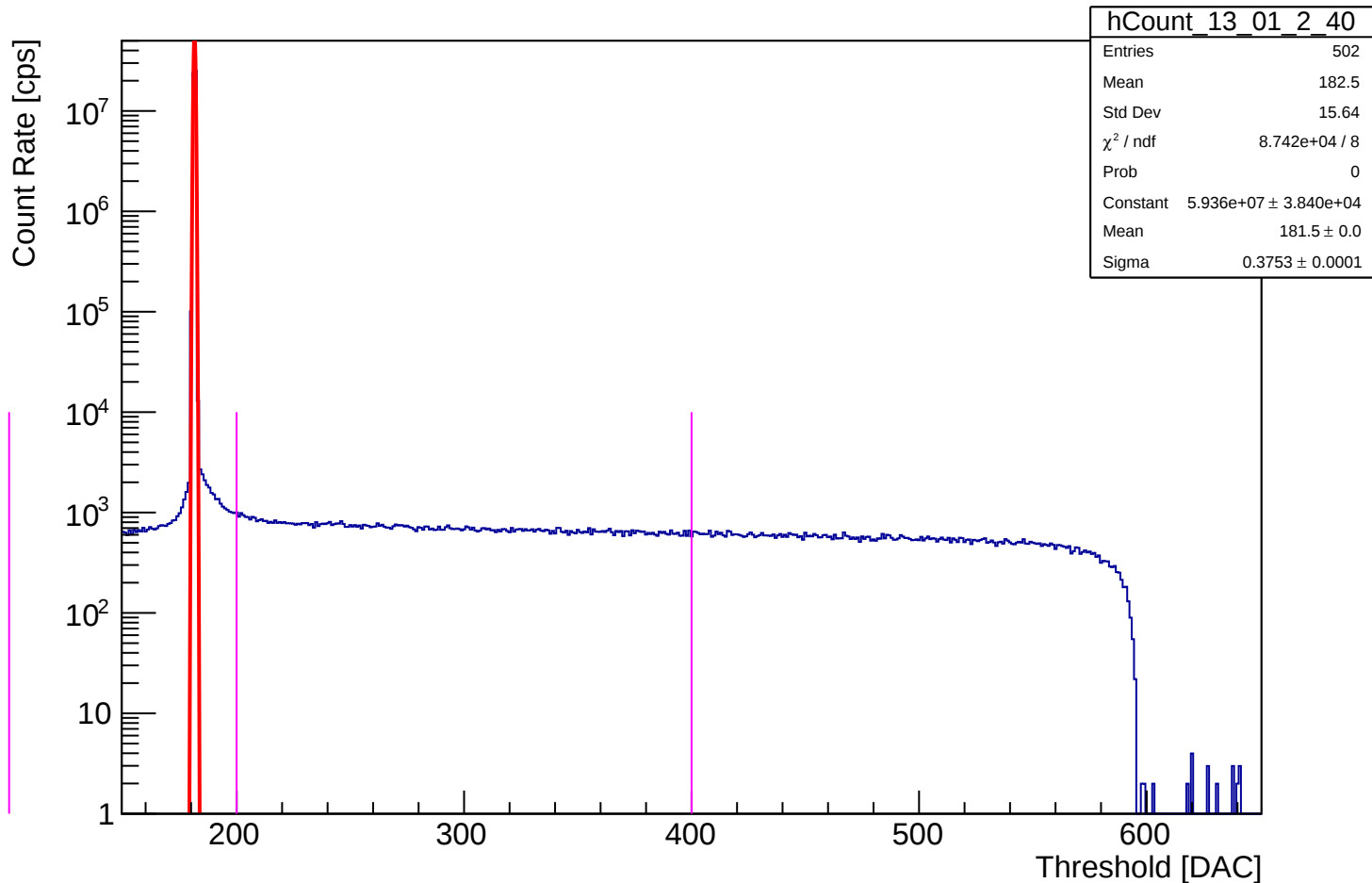
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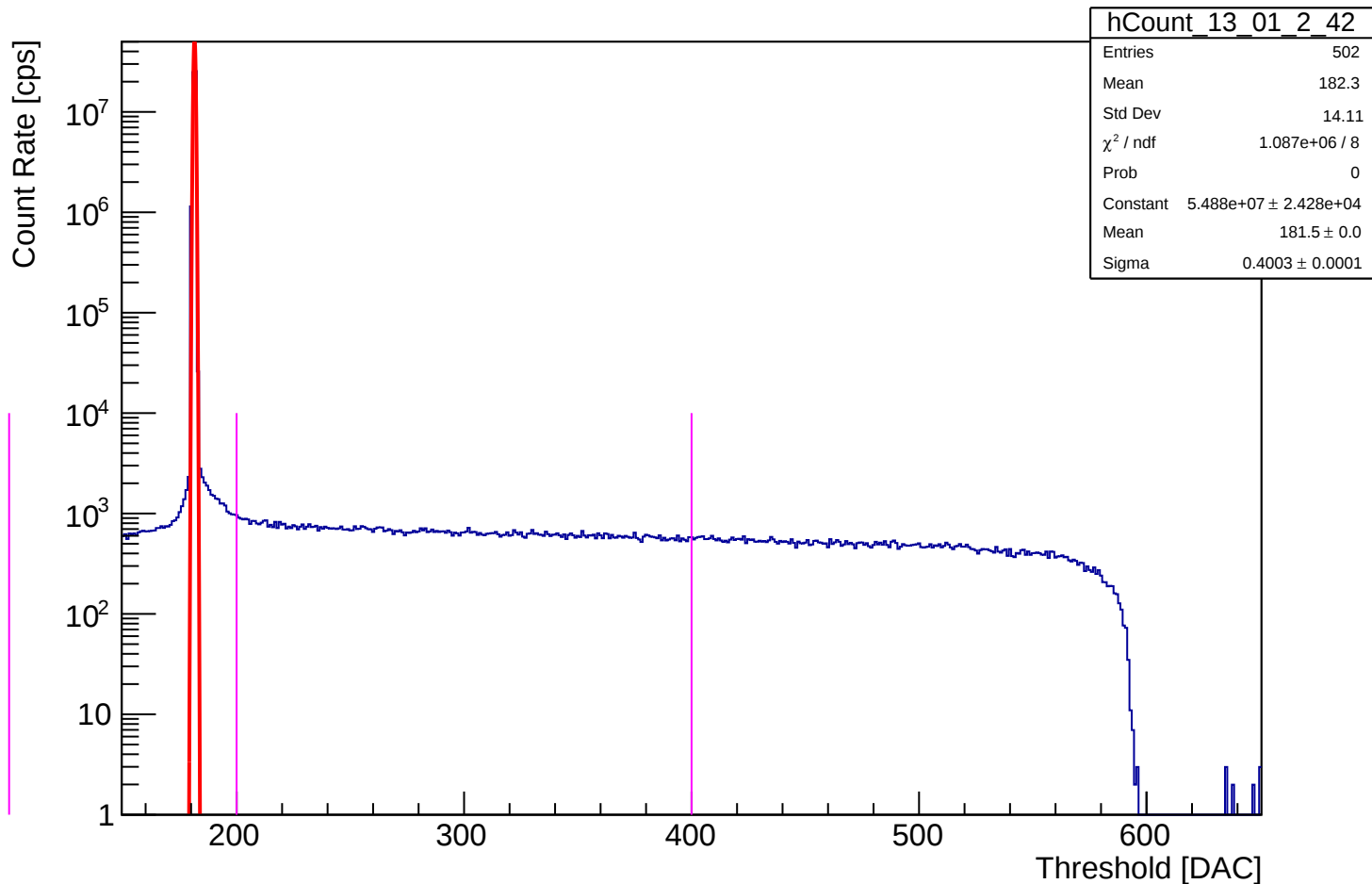
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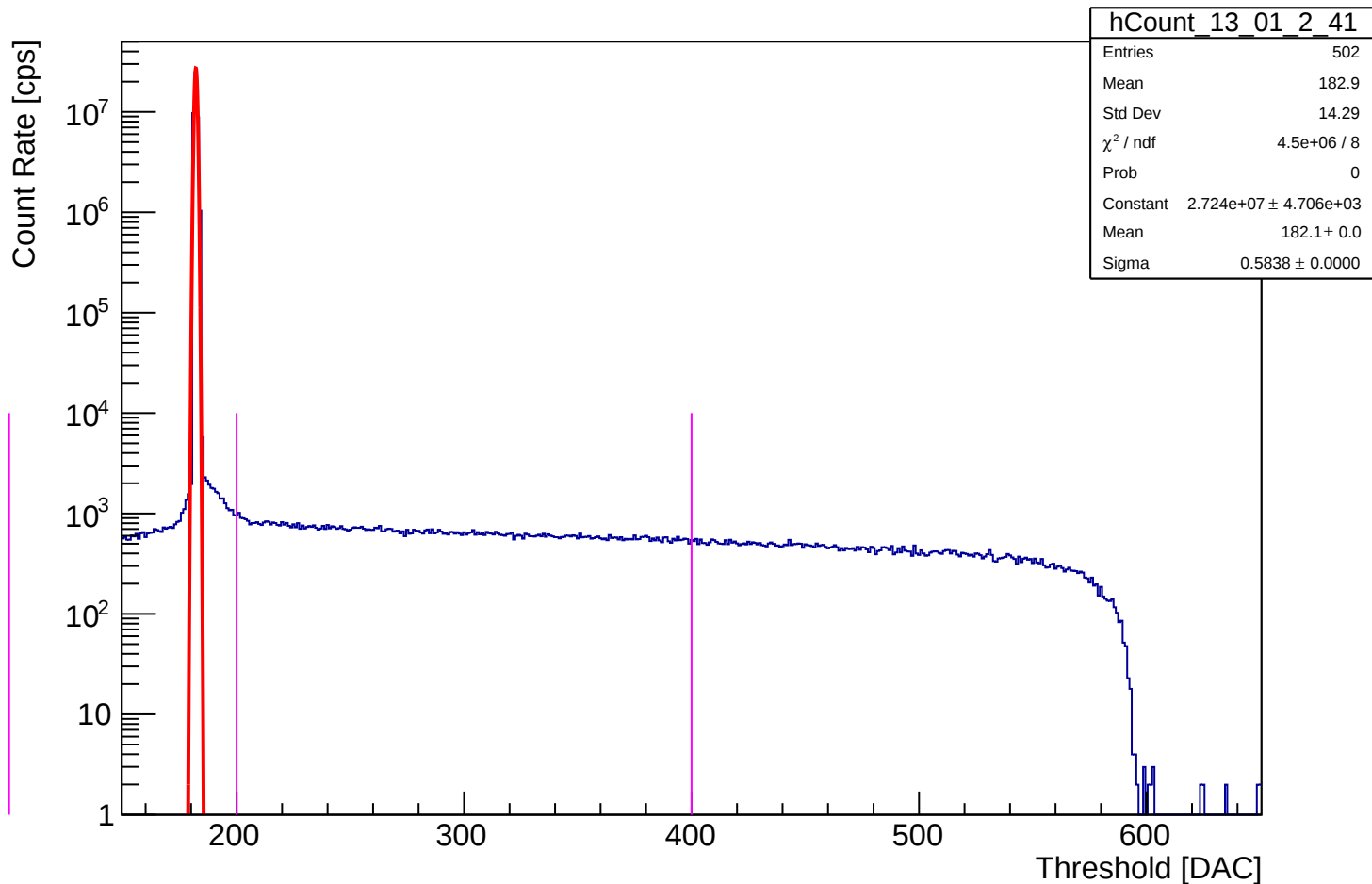
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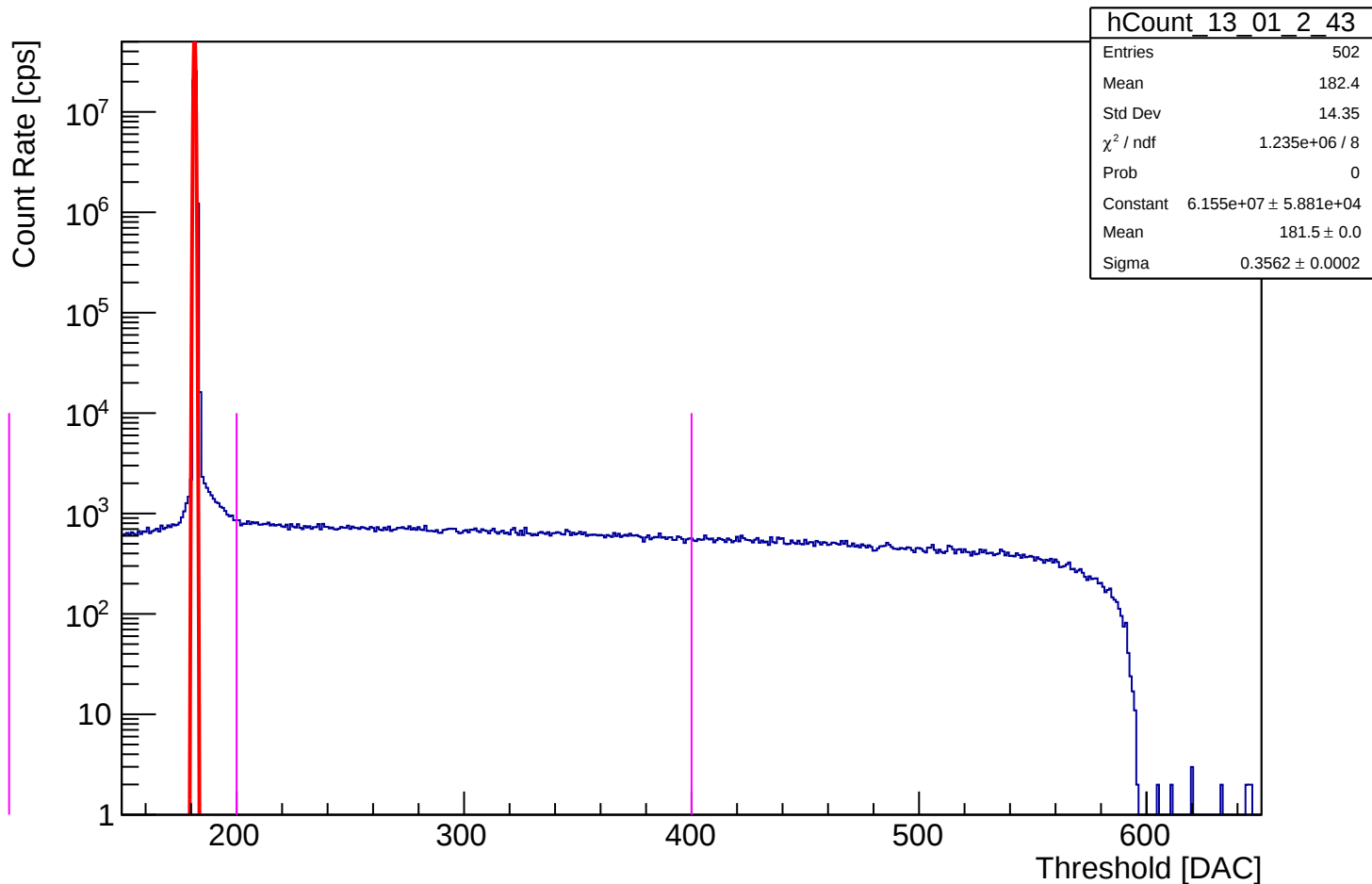
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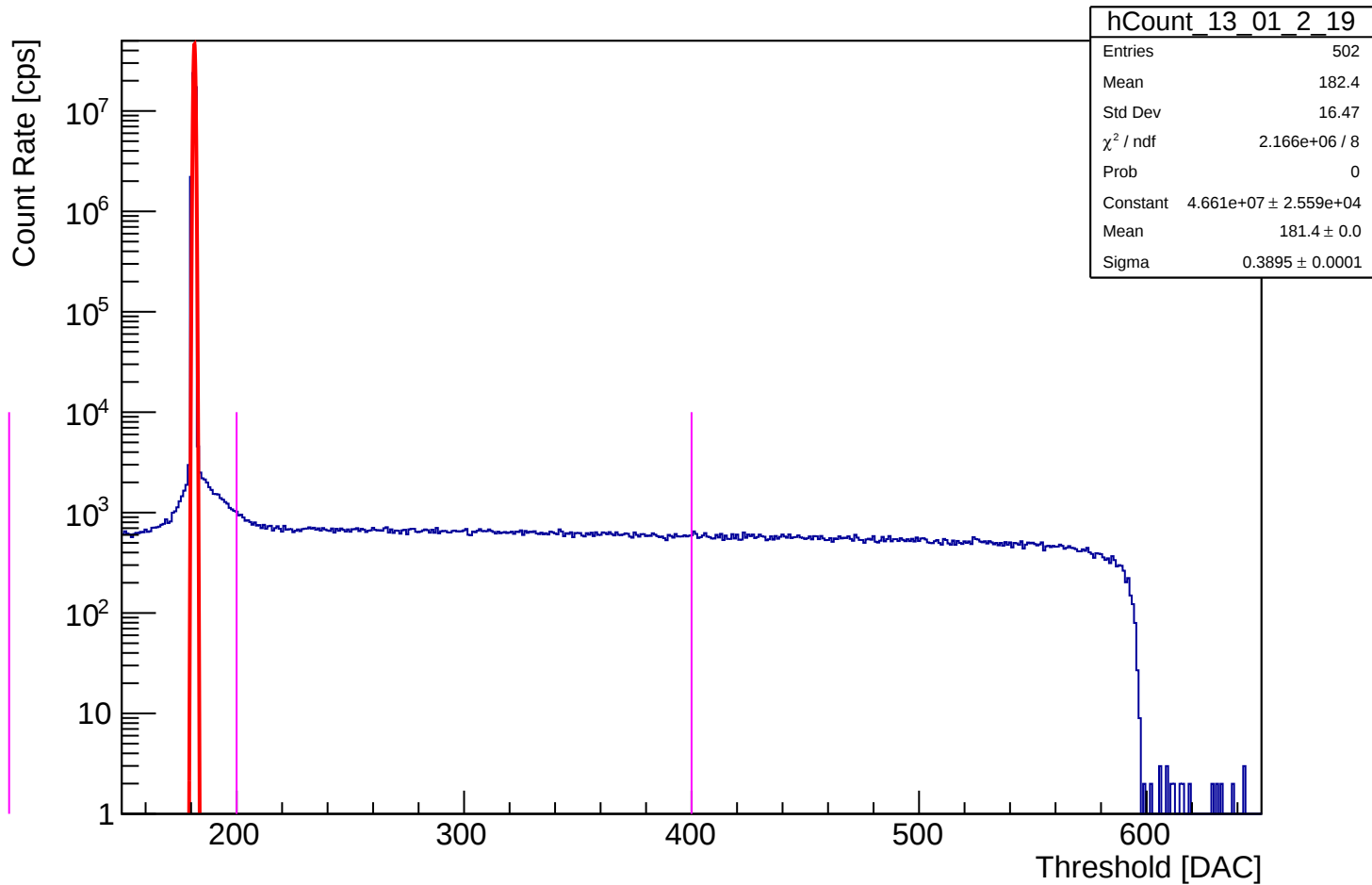
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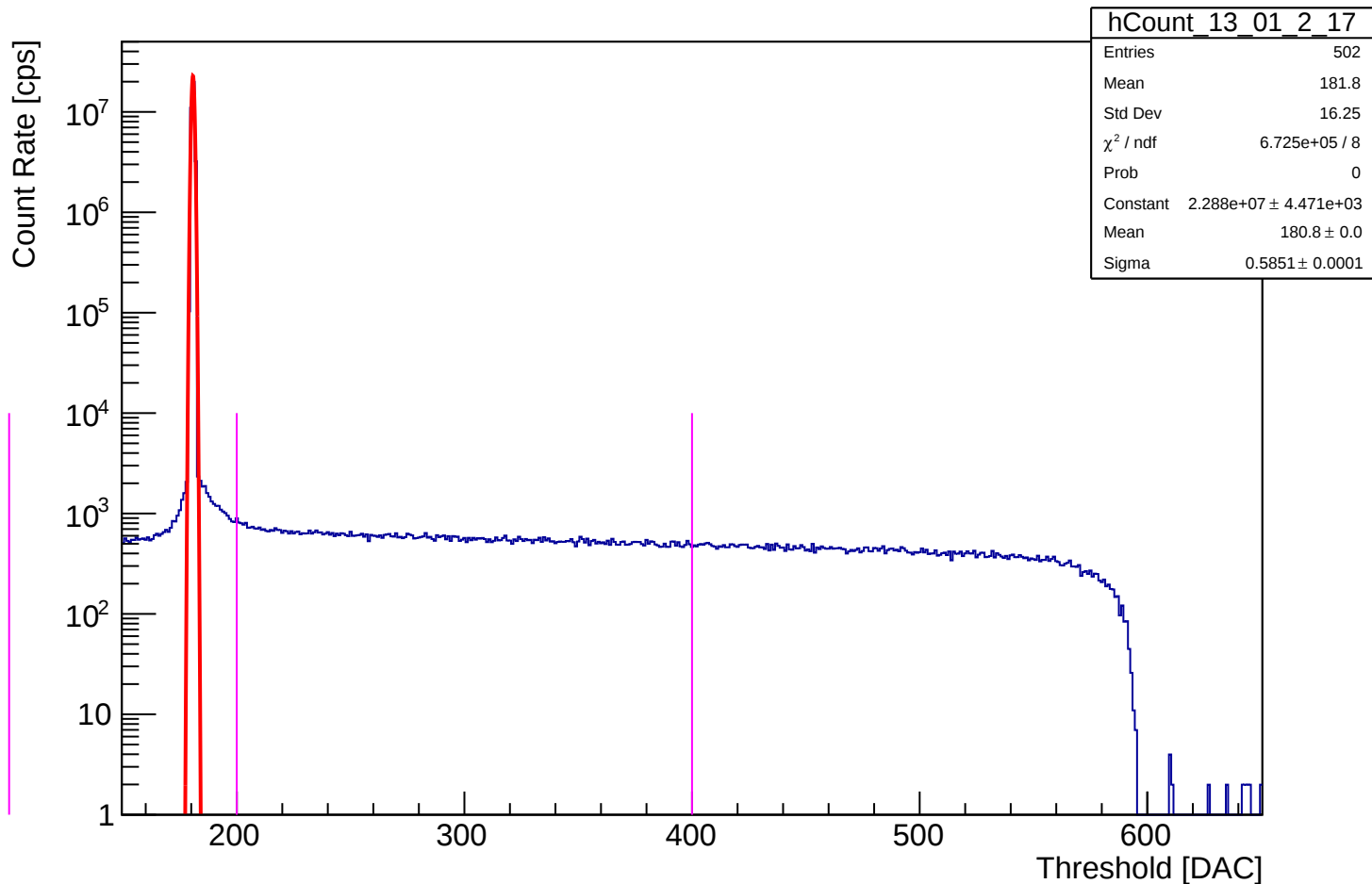


Row 1 Tile 1 Pmt 6 Pixel 24 Slot 13 Fiber 1 Asic 2 Channel 43

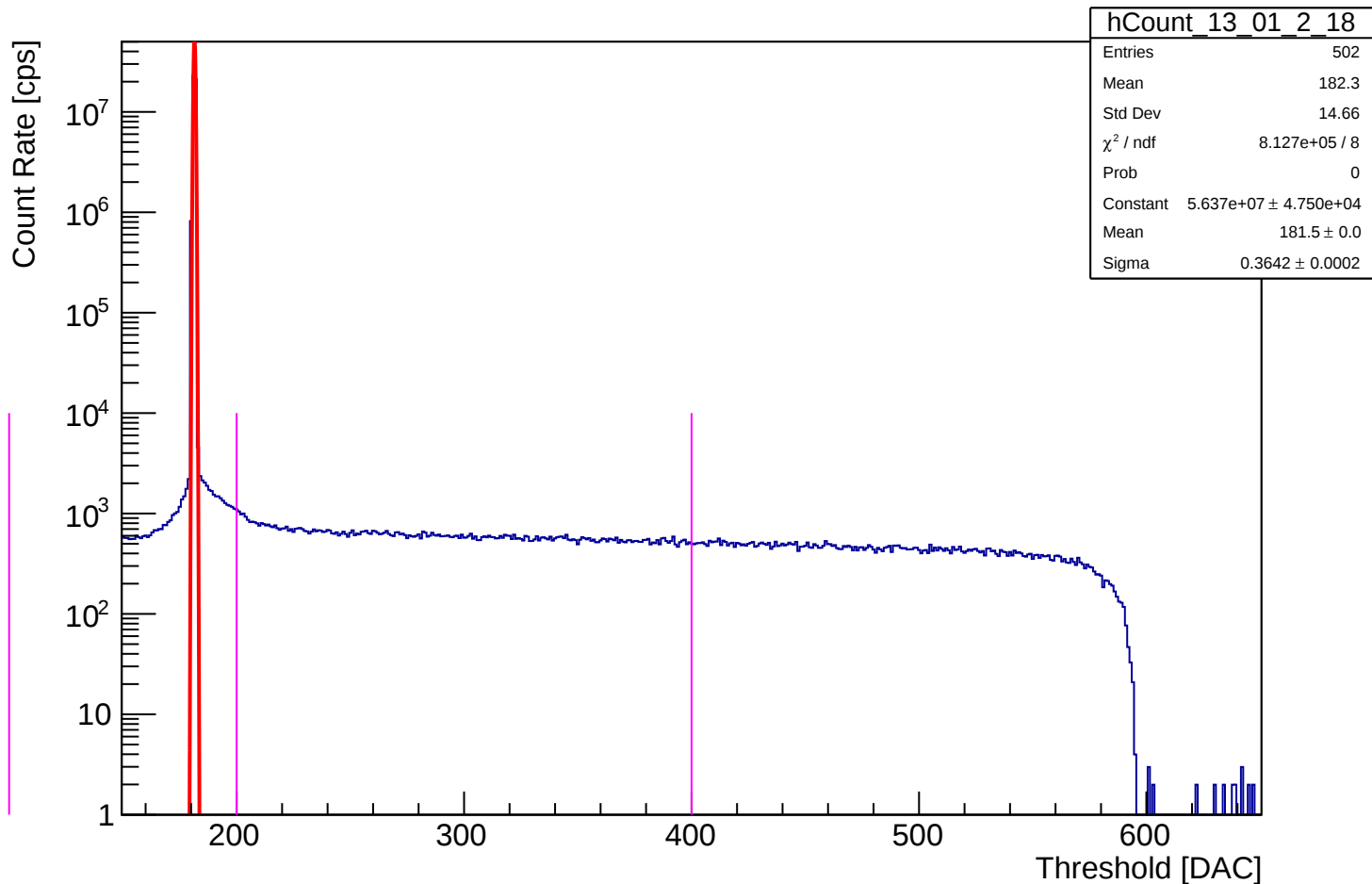


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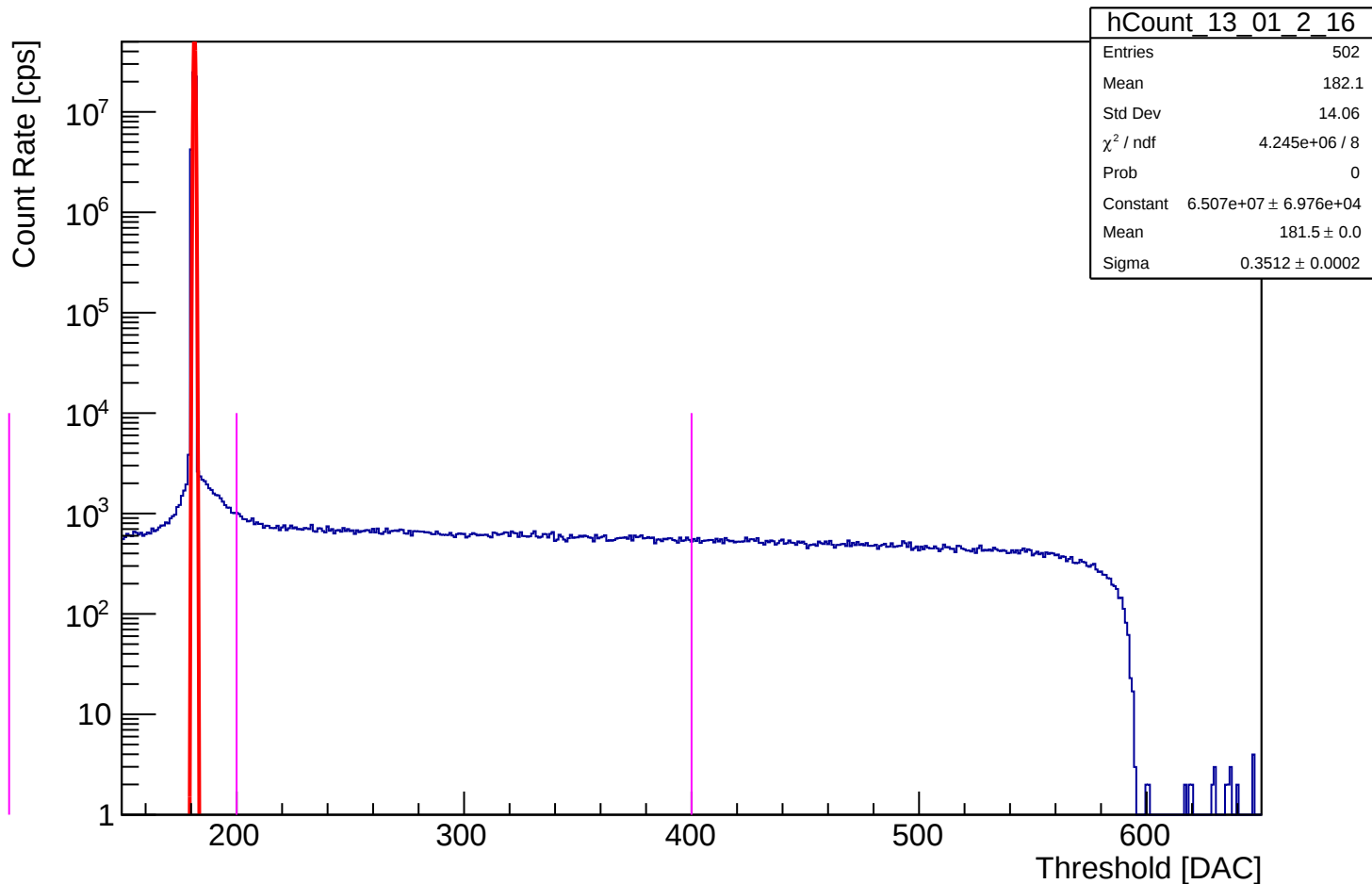




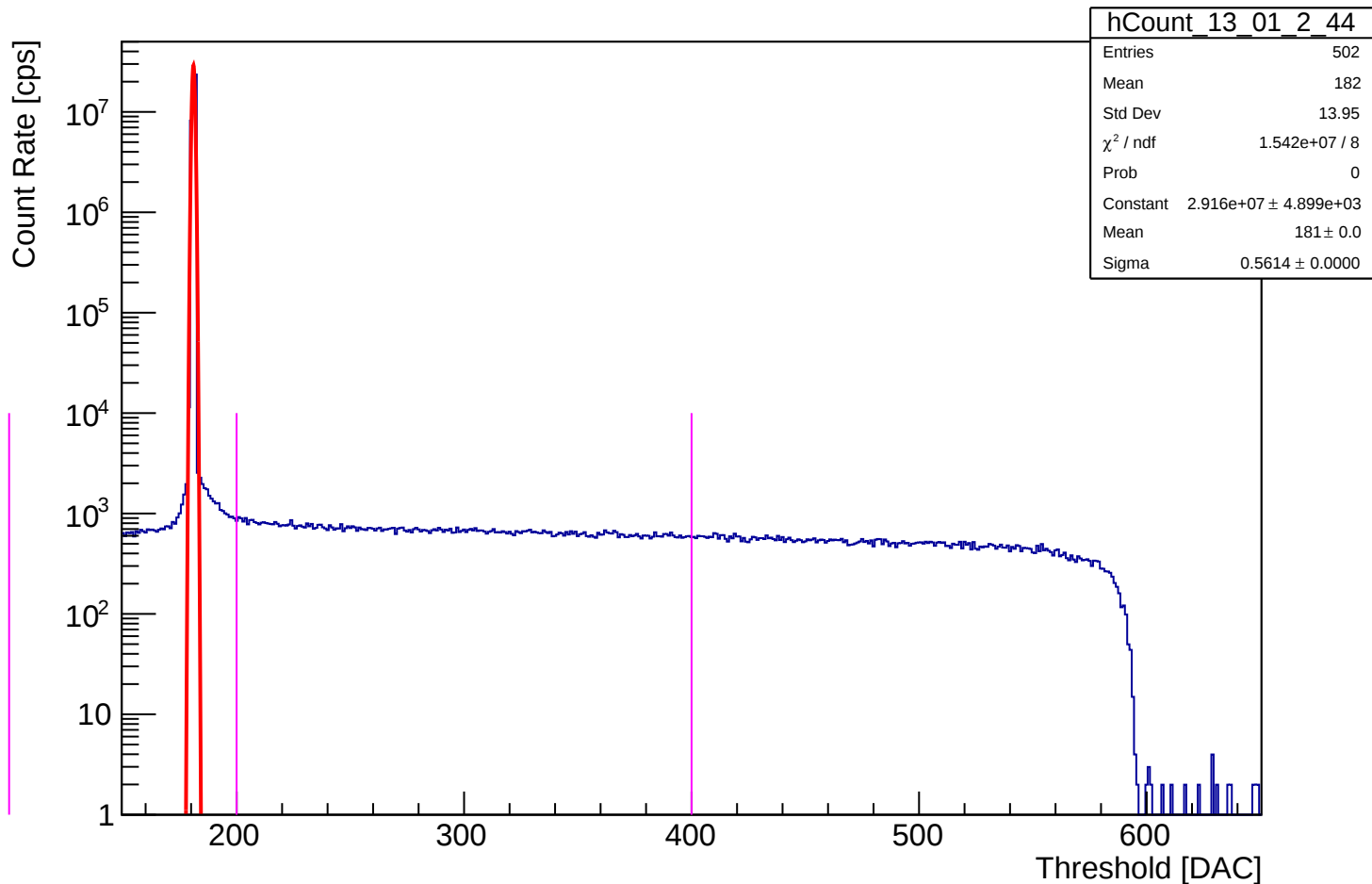
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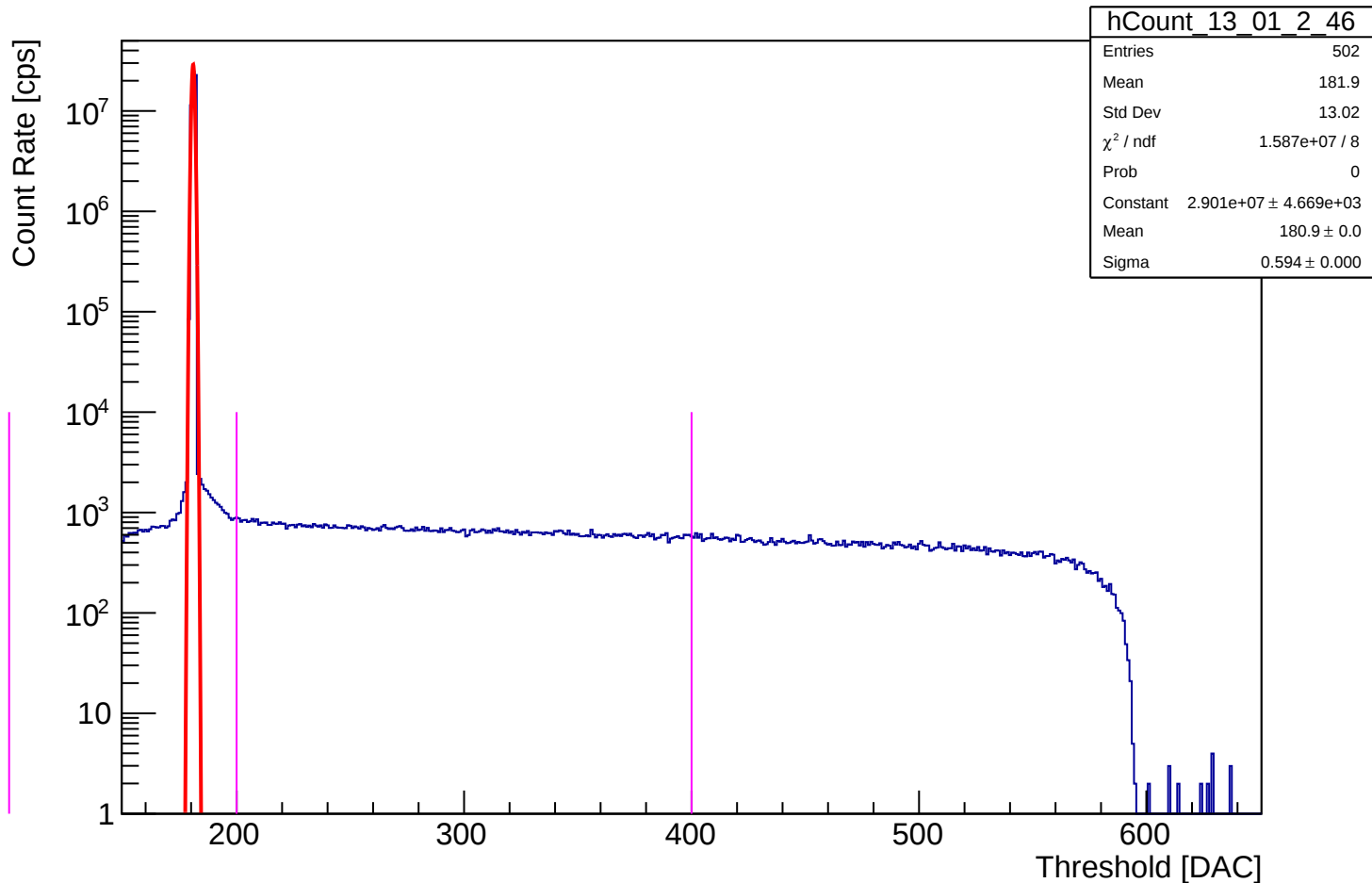
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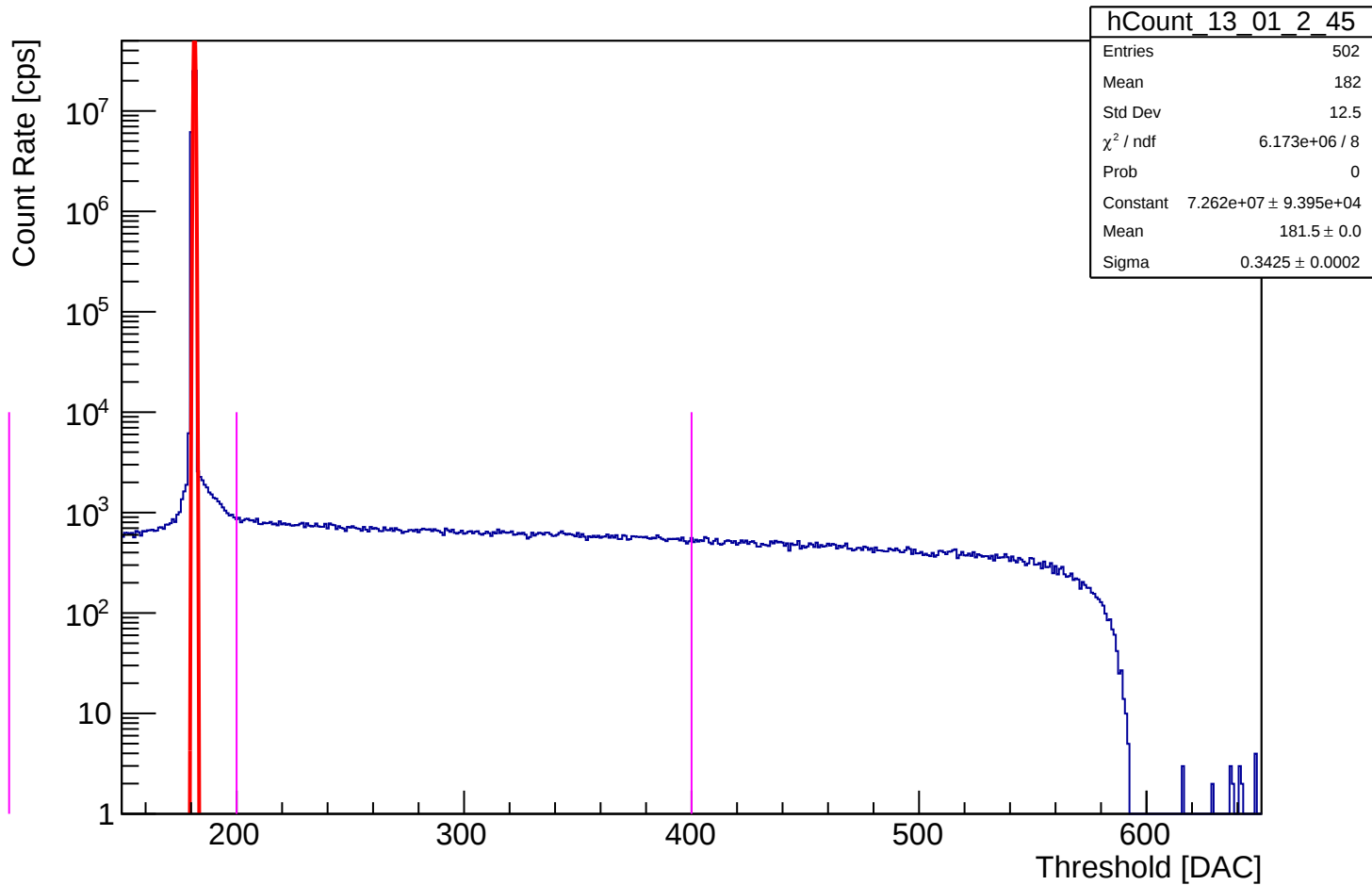
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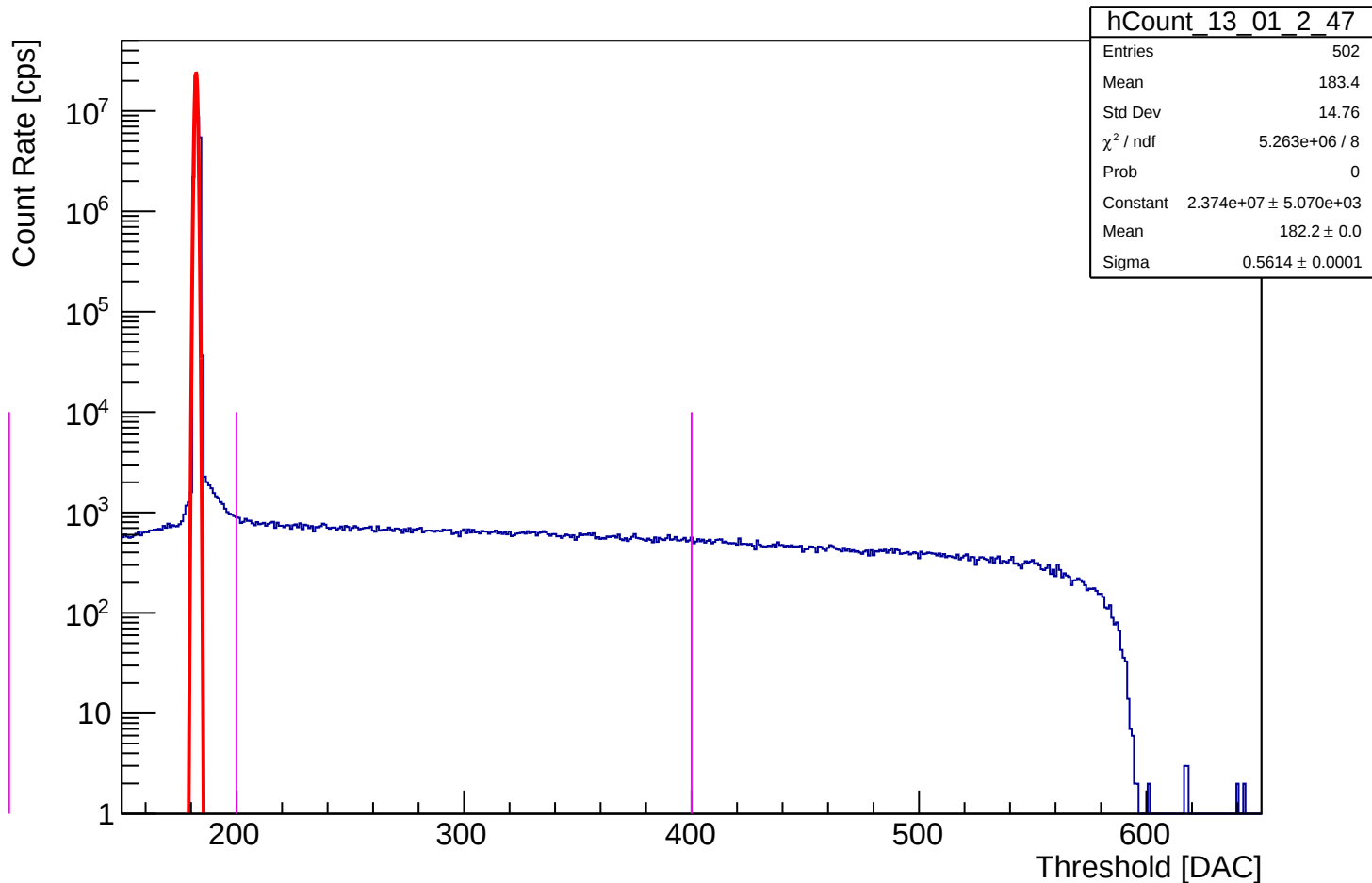
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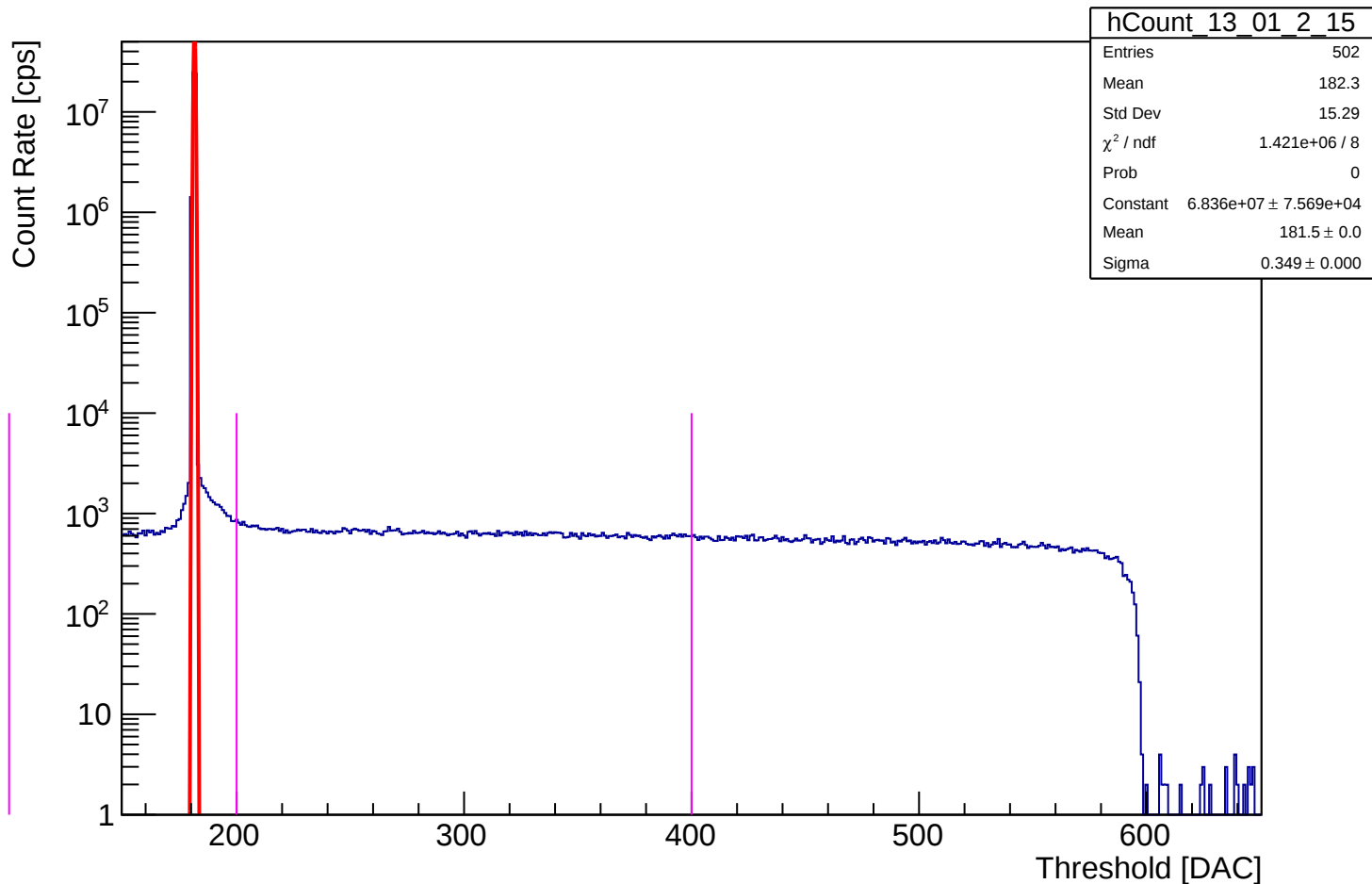
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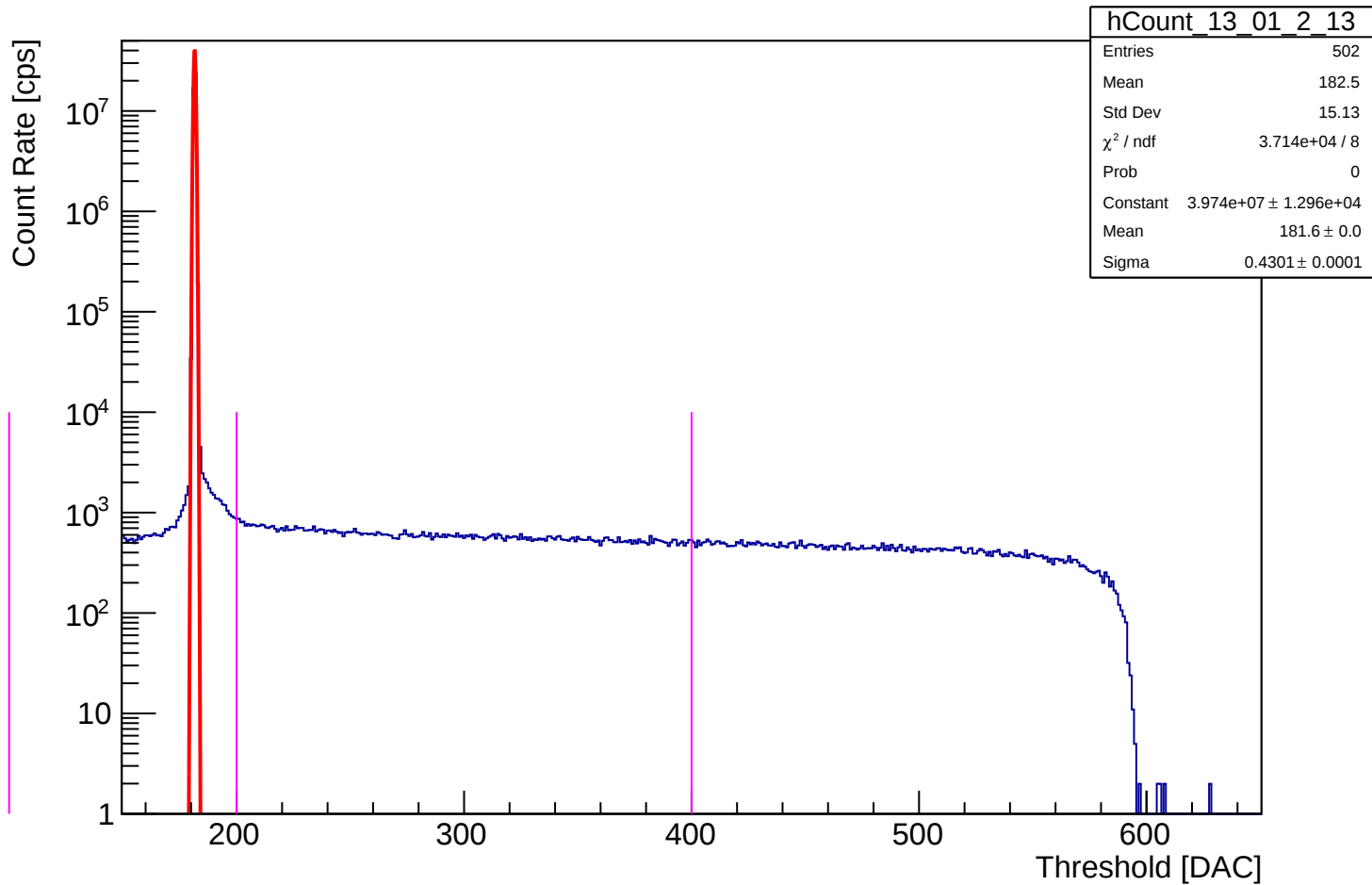
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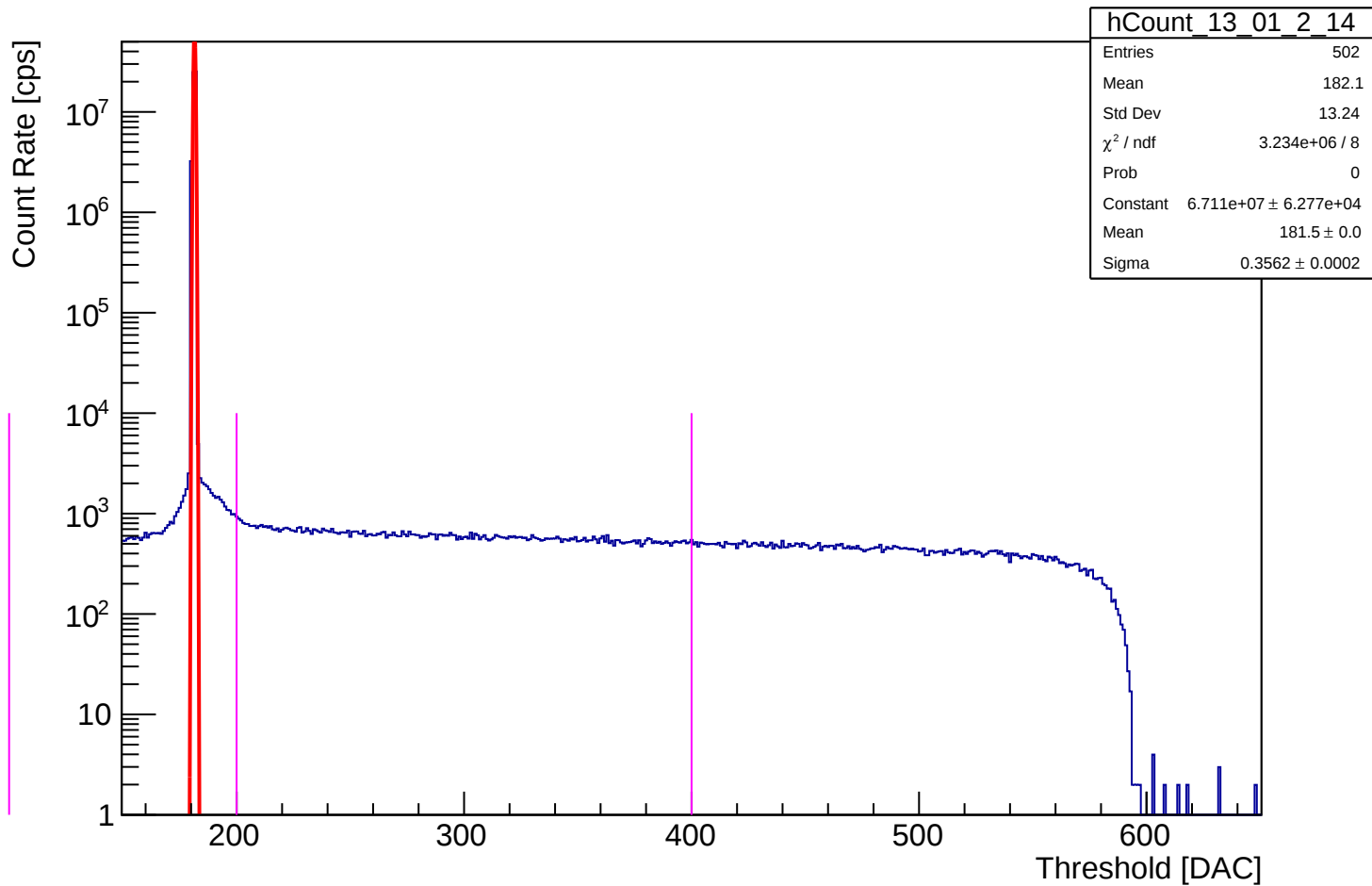
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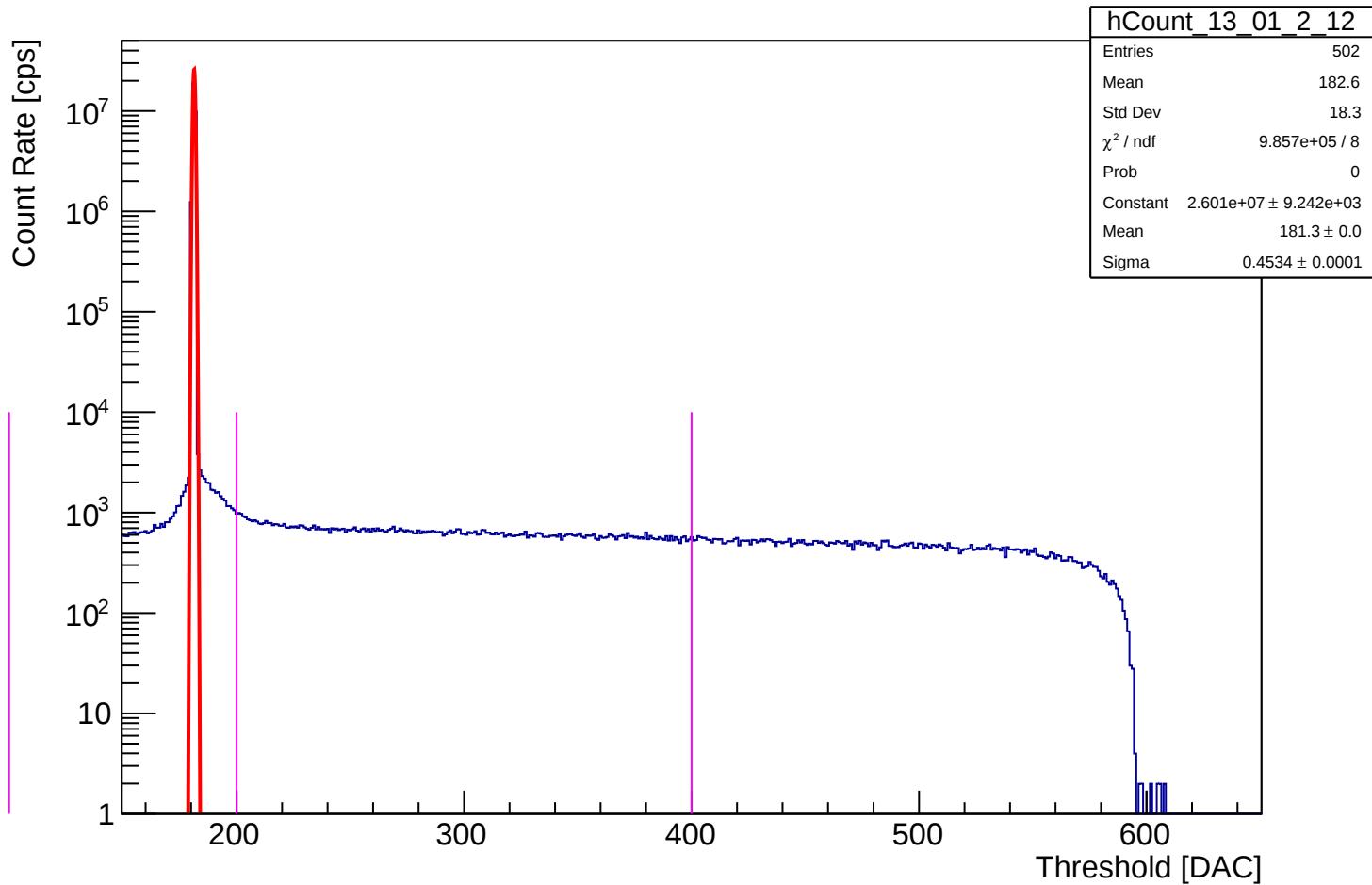
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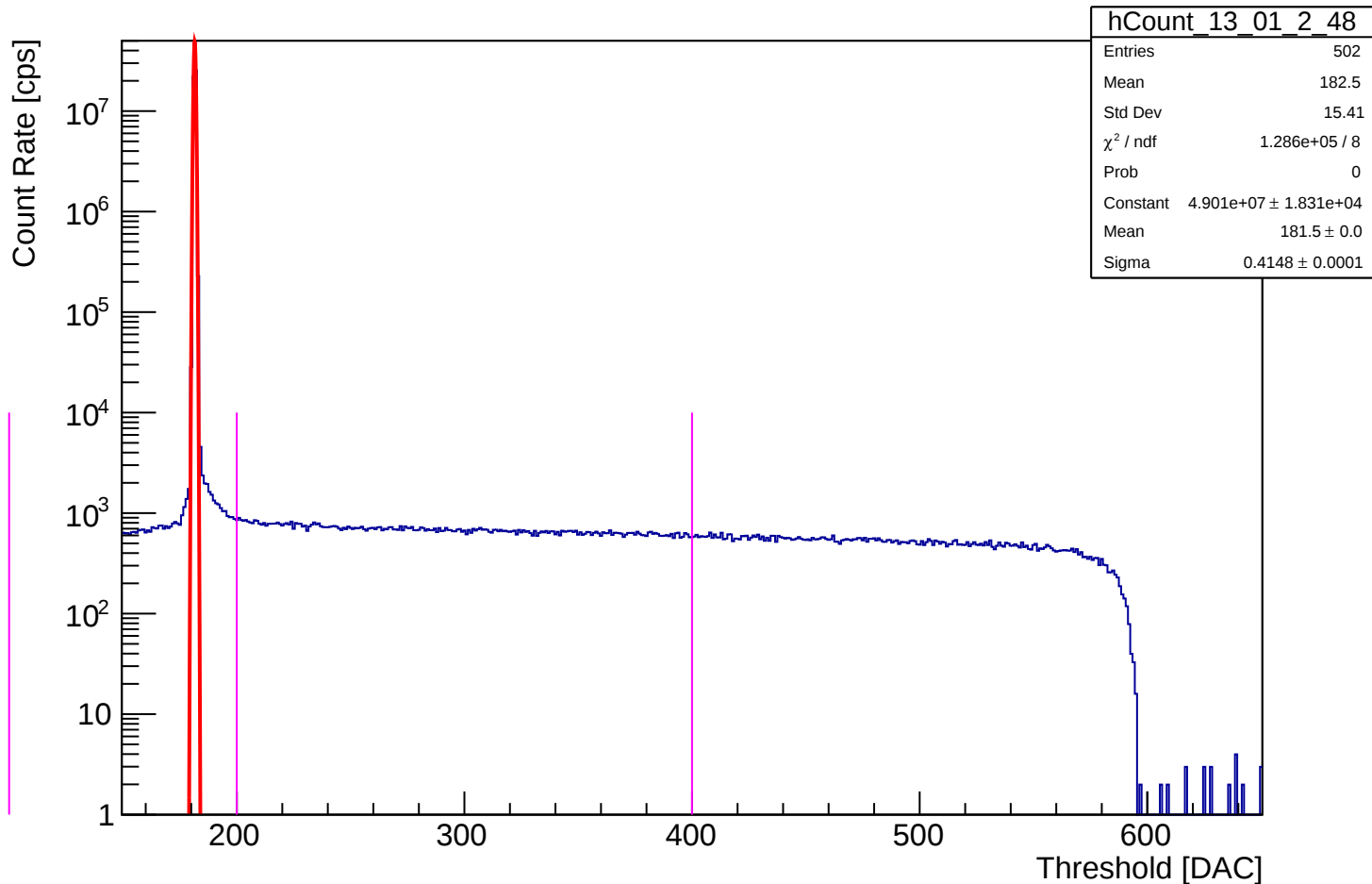


Row 1 Tile 1 Pmt 6 Pixel 35 Slot 13 Fiber 1 Asic 2 Channel 14

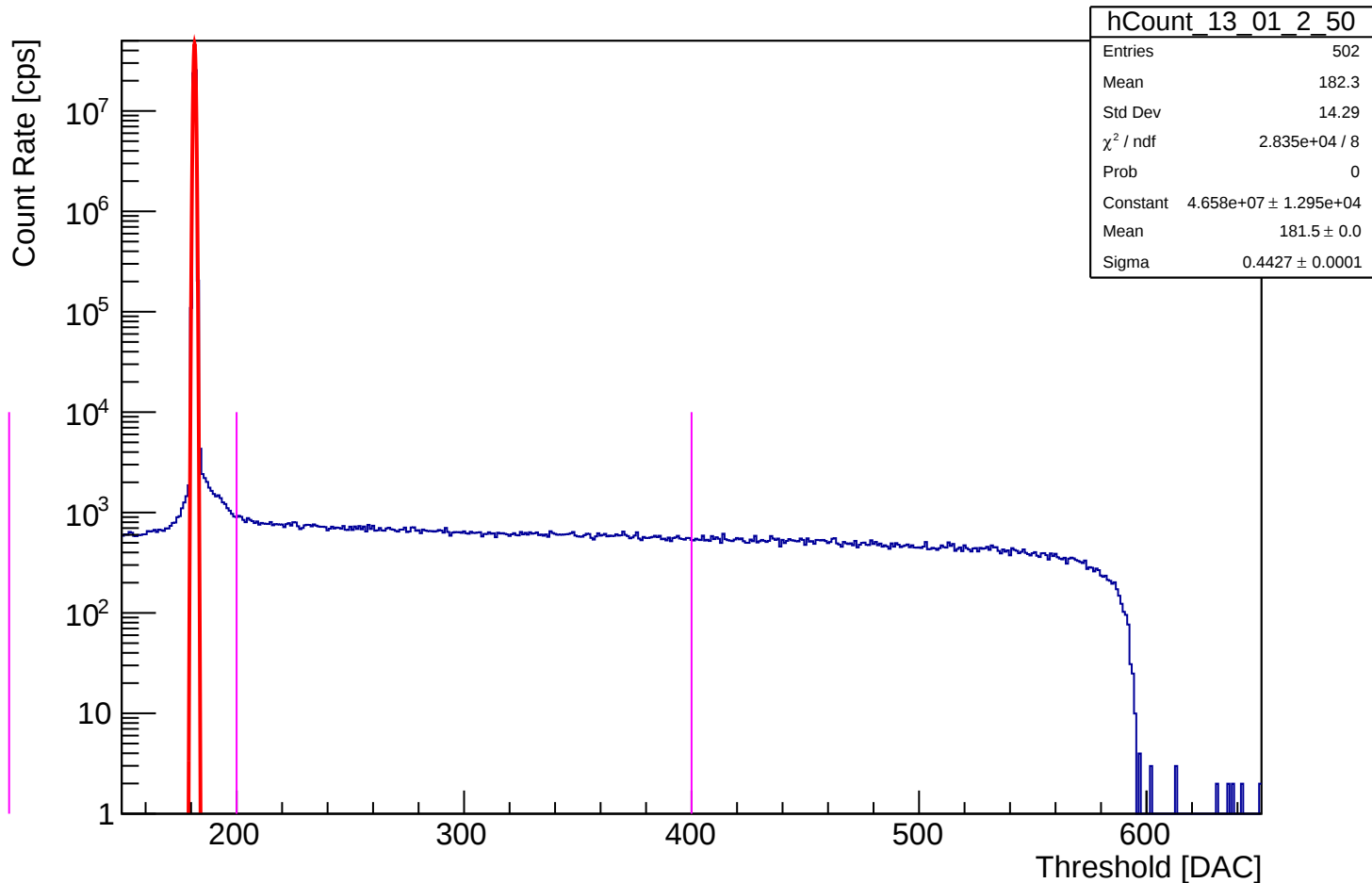


Row 1 Tile 1 Pmt 6 Pixel 36 Slot 13 Fiber 1 Asic 2 Channel 12

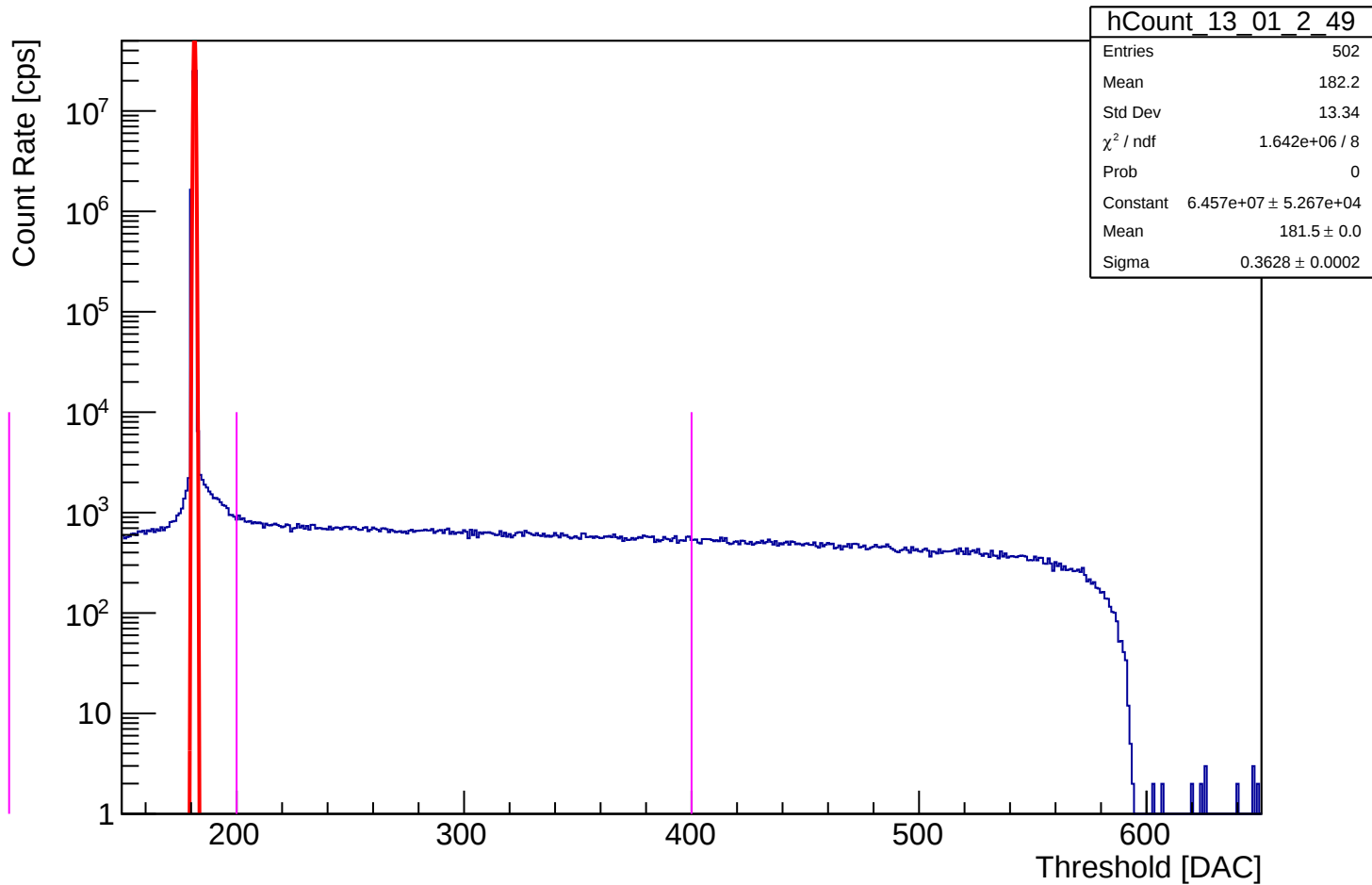




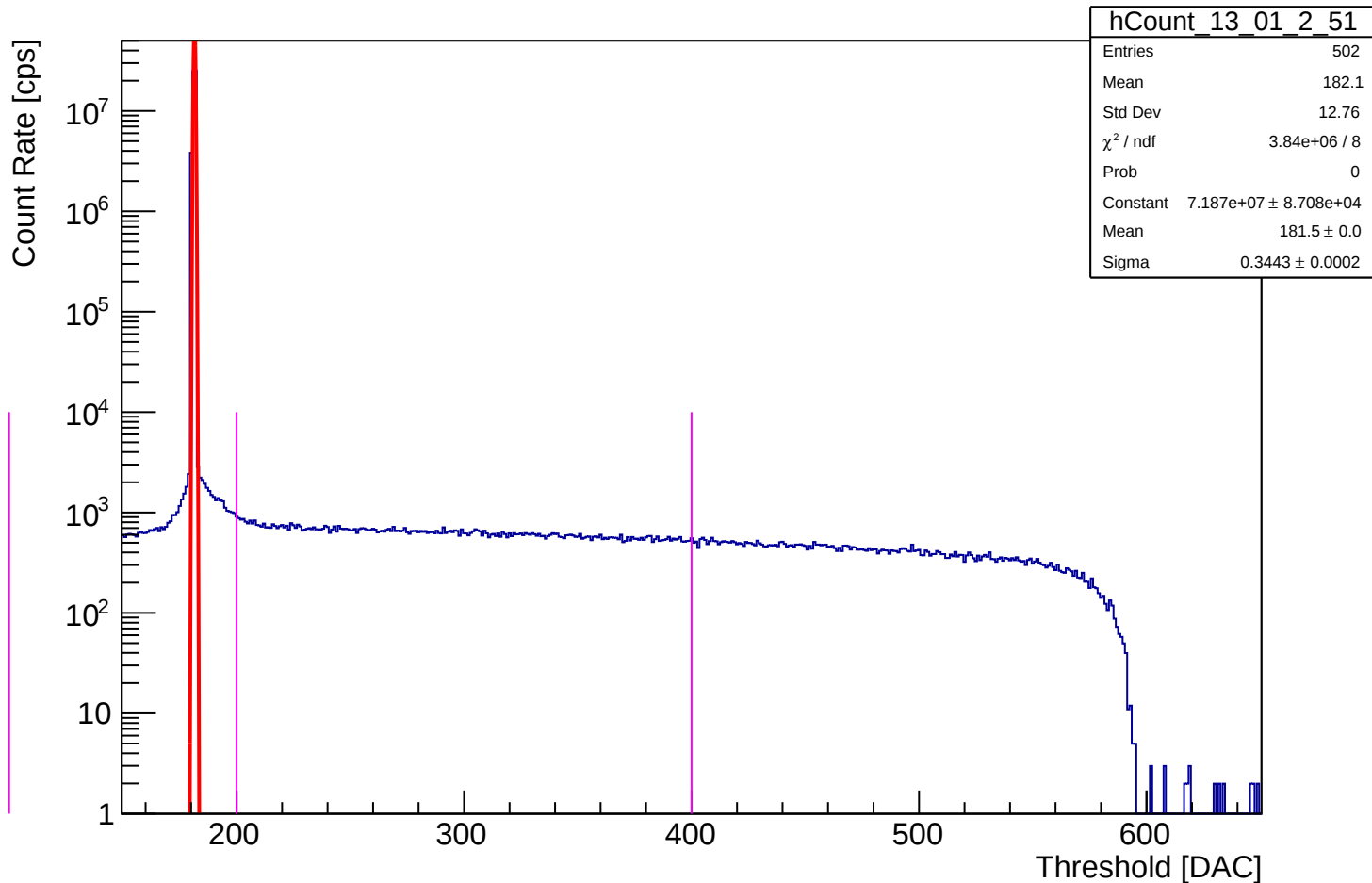
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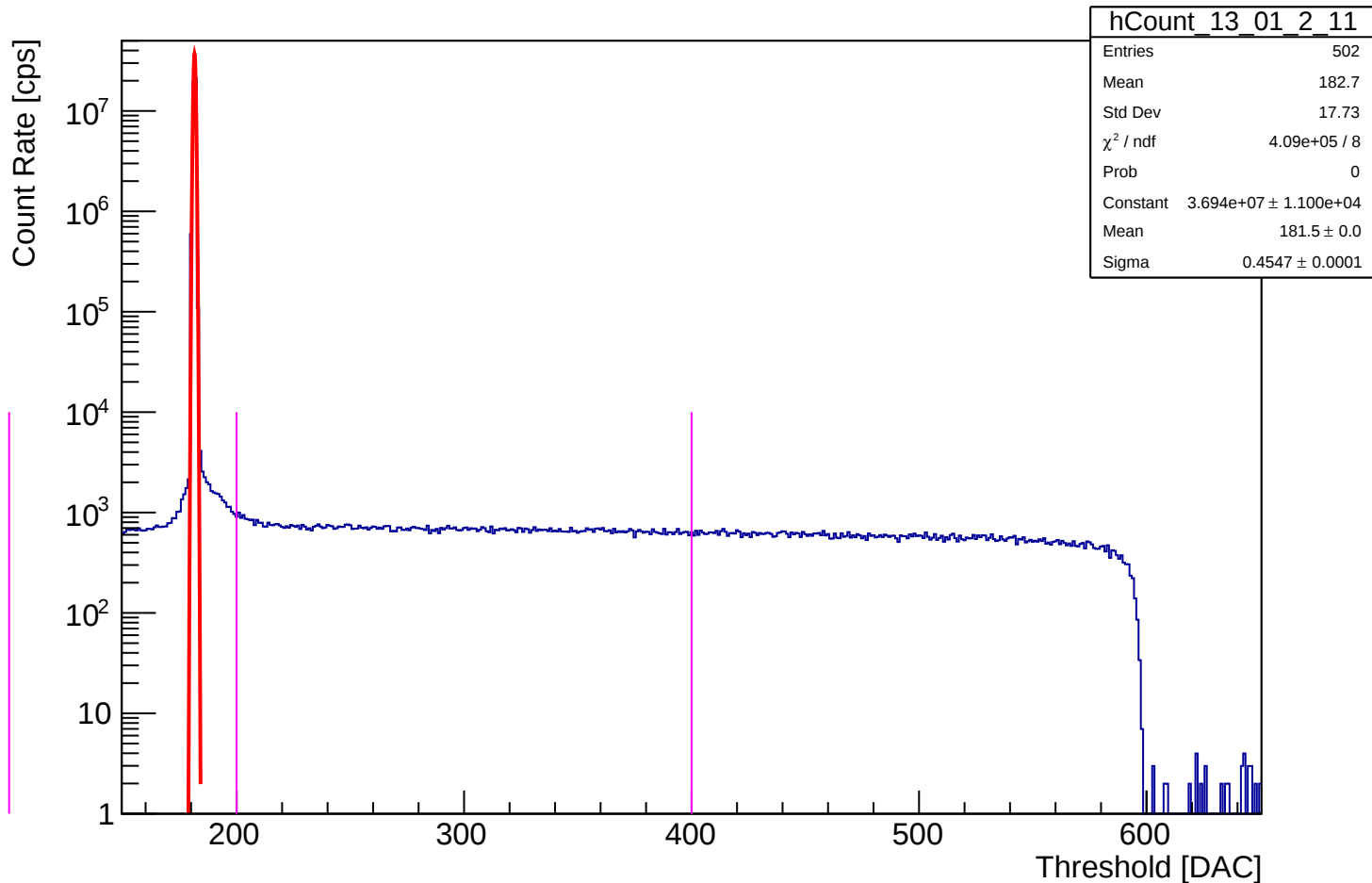
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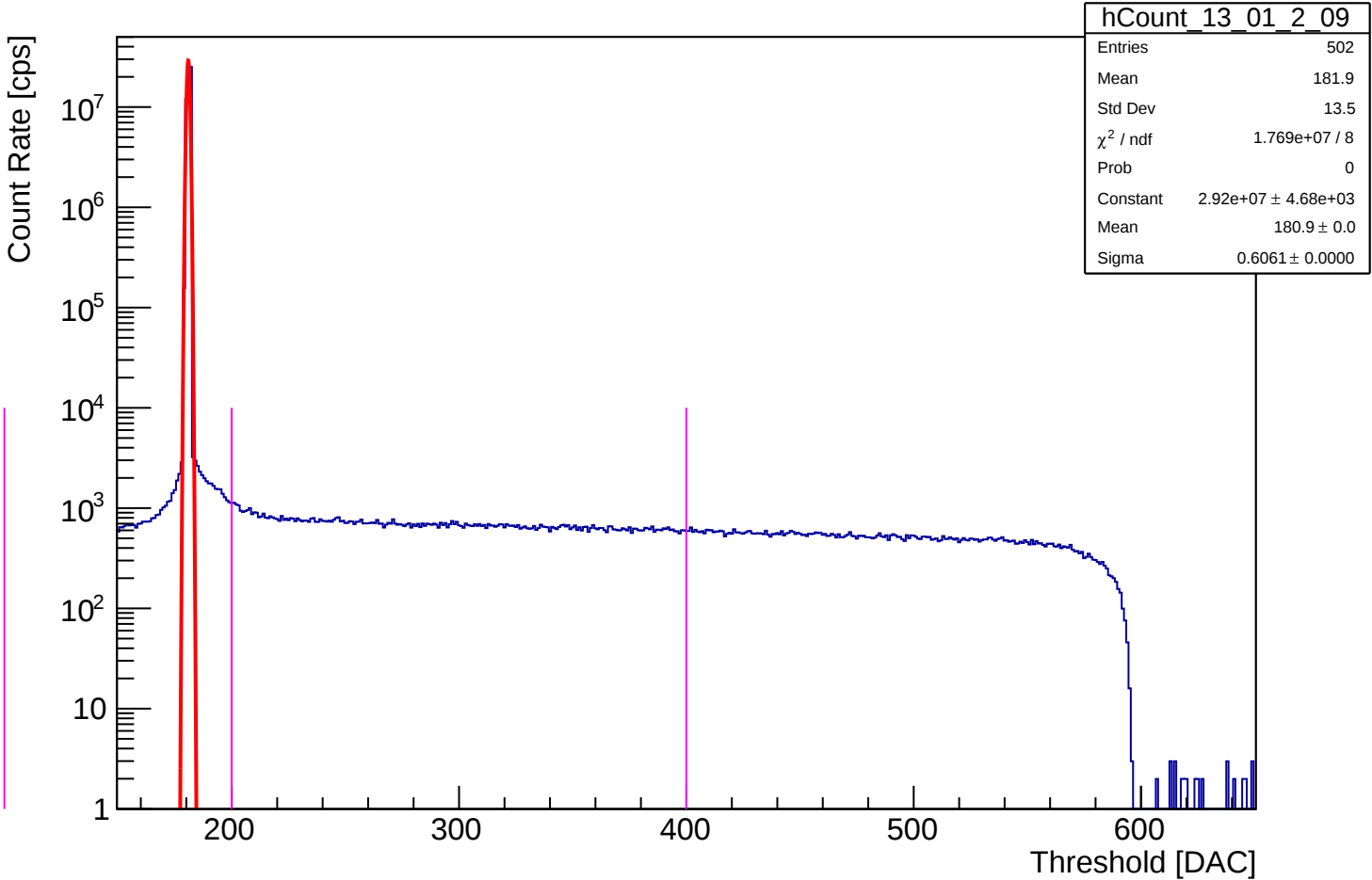


Row 1 Tile 1 Pmt 6 Pixel 40 Slot 13 Fiber 1 Asic 2 Channel 51

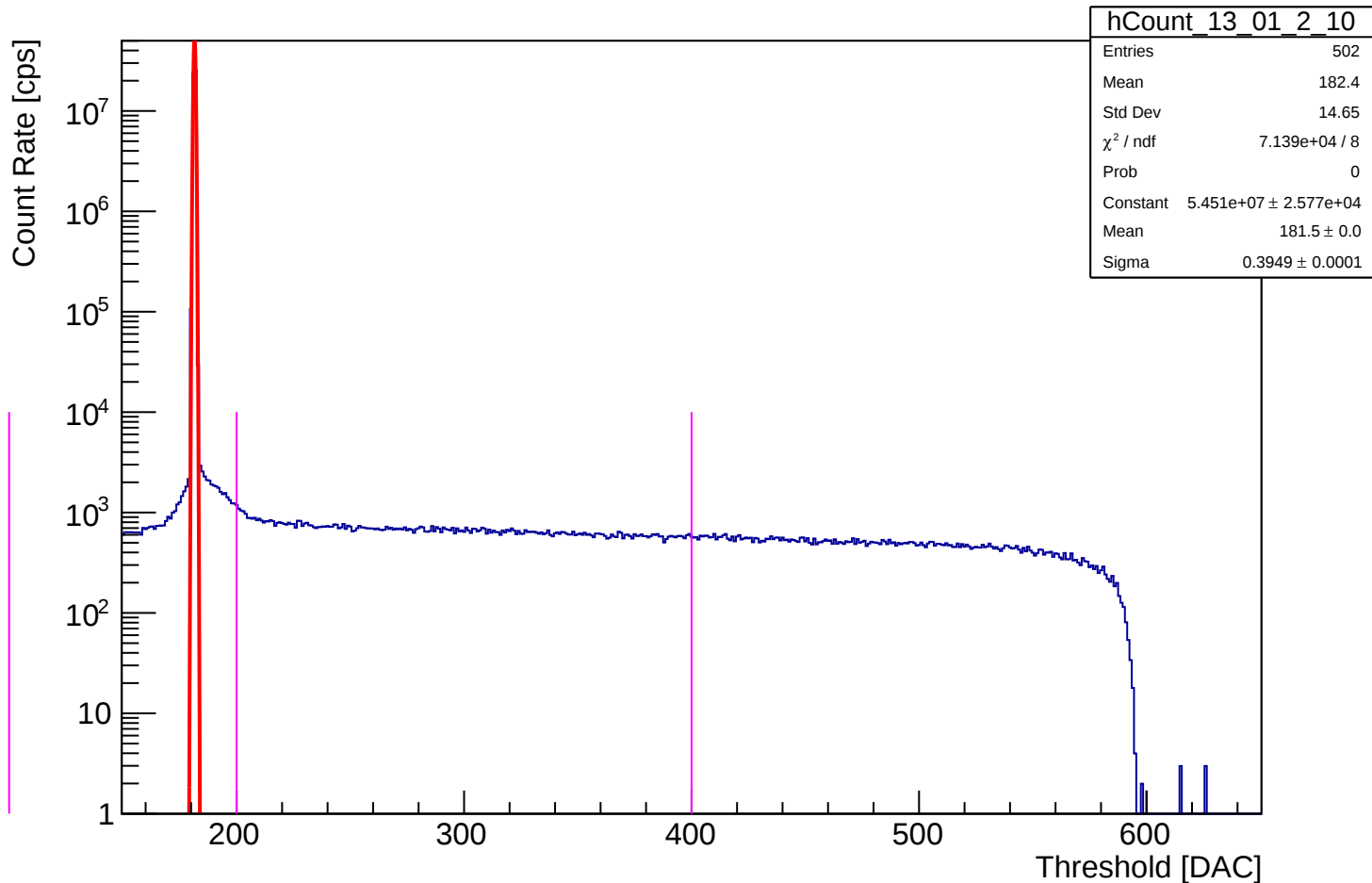


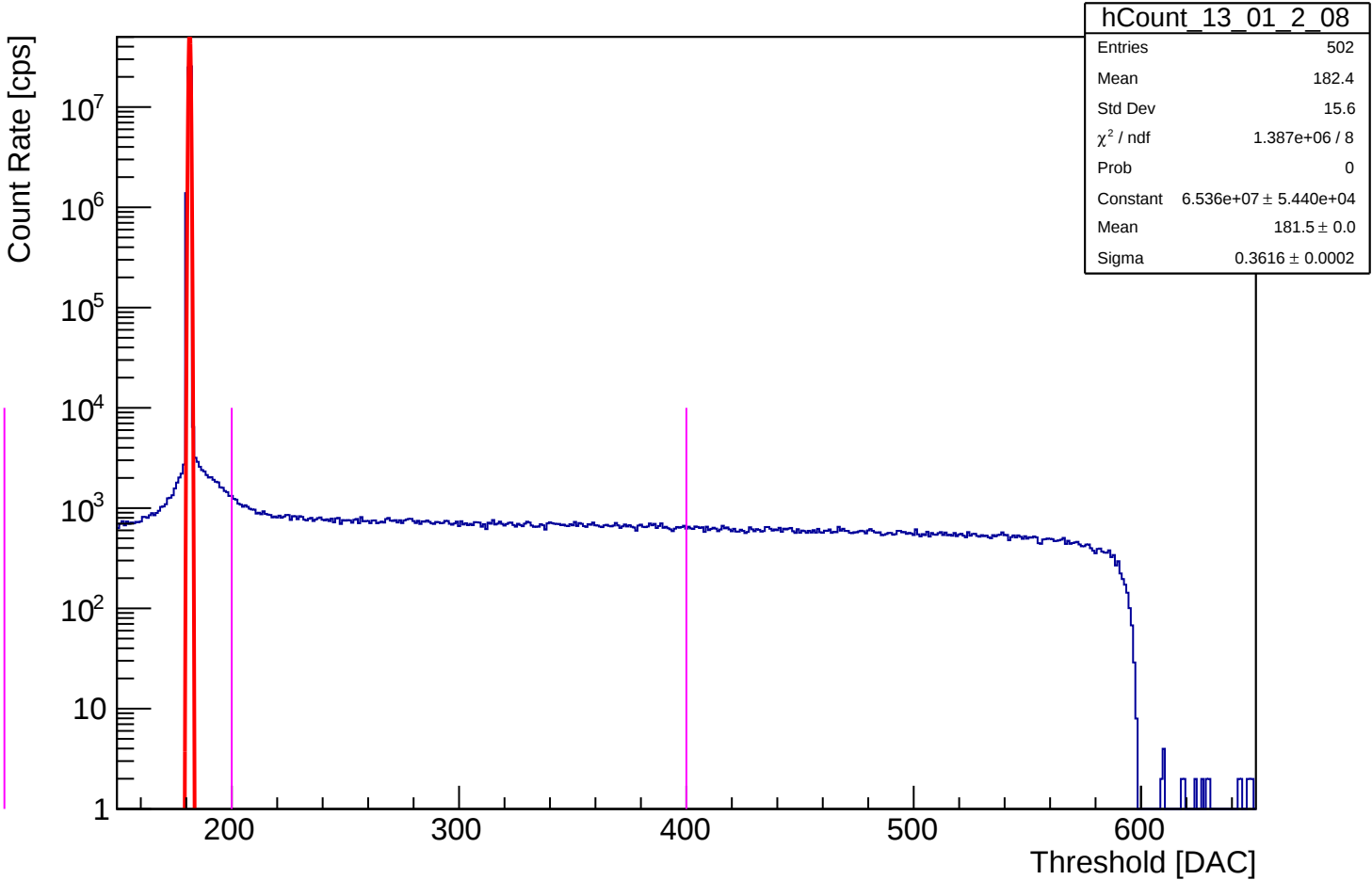
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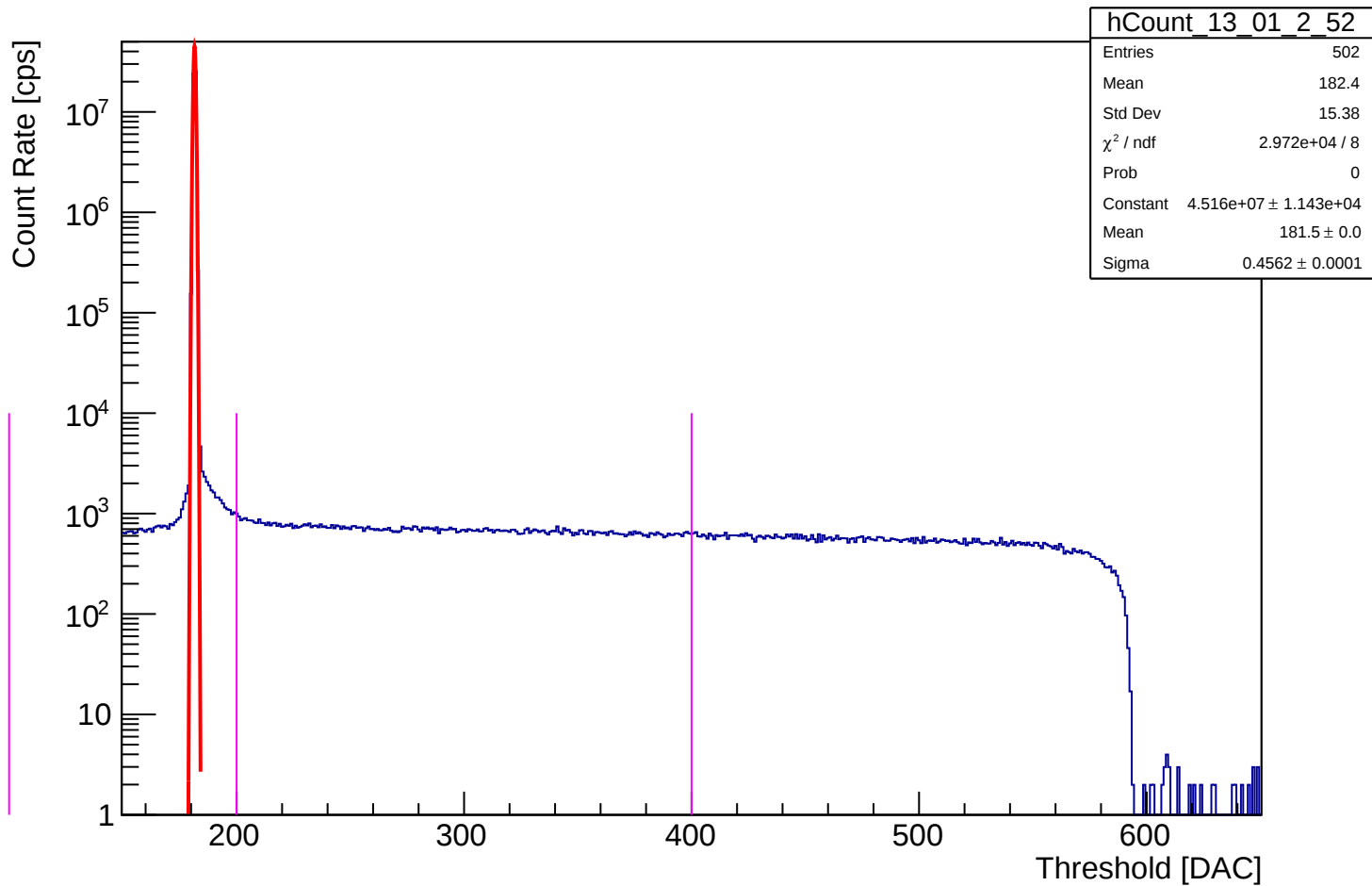




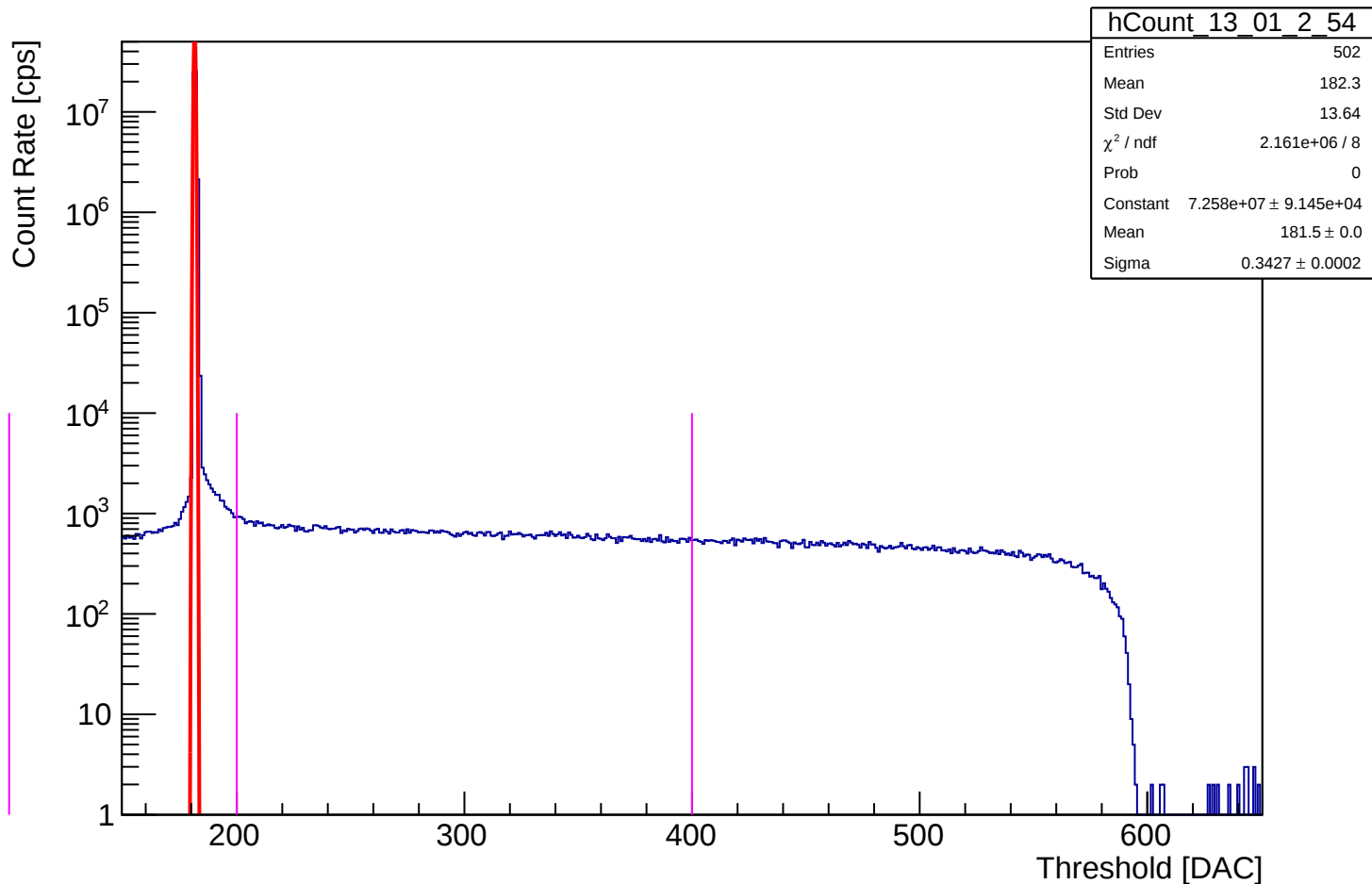
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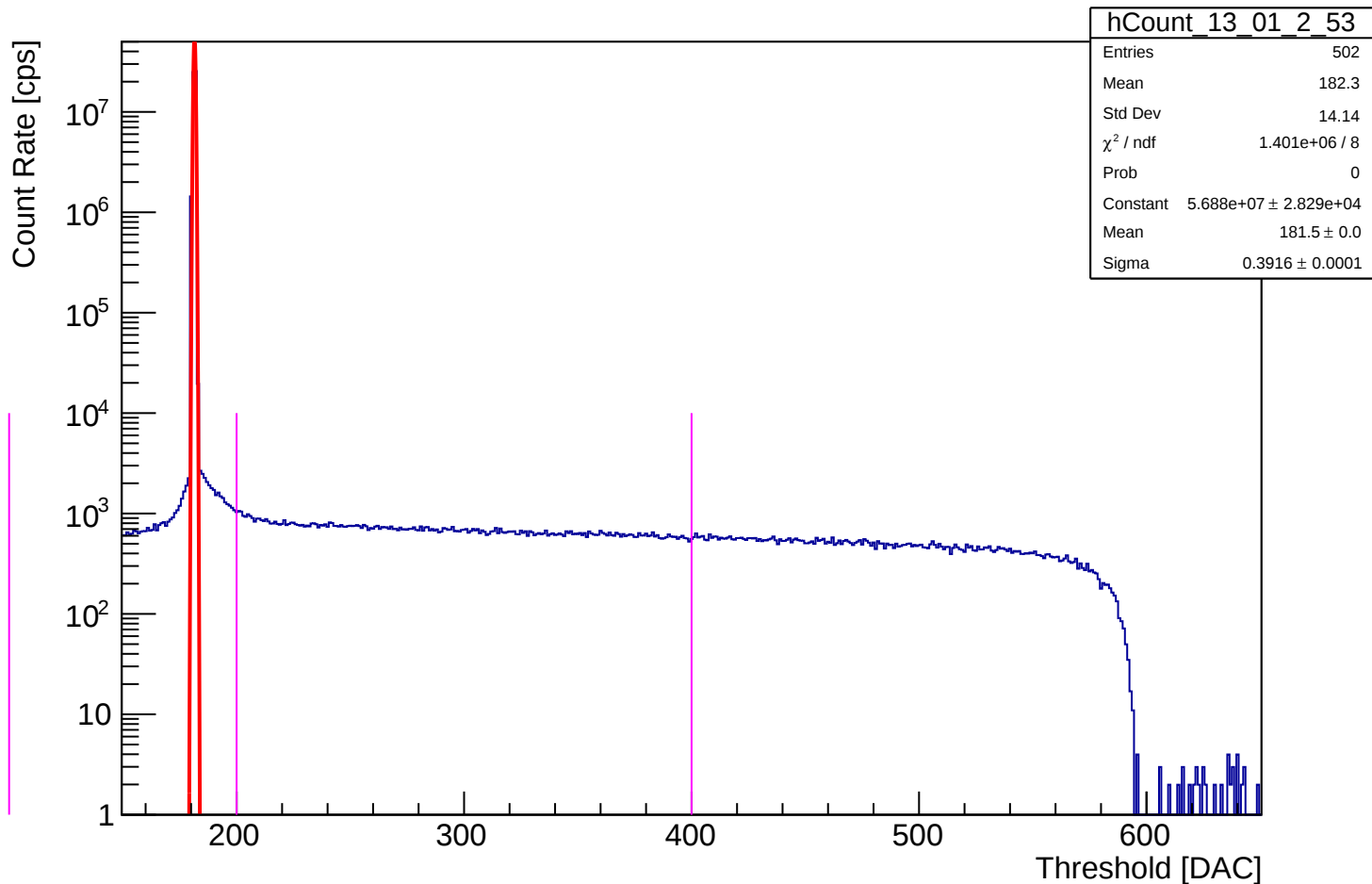




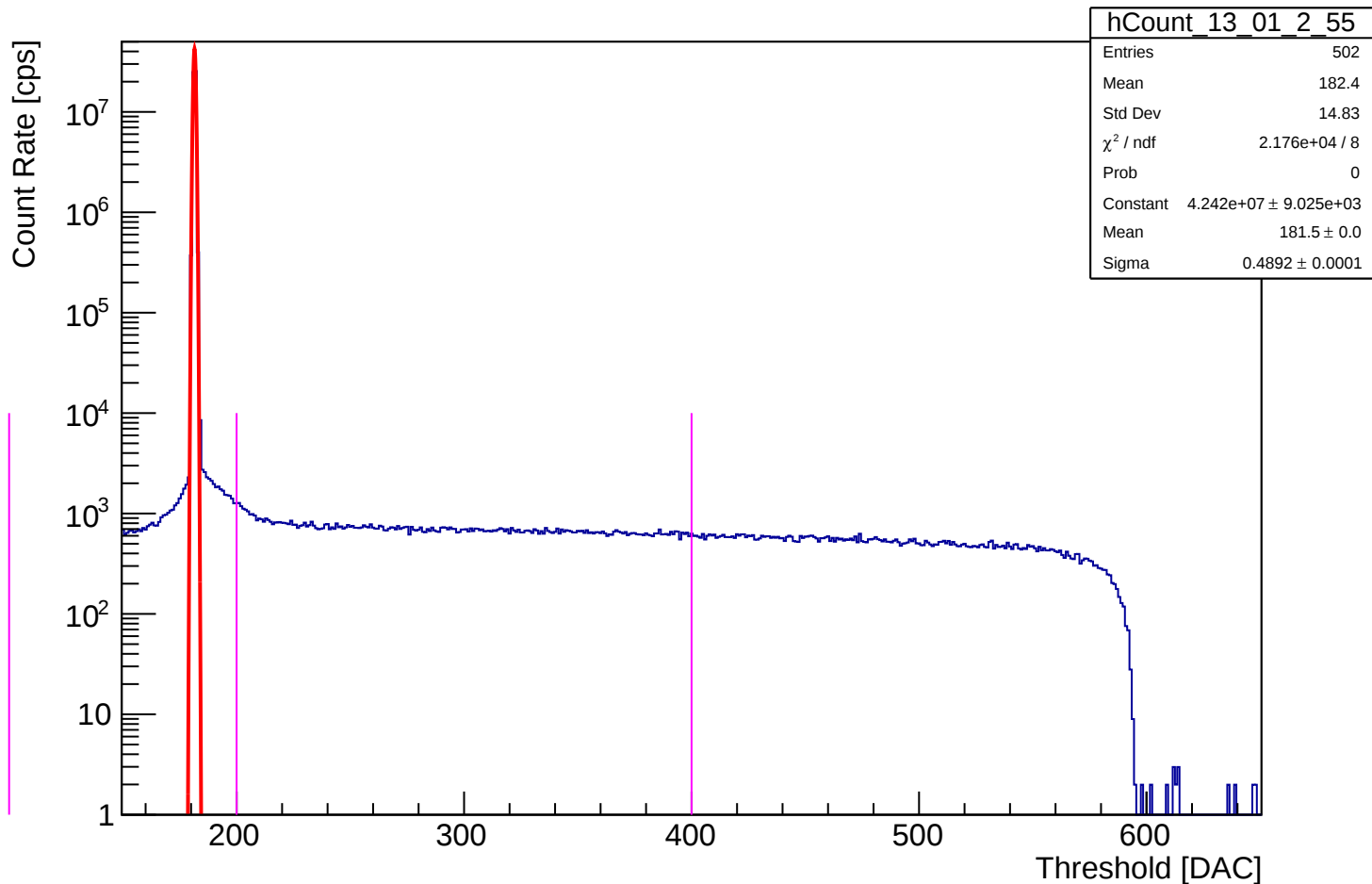
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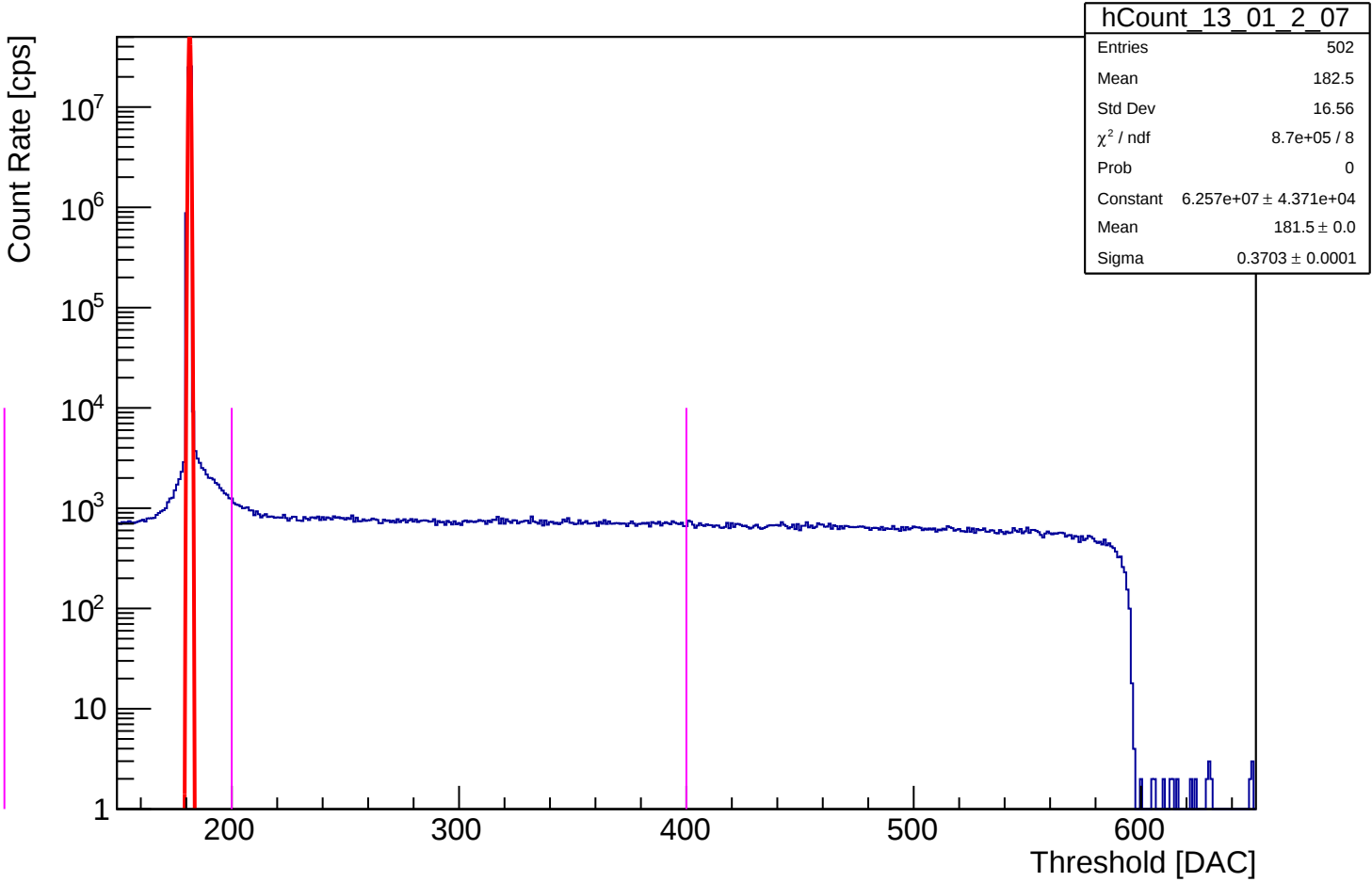


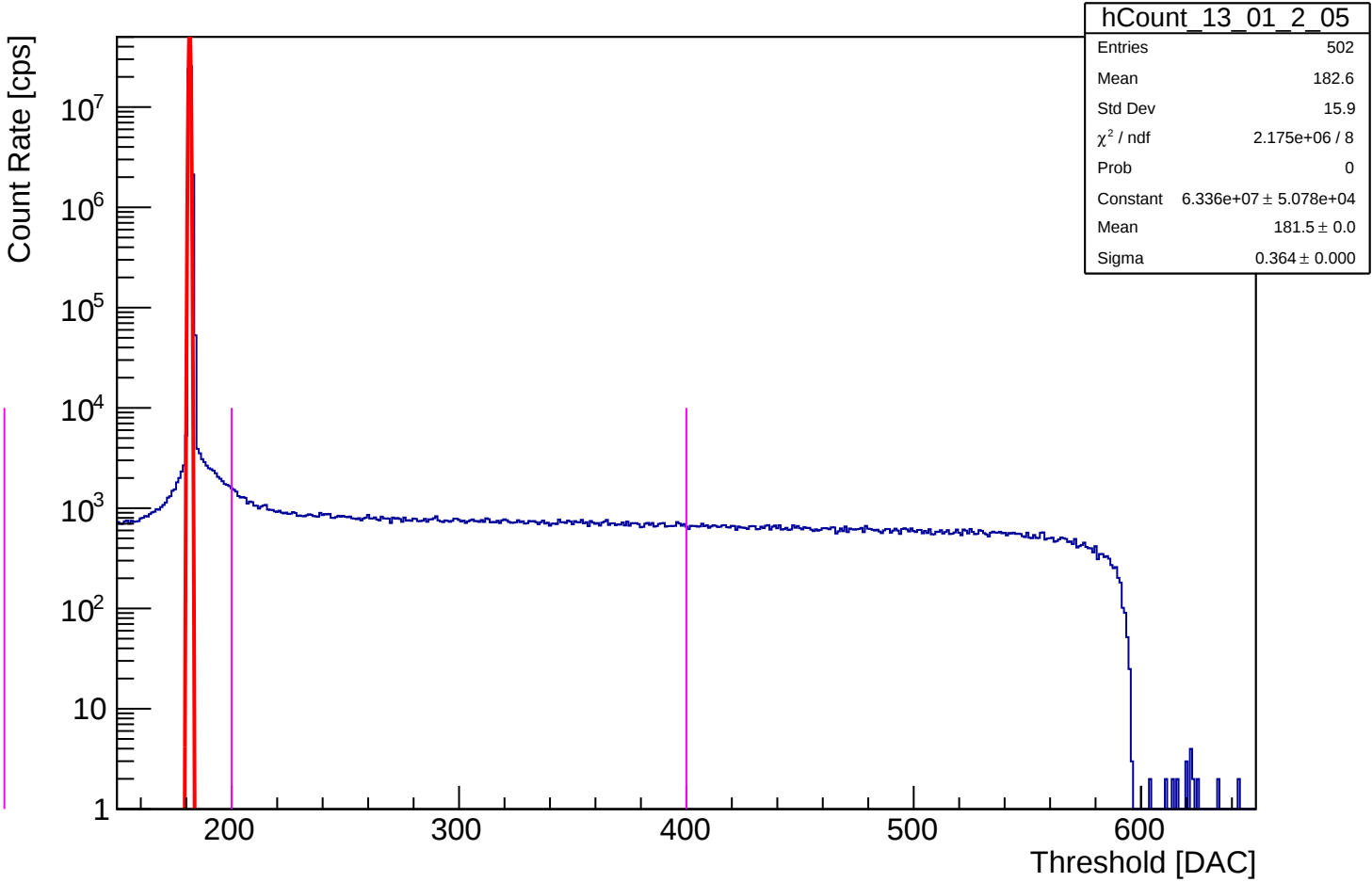
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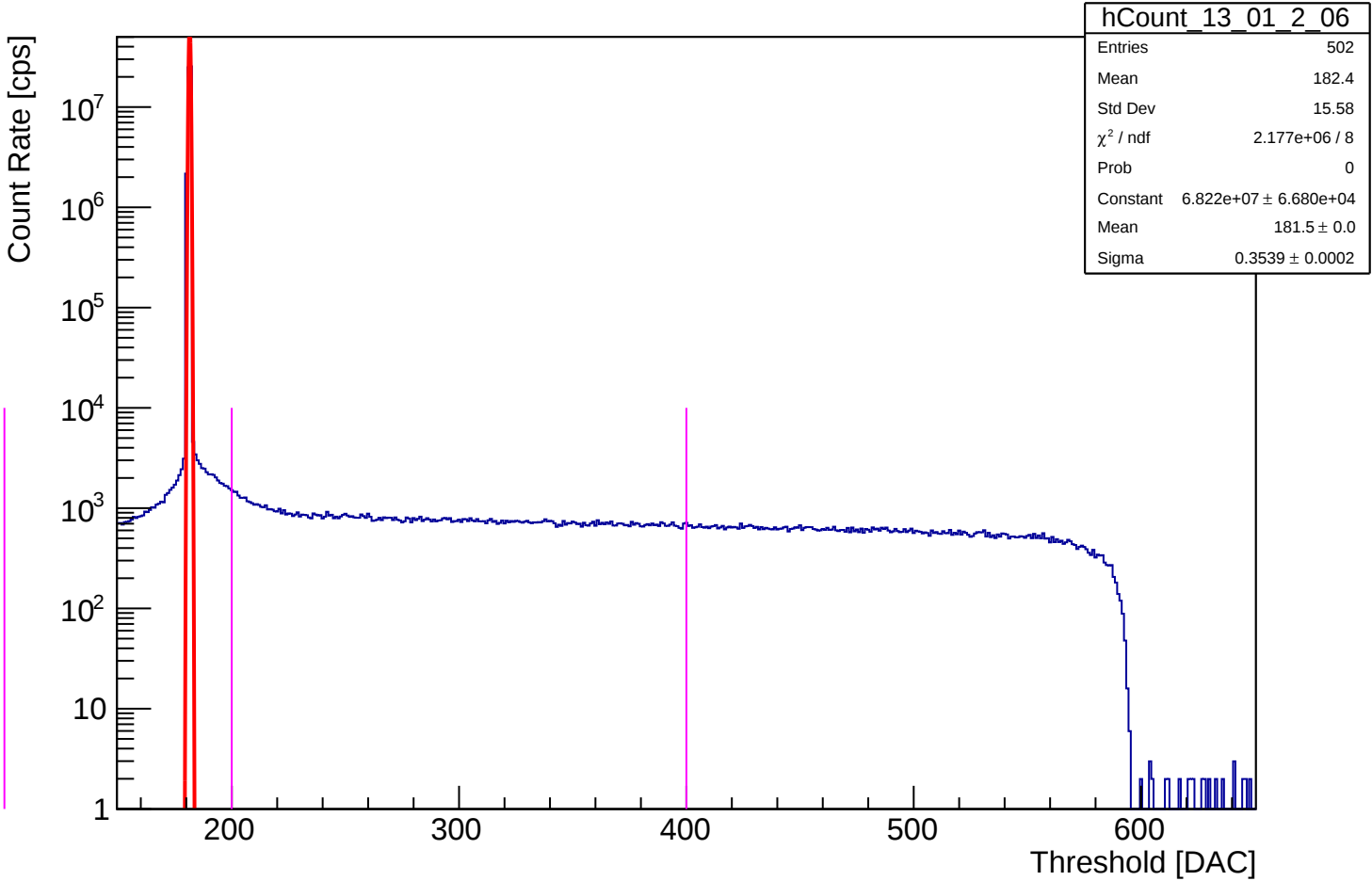


Row 1 Tile 1 Pmt 6 Pixel 48 Slot 13 Fiber 1 Asic 2 Channel 55

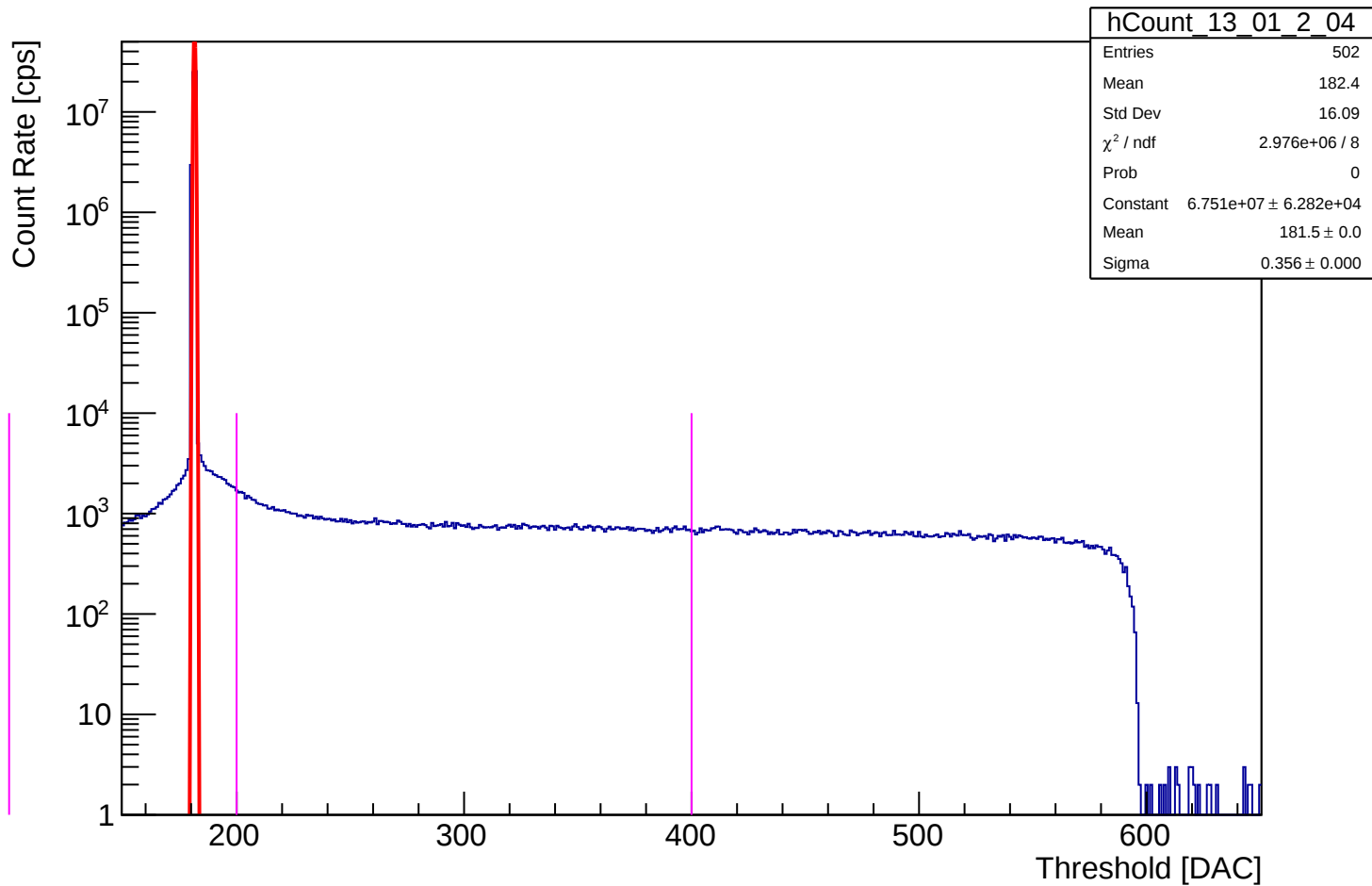




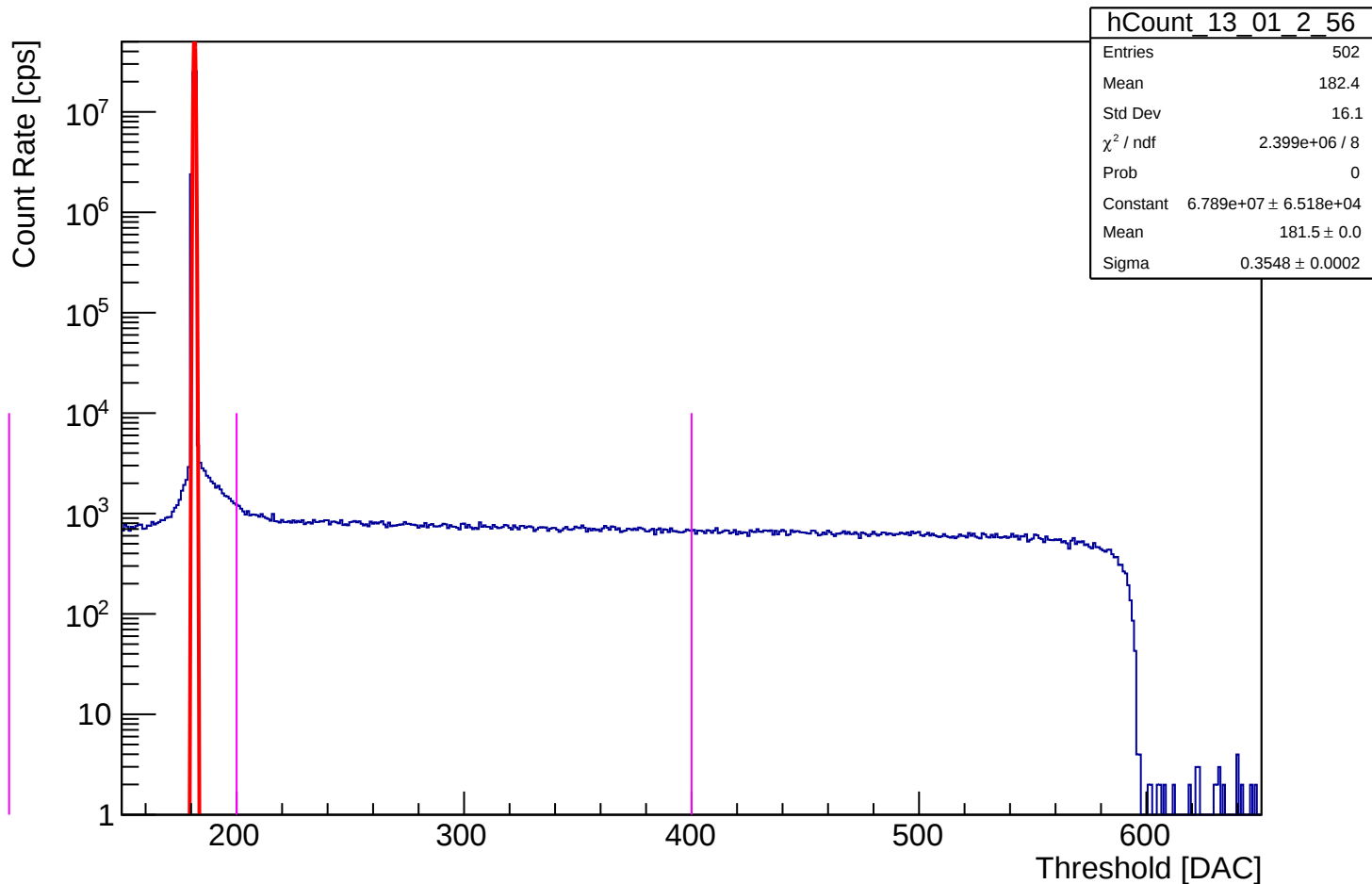




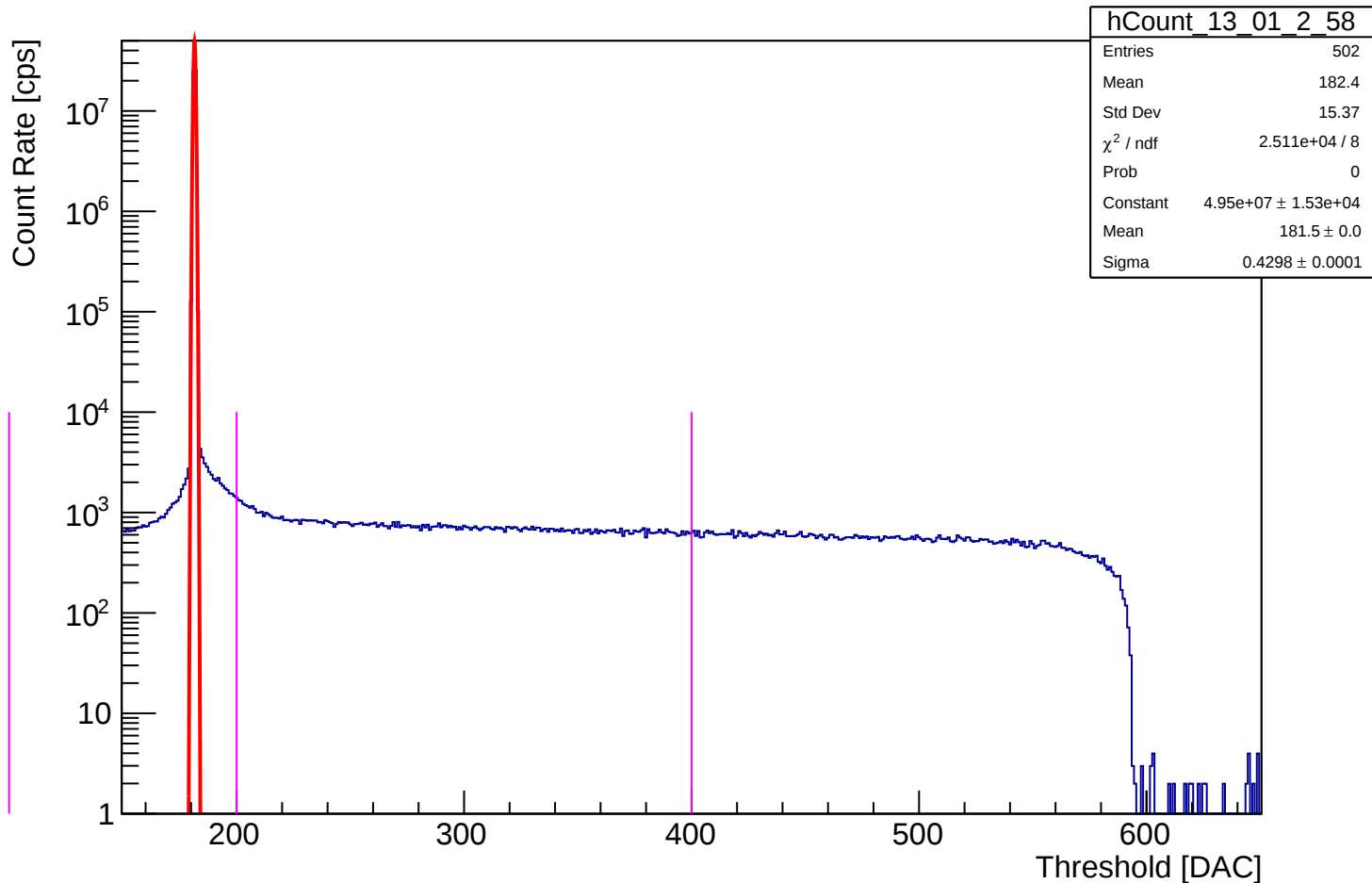
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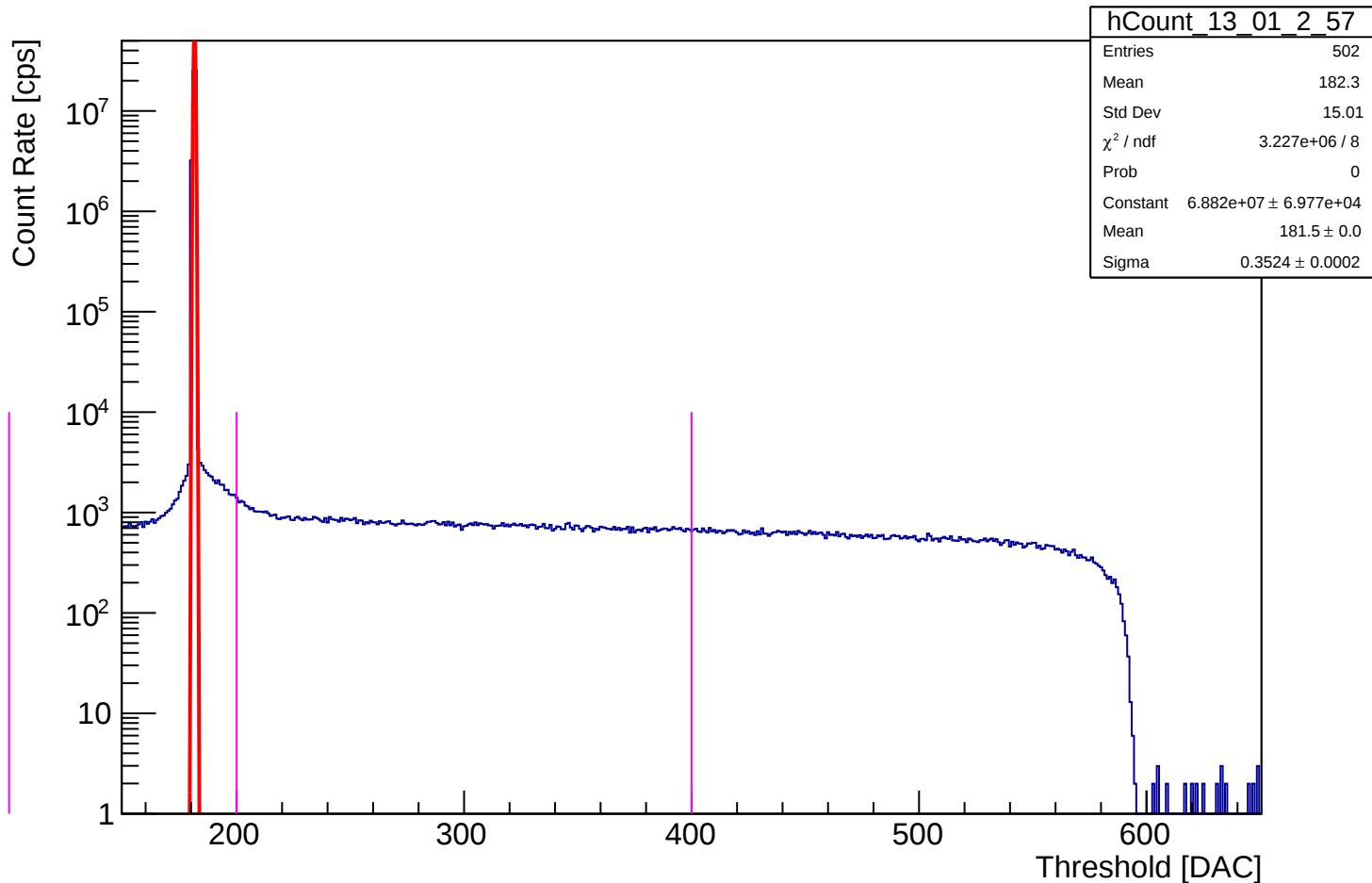
Row 1 Tile 1 Pmt 6 Pixel 53 Slot 13 Fiber 1 Asic 2 Channel 56



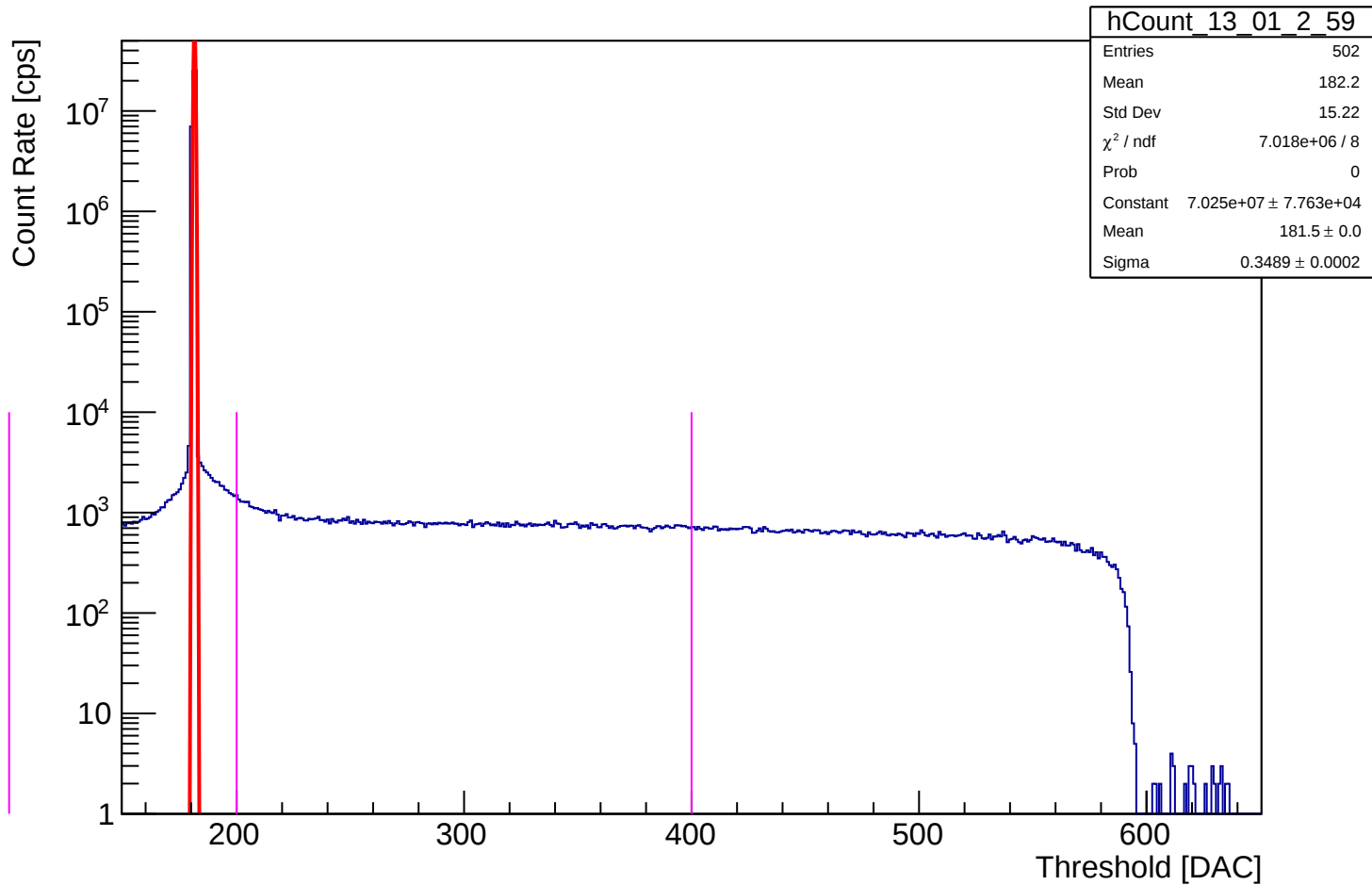
Row 1 Tile 1 Pmt 6 Pixel 54 Slot 13 Fiber 1 Asic 2 Channel 58

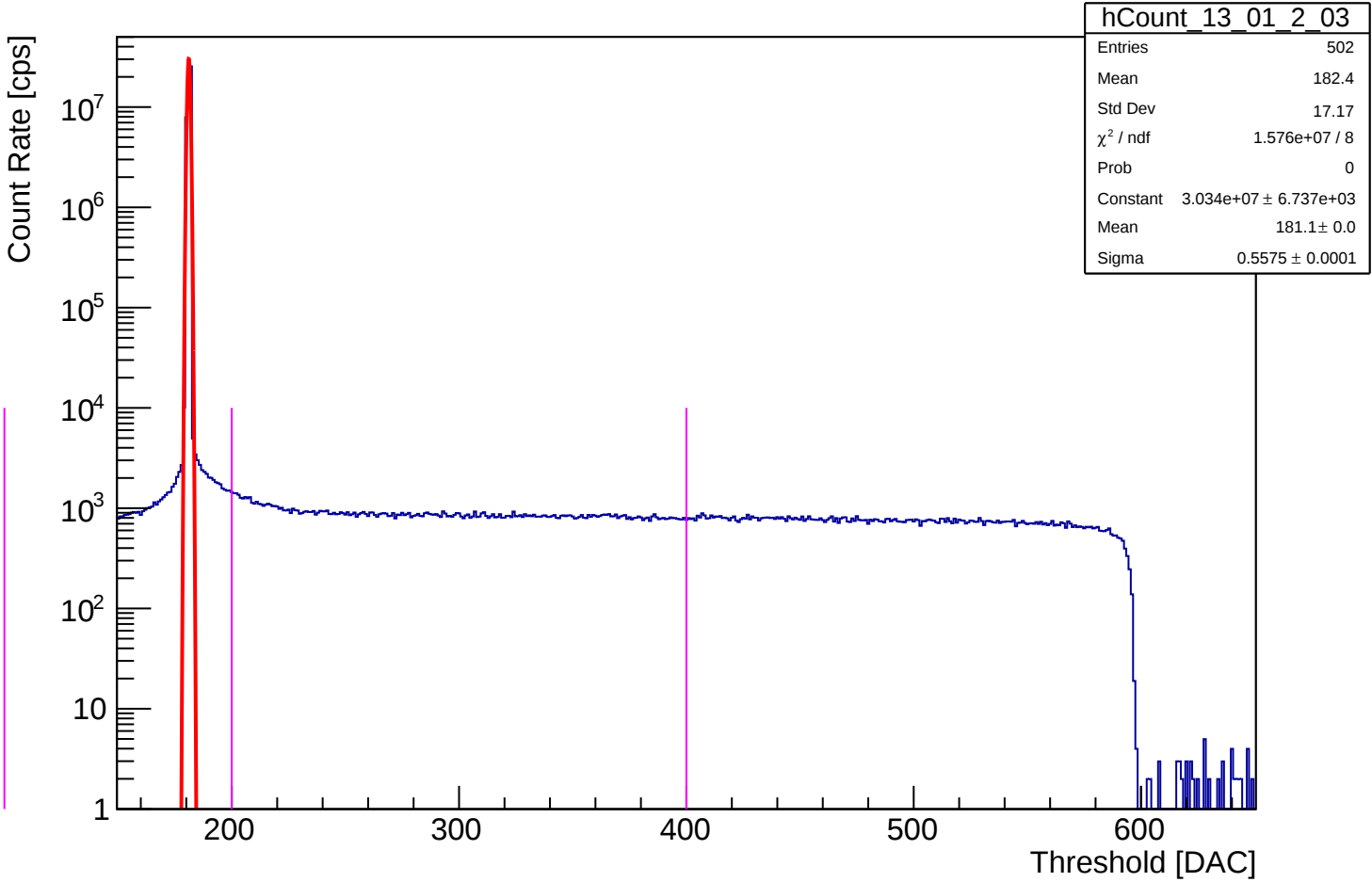


Row 1 Tile 1 Pmt 6 Pixel 55 Slot 13 Fiber 1 Asic 2 Channel 57

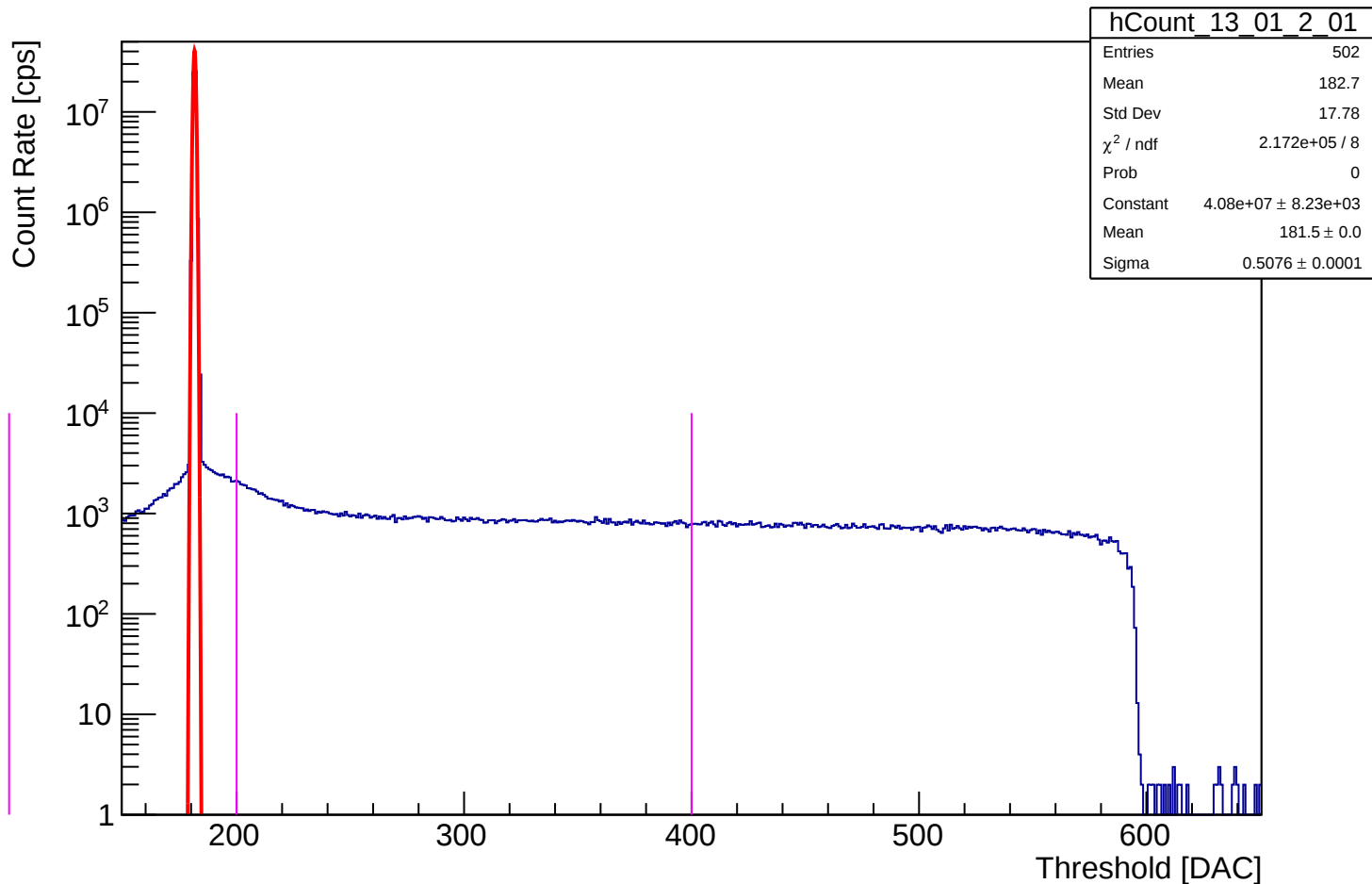


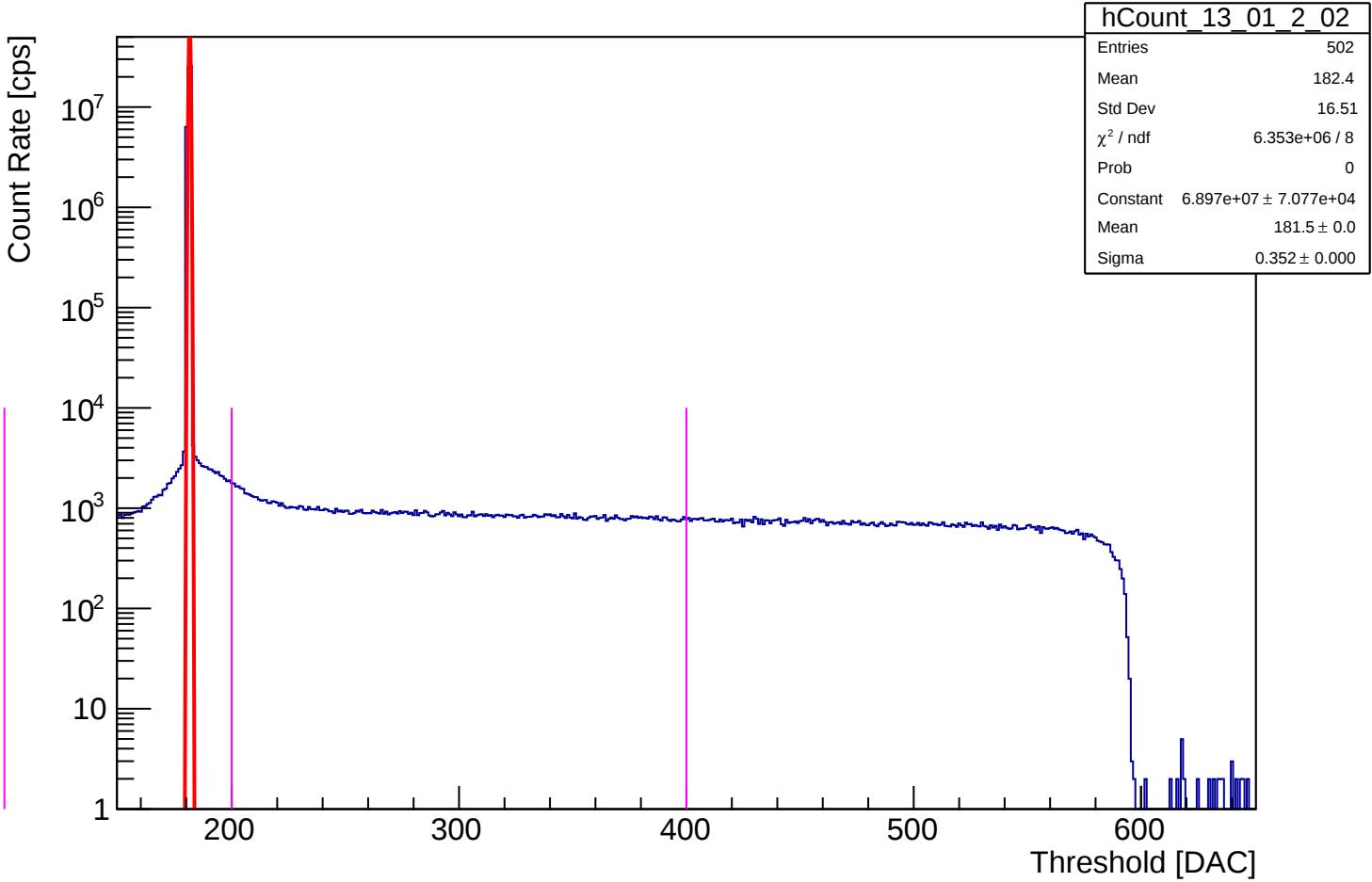
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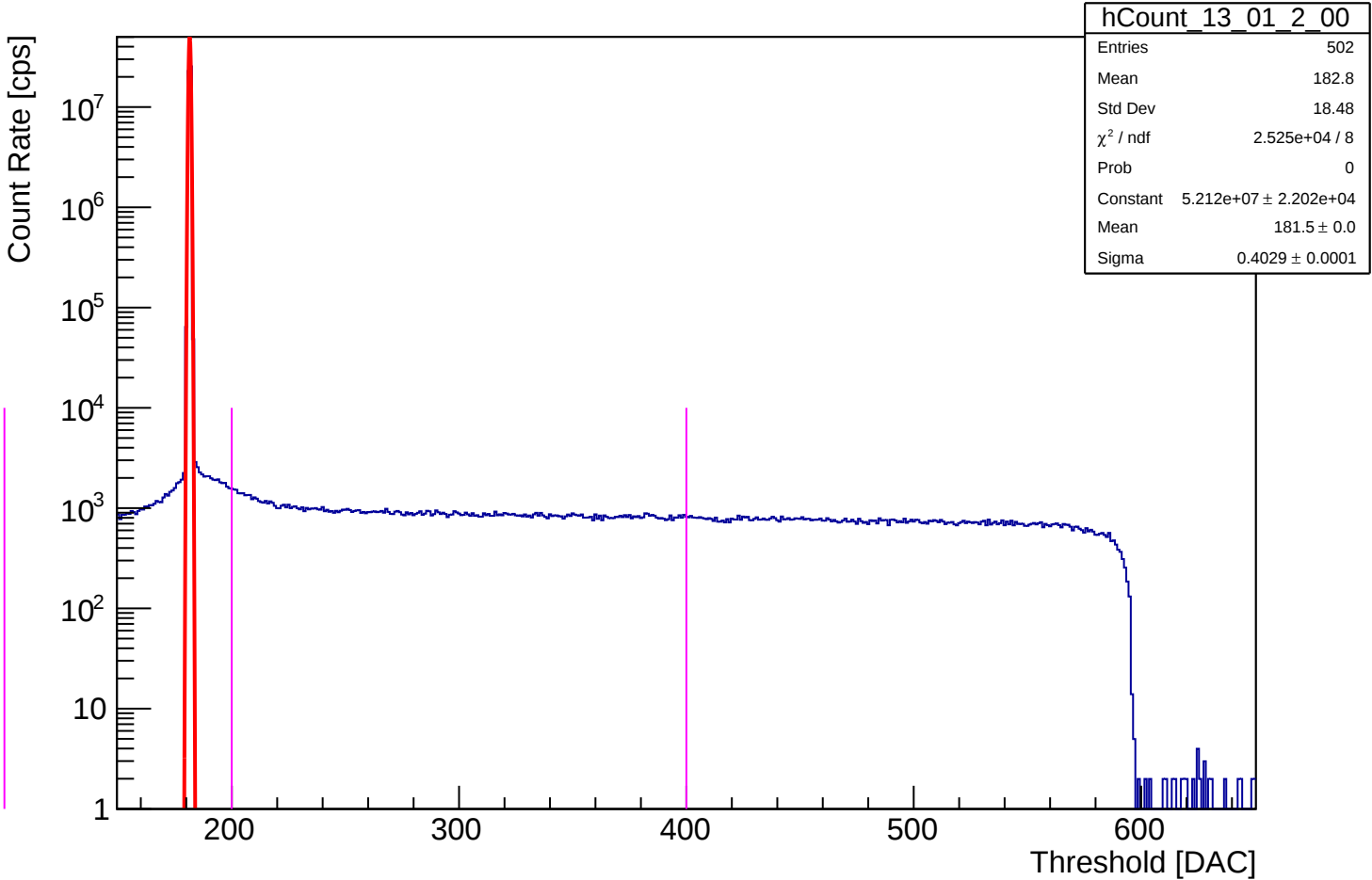




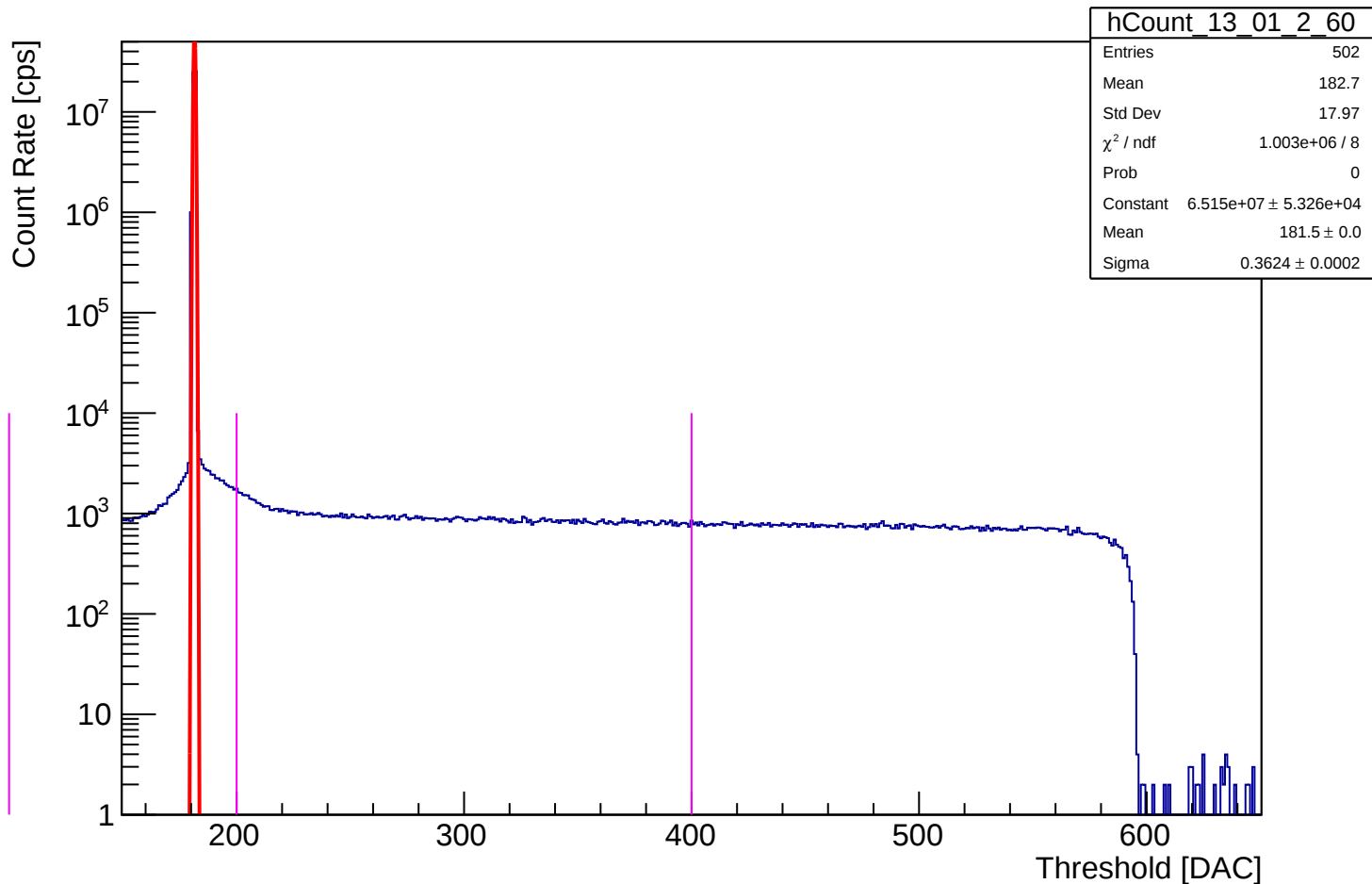
Row 1 Tile 1 Pmt 6 Pixel 58 Slot 13 Fiber 1 Asic 2 Channel 1



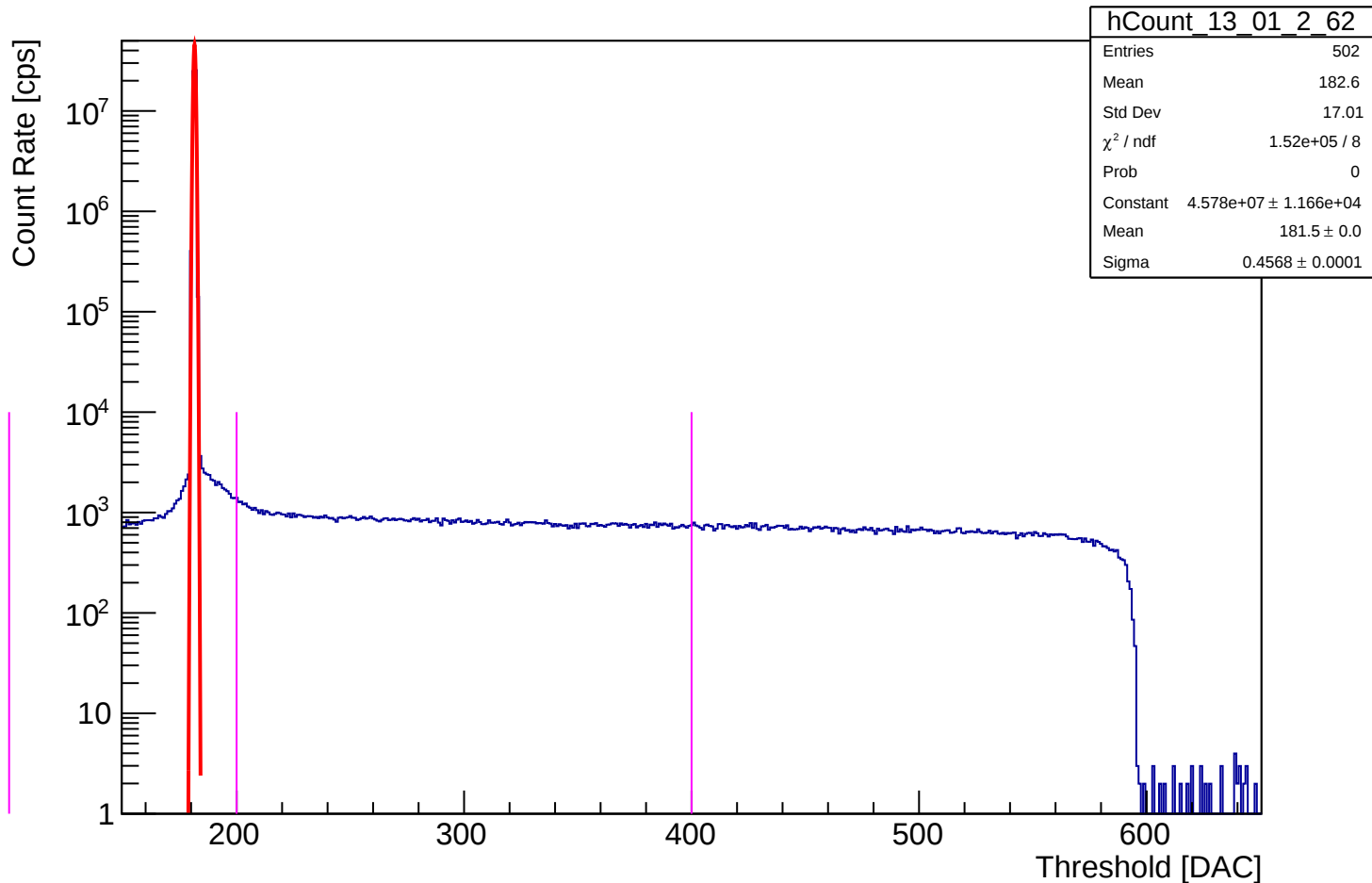




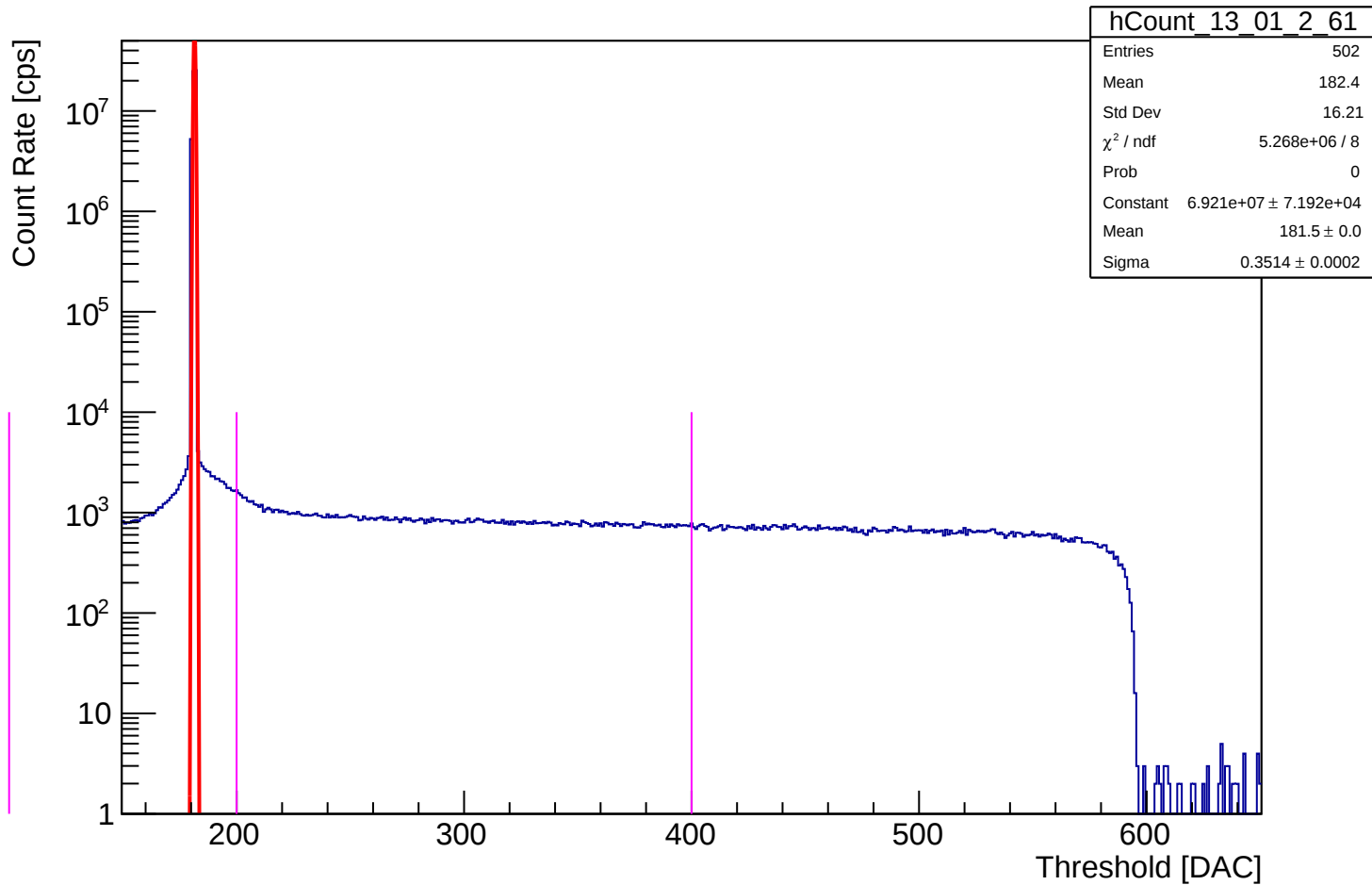
Row 1 Tile 1 Pmt 6 Pixel 61 Slot 13 Fiber 1 Asic 2 Channel 60



Row 1 Tile 1 Pmt 6 Pixel 62 Slot 13 Fiber 1 Asic 2 Channel 62



Row 1 Tile 1 Pmt 6 Pixel 63 Slot 13 Fiber 1 Asic 2 Channel 61



Row 1 Tile 1 Pmt 6 Pixel 64 Slot 13 Fiber 1 Asic 2 Channel 63

