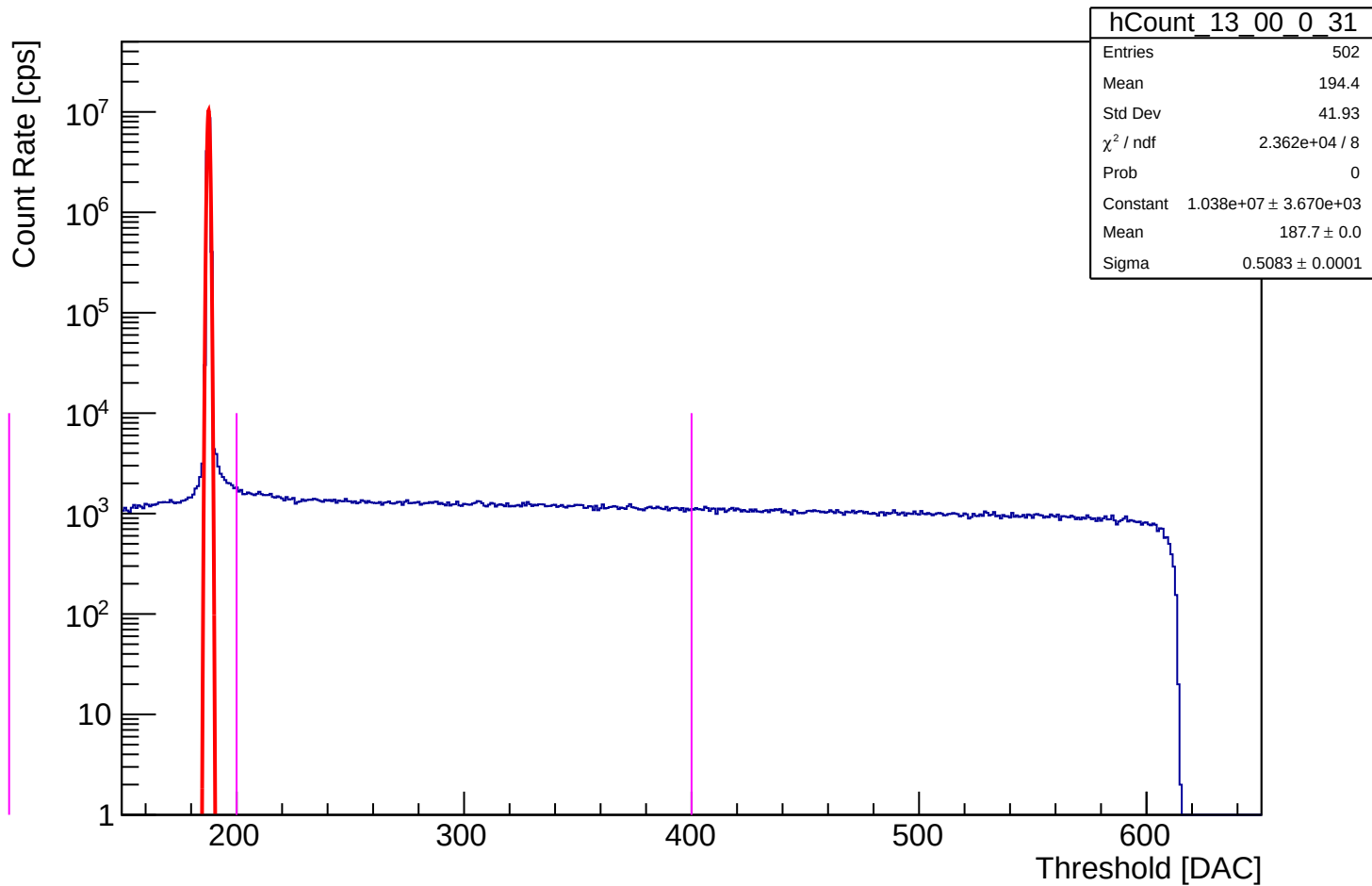
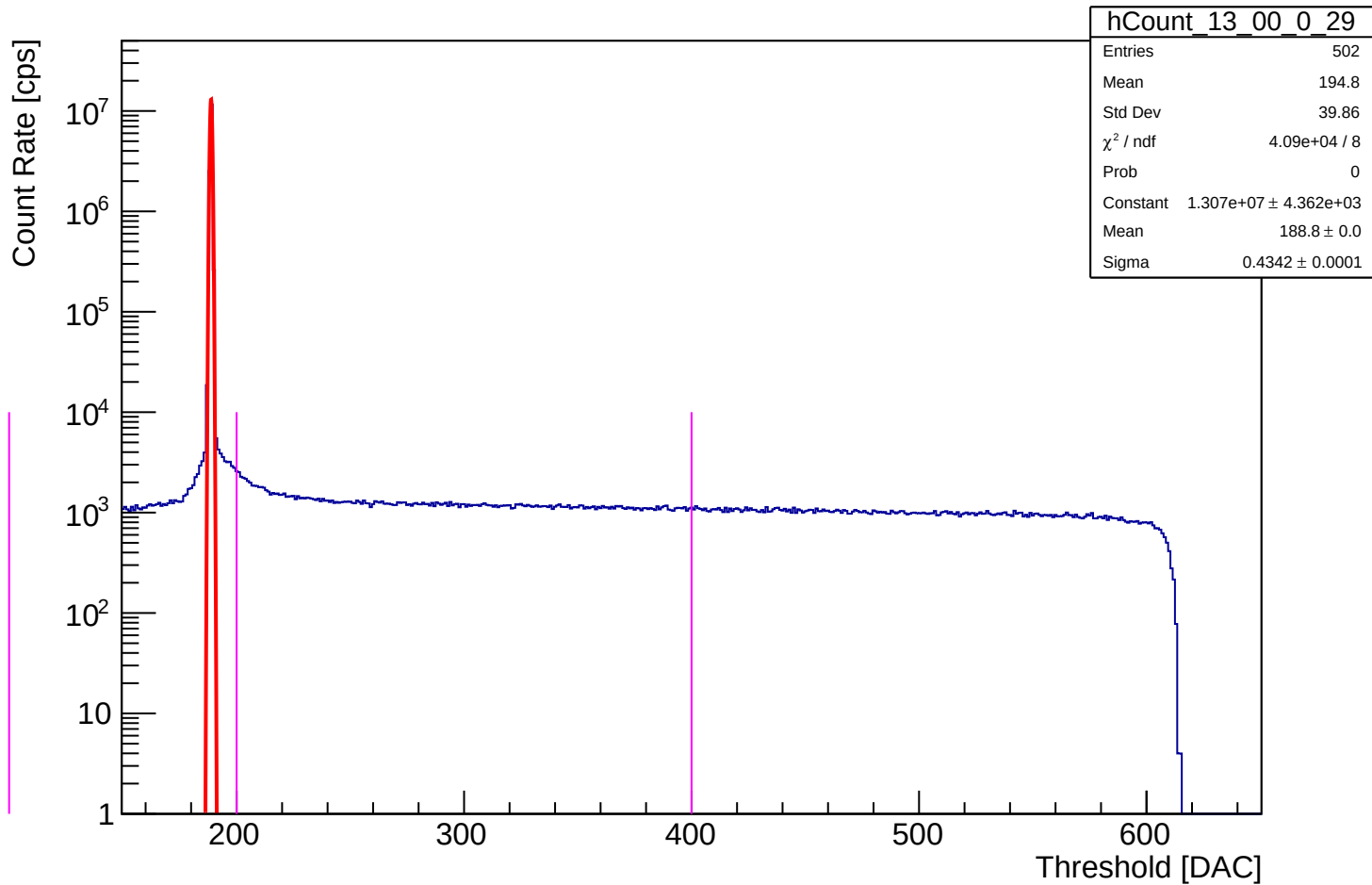


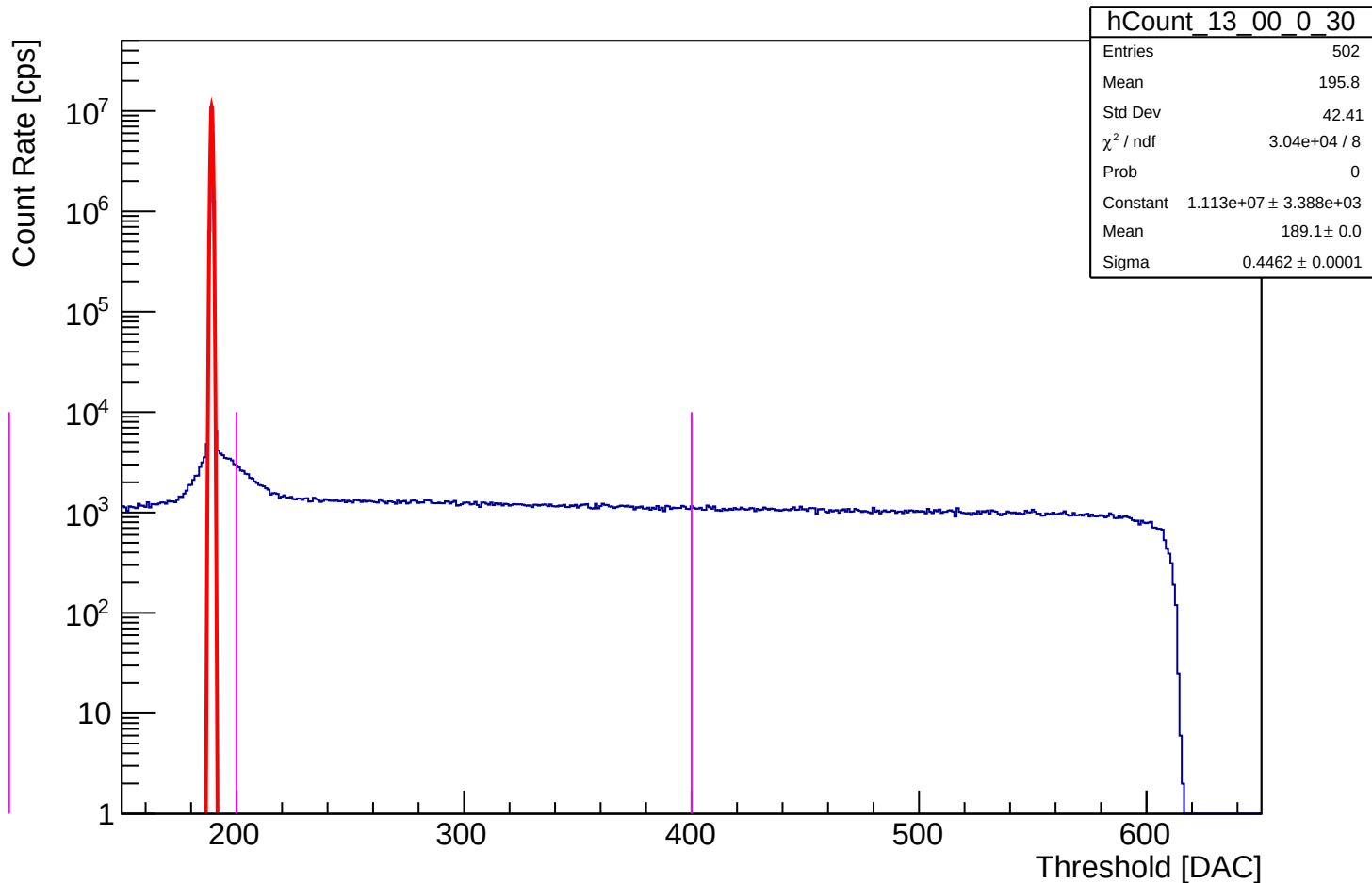
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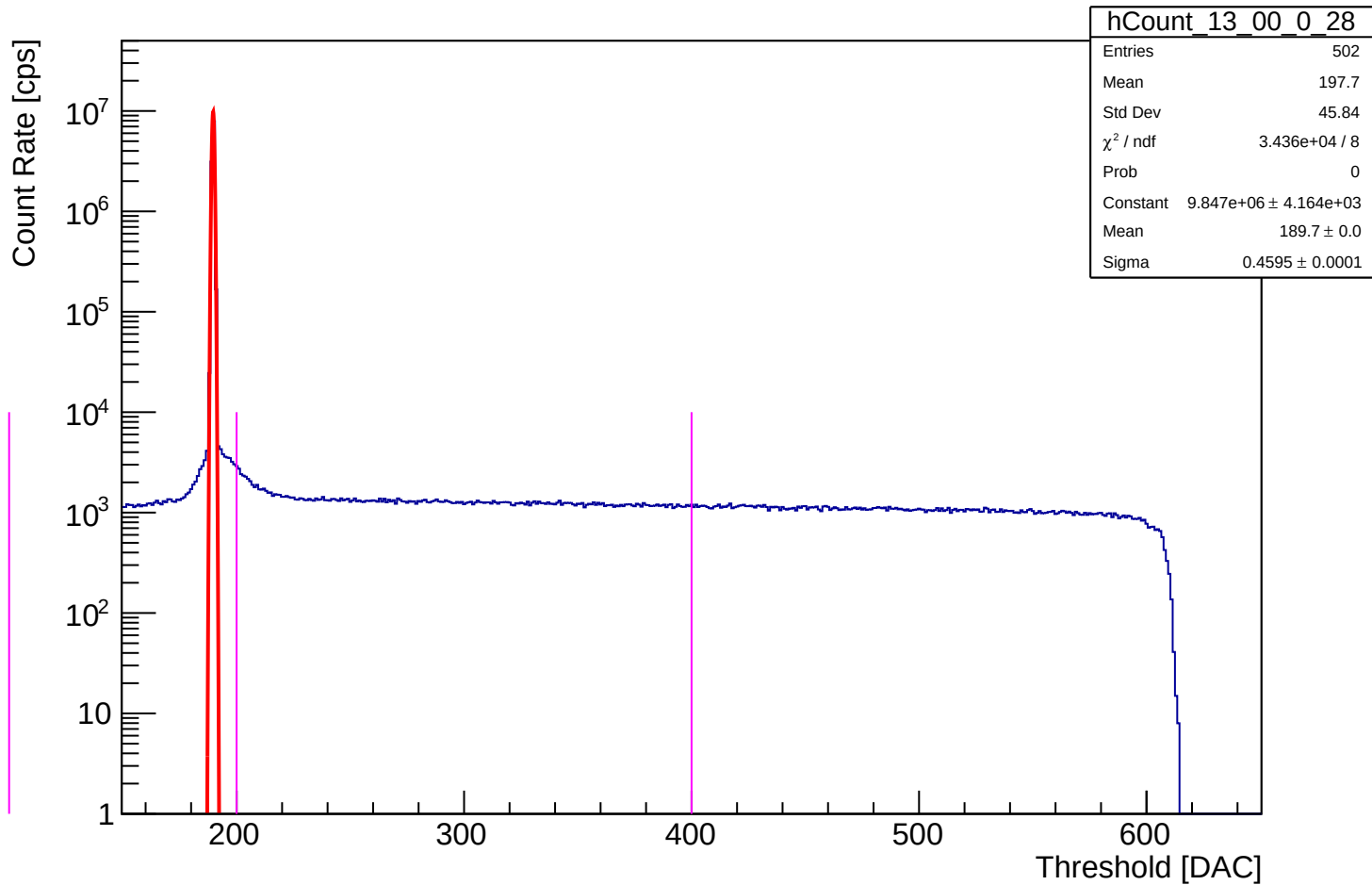
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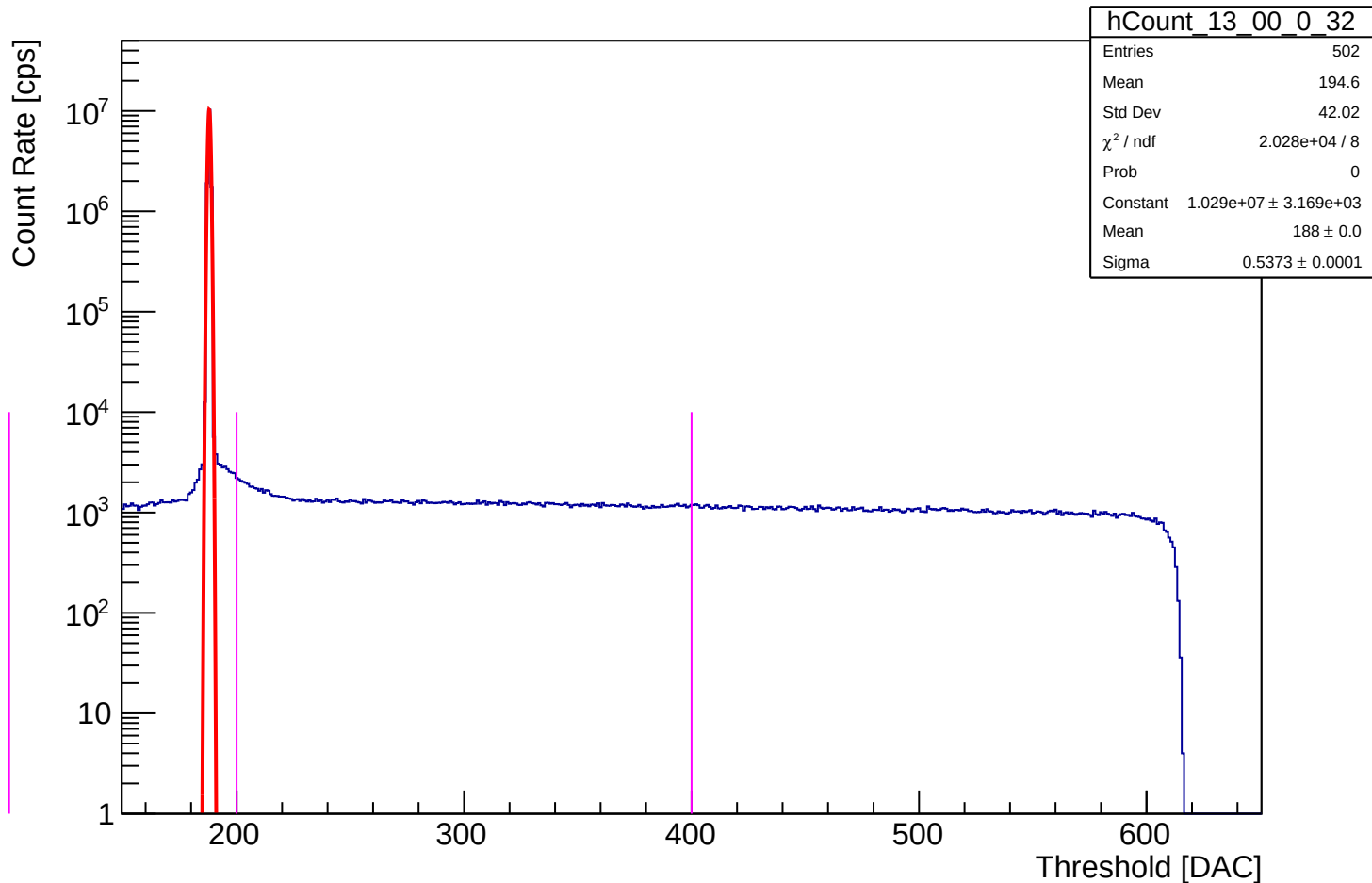
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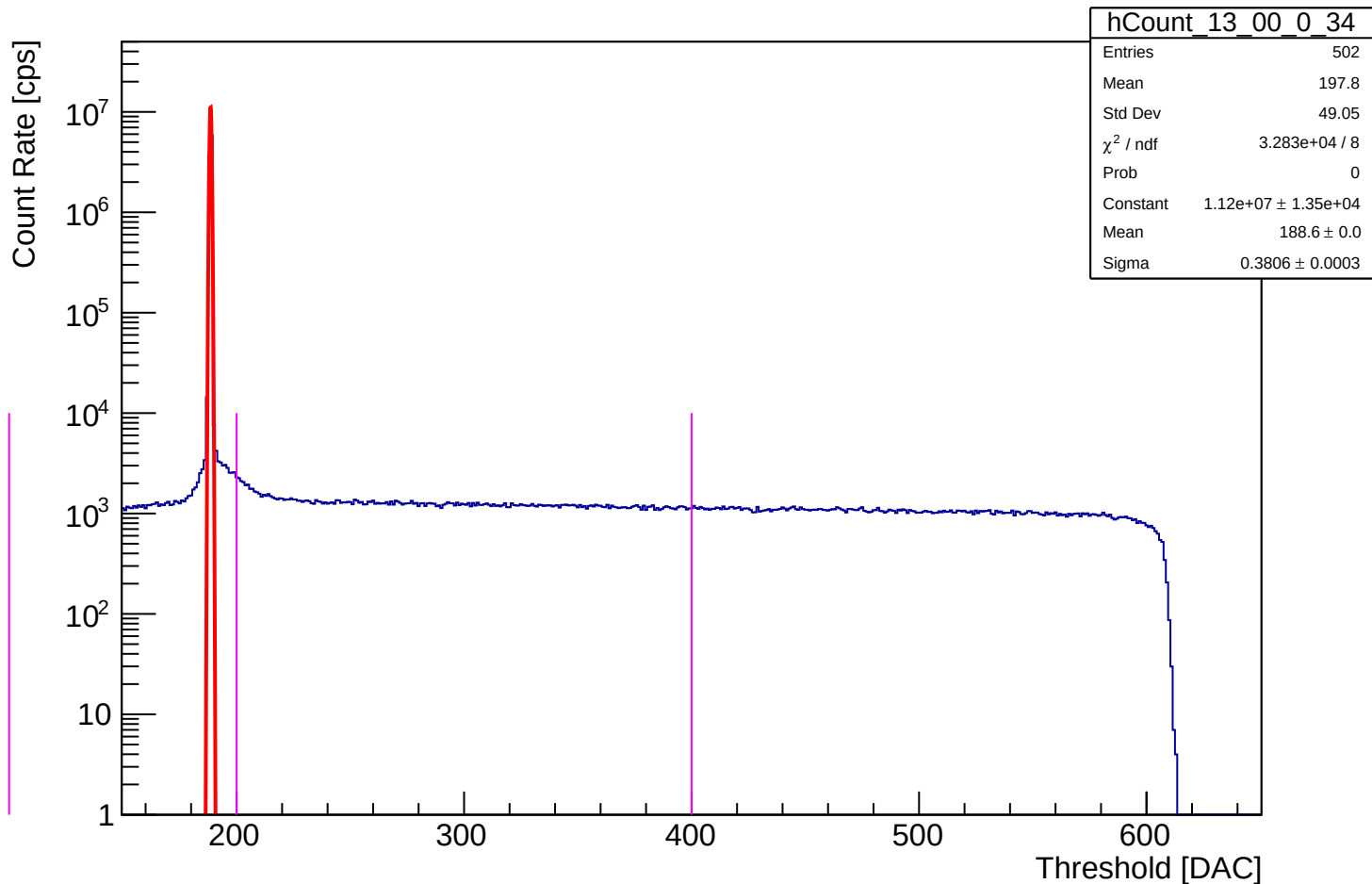
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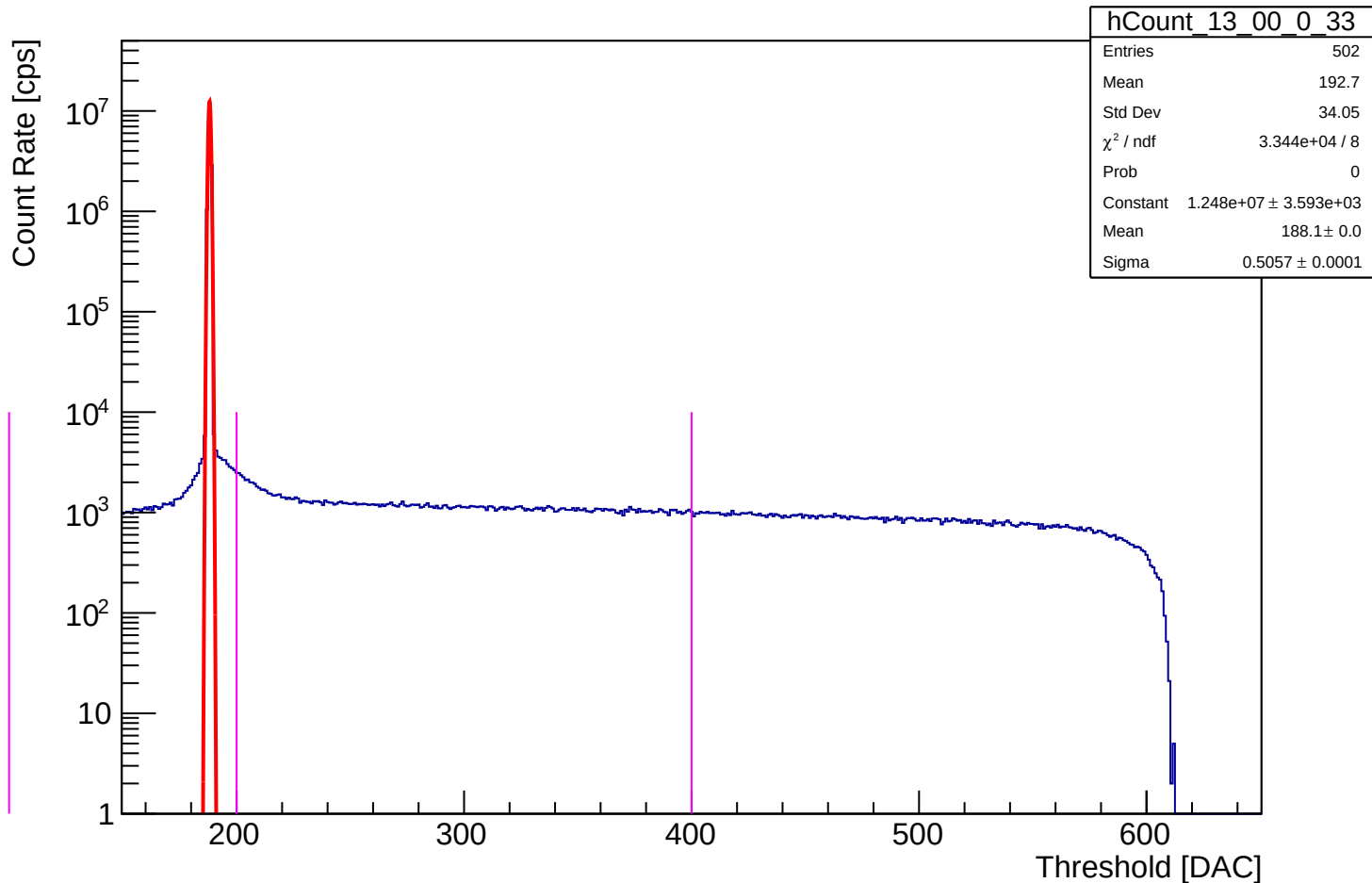
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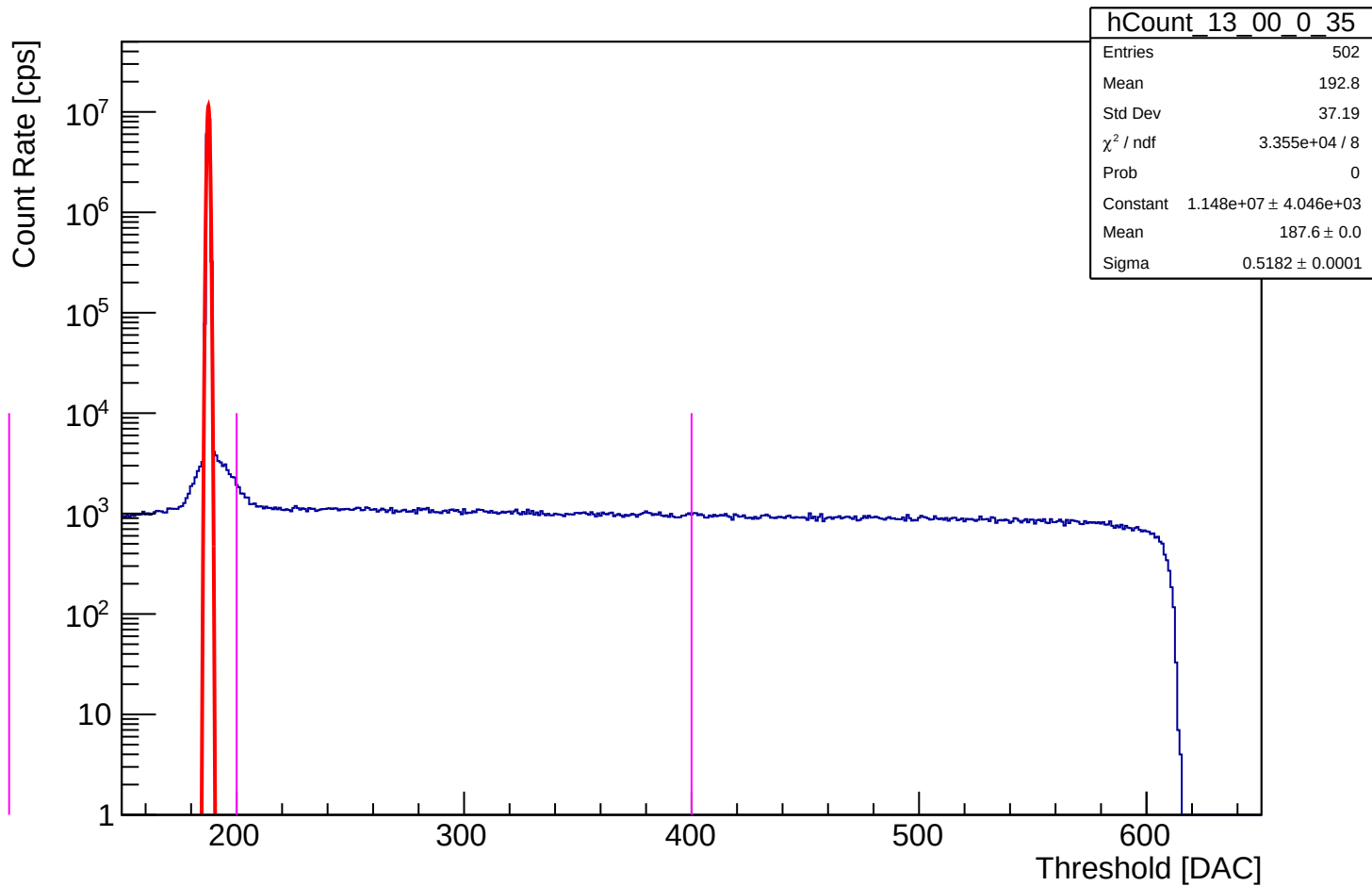
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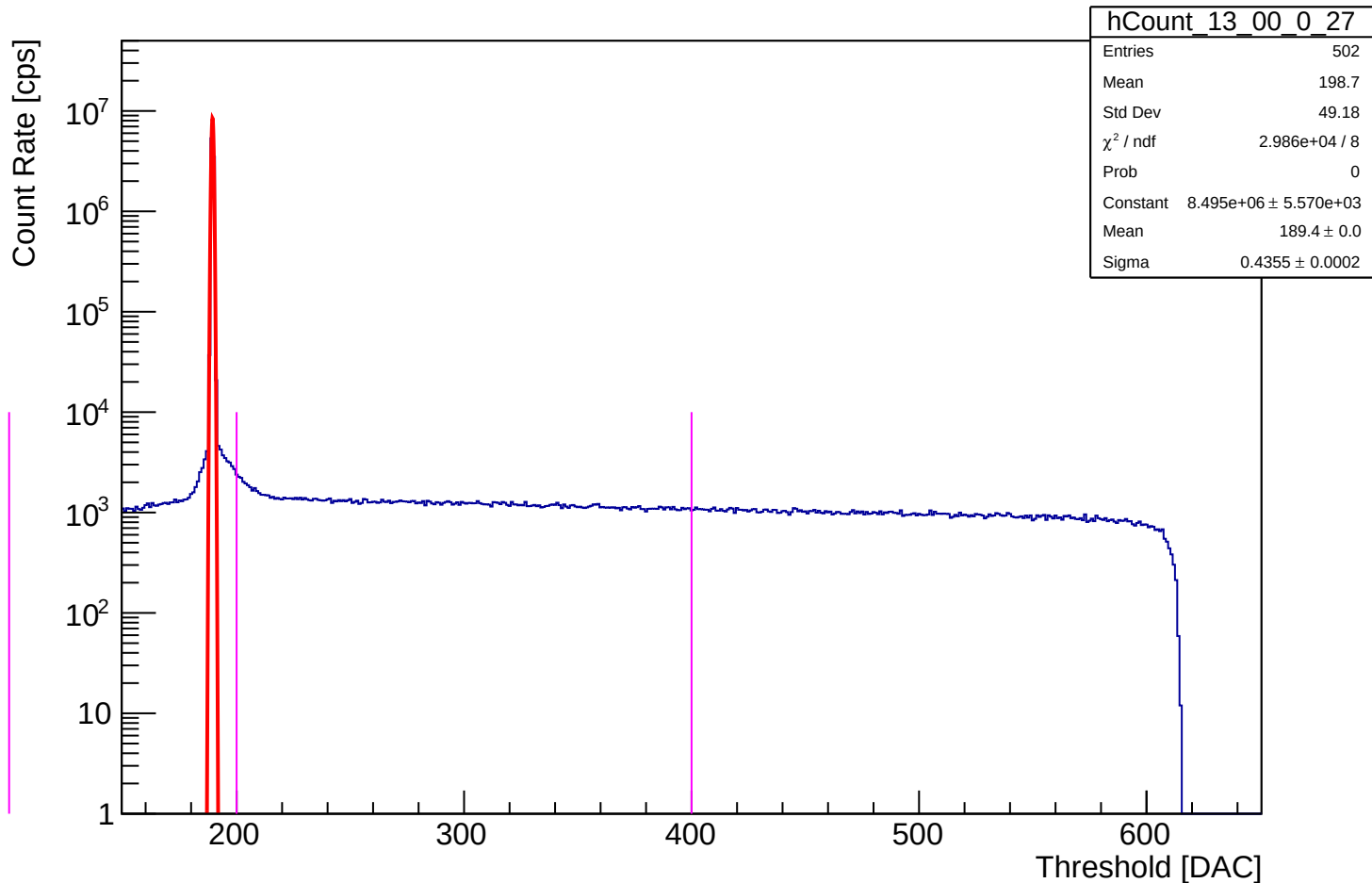
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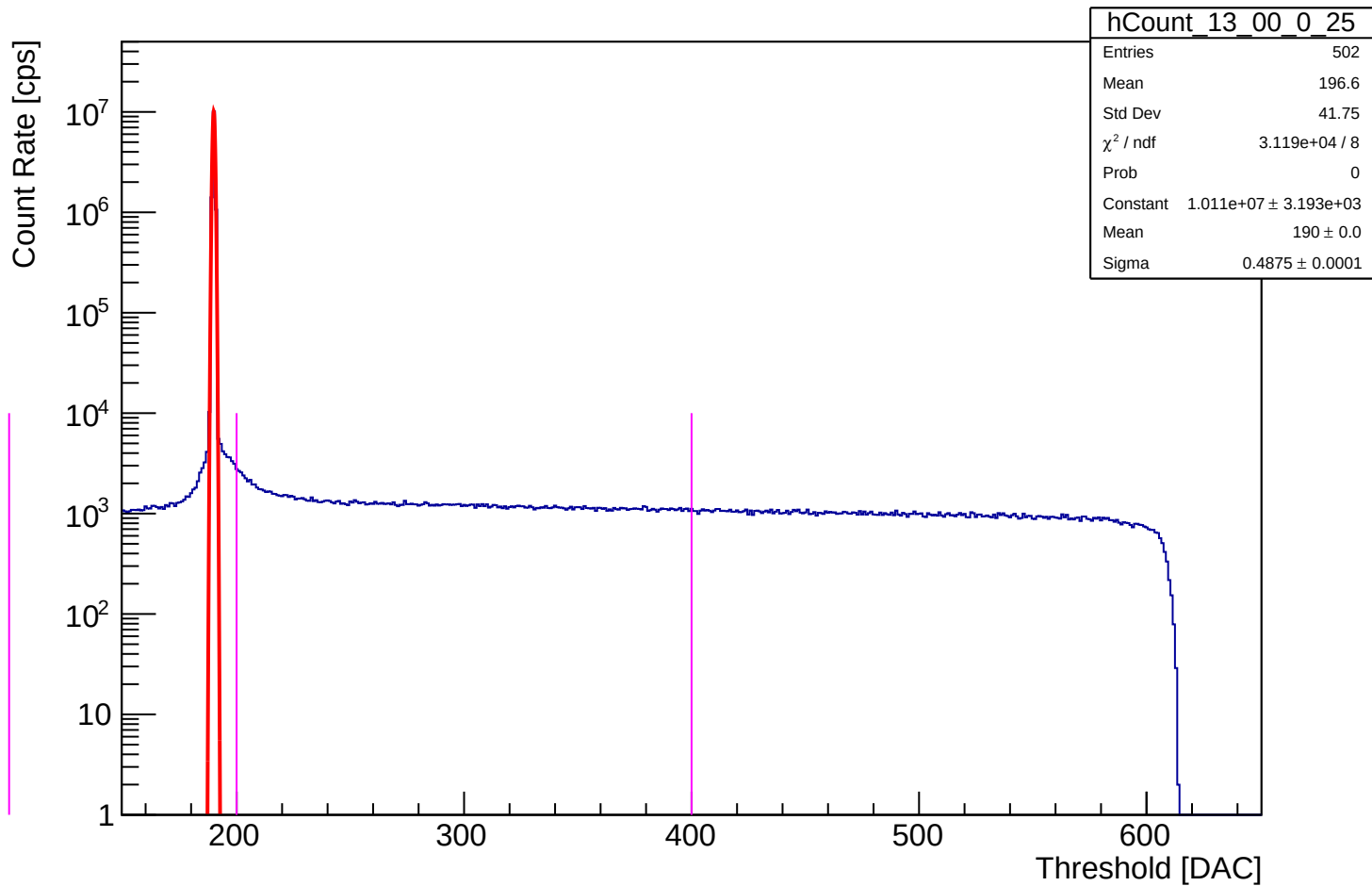
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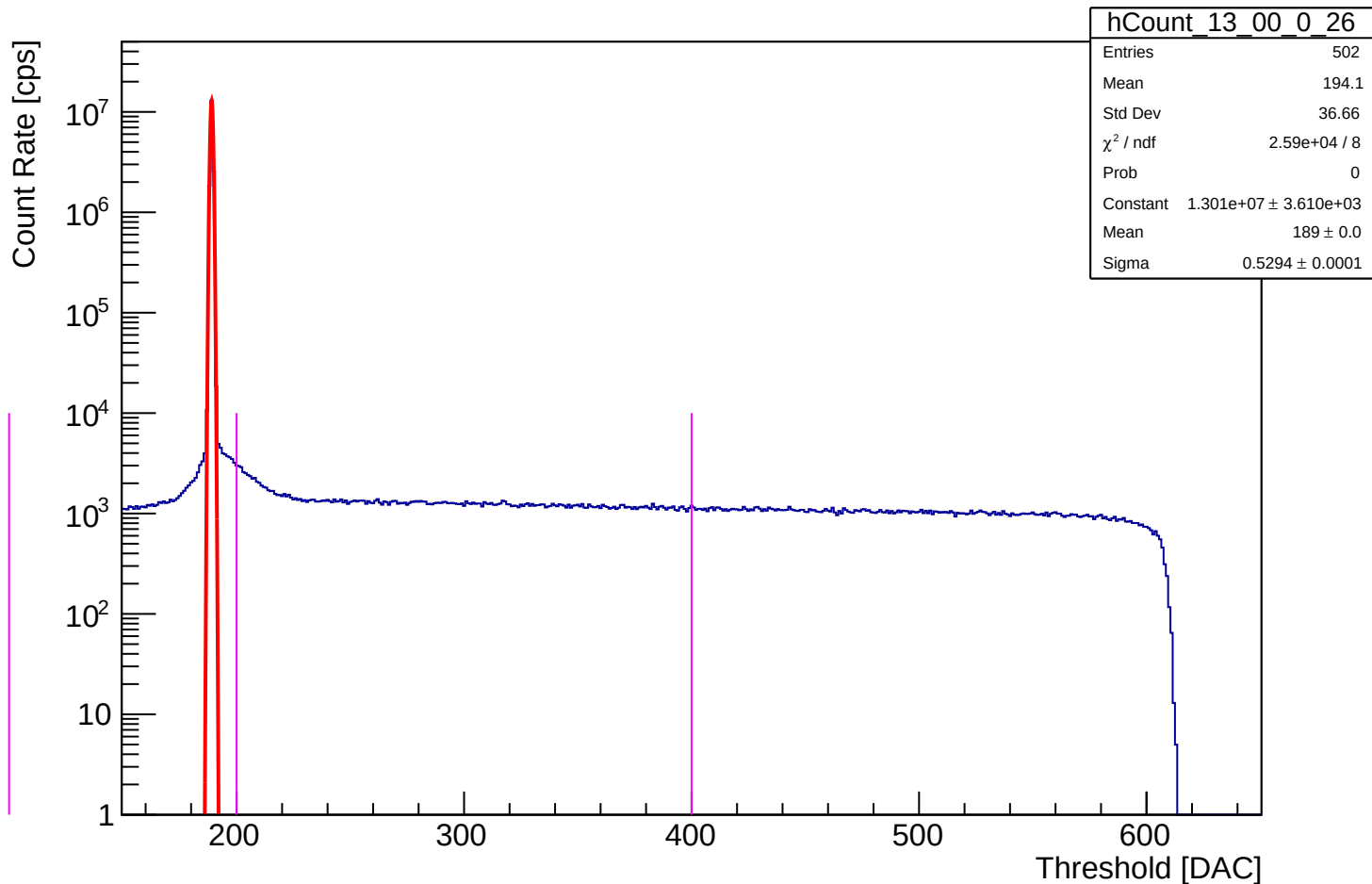
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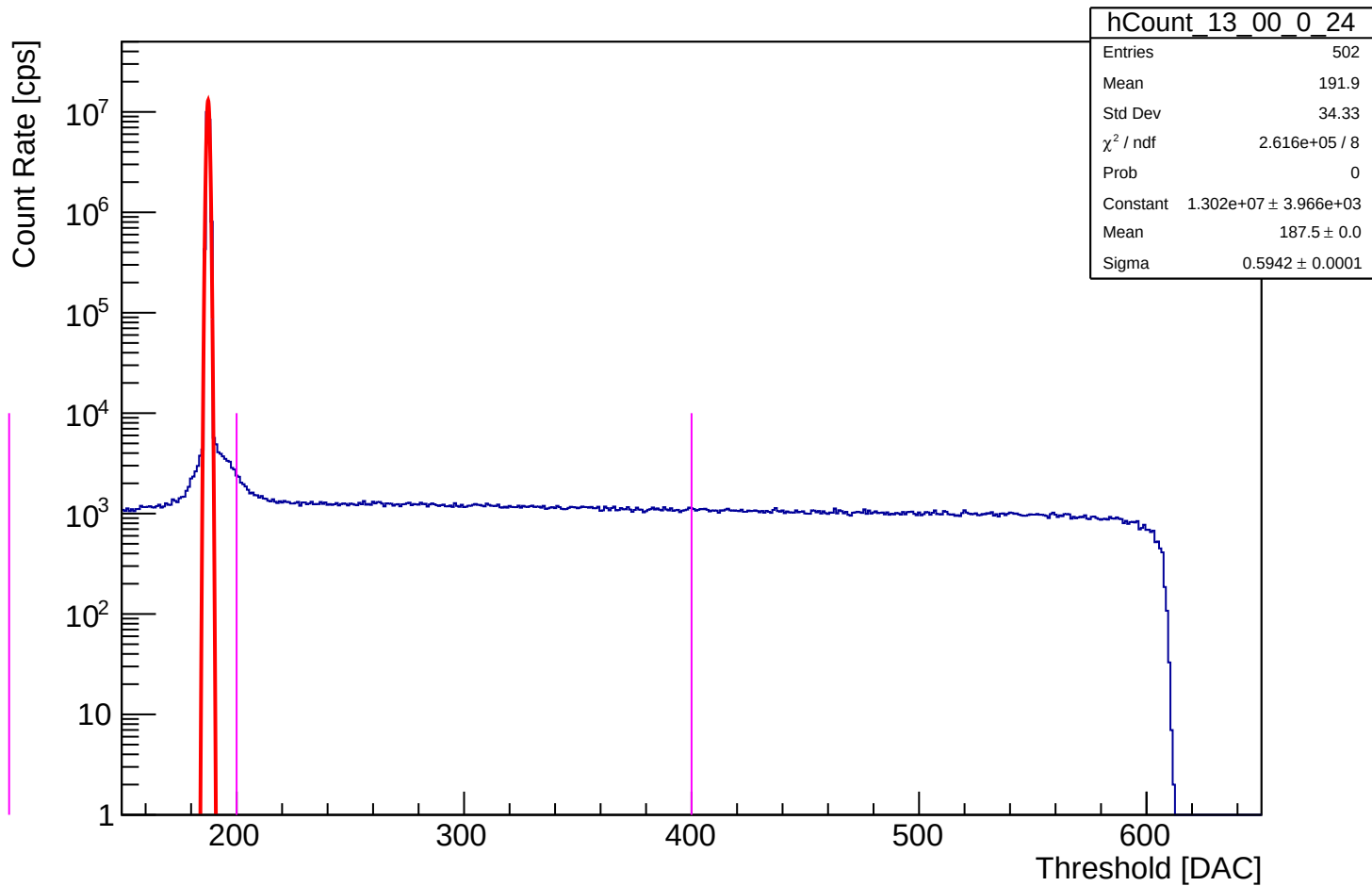
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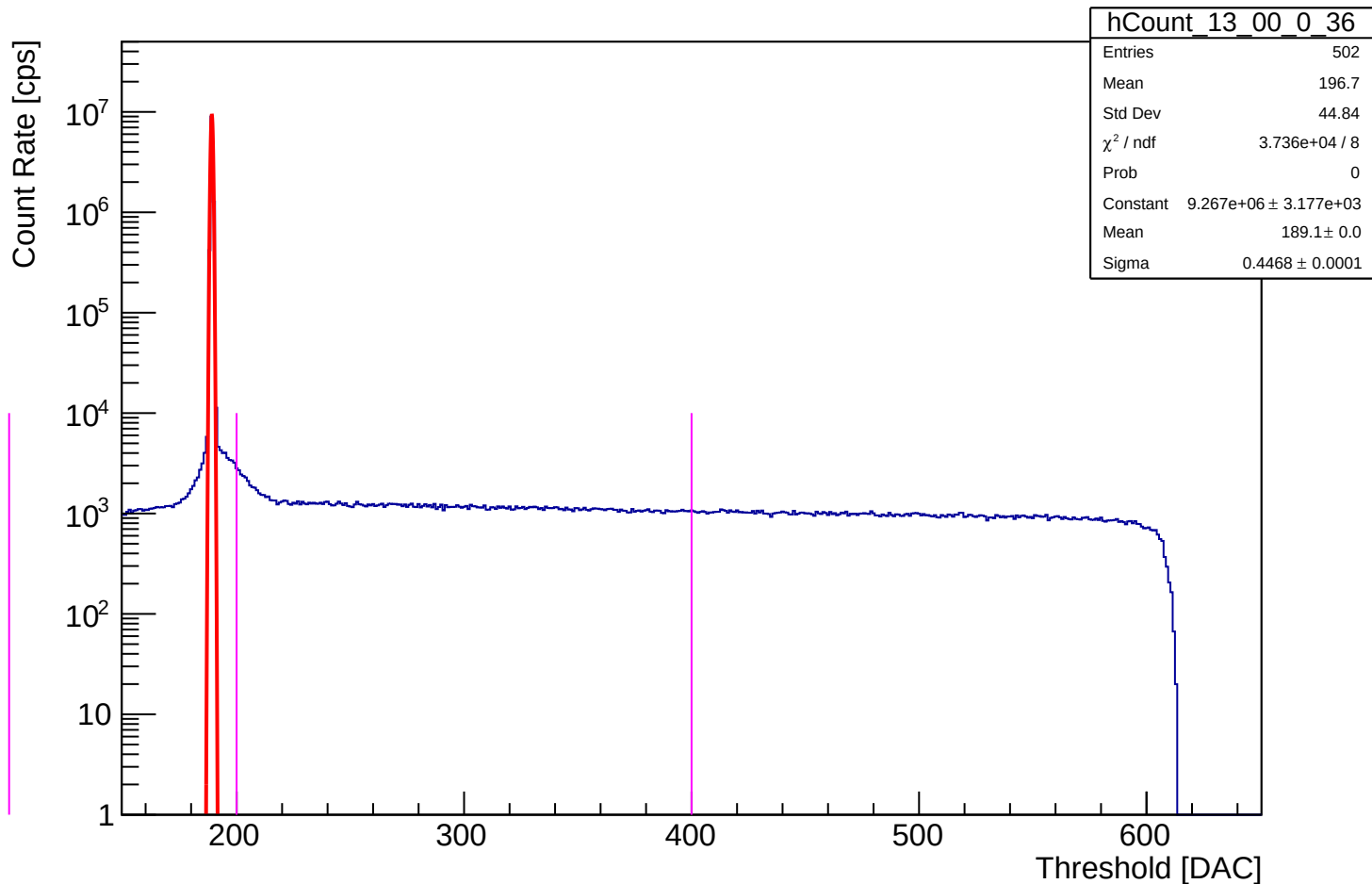
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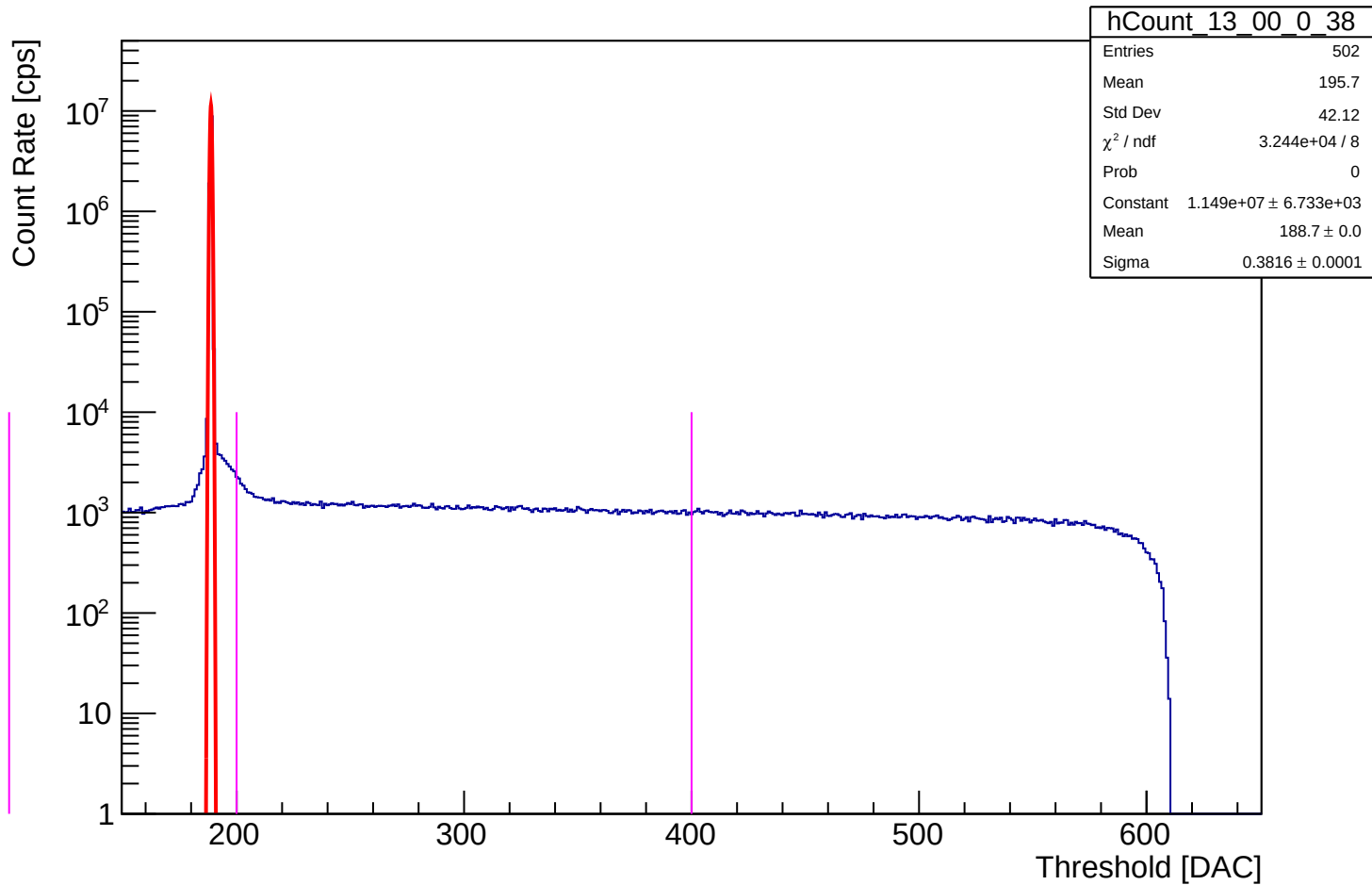
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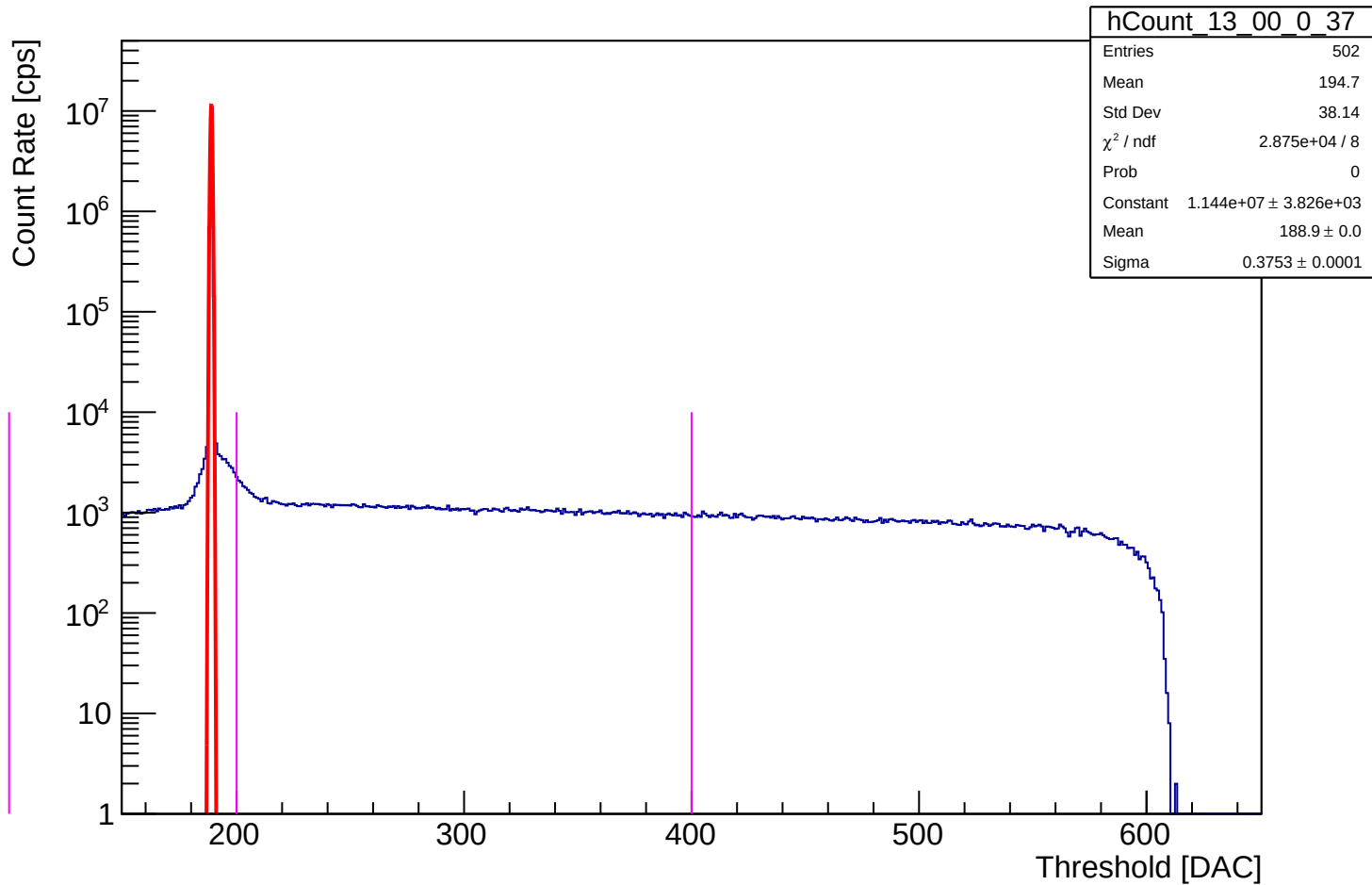
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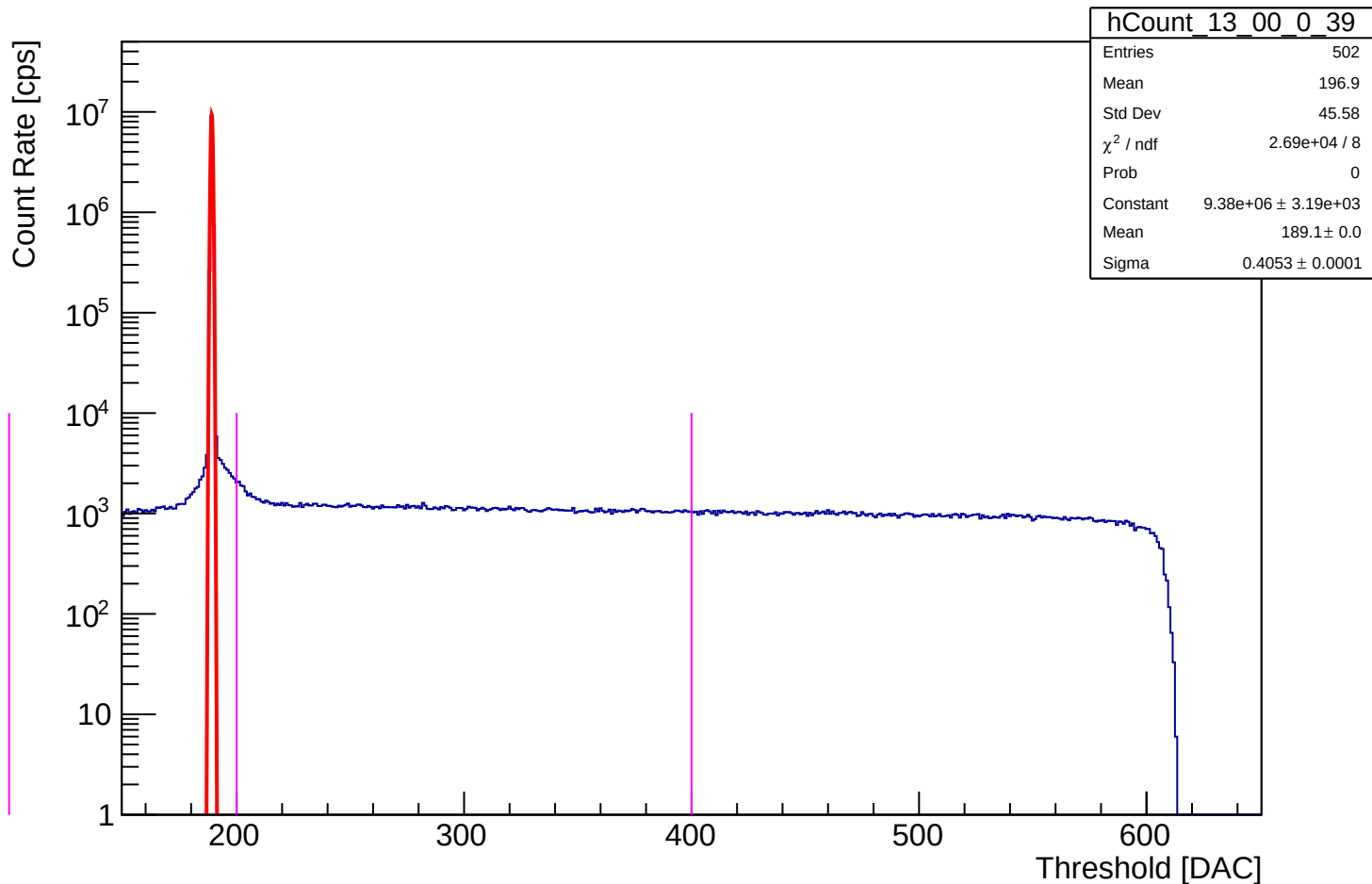
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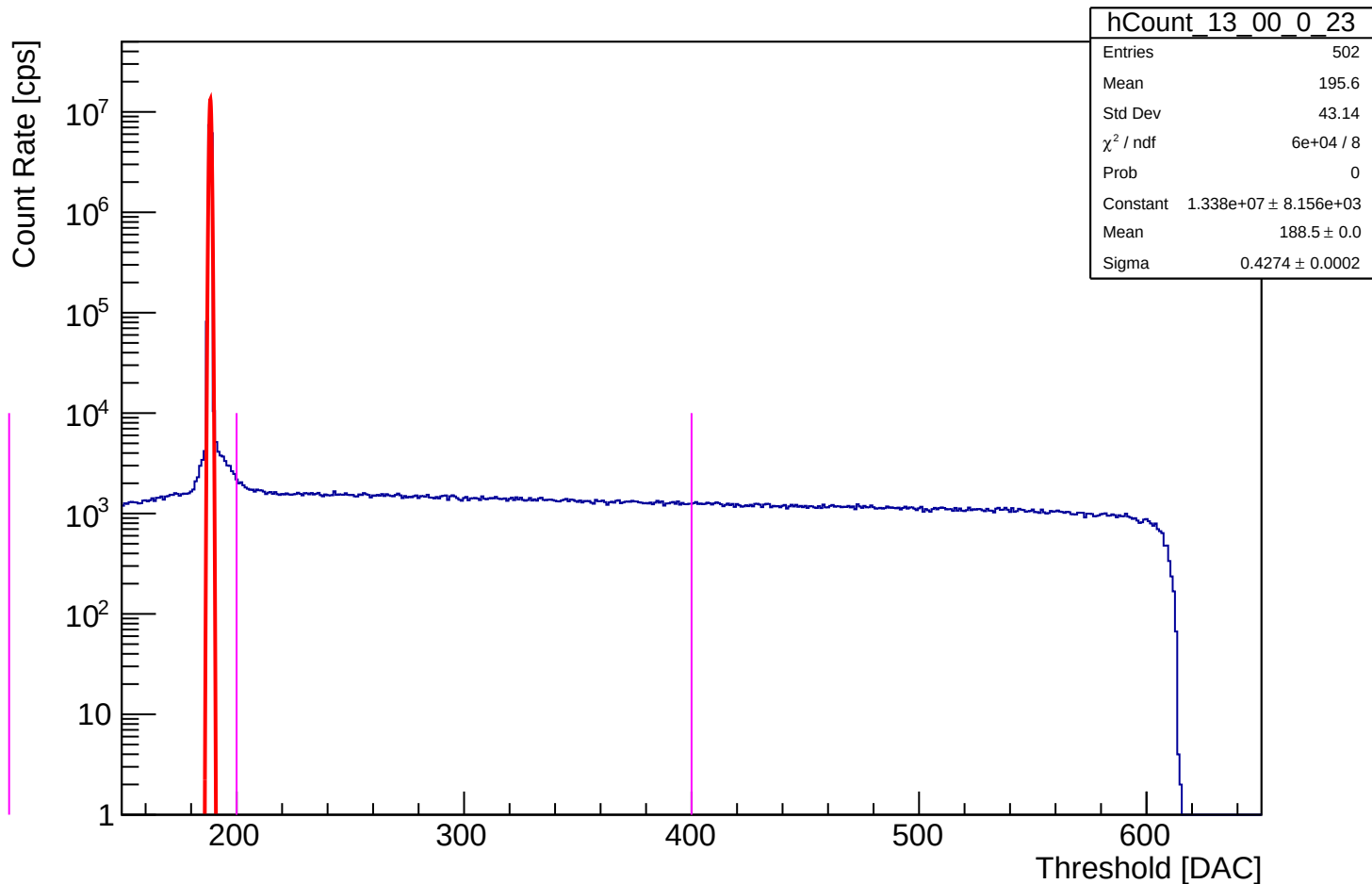
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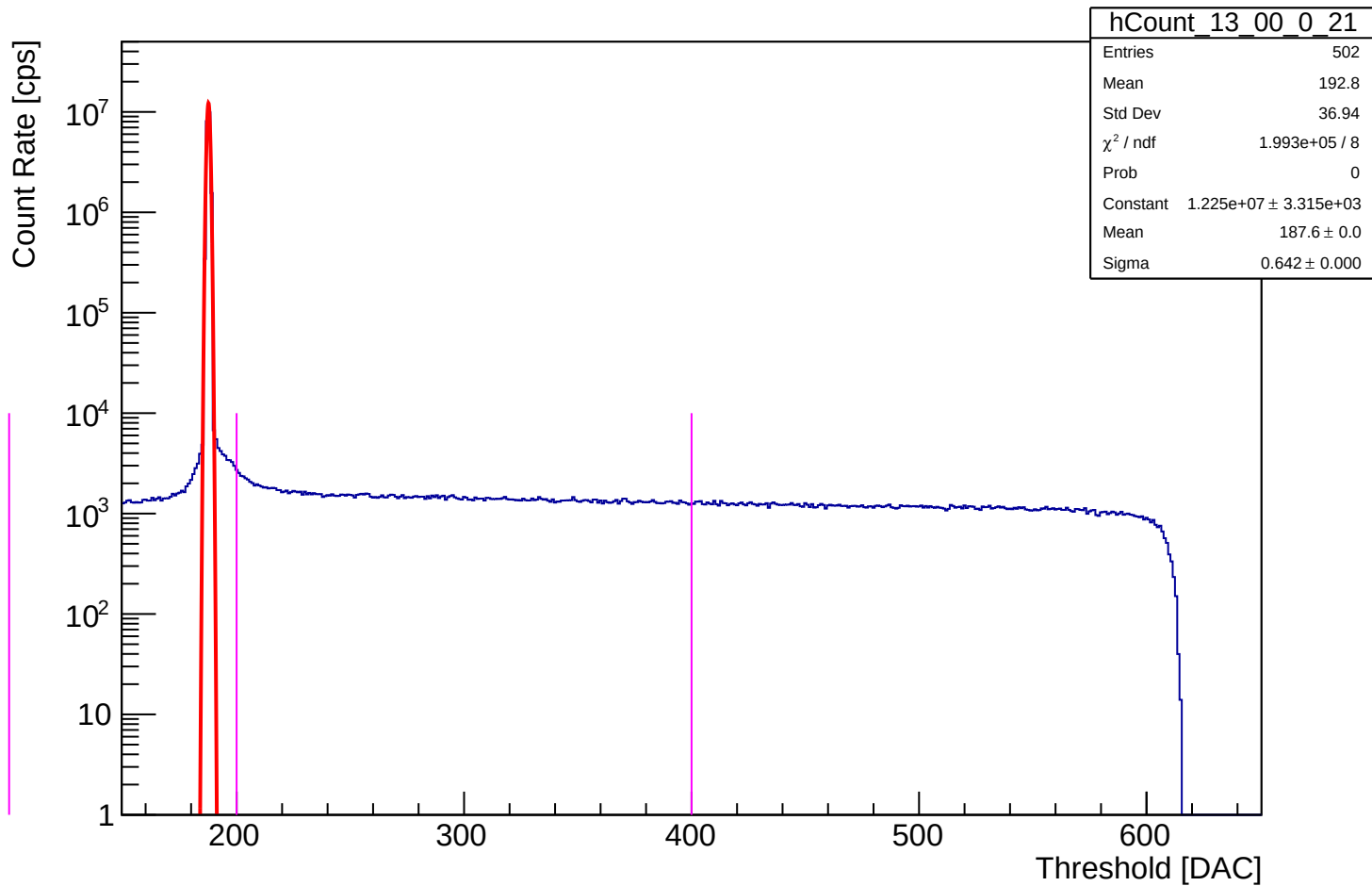
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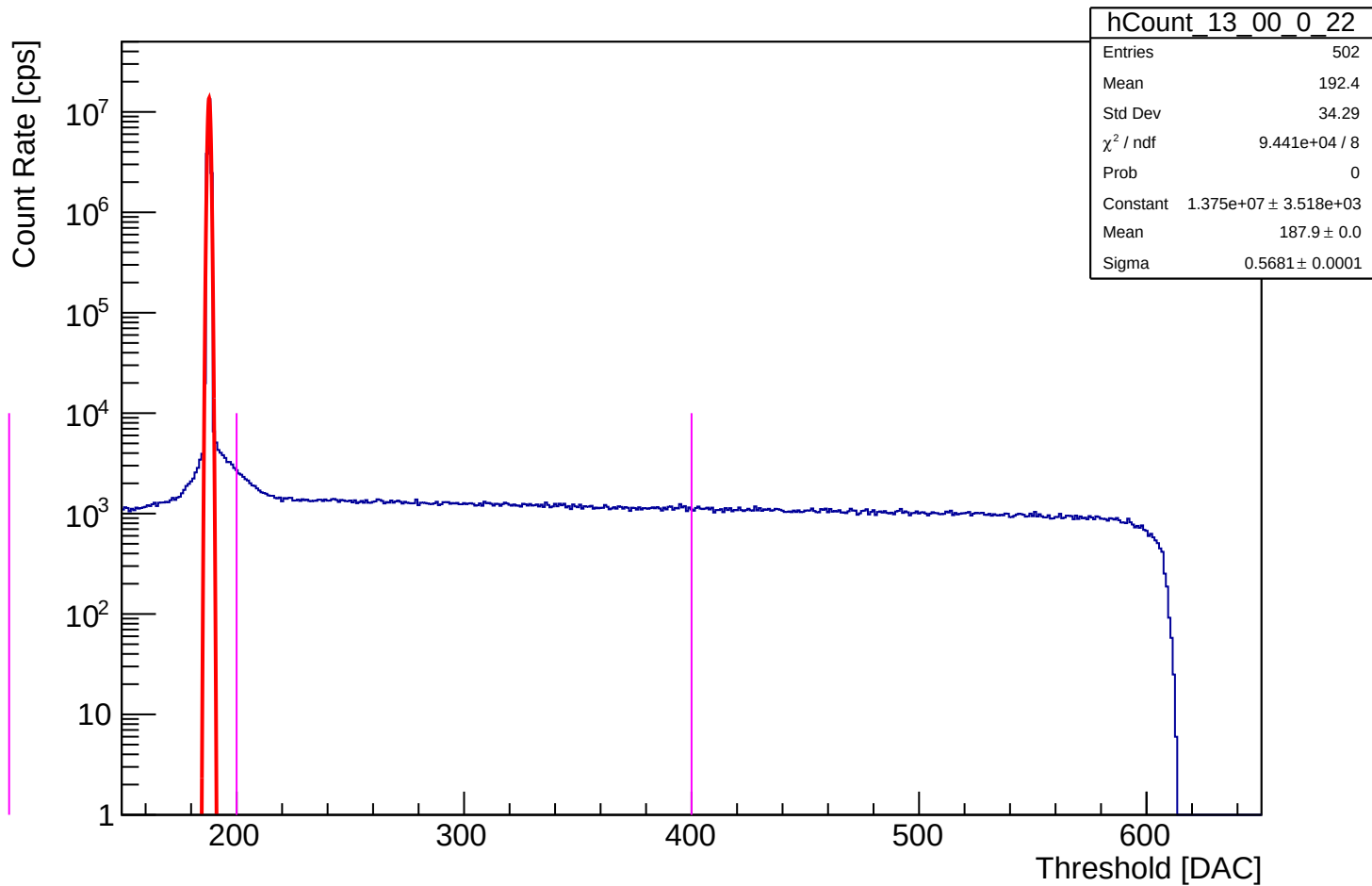
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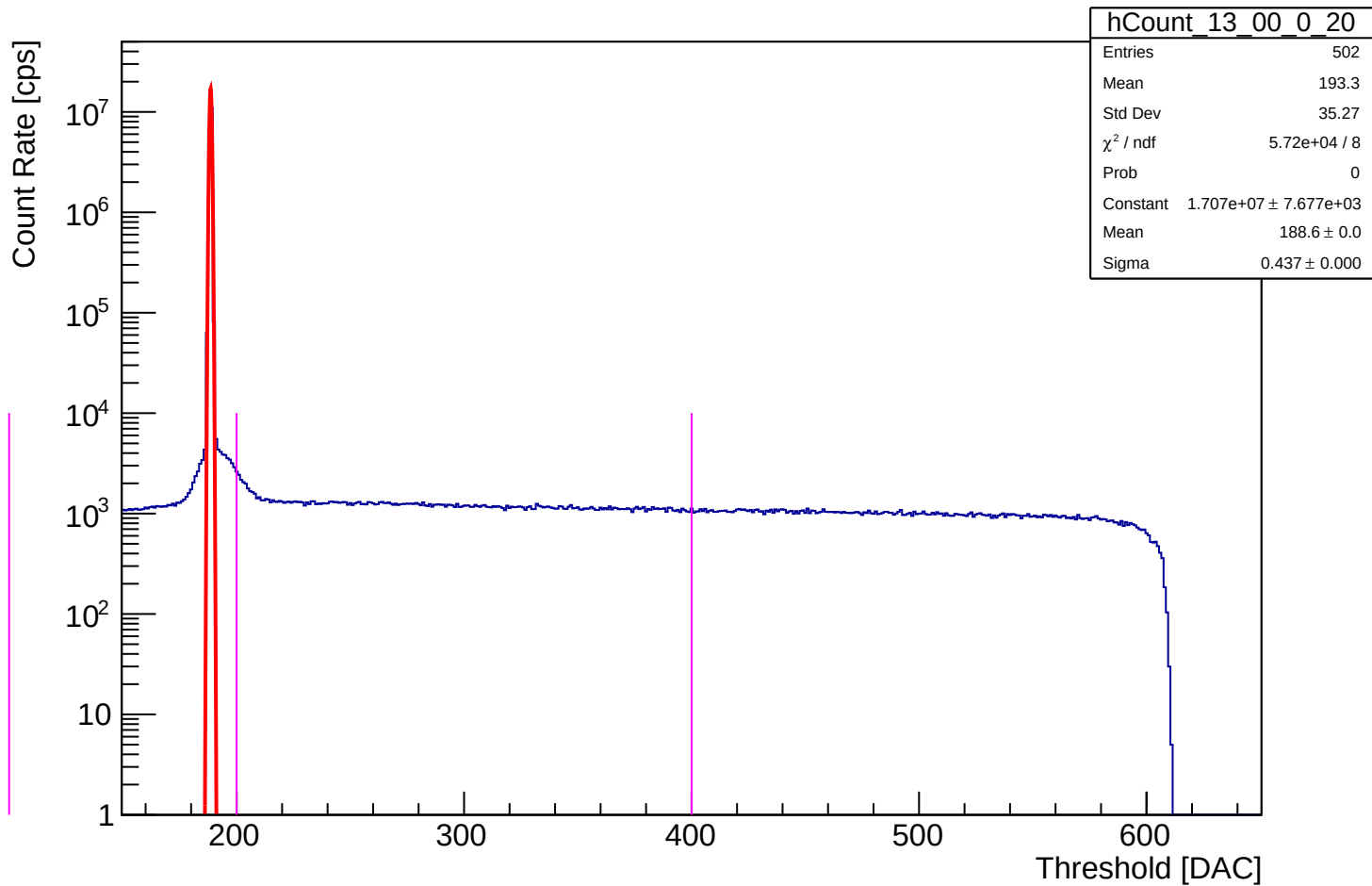
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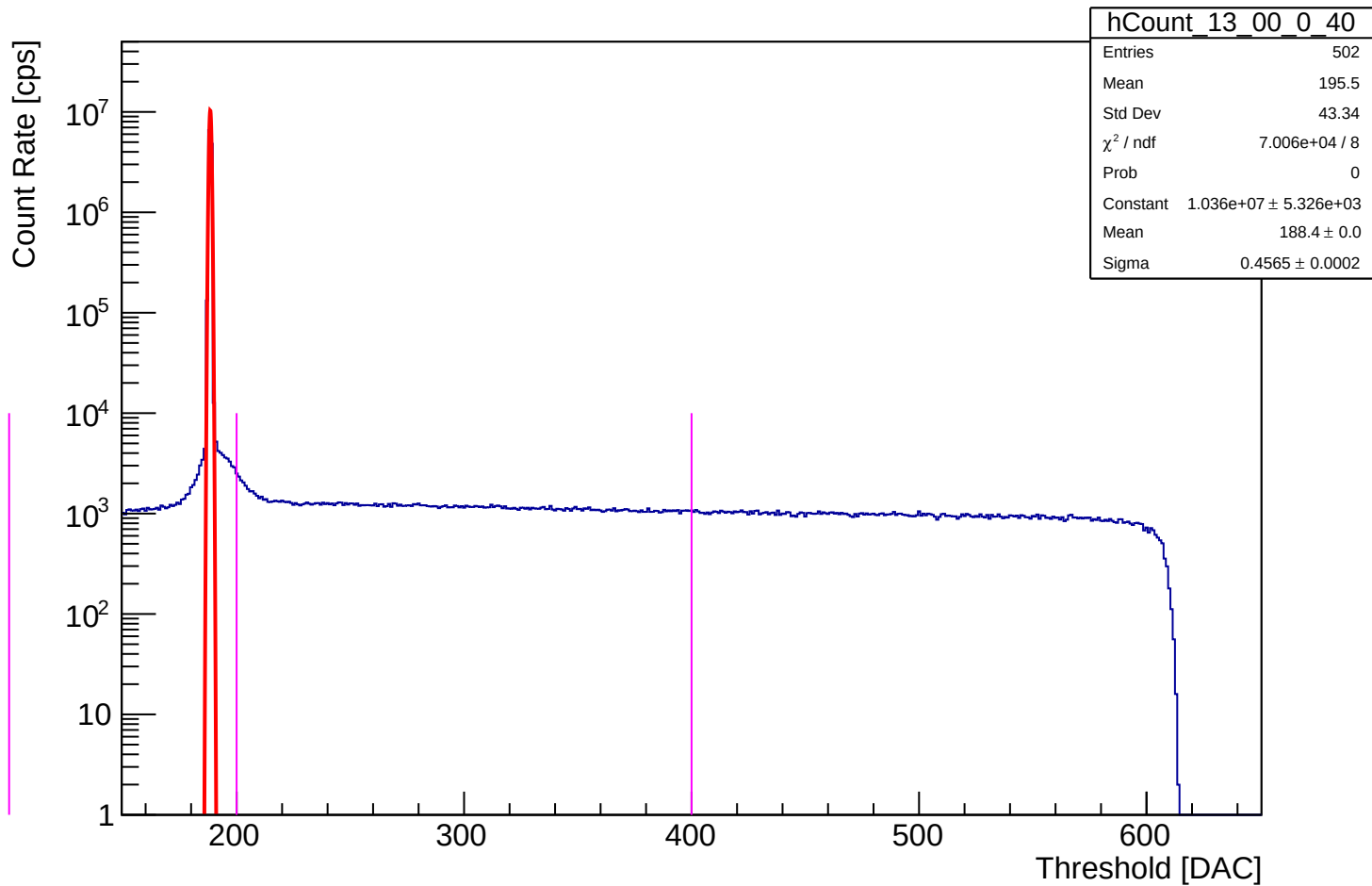
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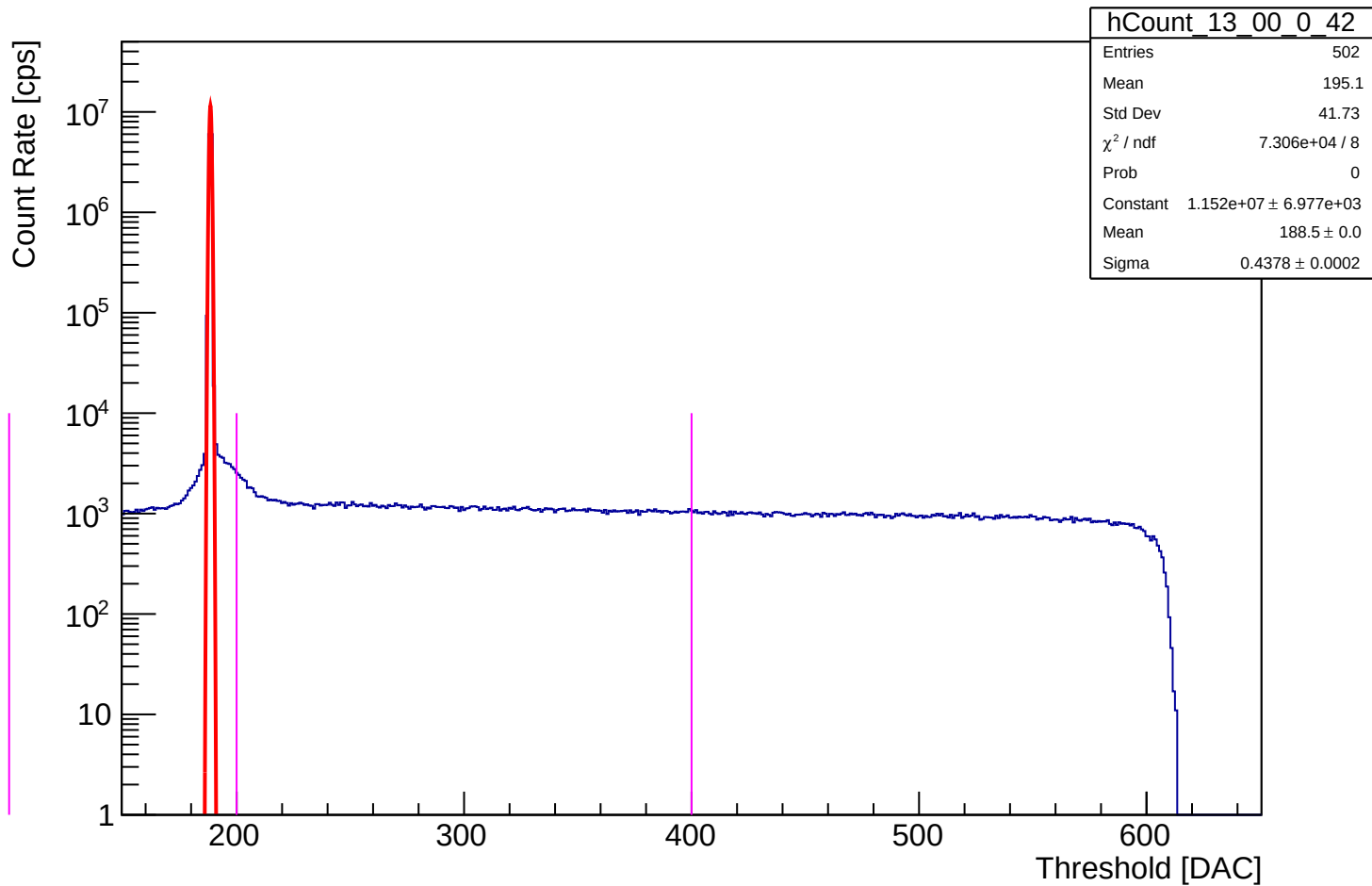
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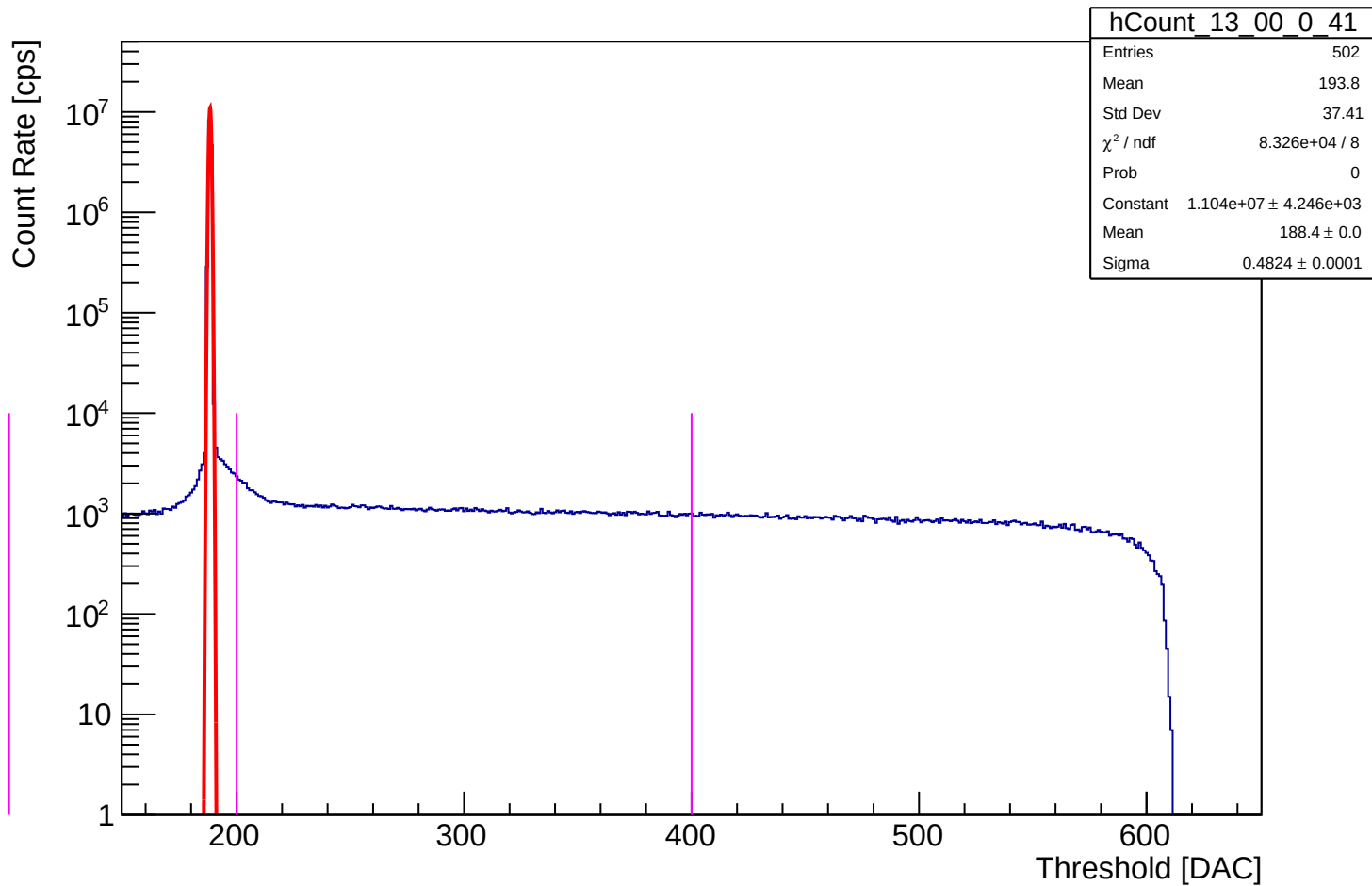
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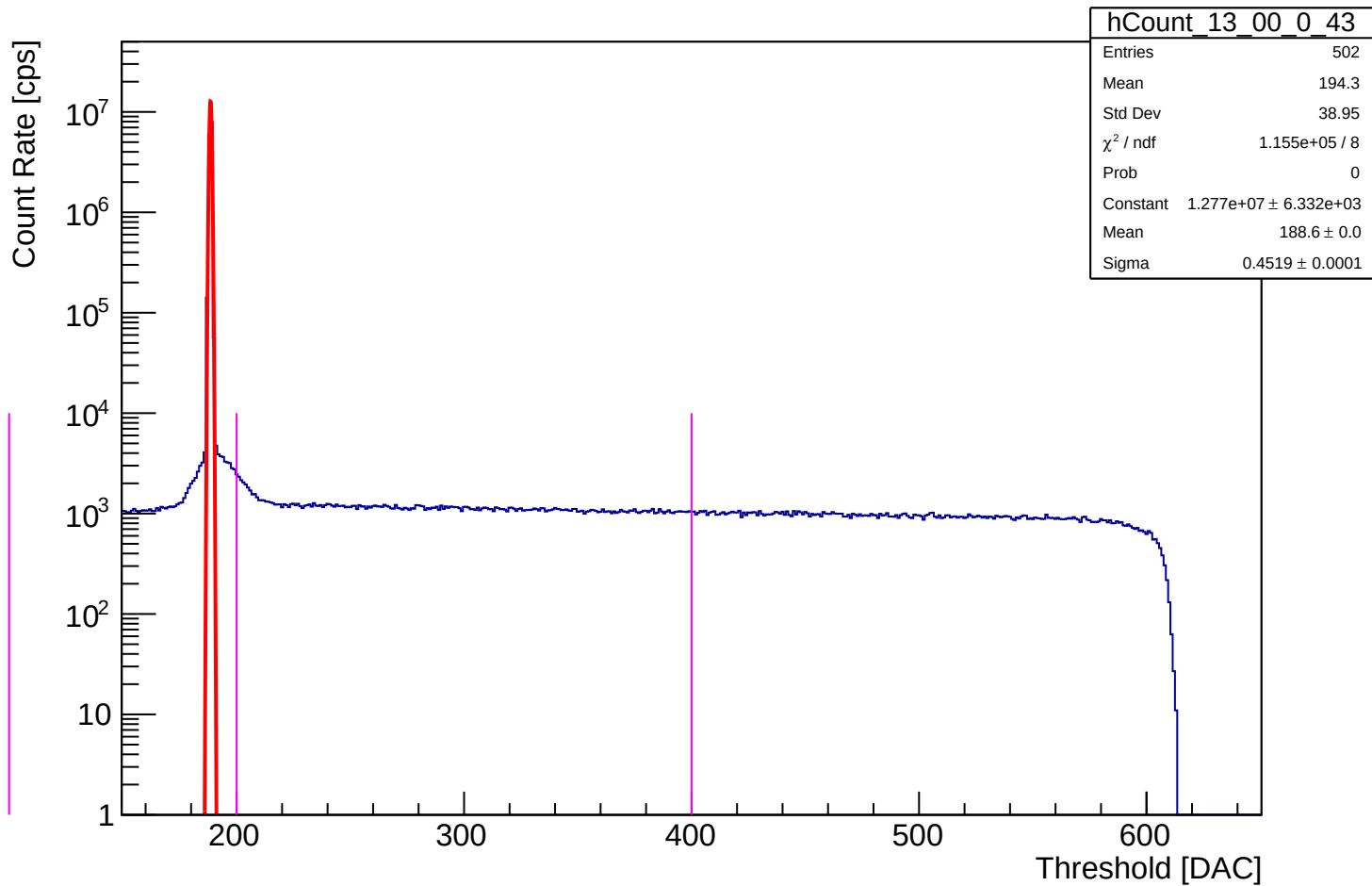
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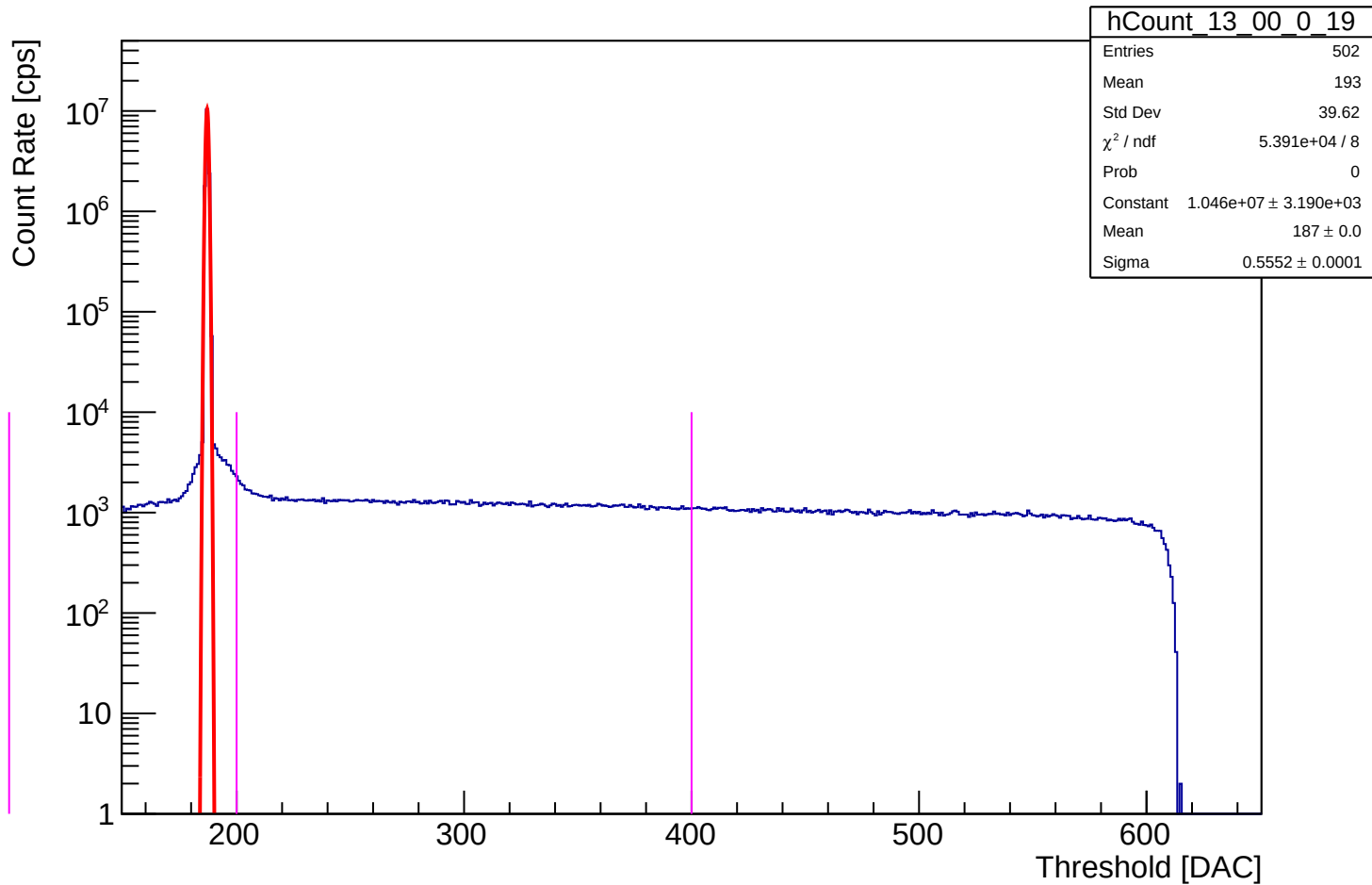
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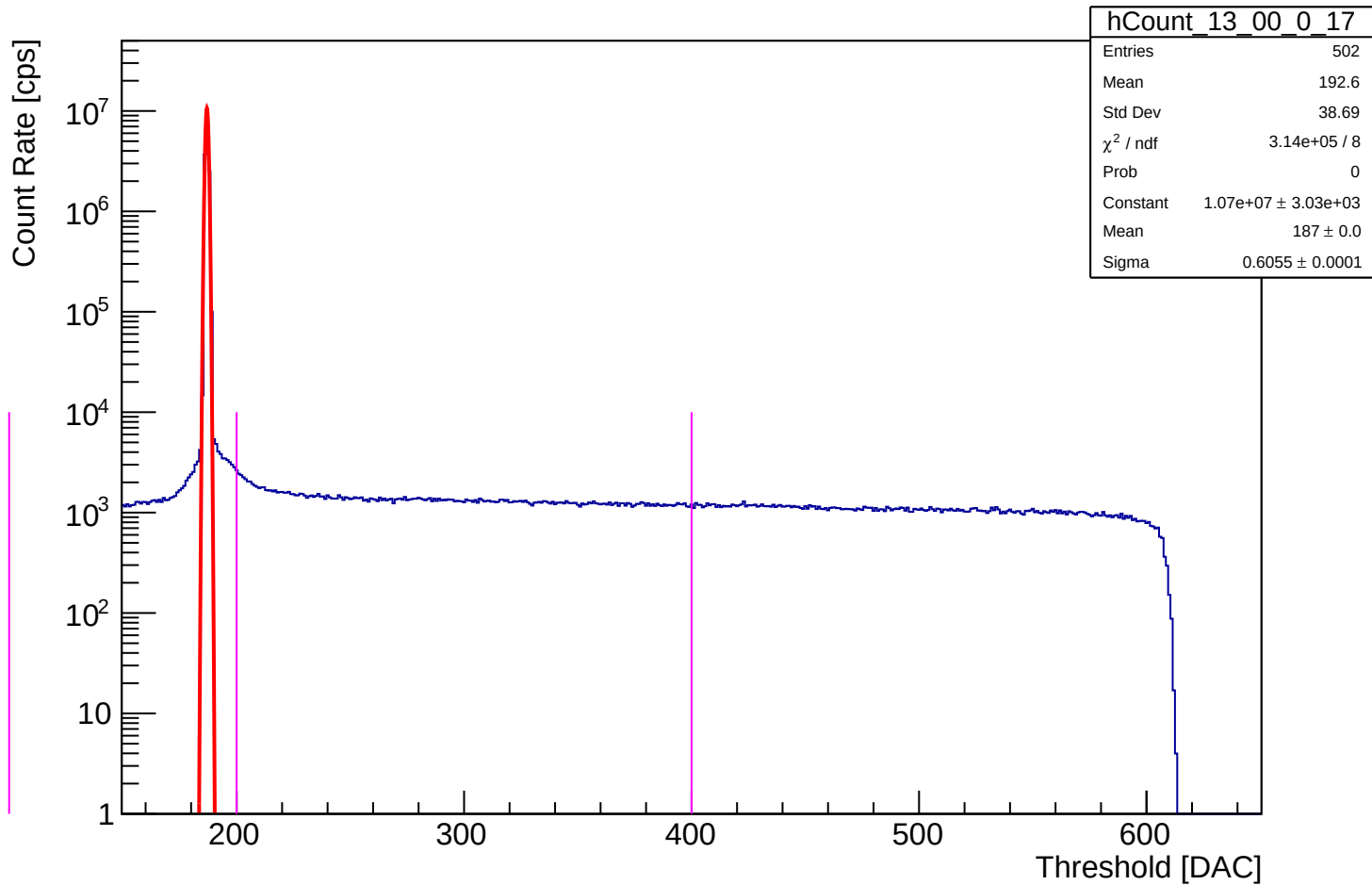
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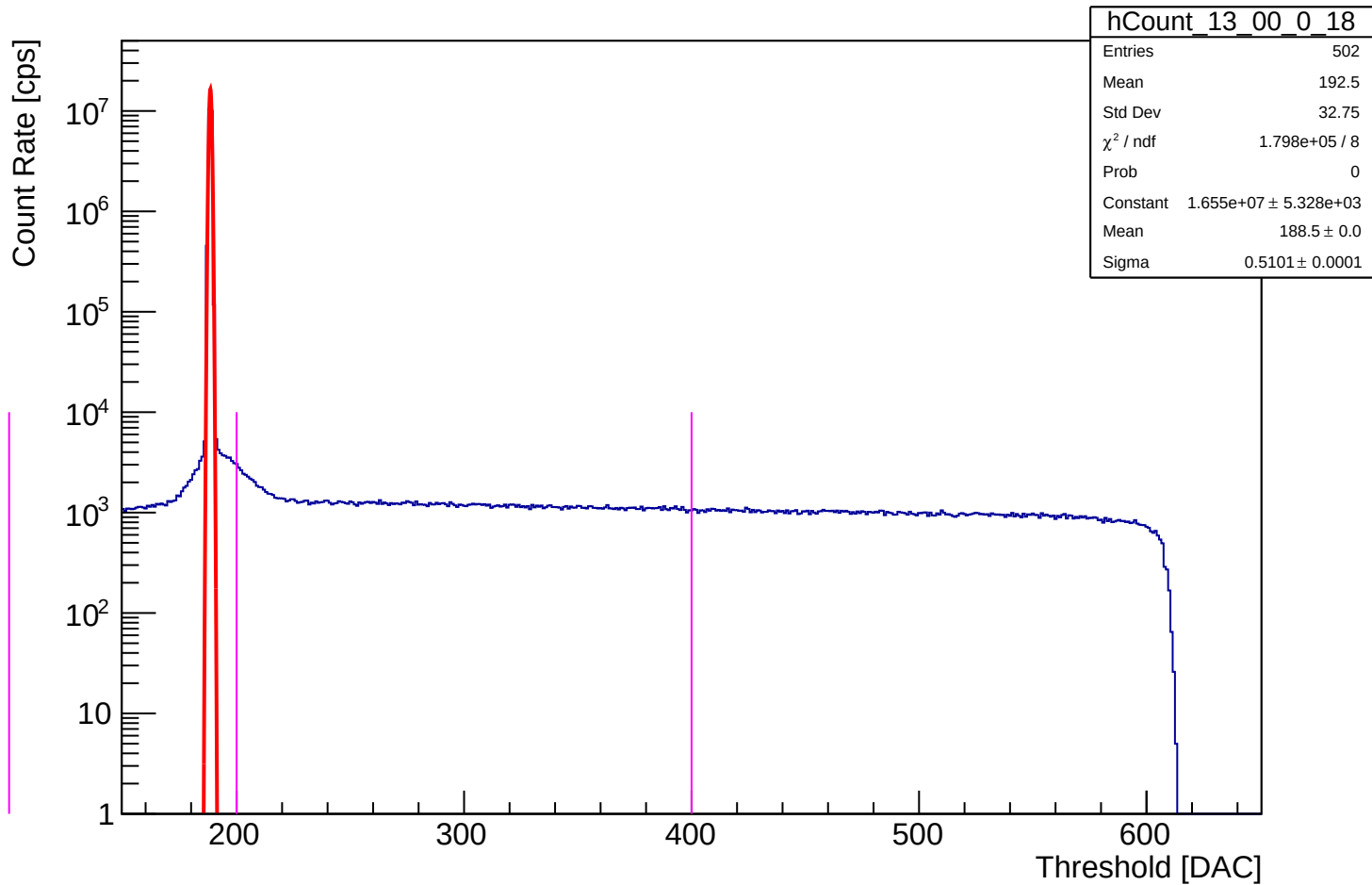
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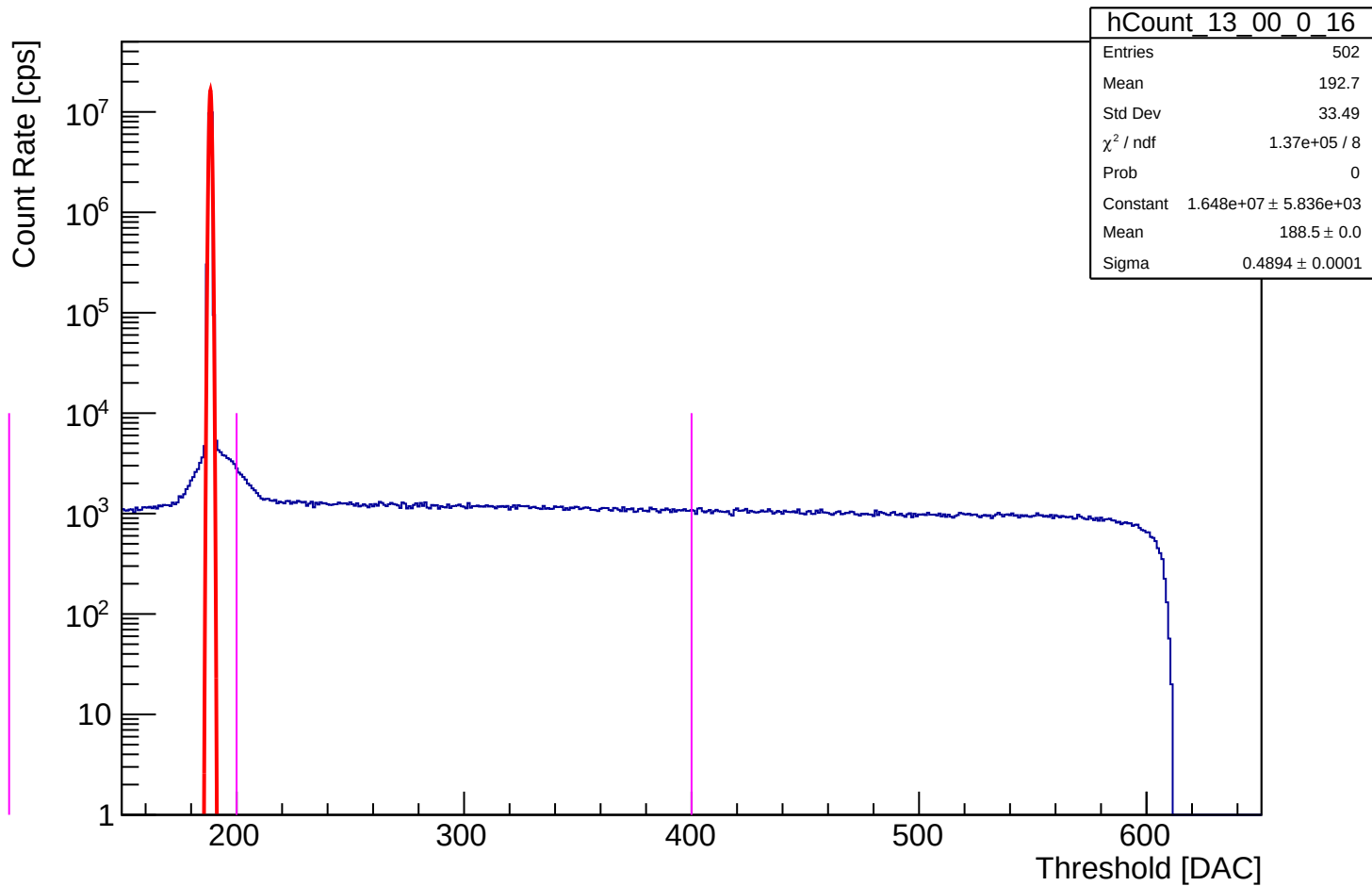
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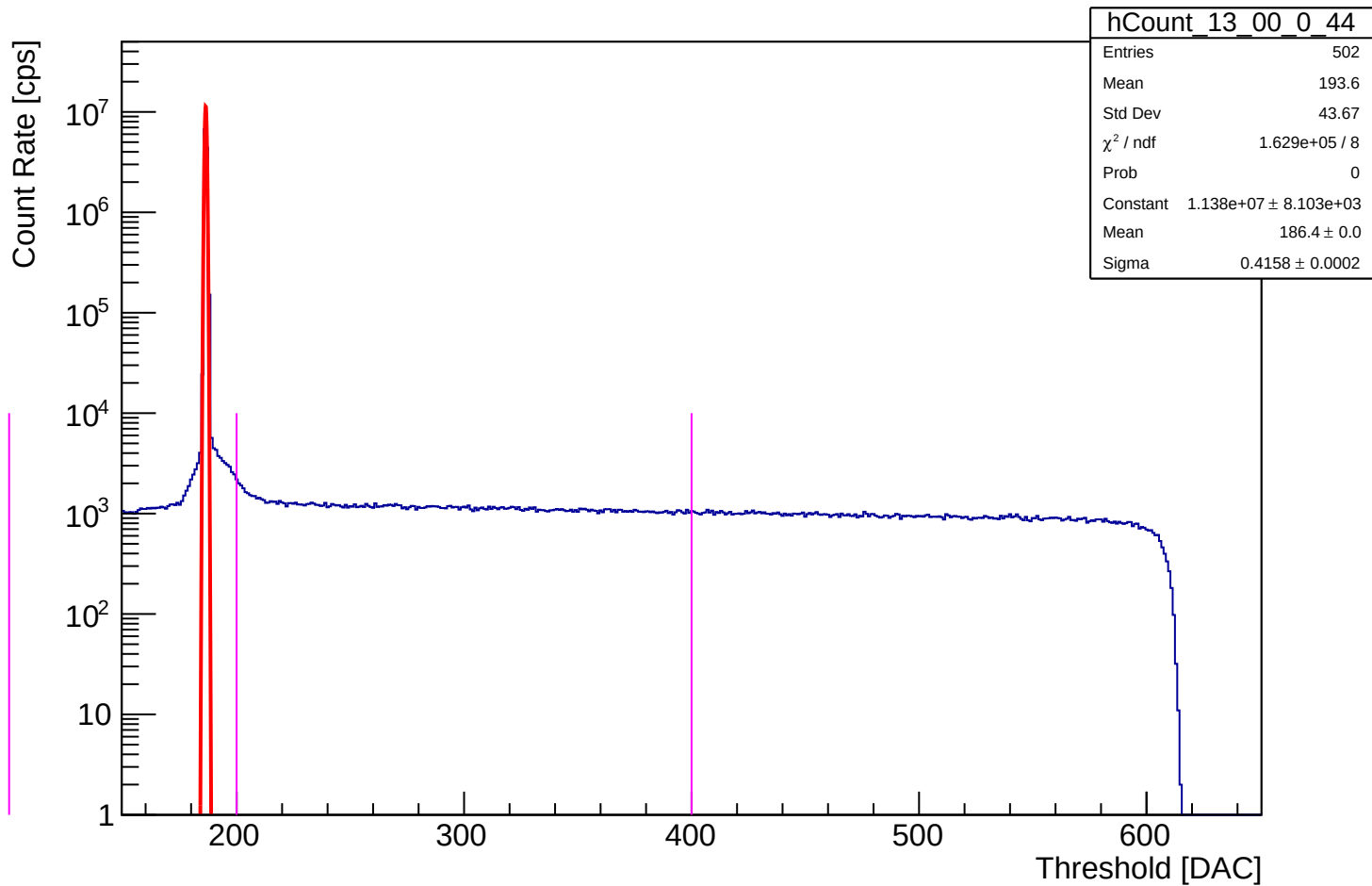
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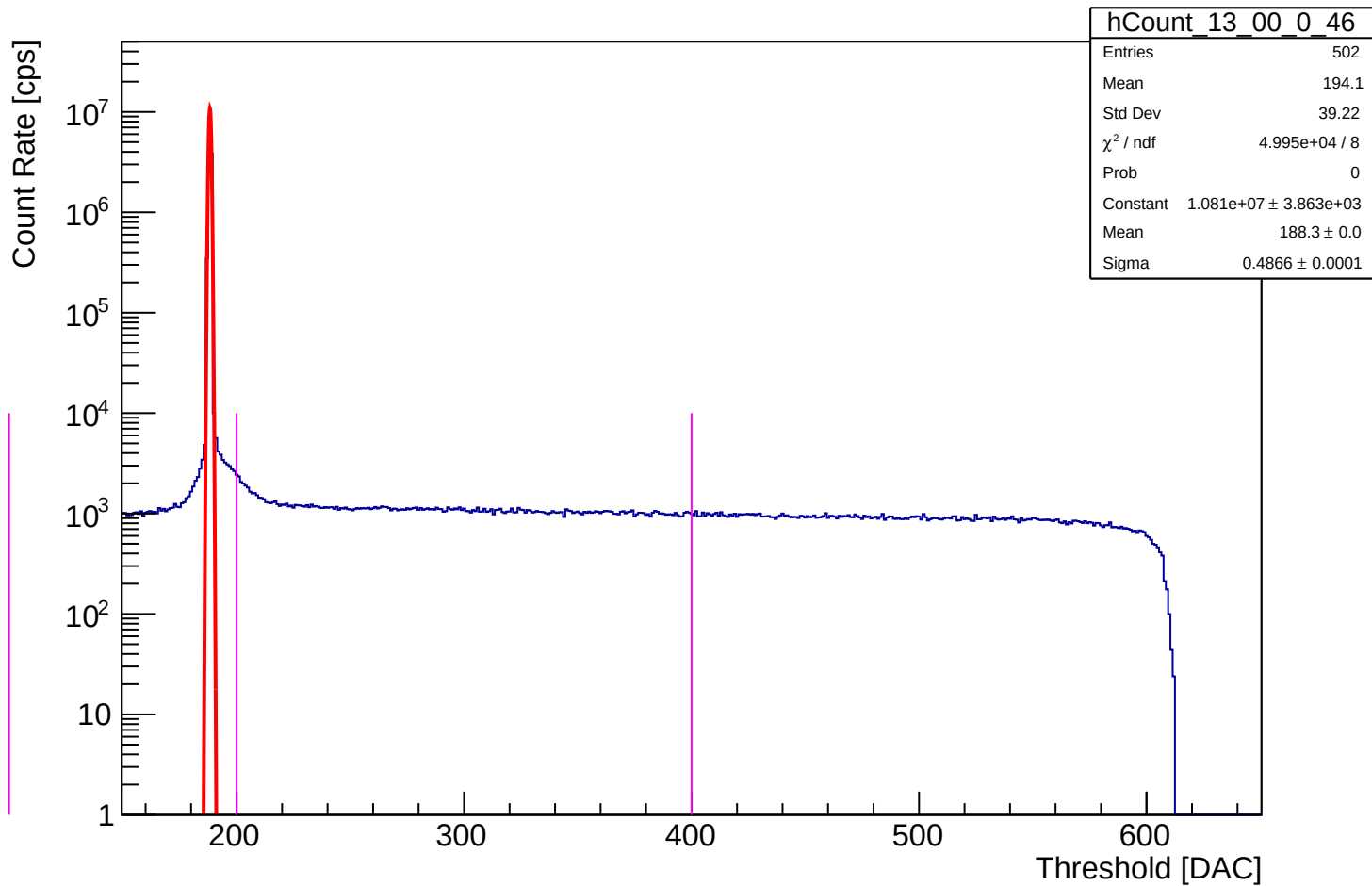
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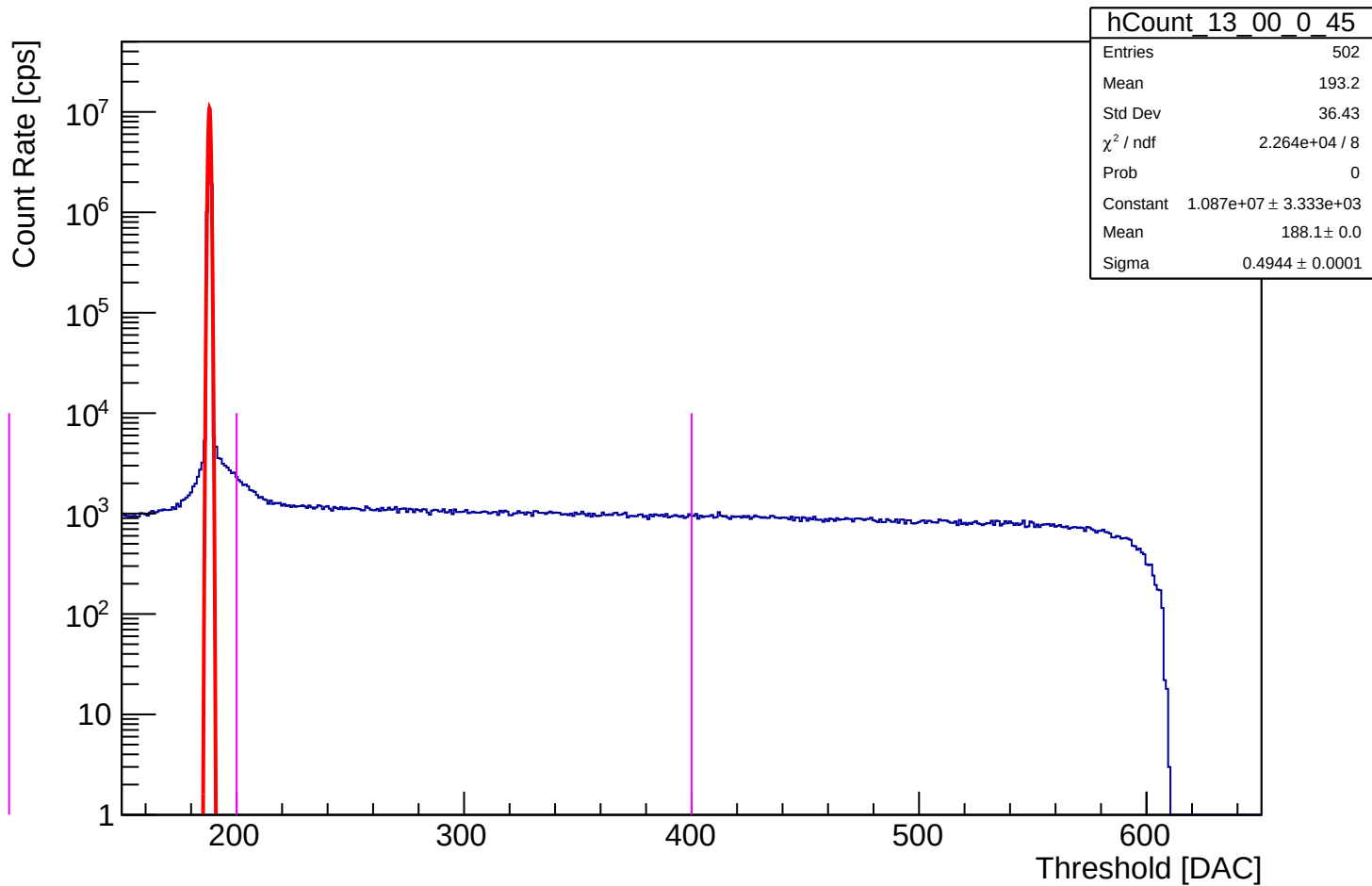
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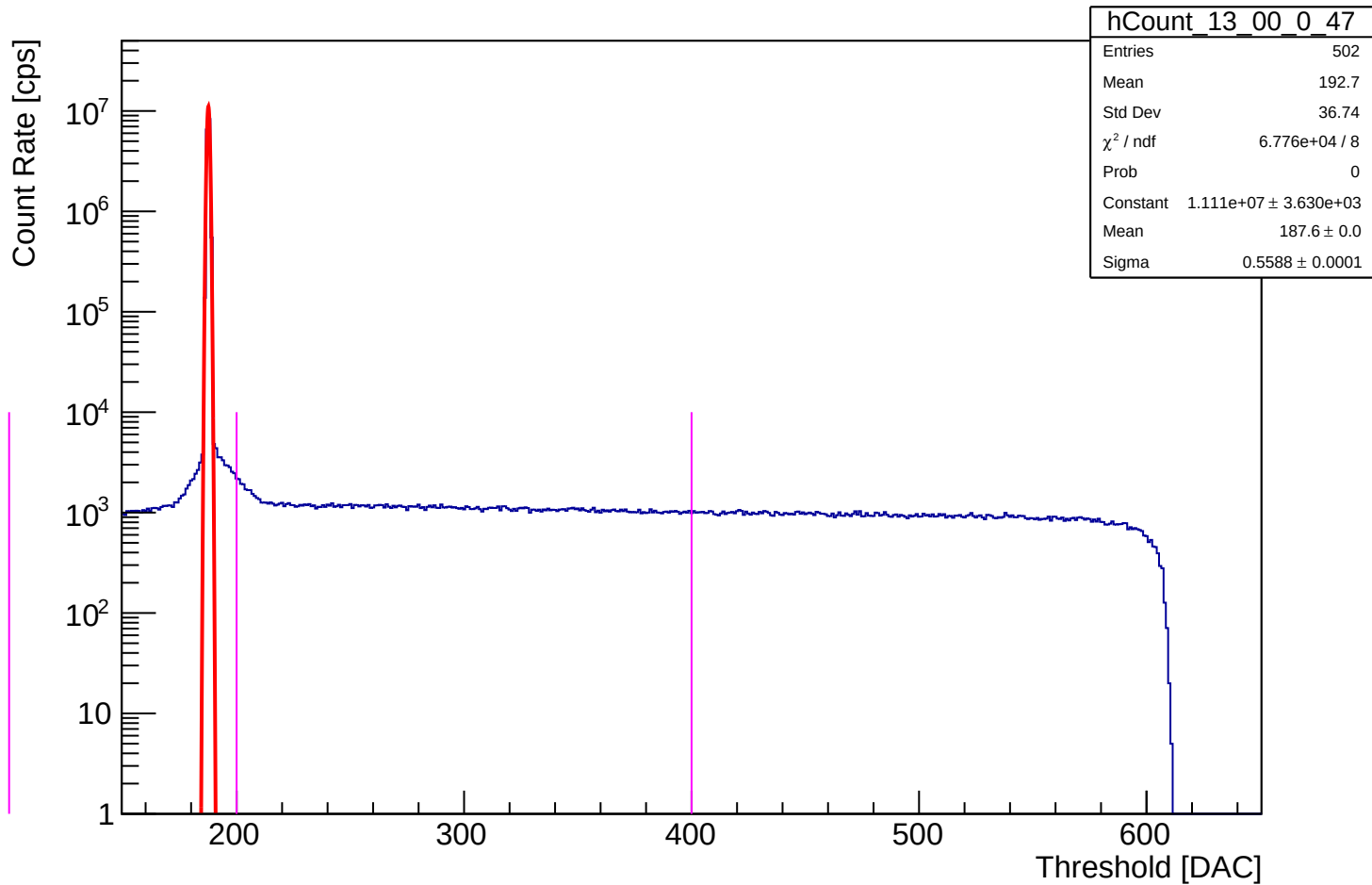
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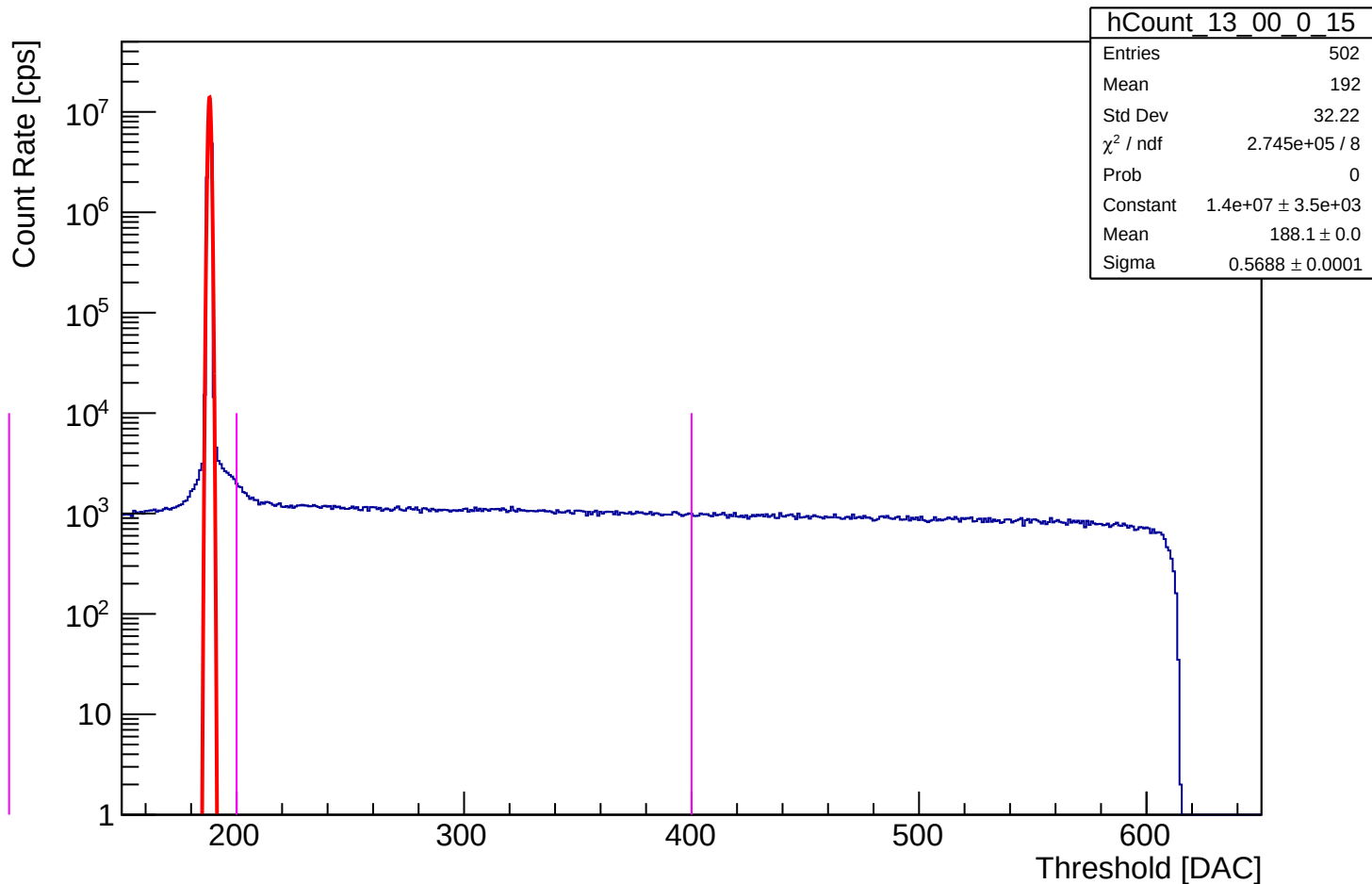
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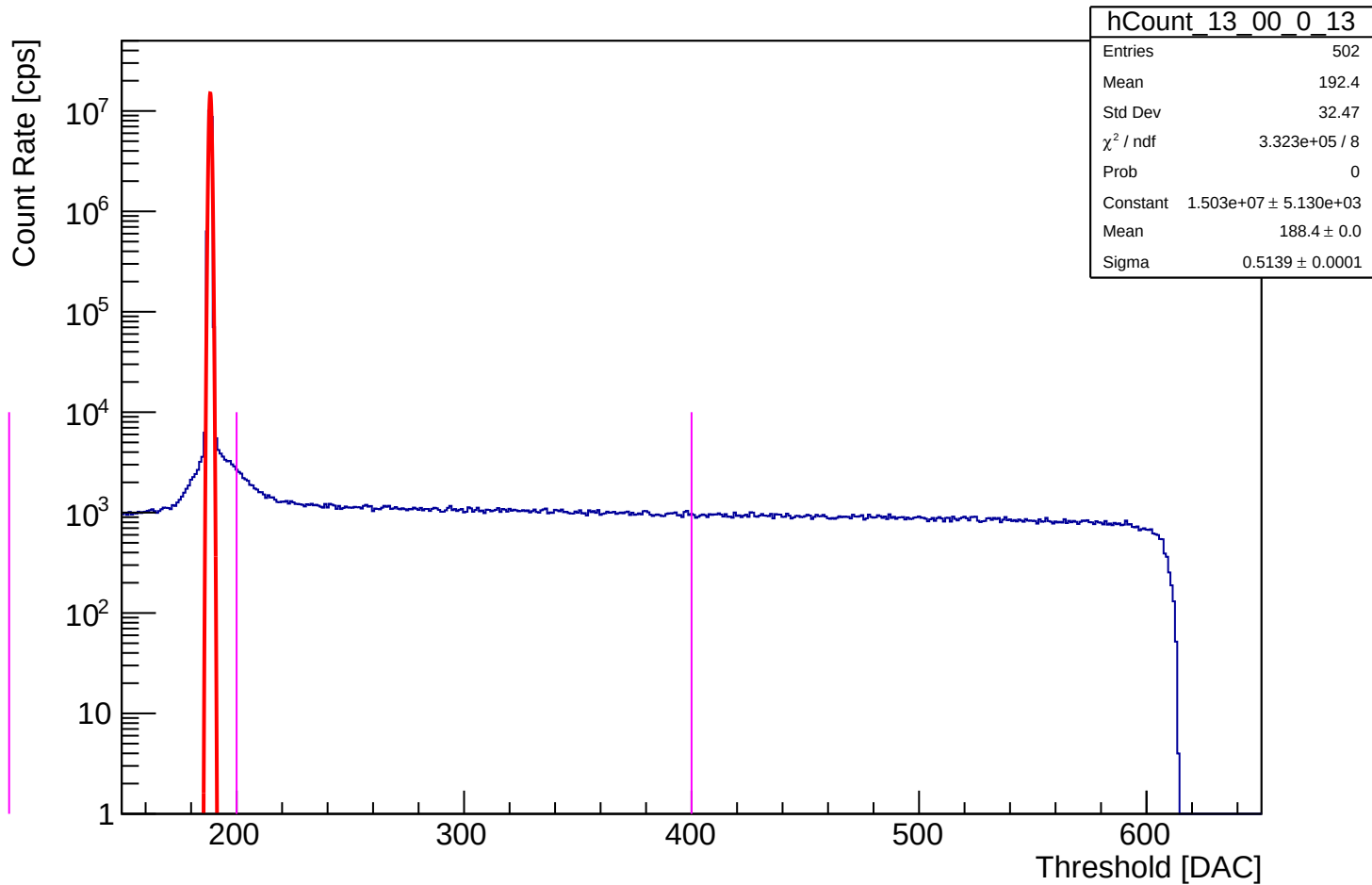
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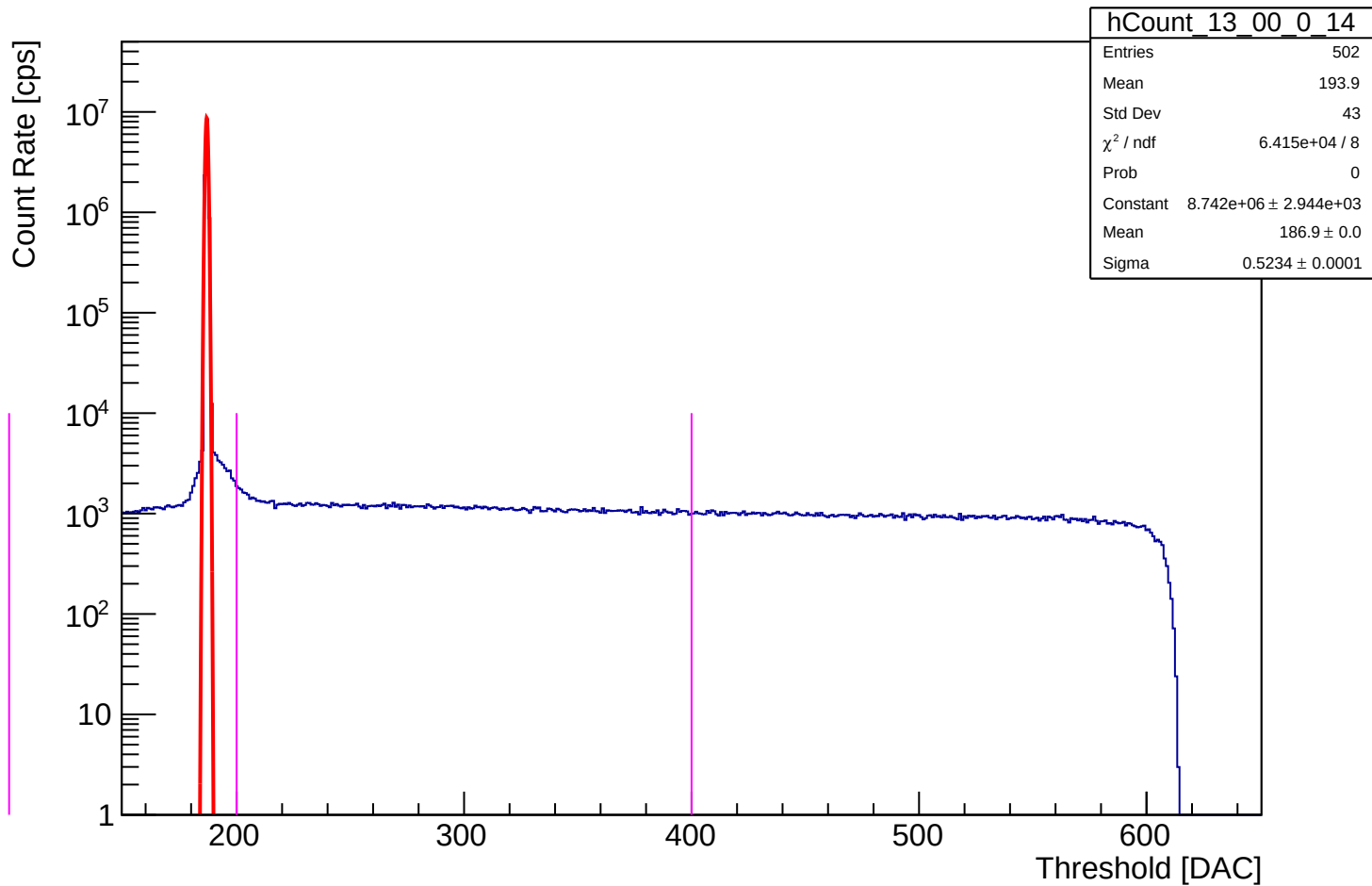
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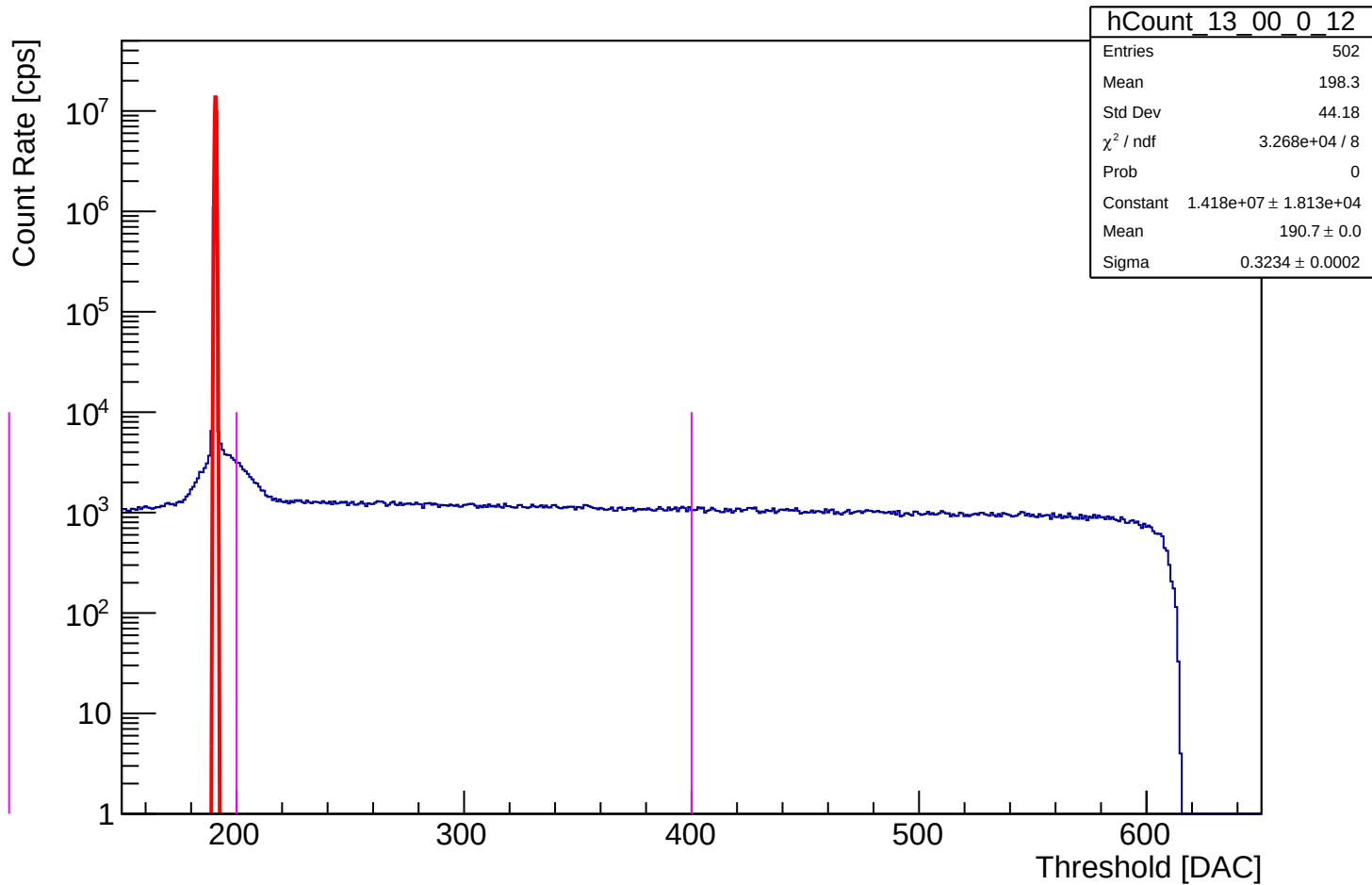
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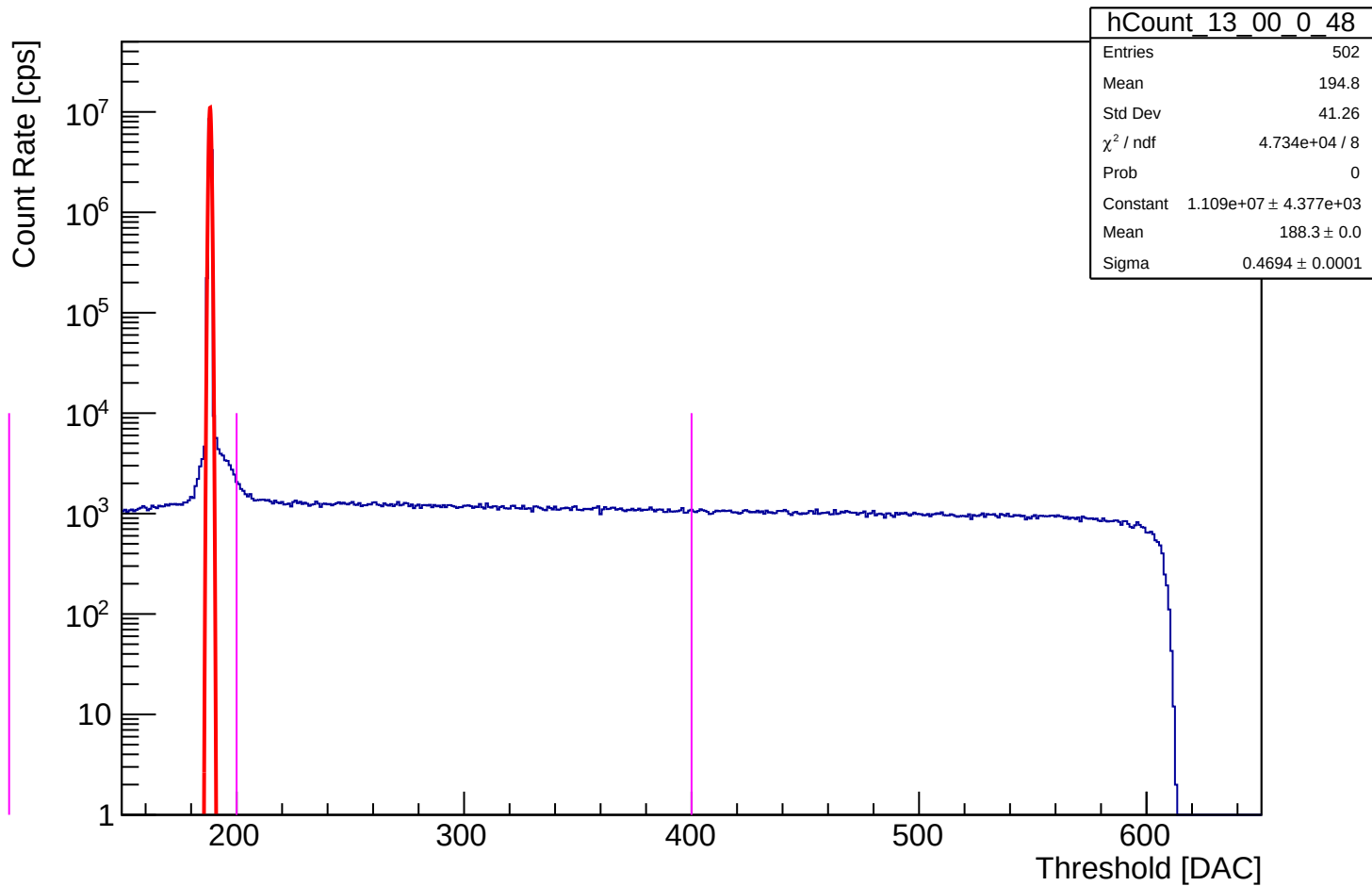
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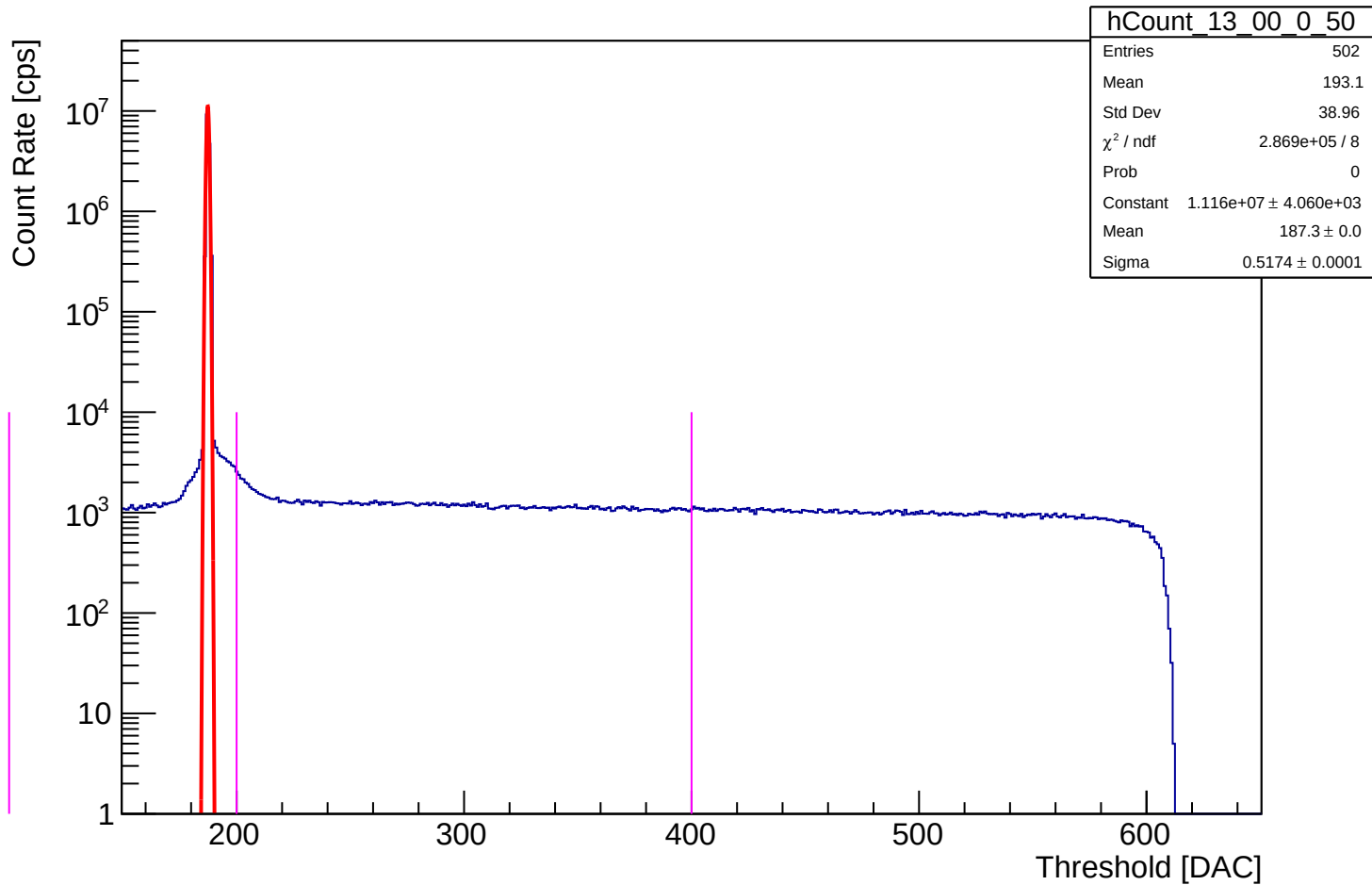
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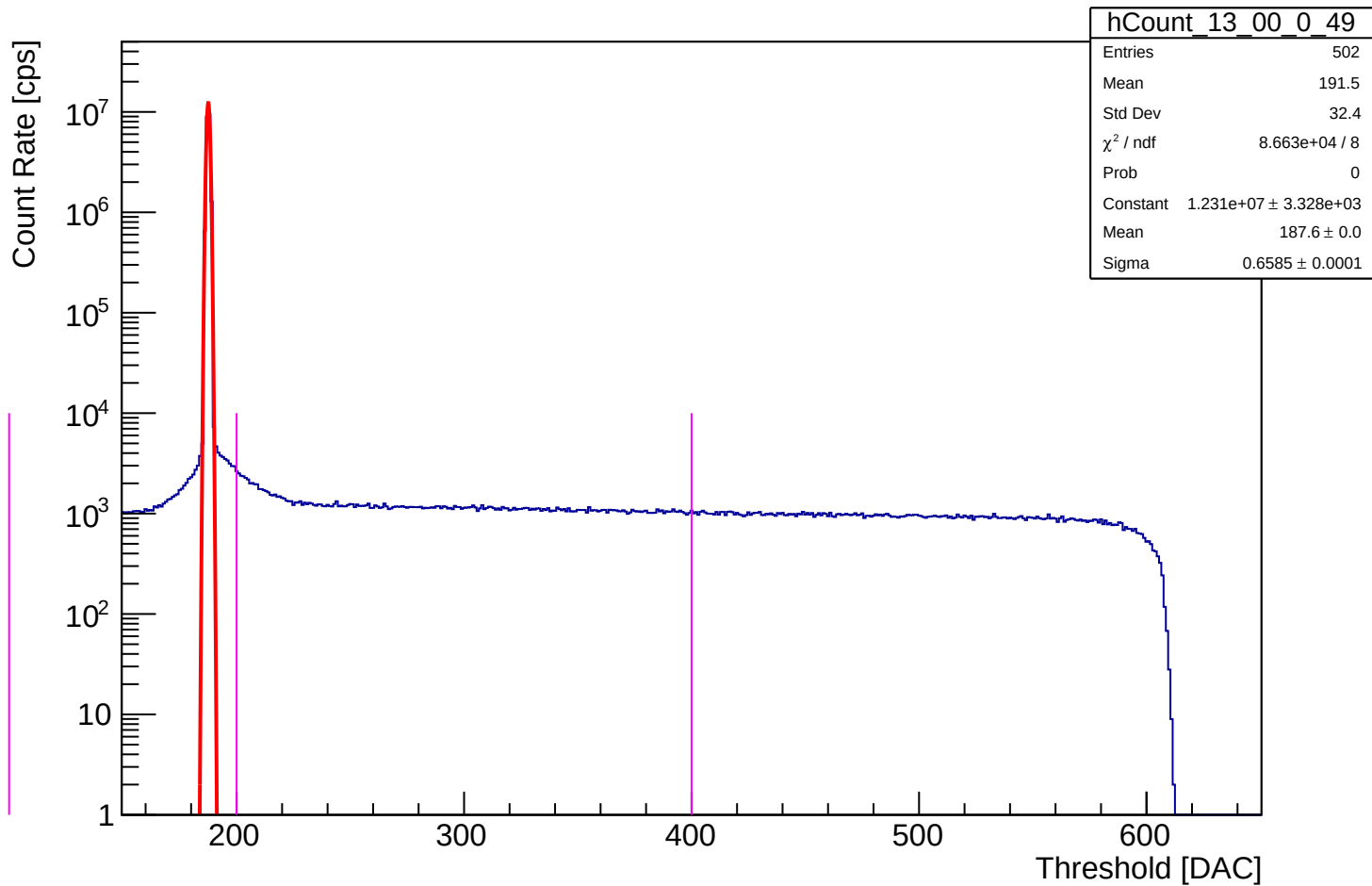
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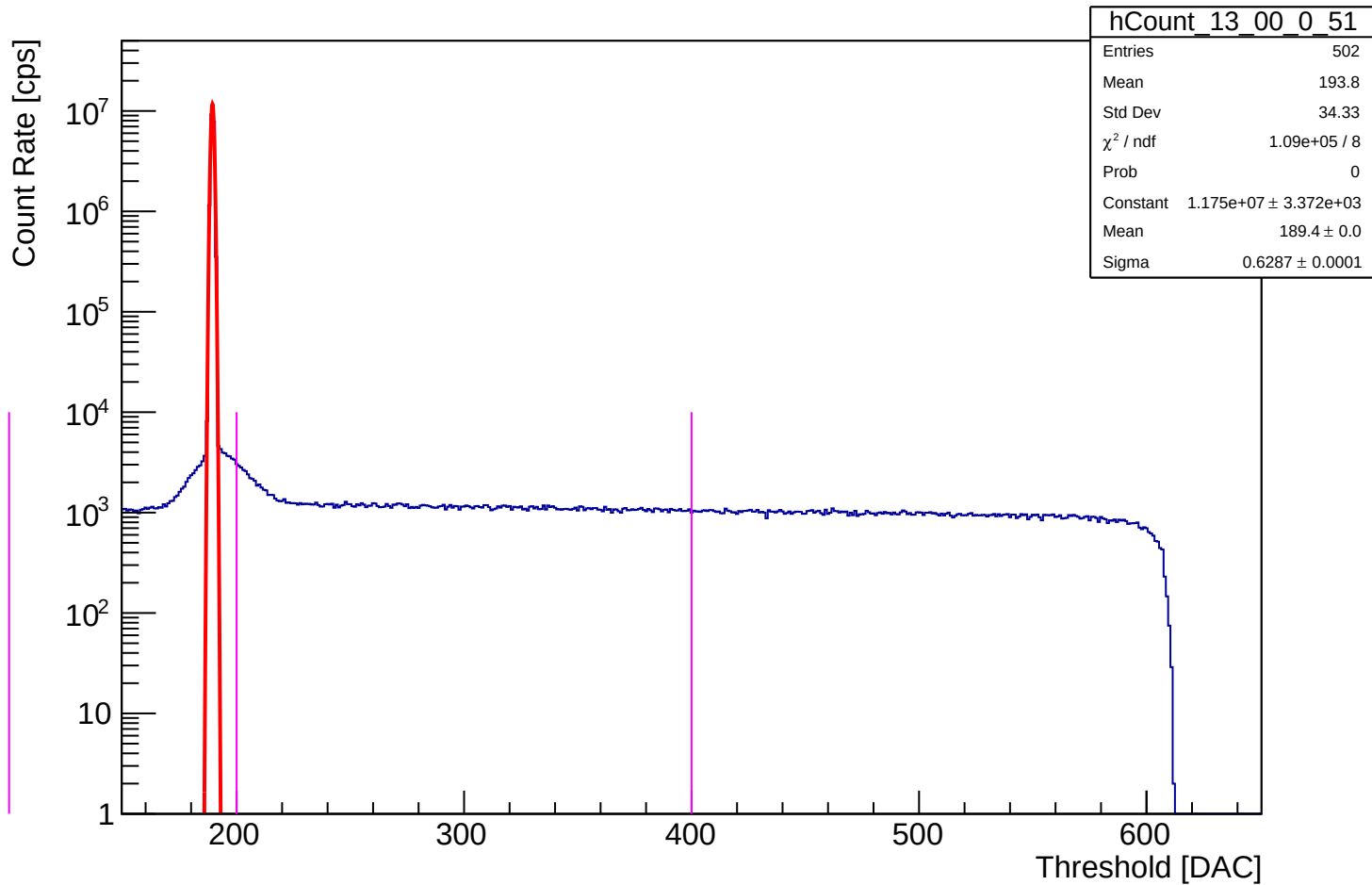
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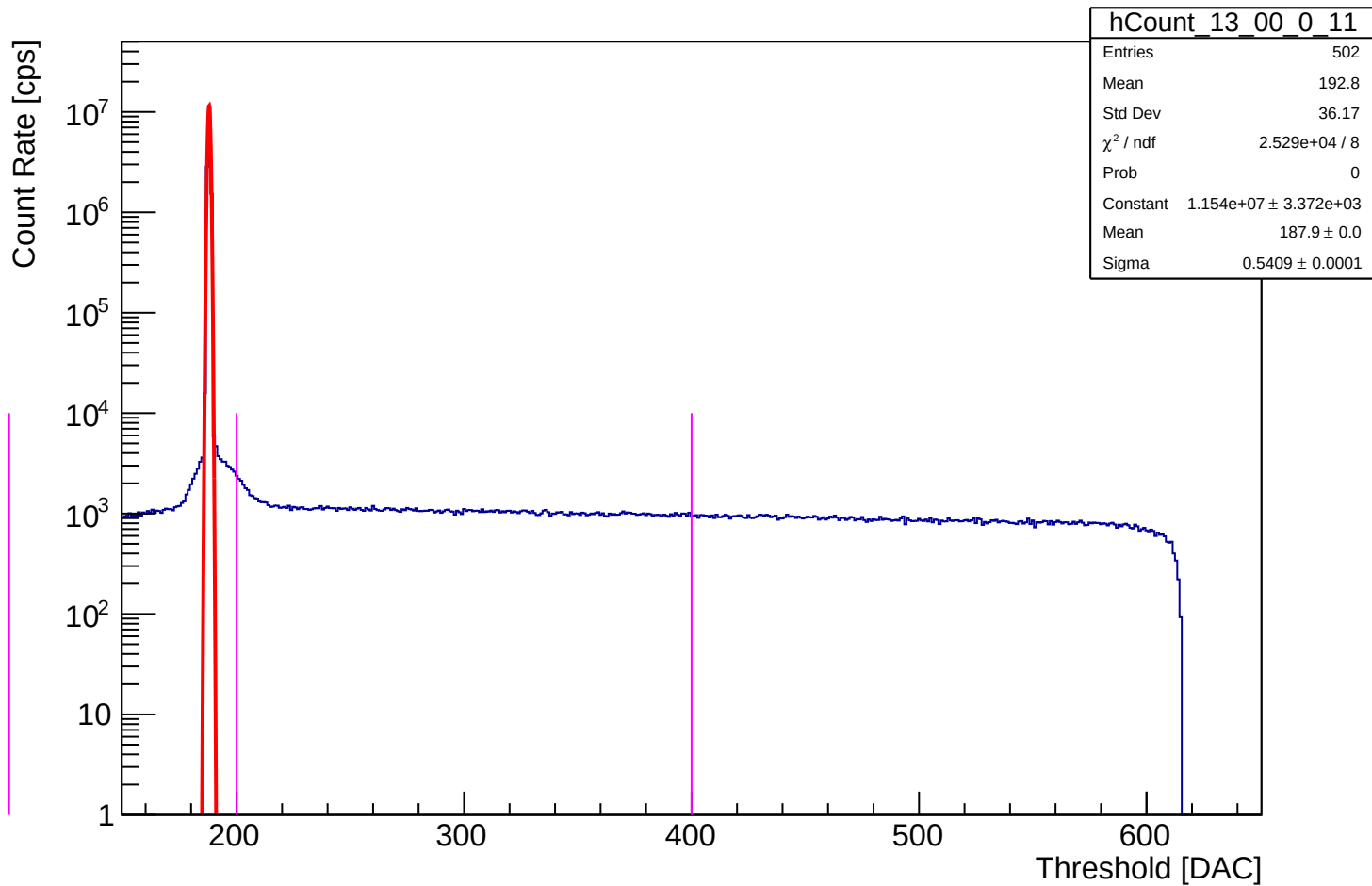
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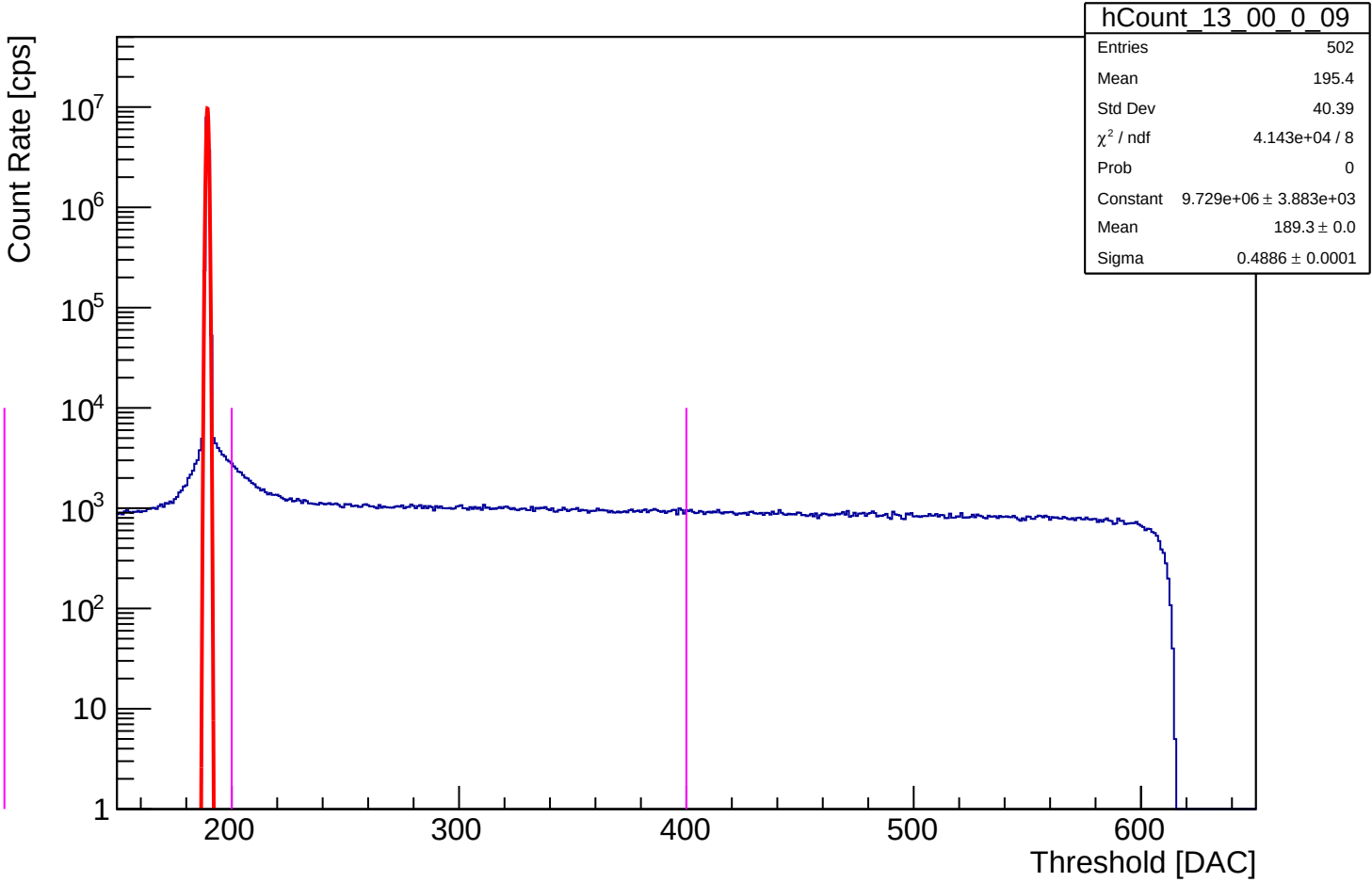


Row 1 Tile 1 Pmt 1 Pixel 40 Slot 13 Fiber 0 Asic 0 Channel 51

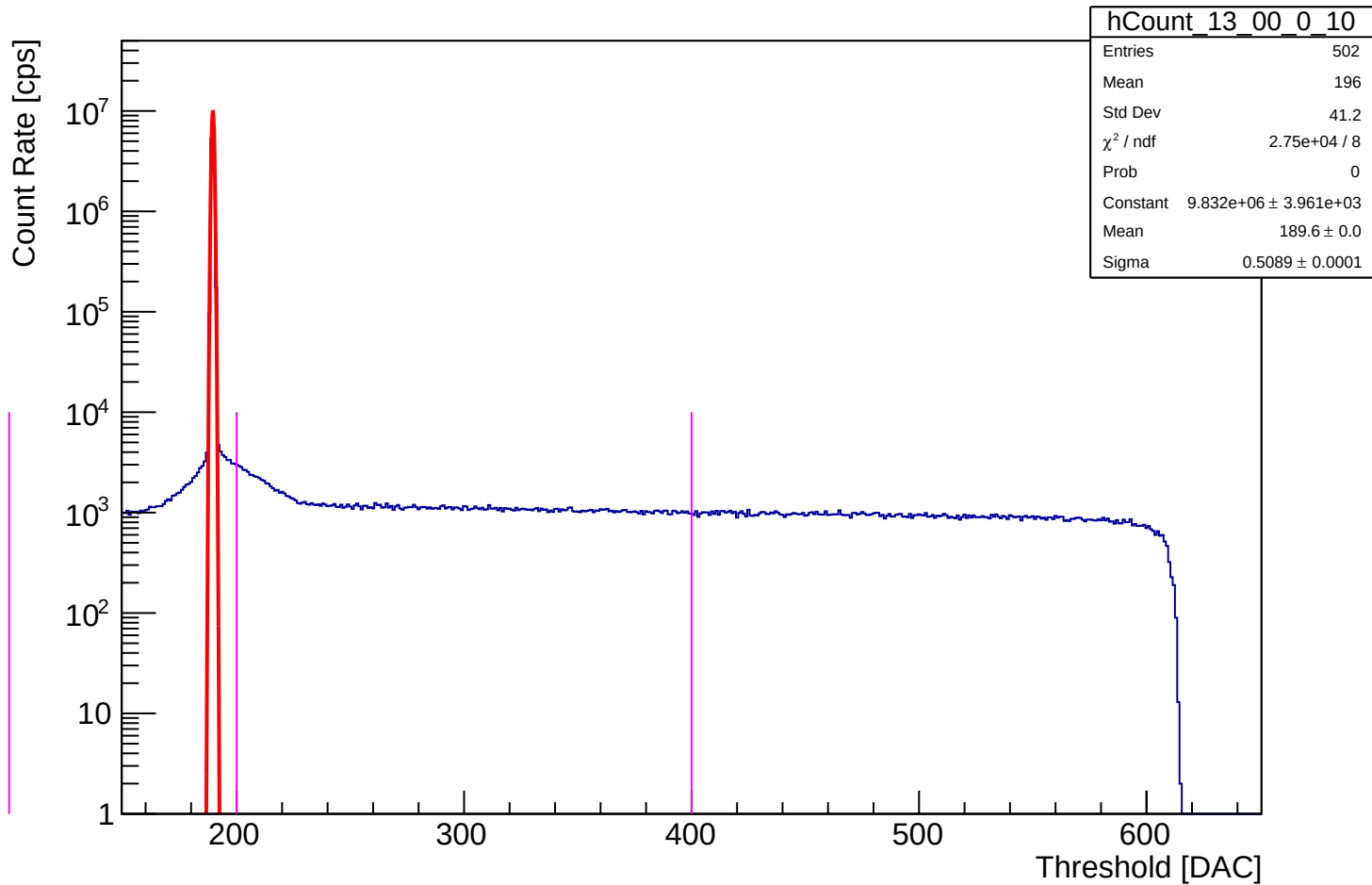


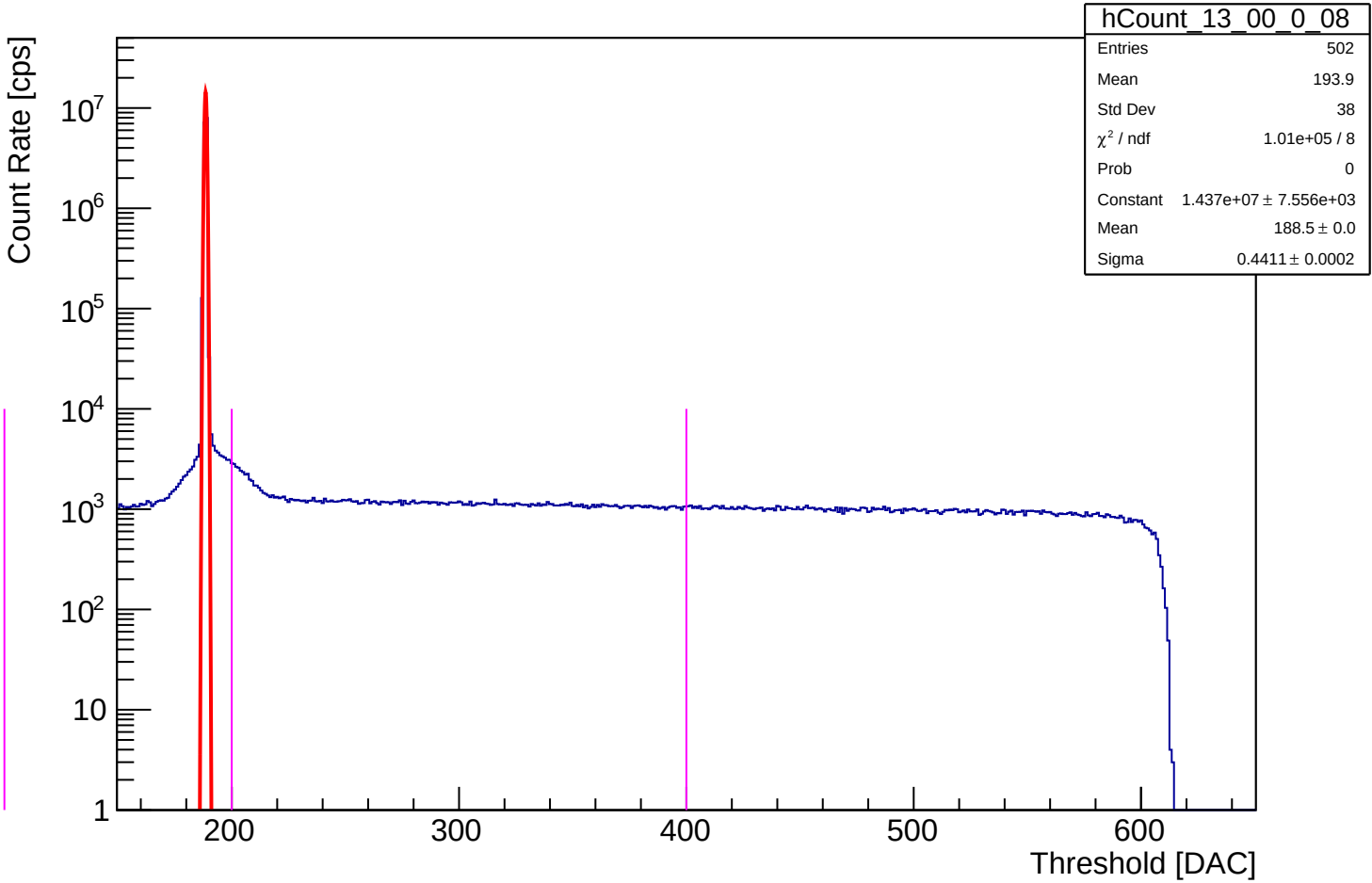
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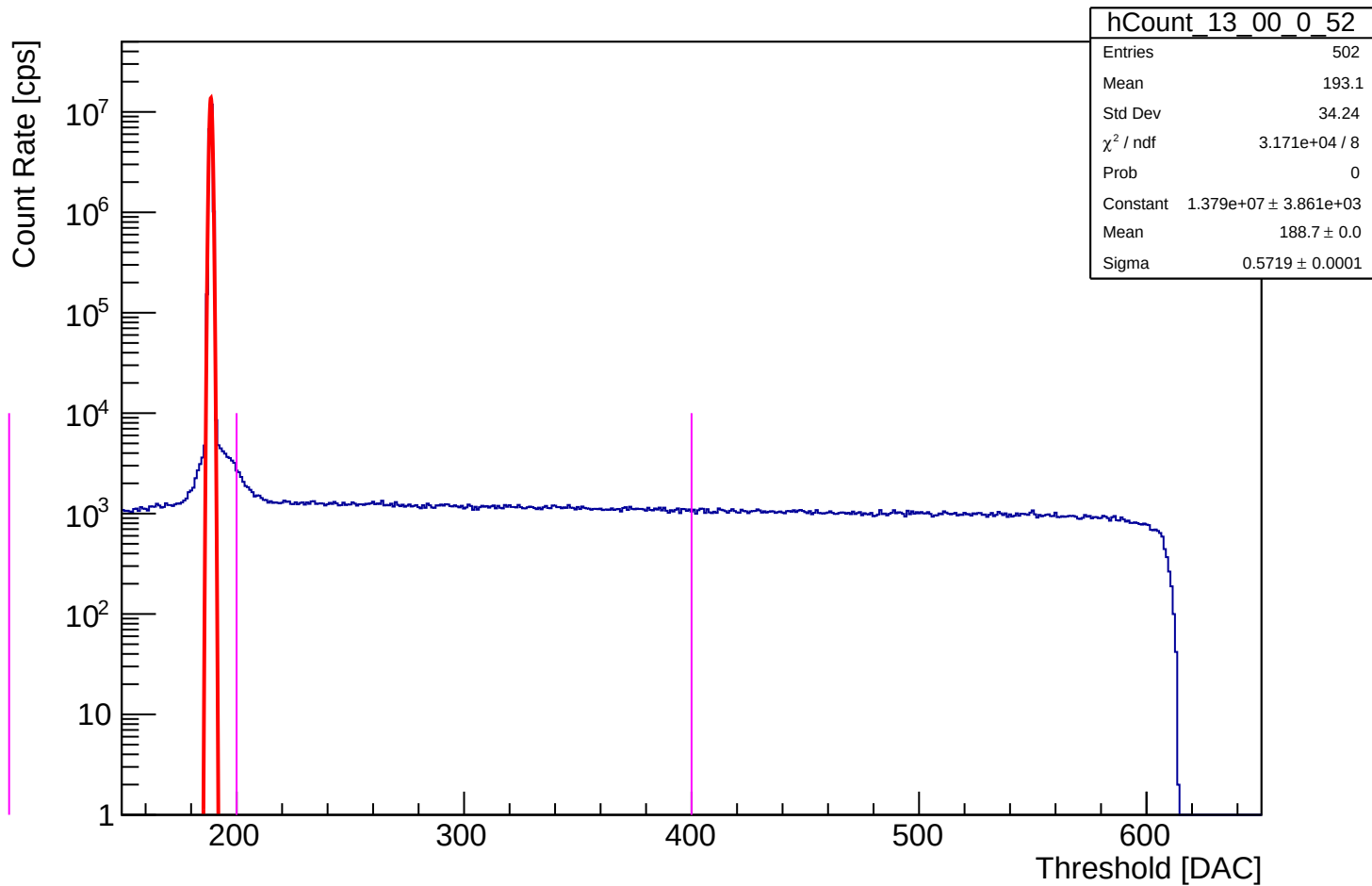


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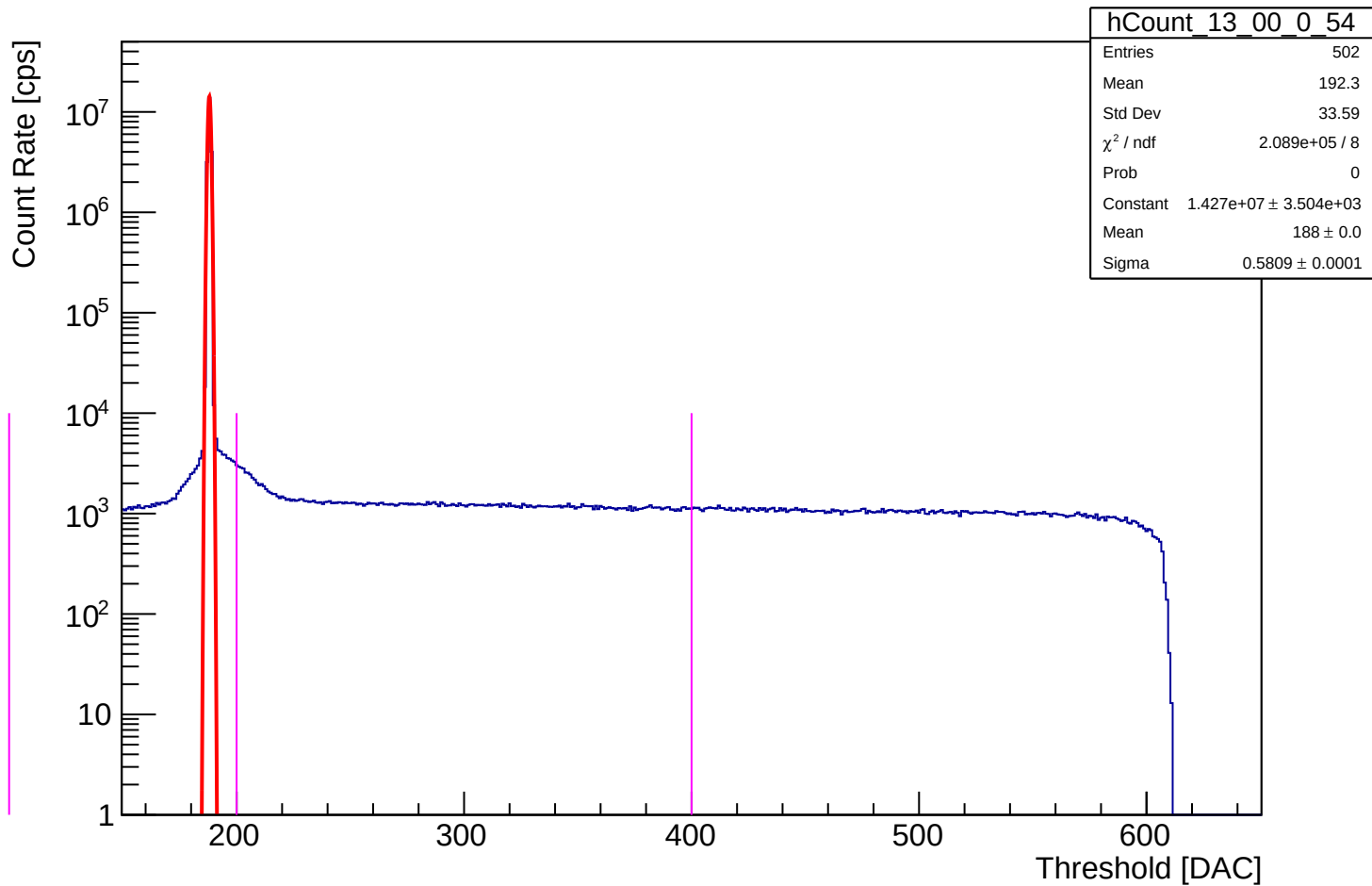




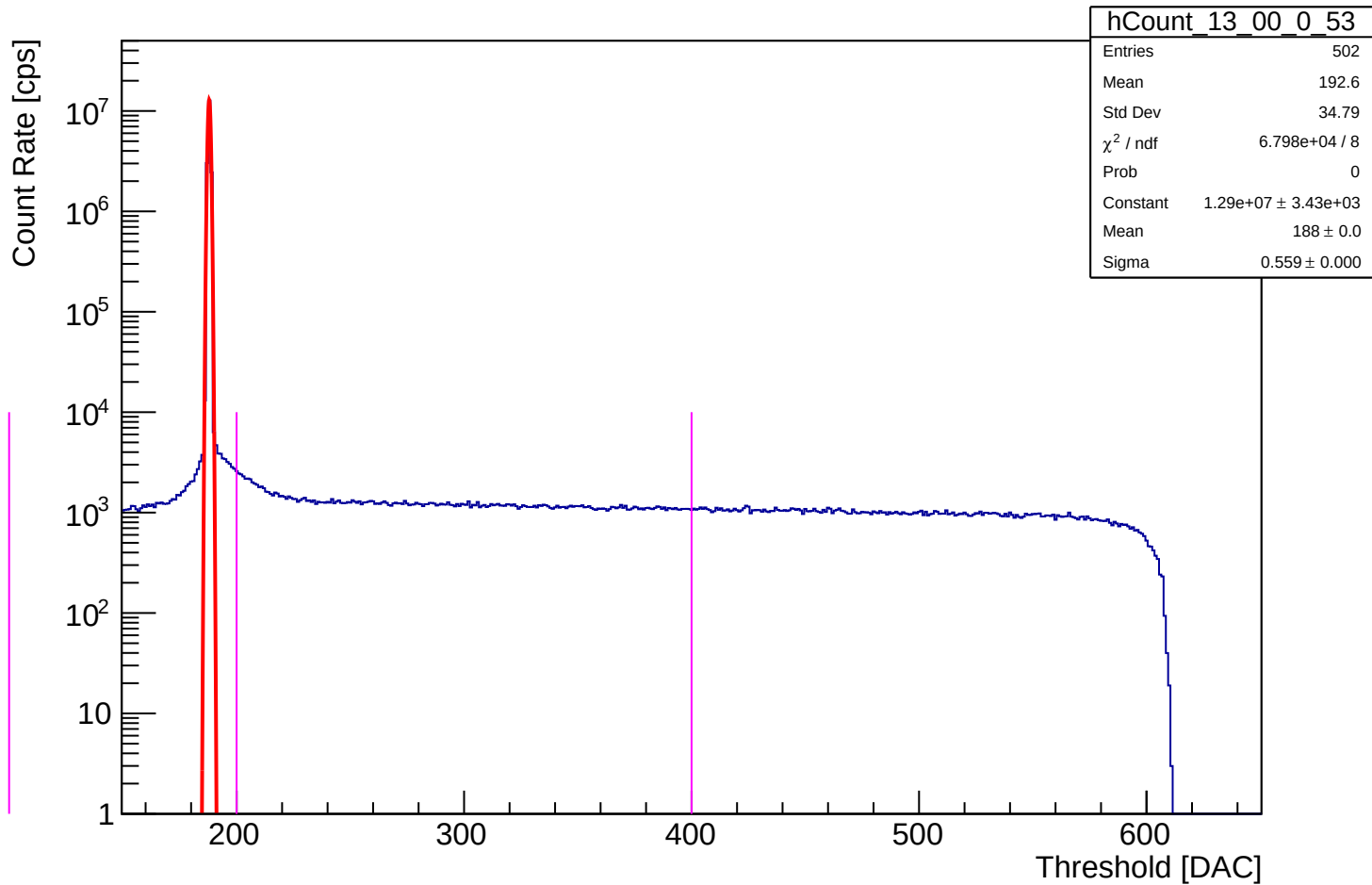
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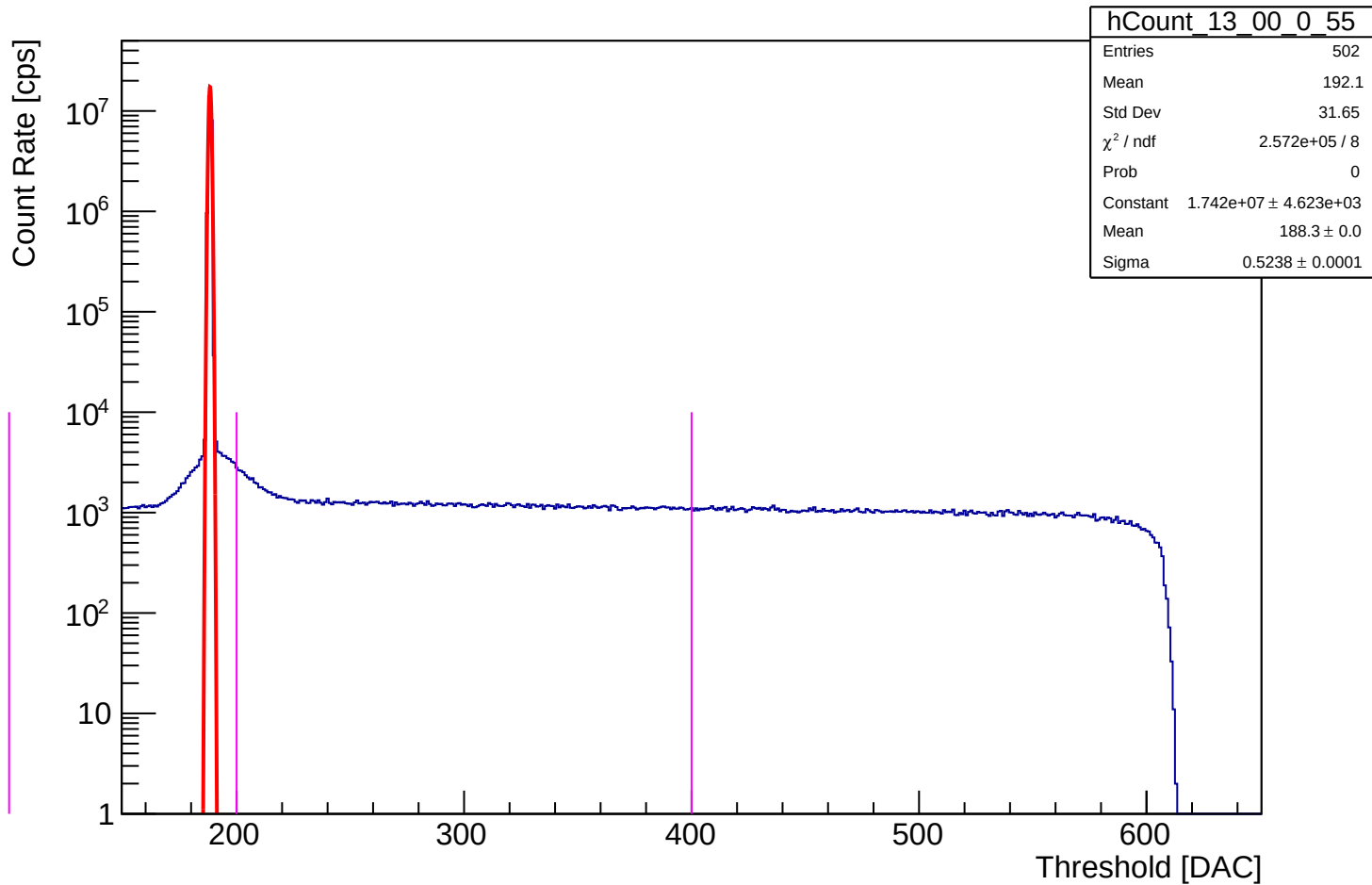
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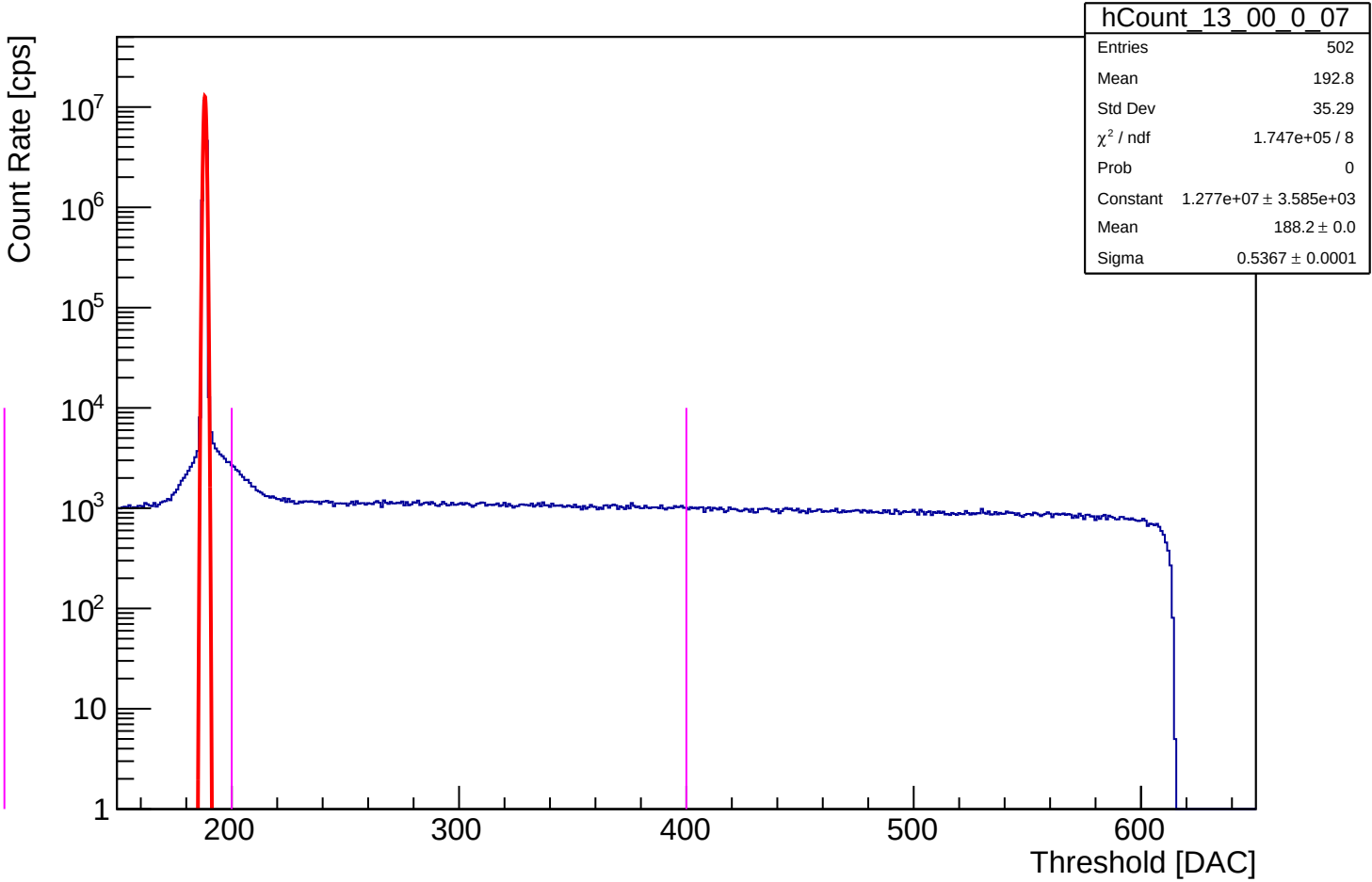


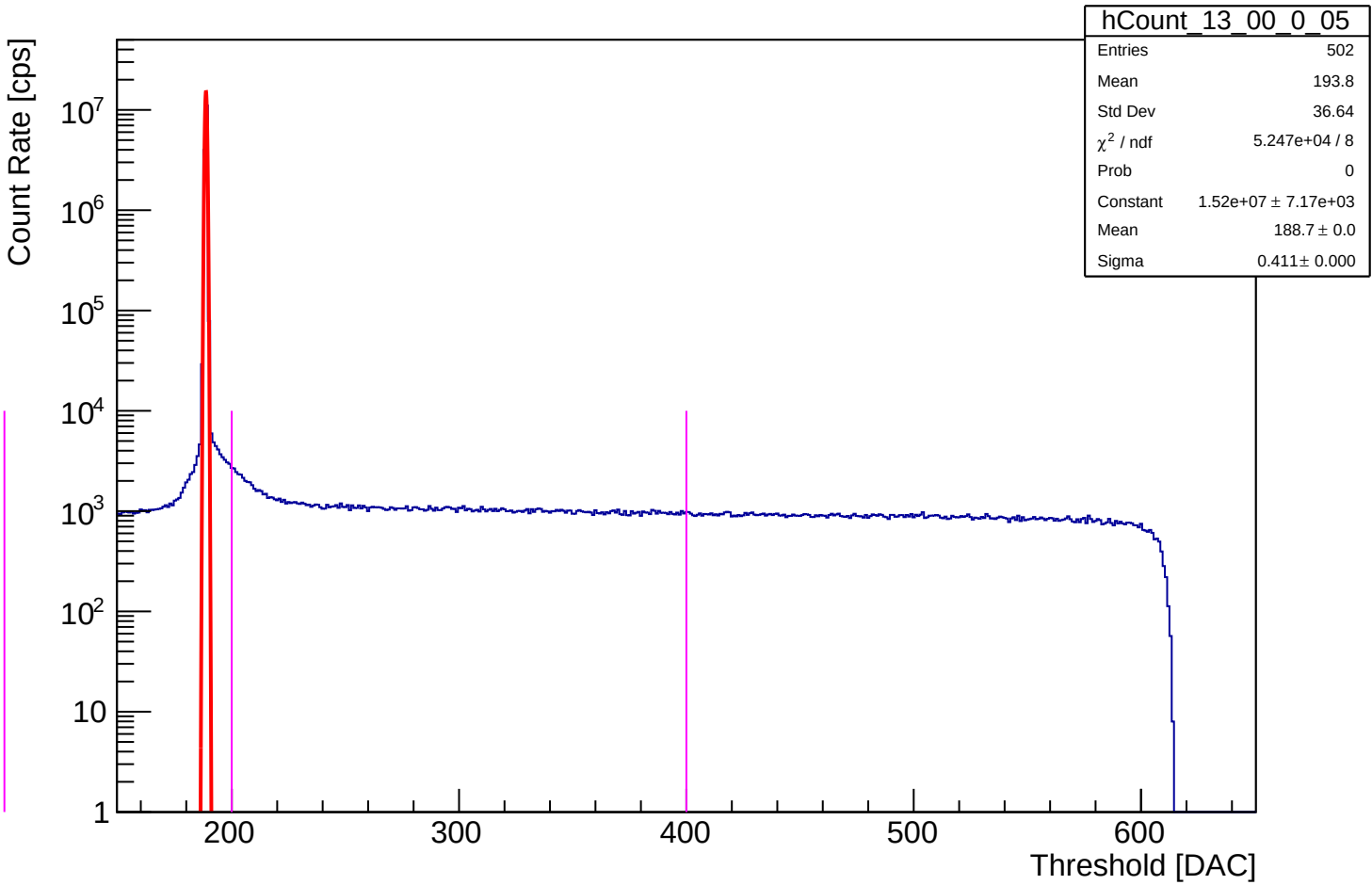
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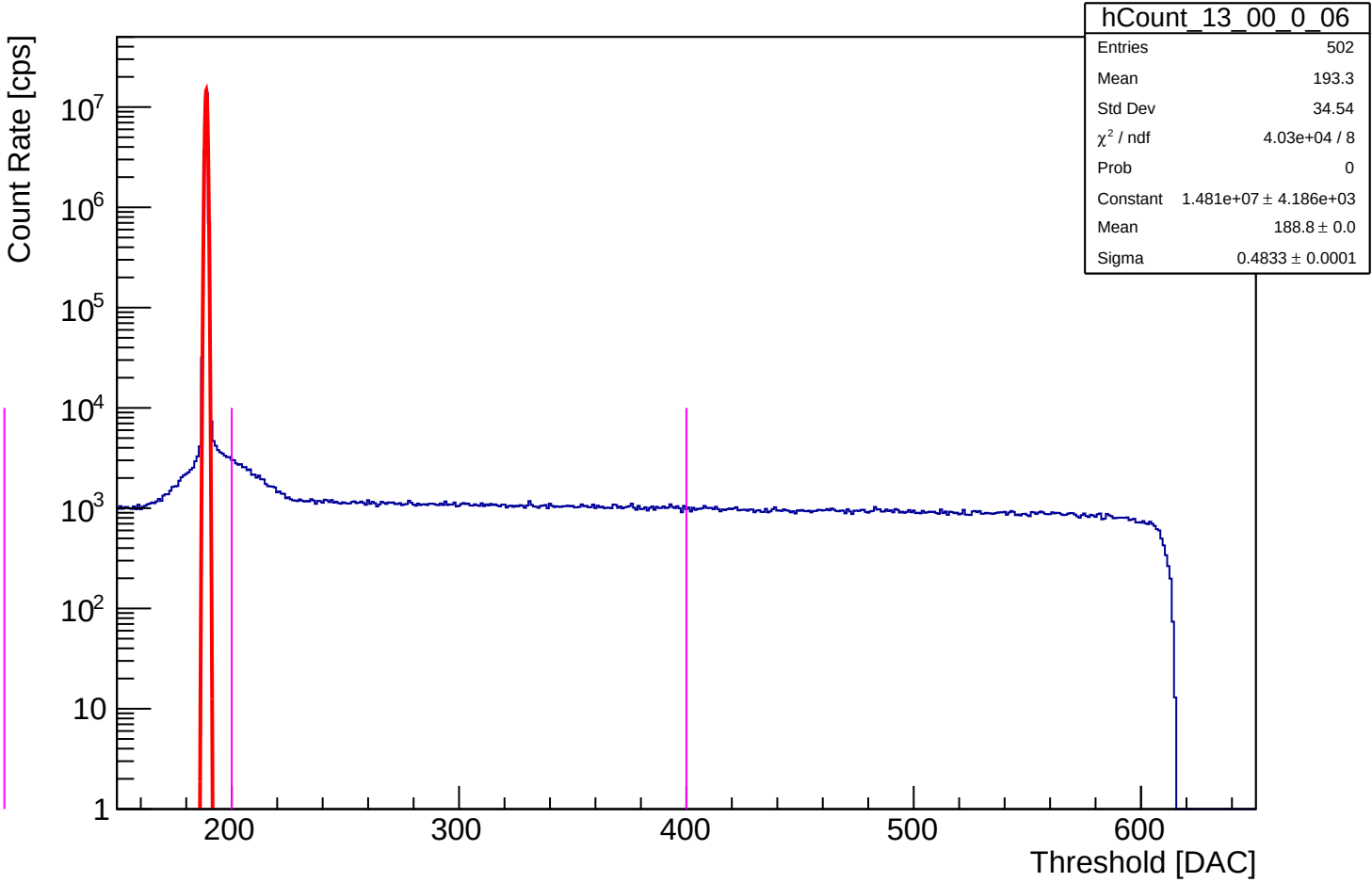


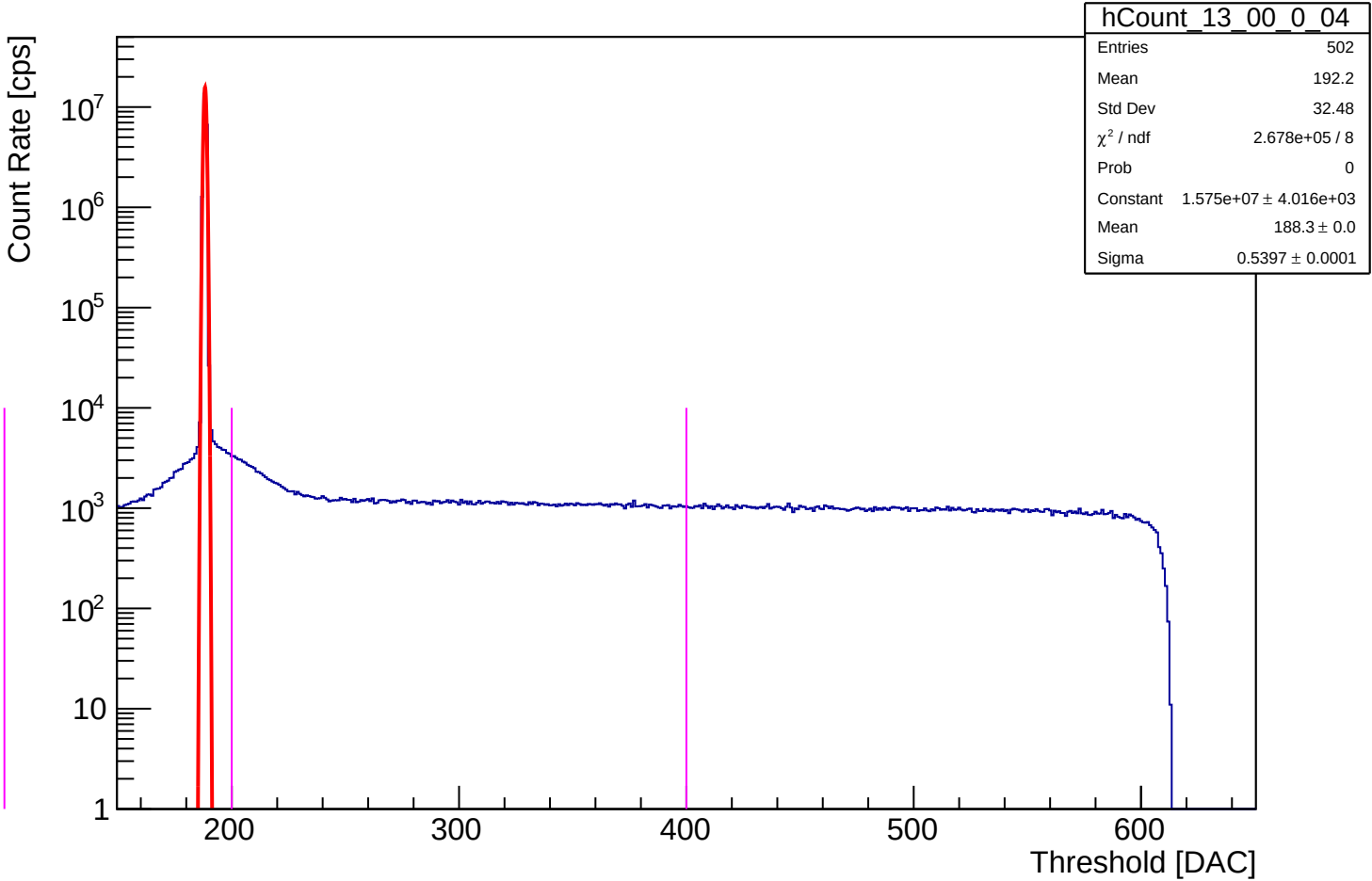
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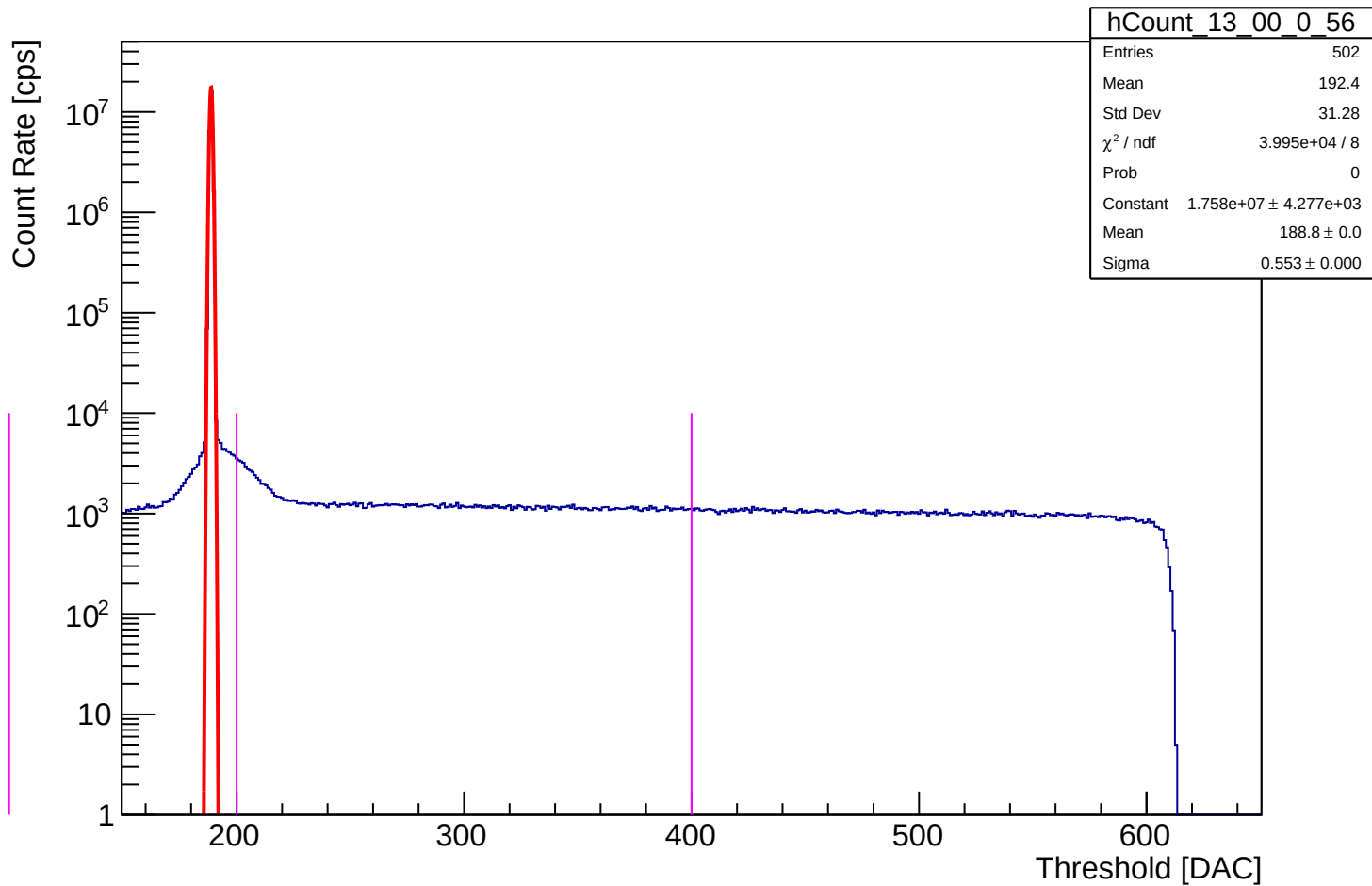




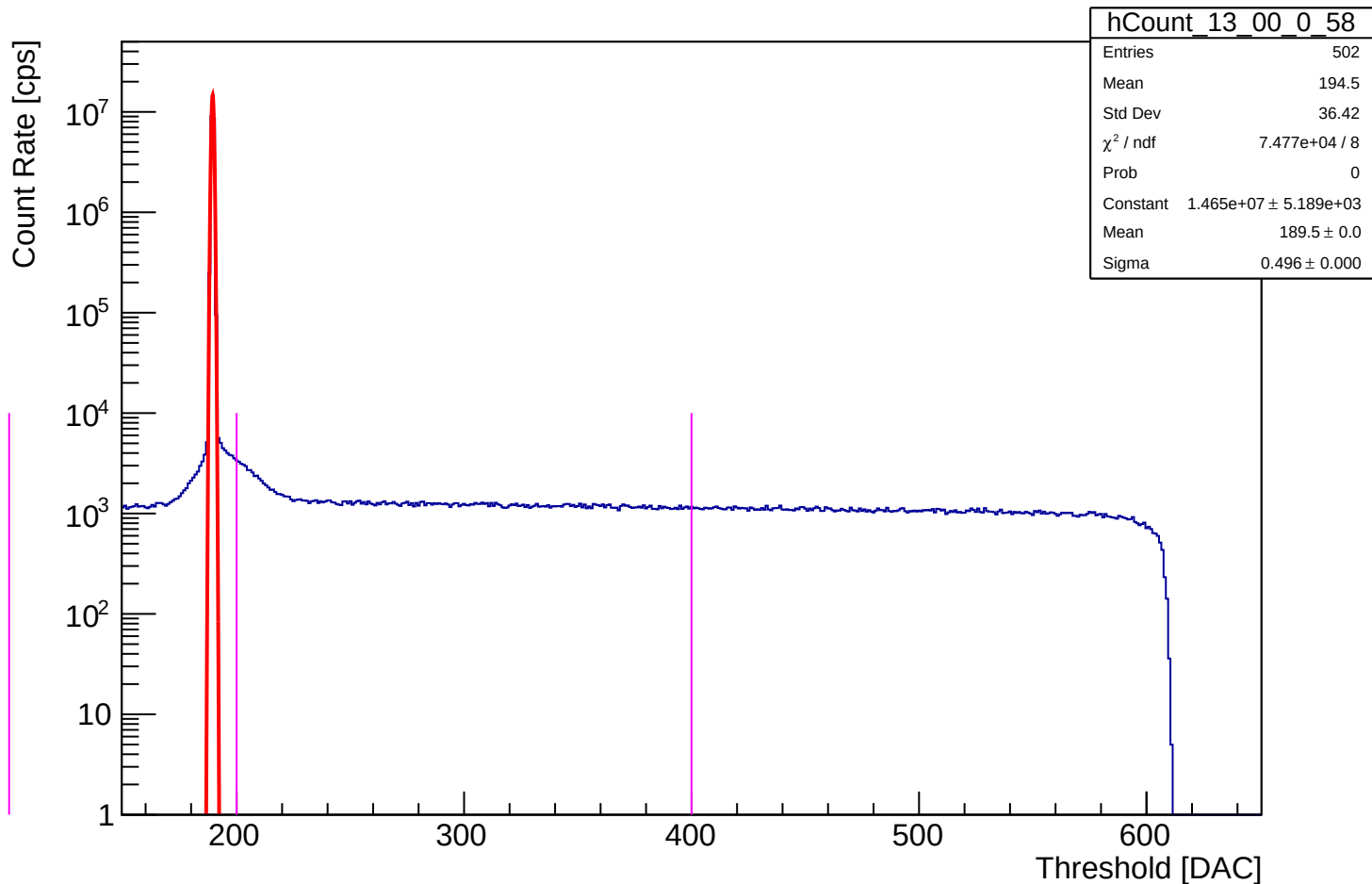




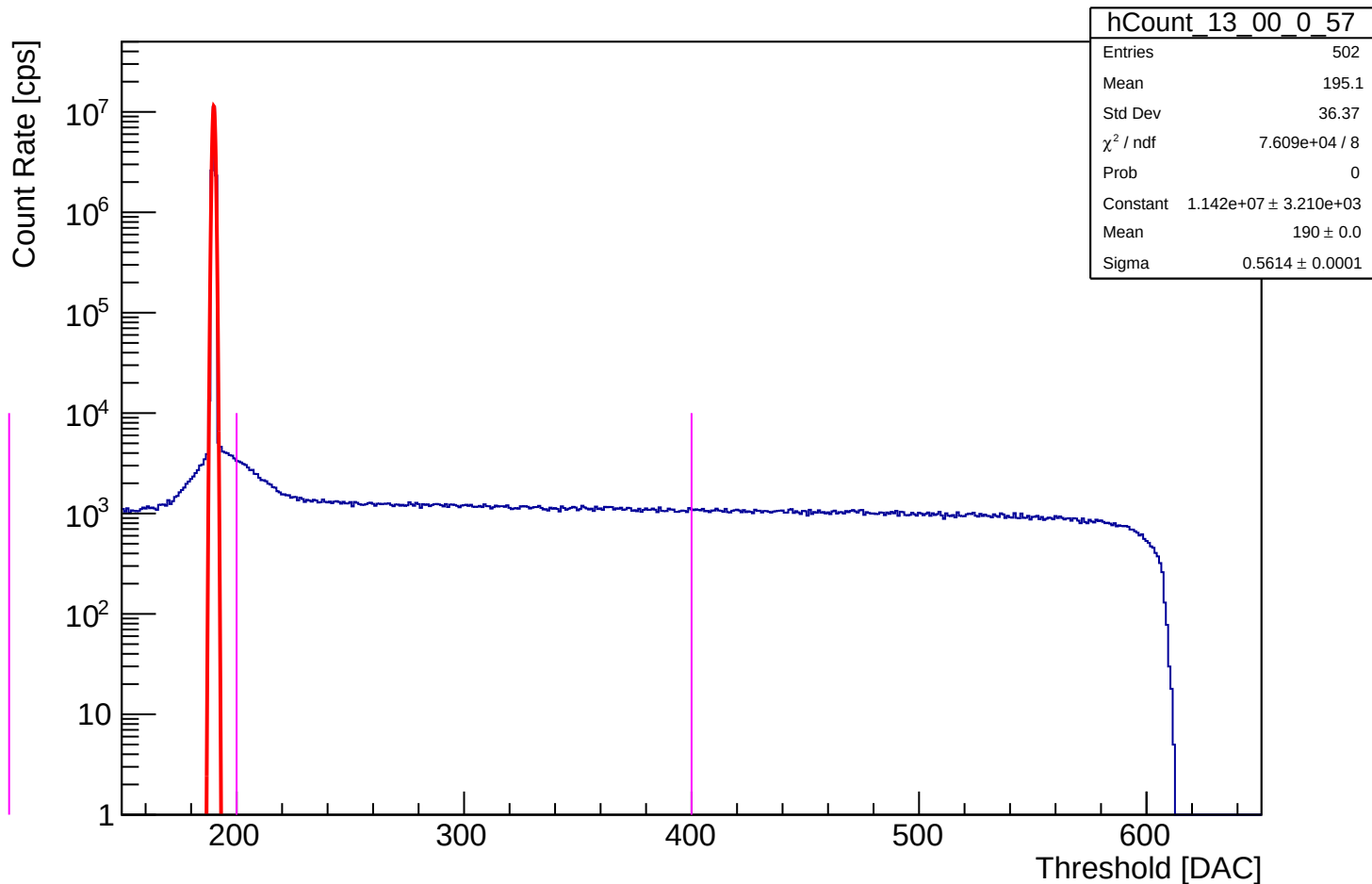
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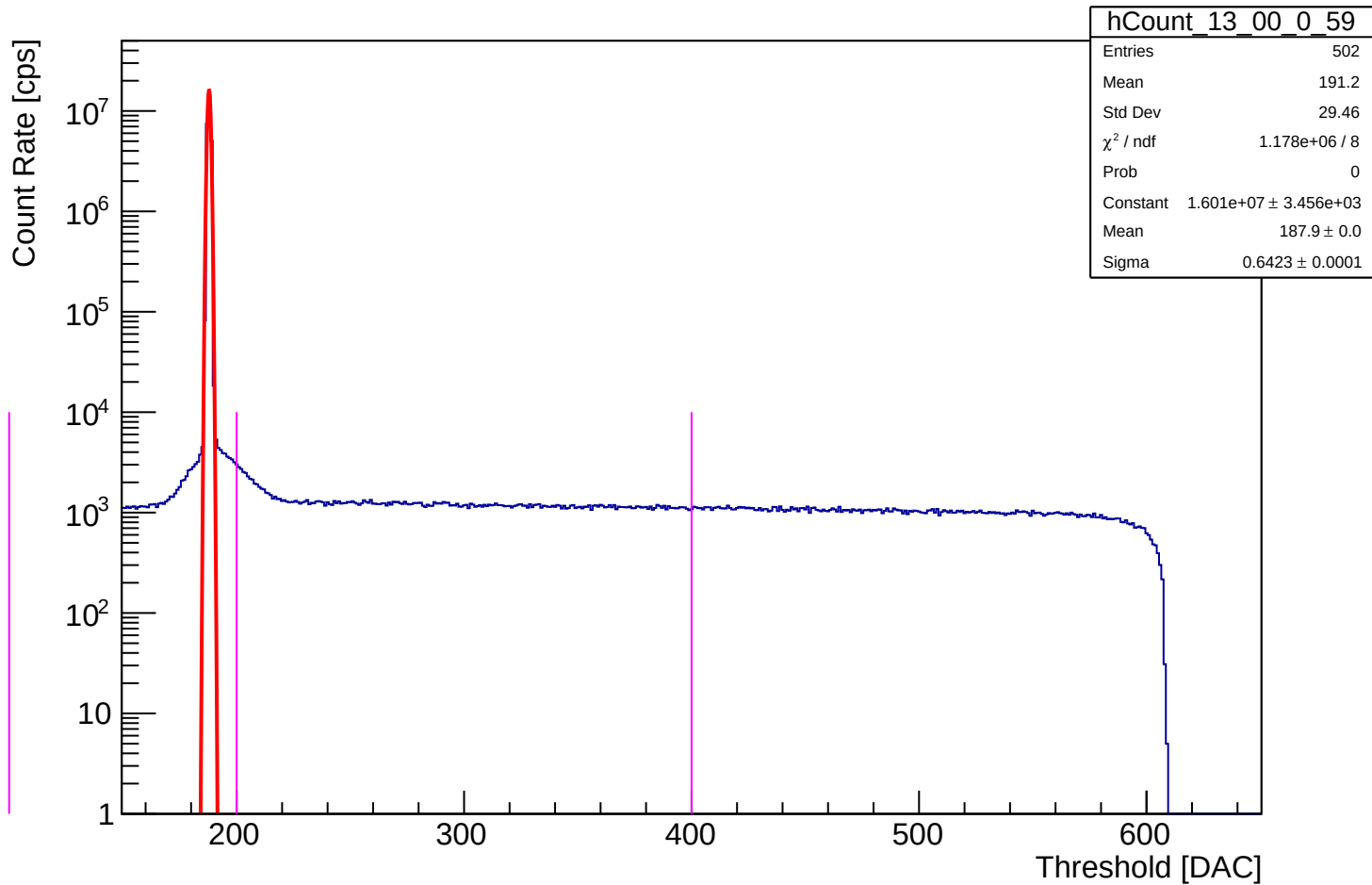
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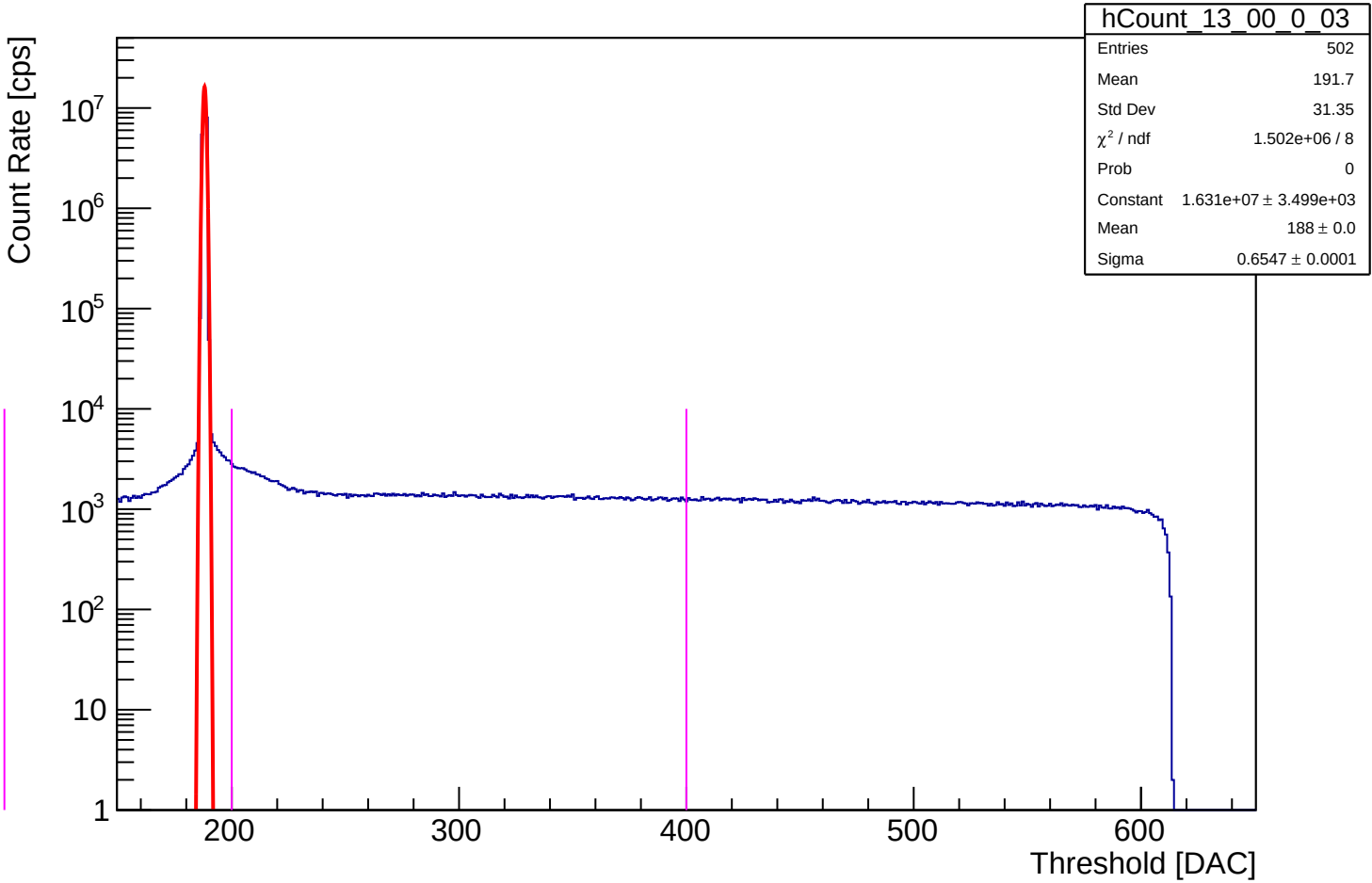


Row 1 Tile 1 Pmt 1 Pixel 55 Slot 13 Fiber 0 Asic 0 Channel 57

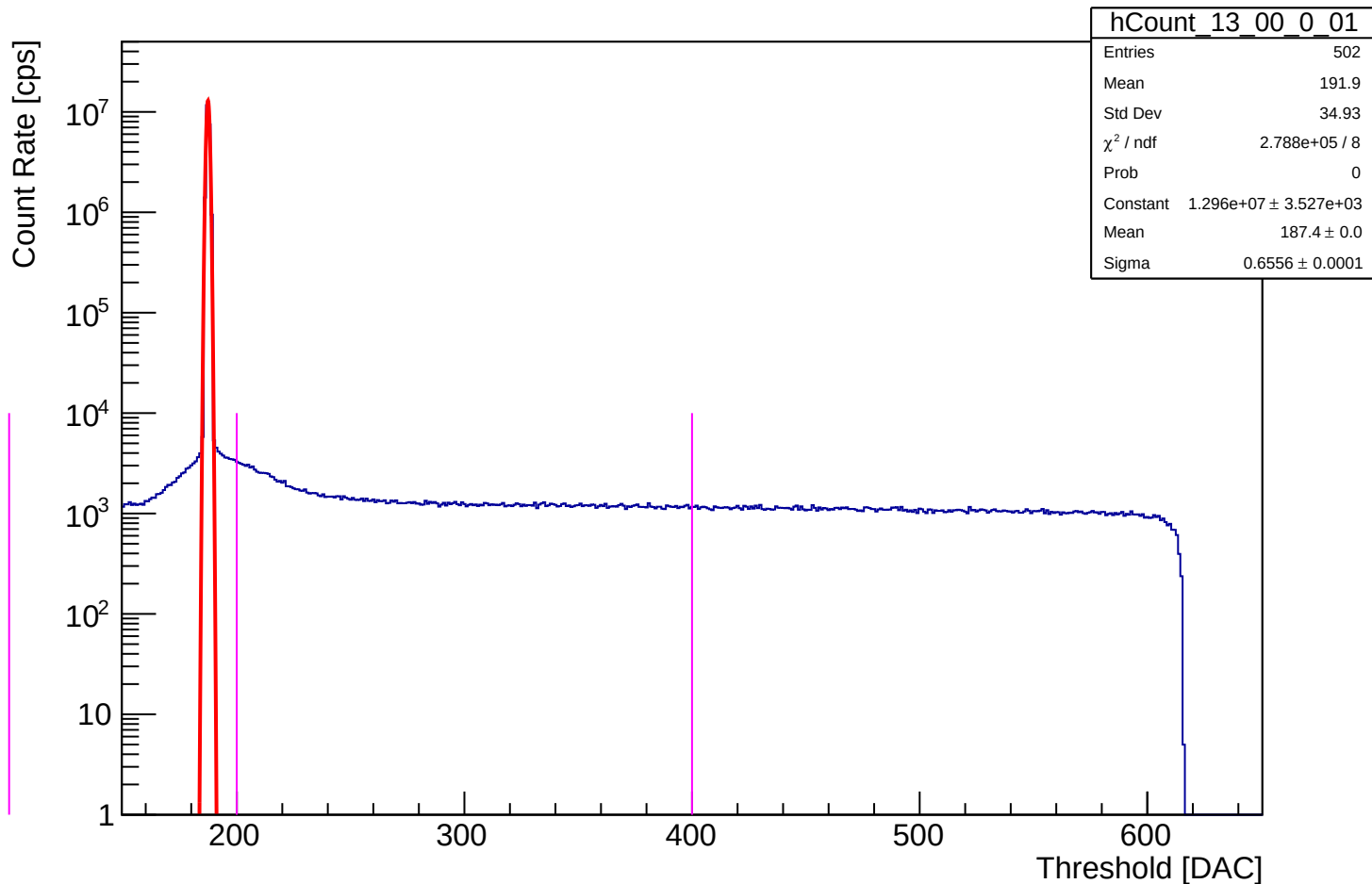


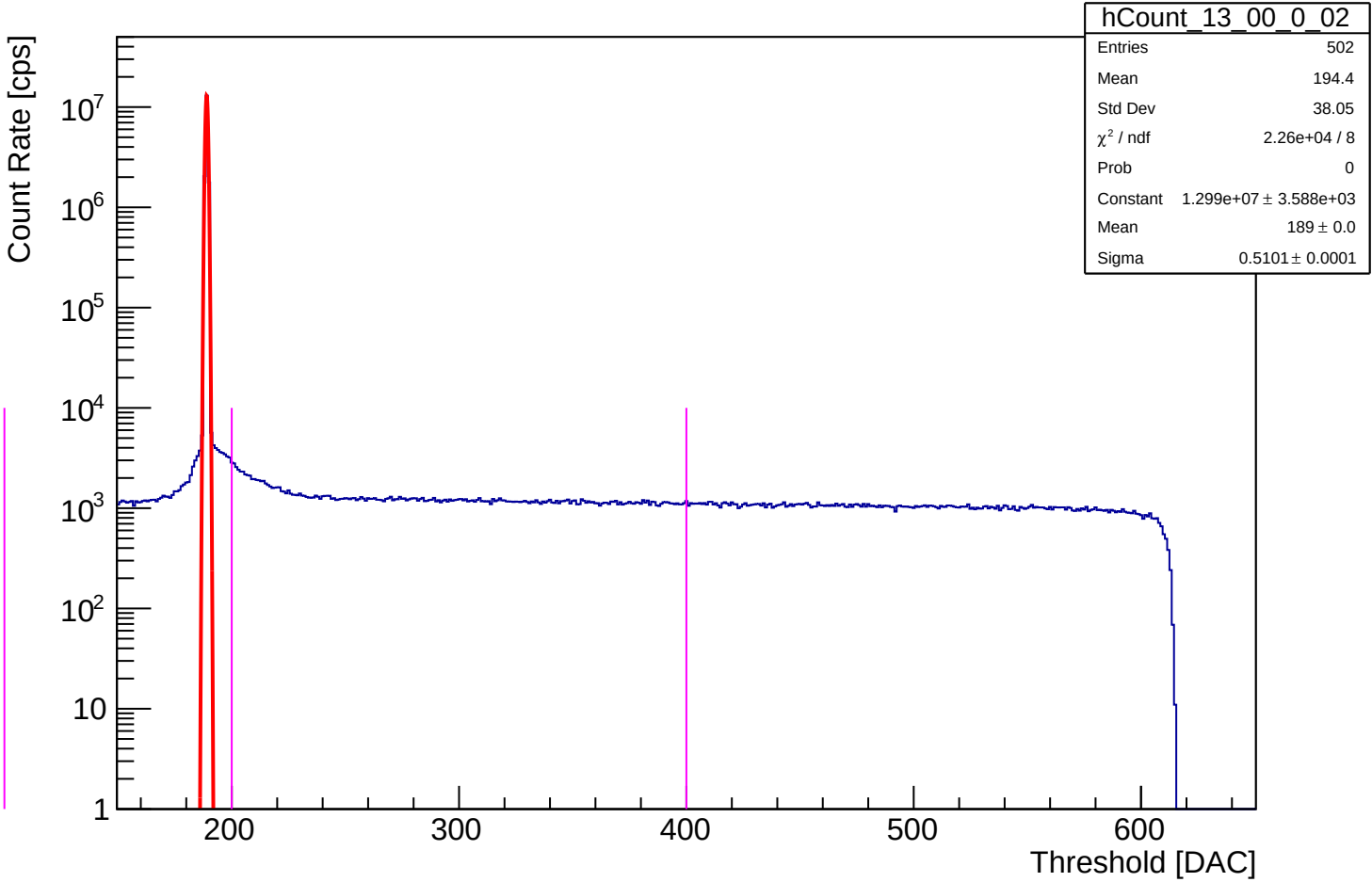
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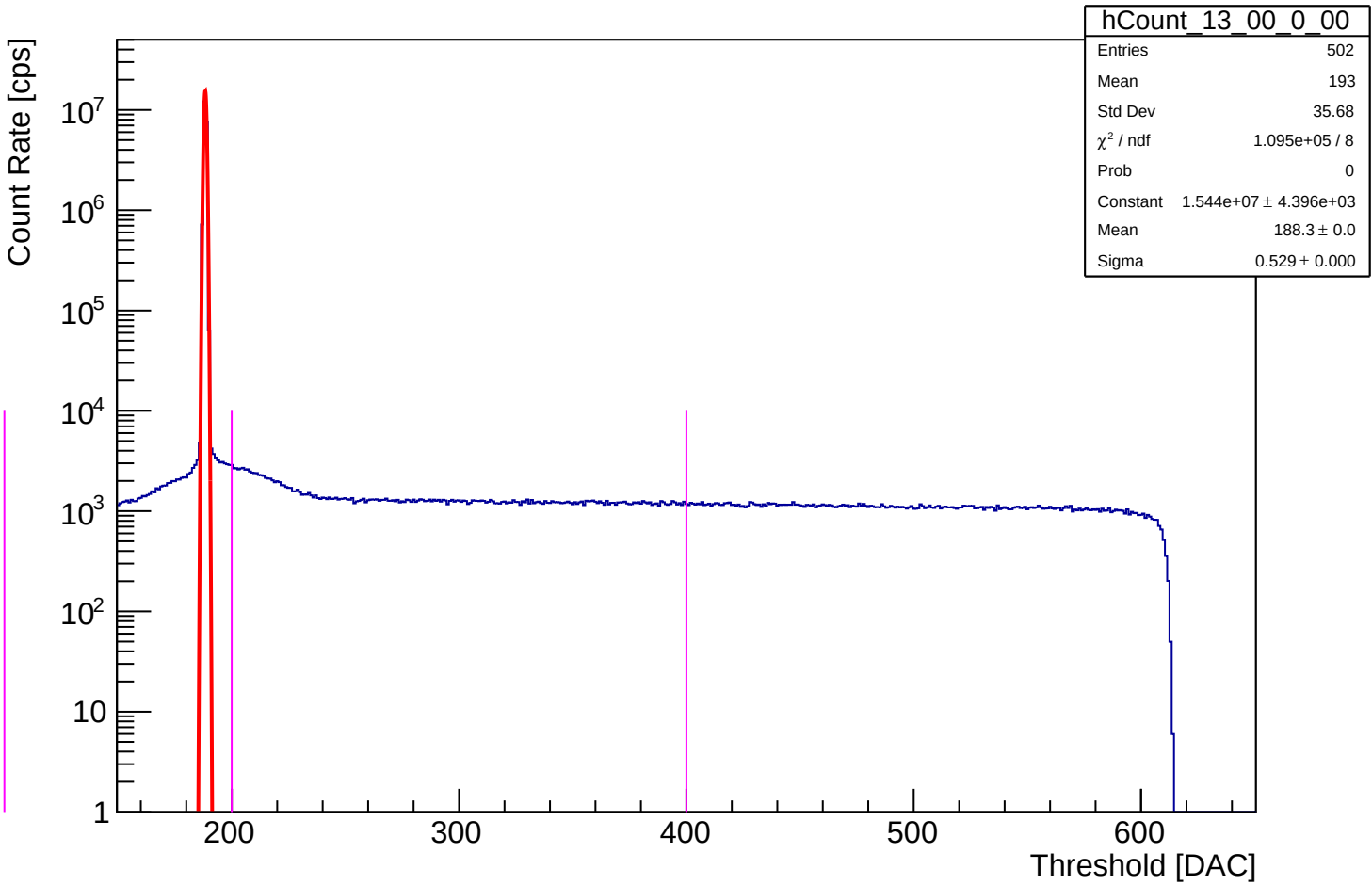




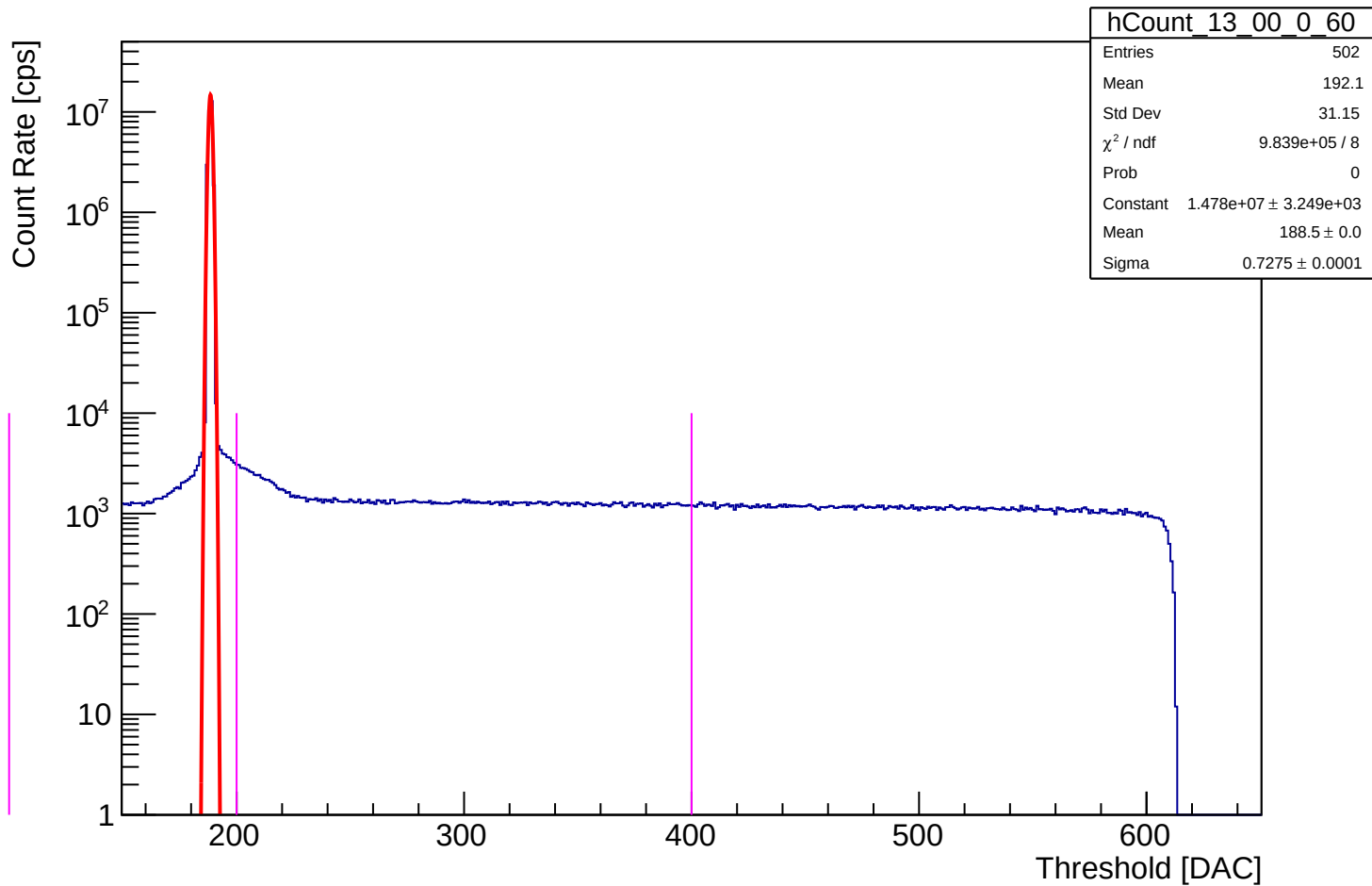
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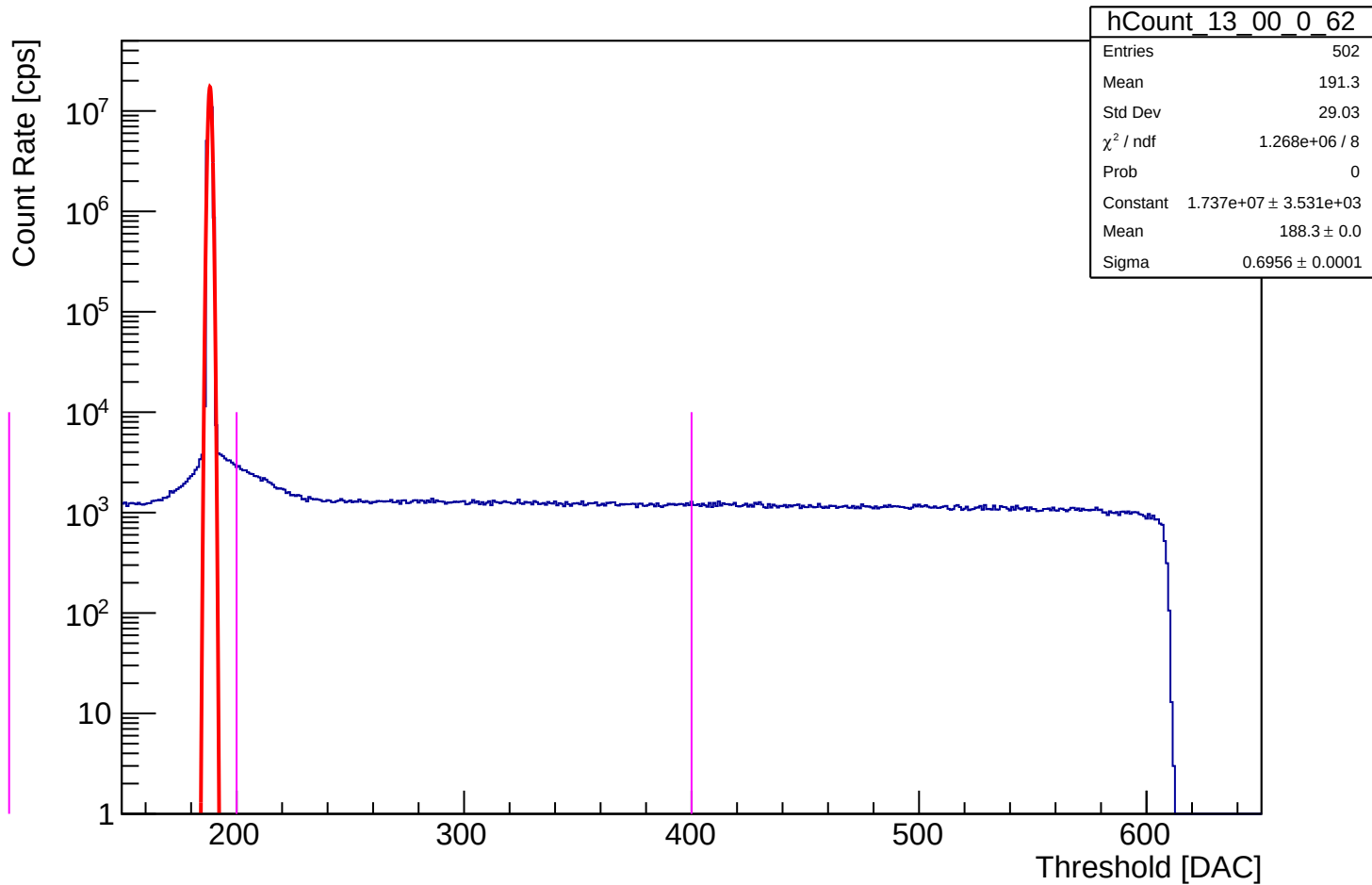




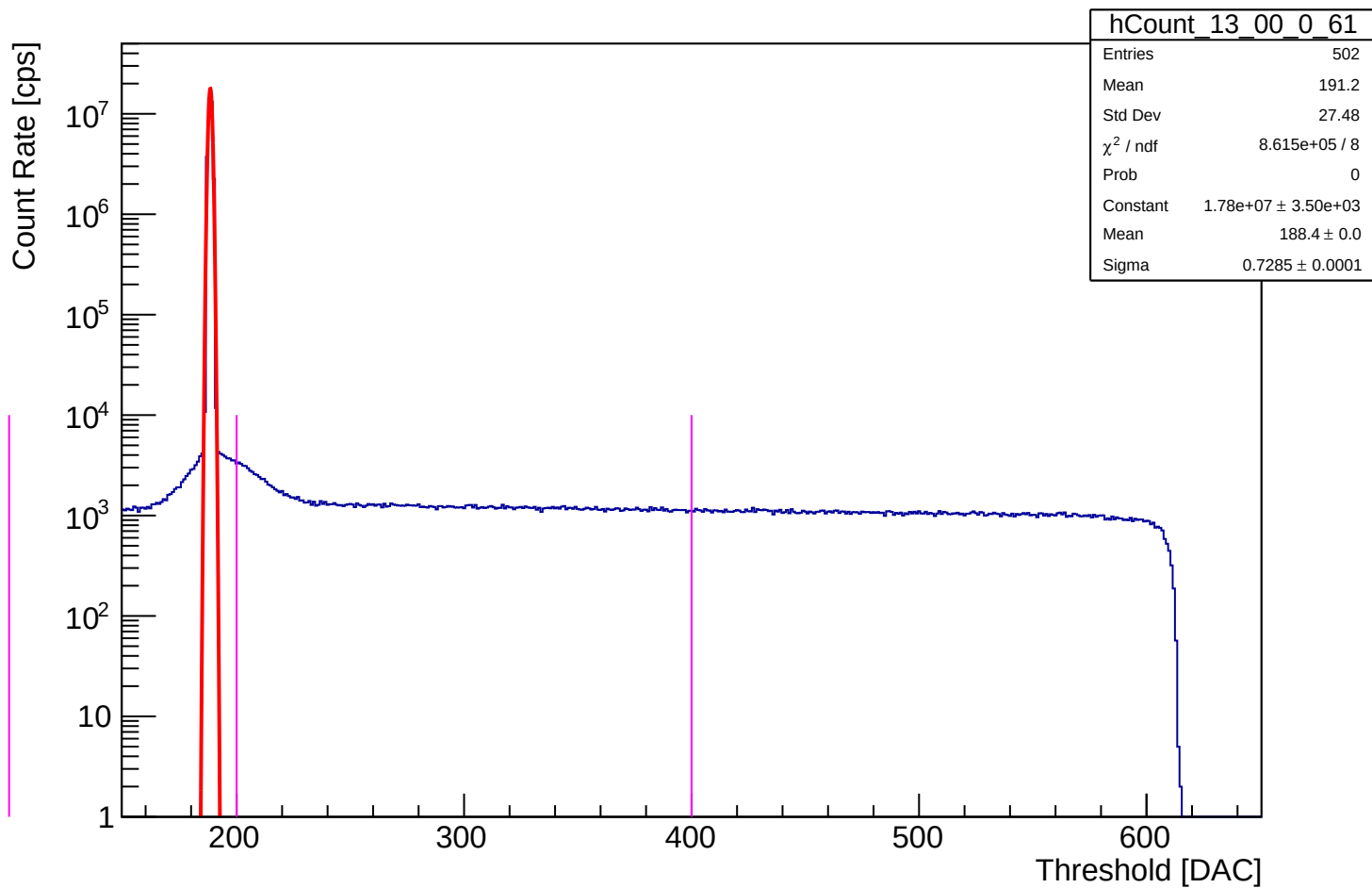
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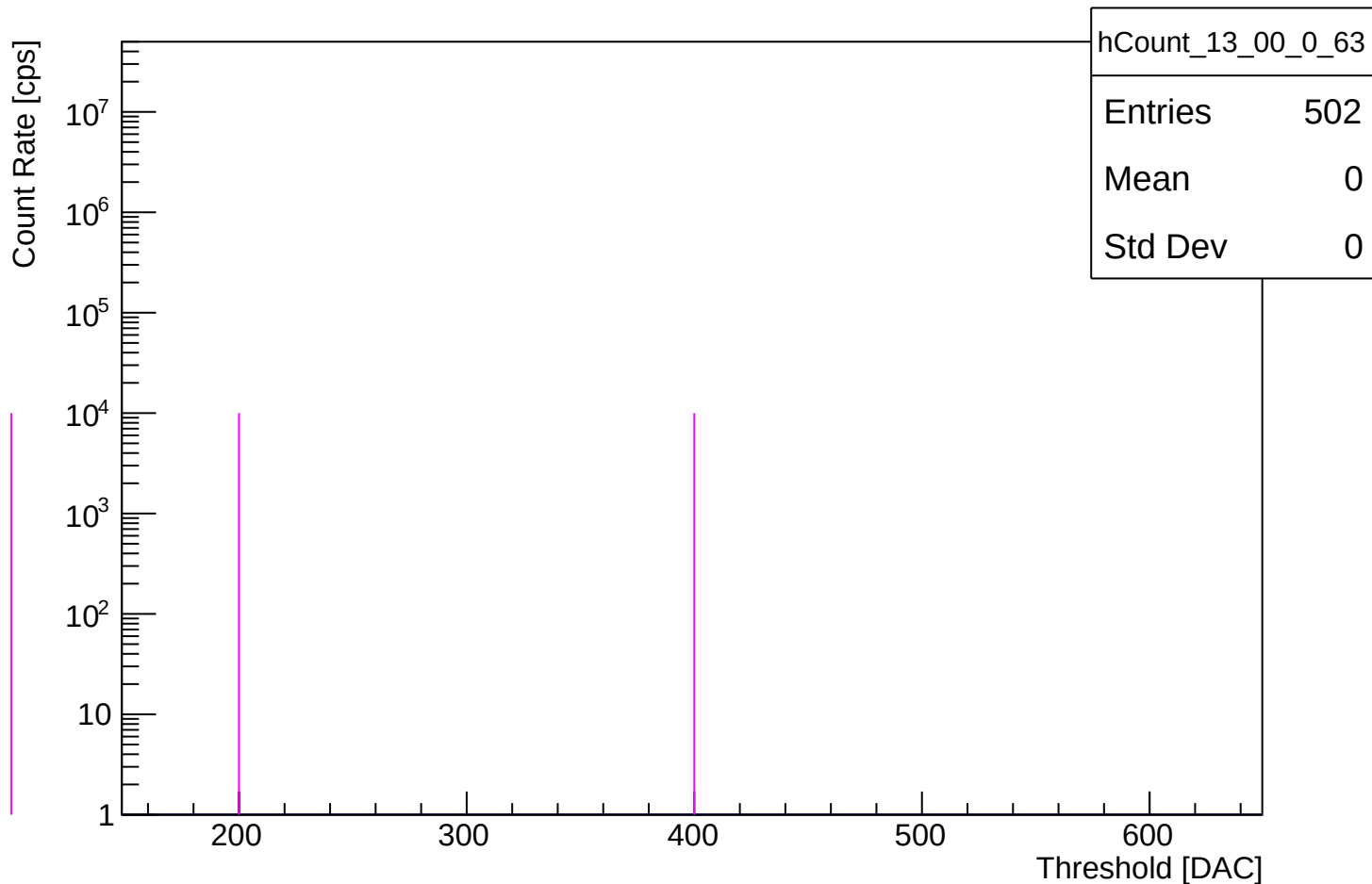
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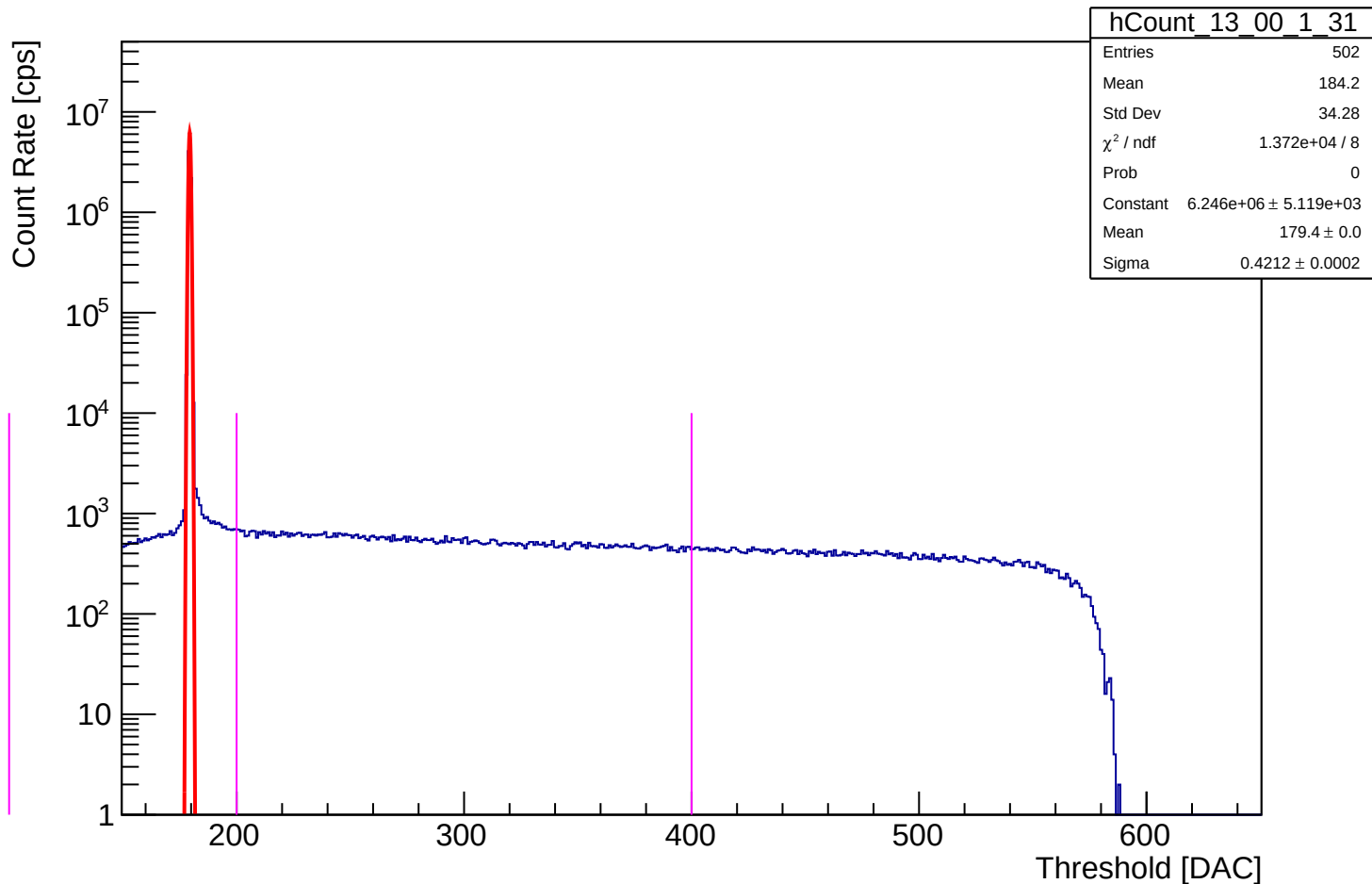
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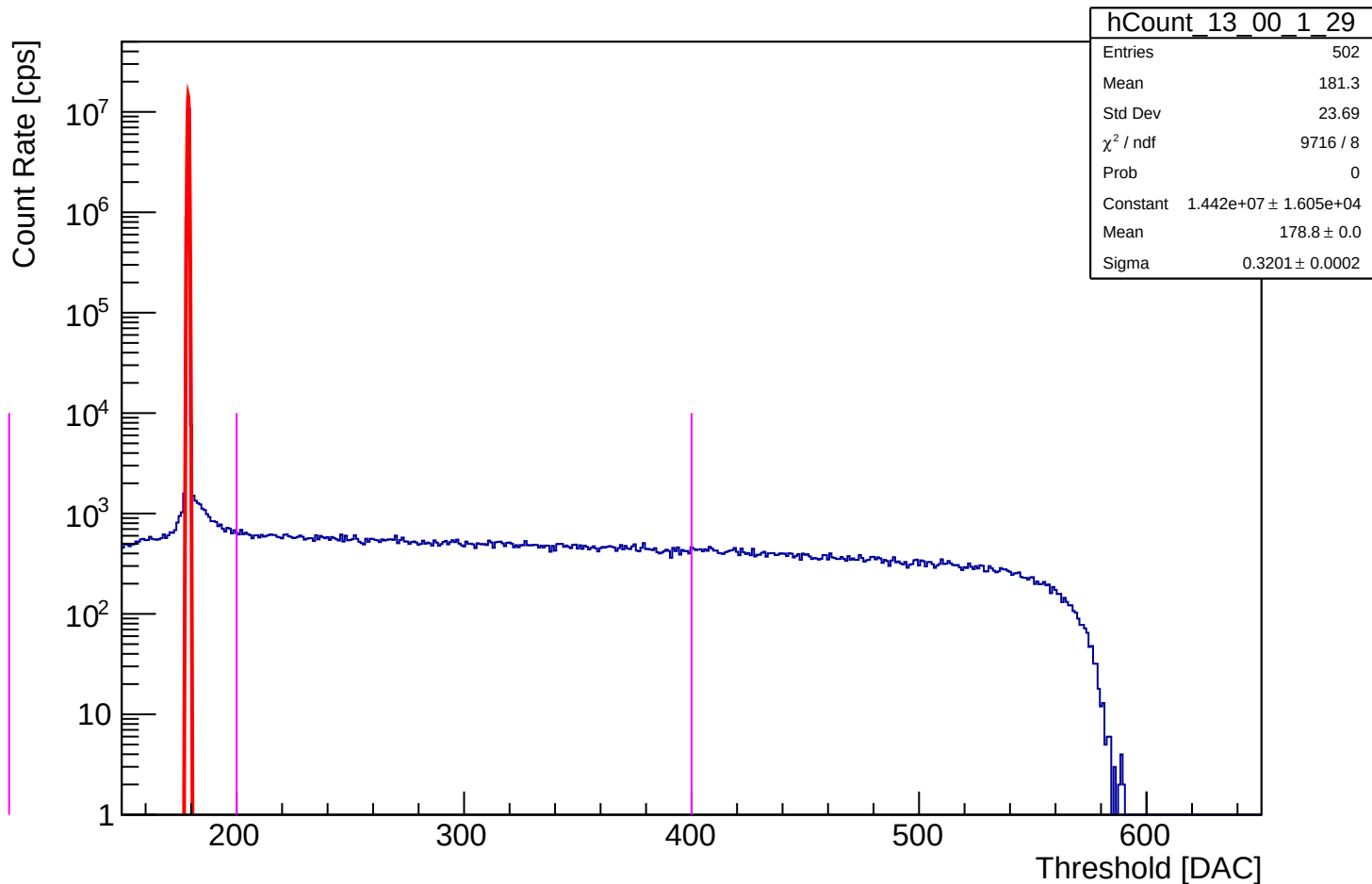
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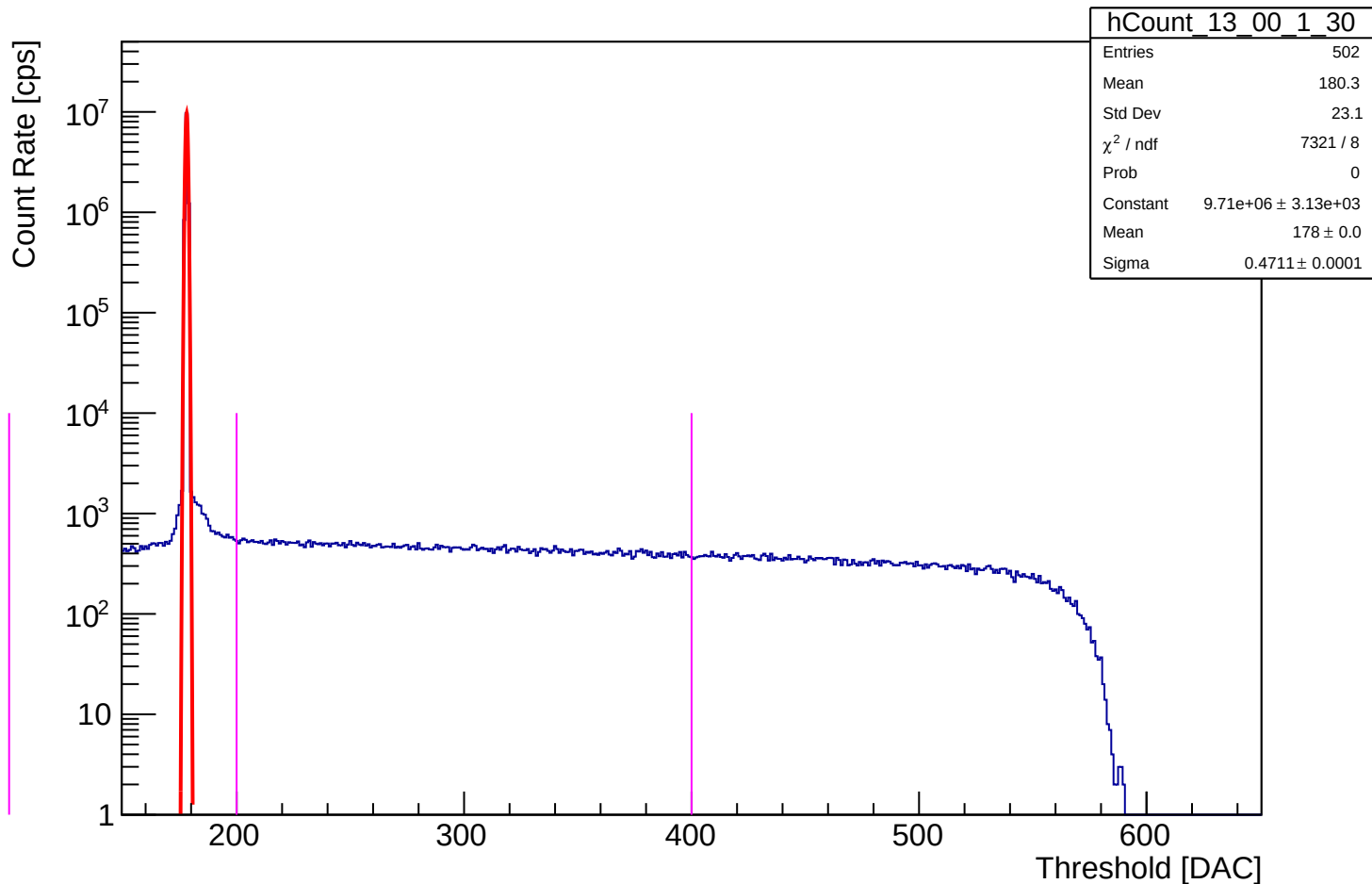
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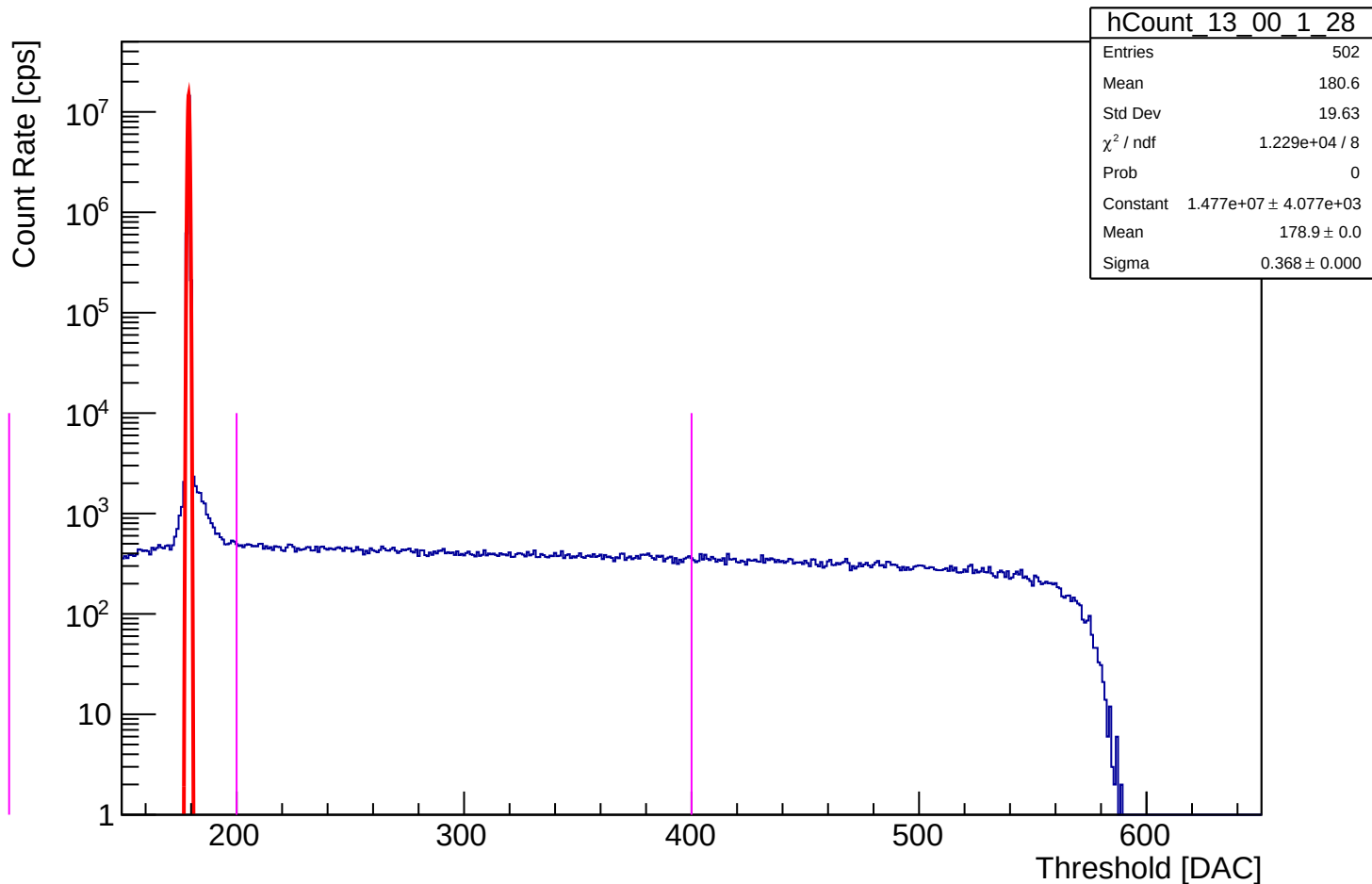
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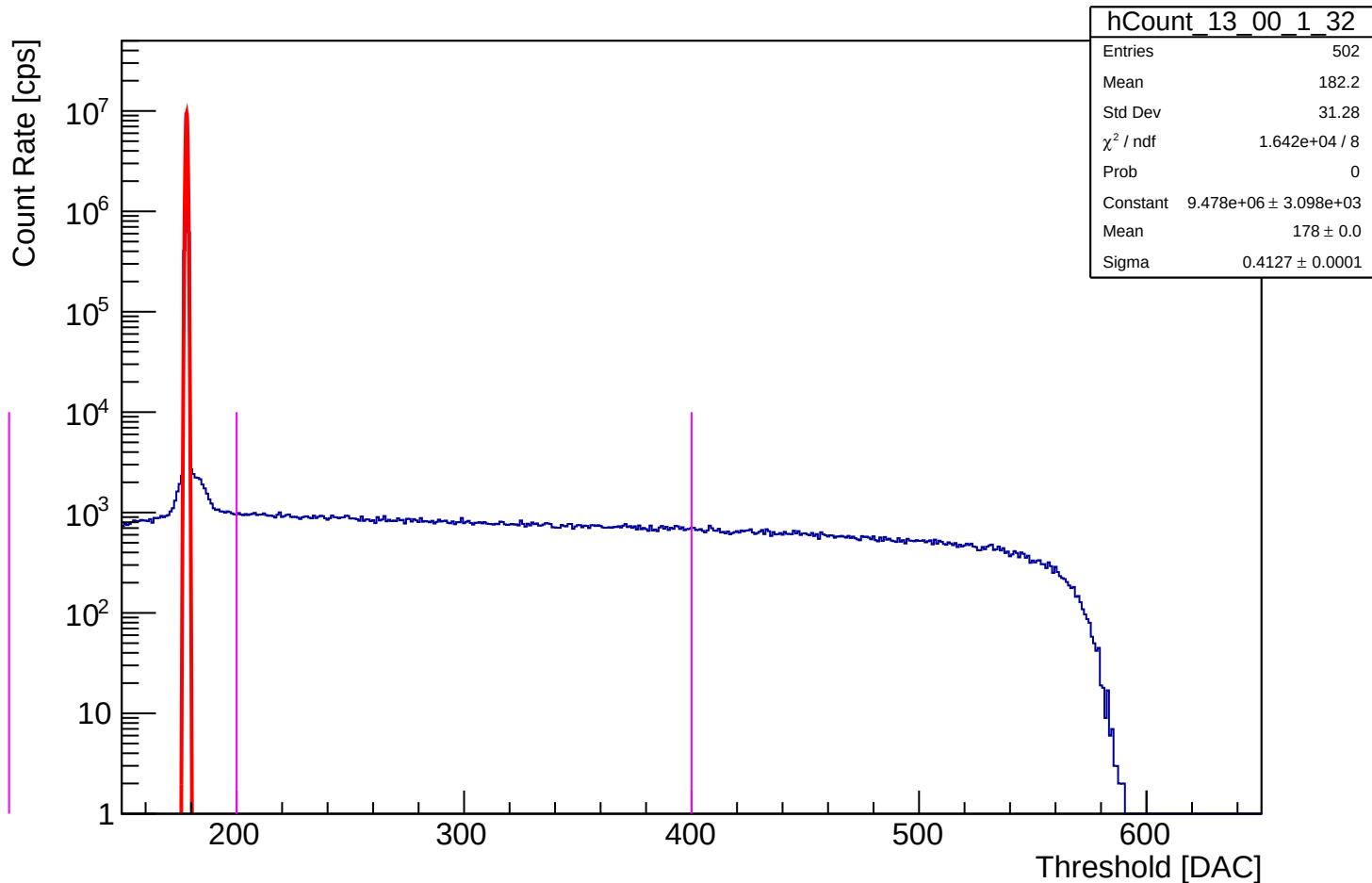
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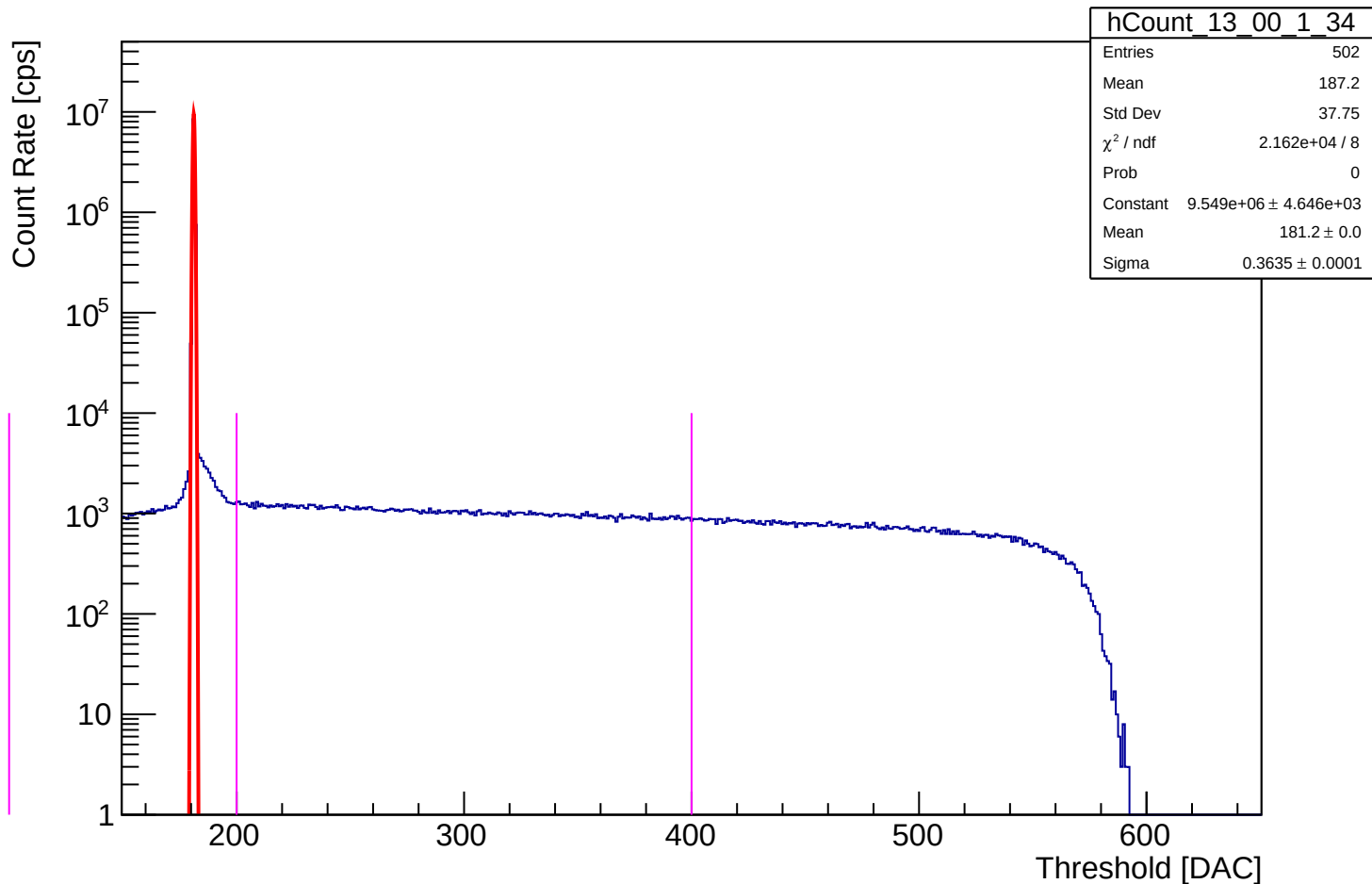
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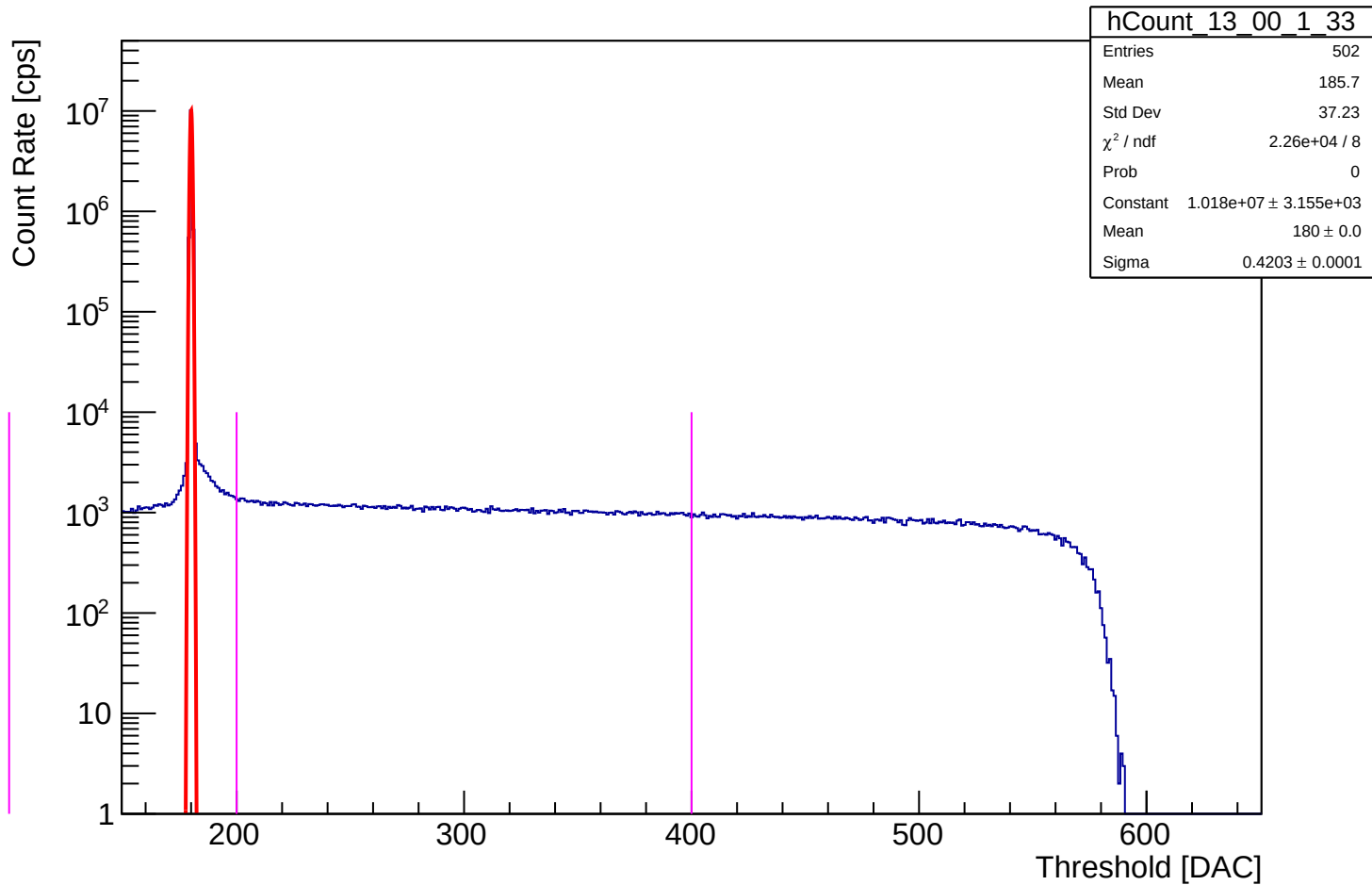
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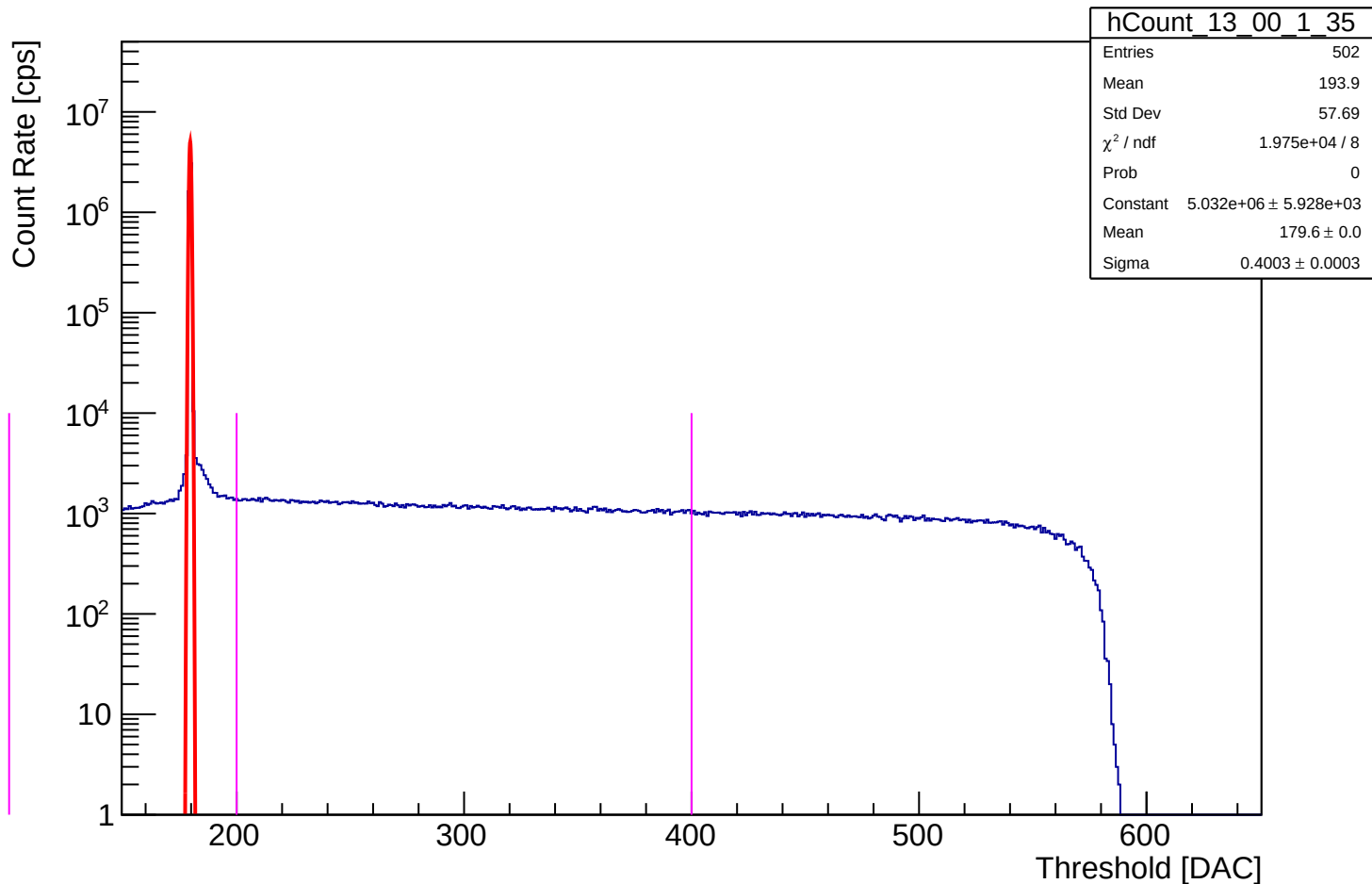
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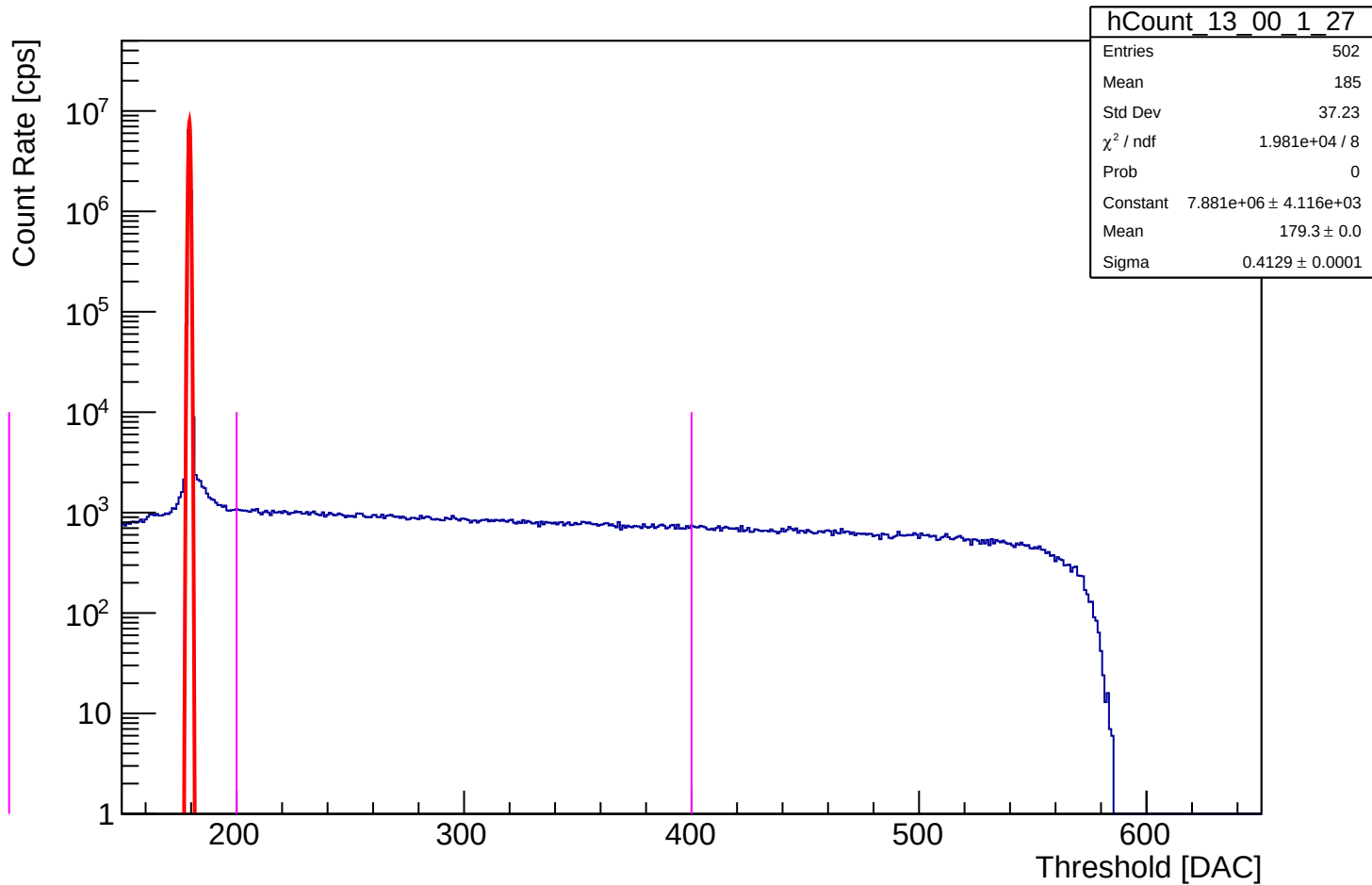
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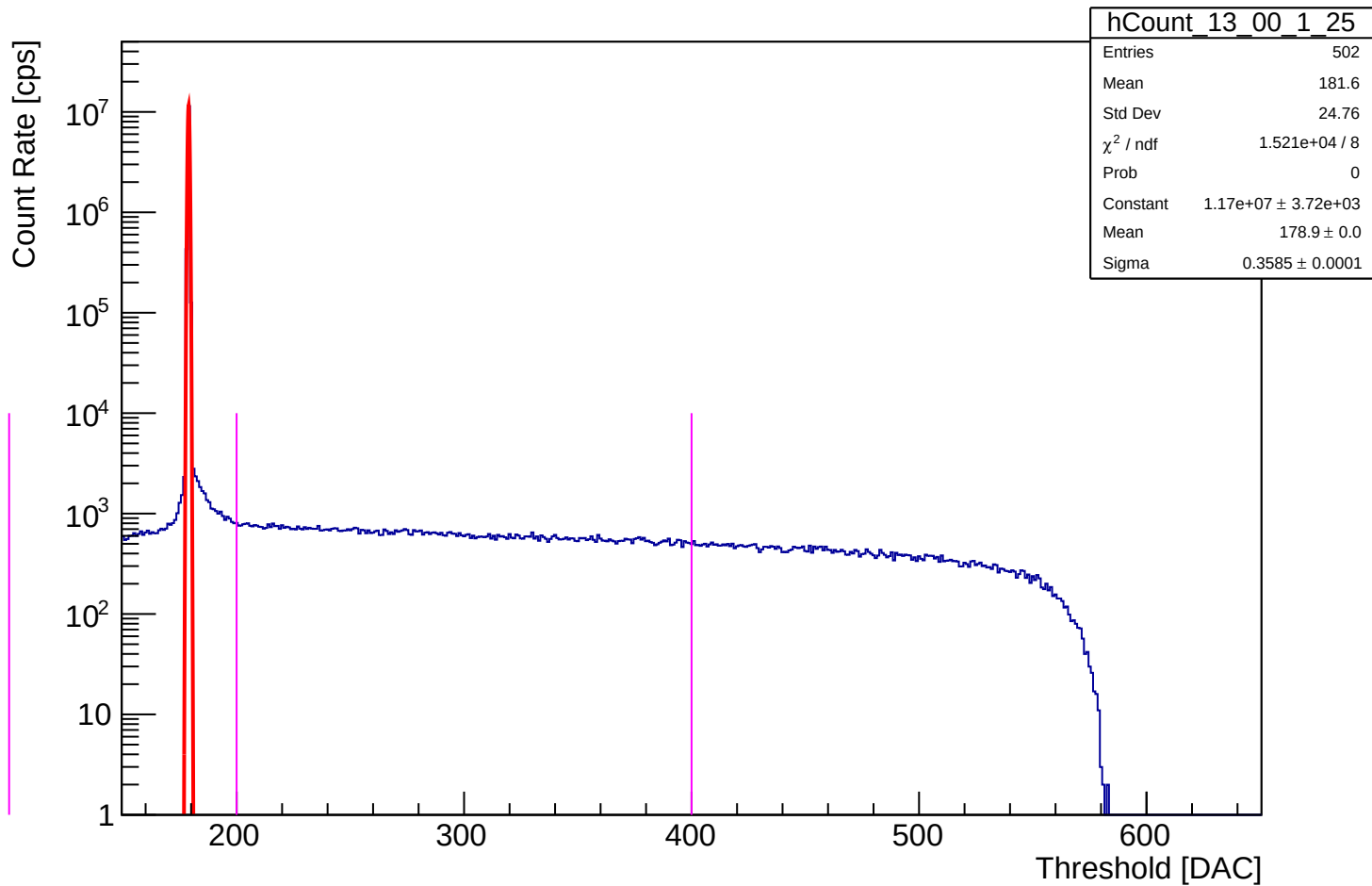
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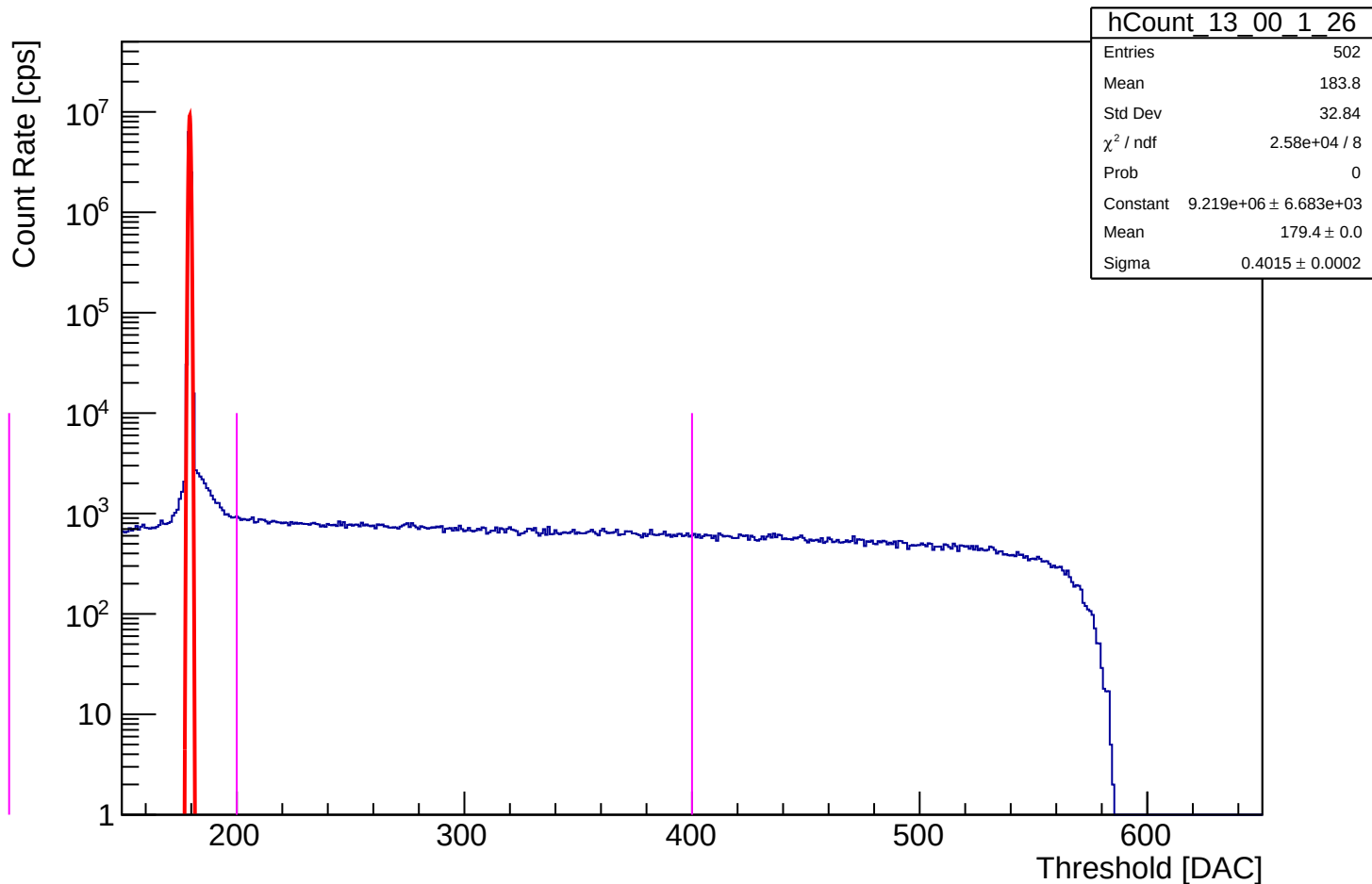
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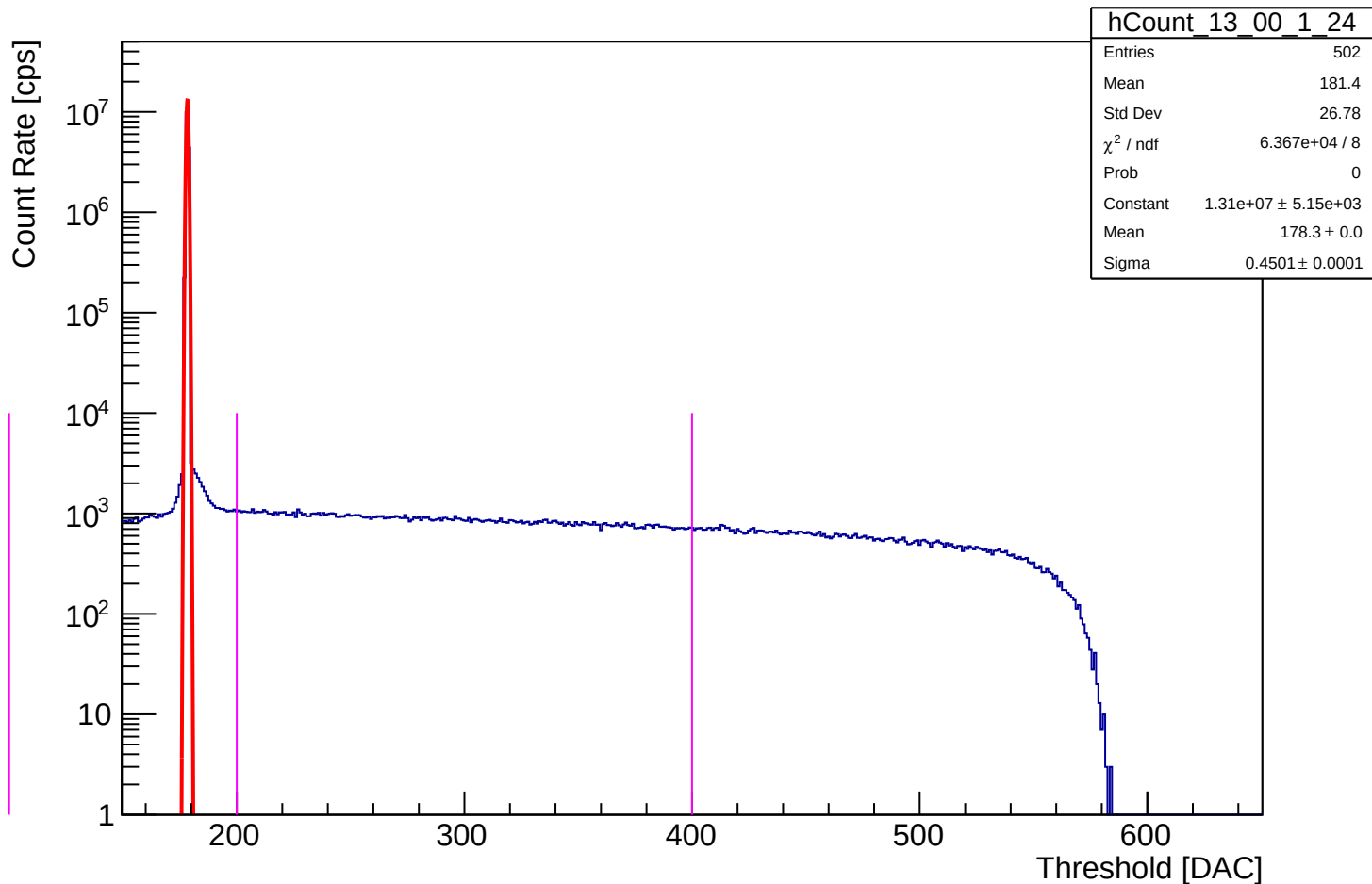
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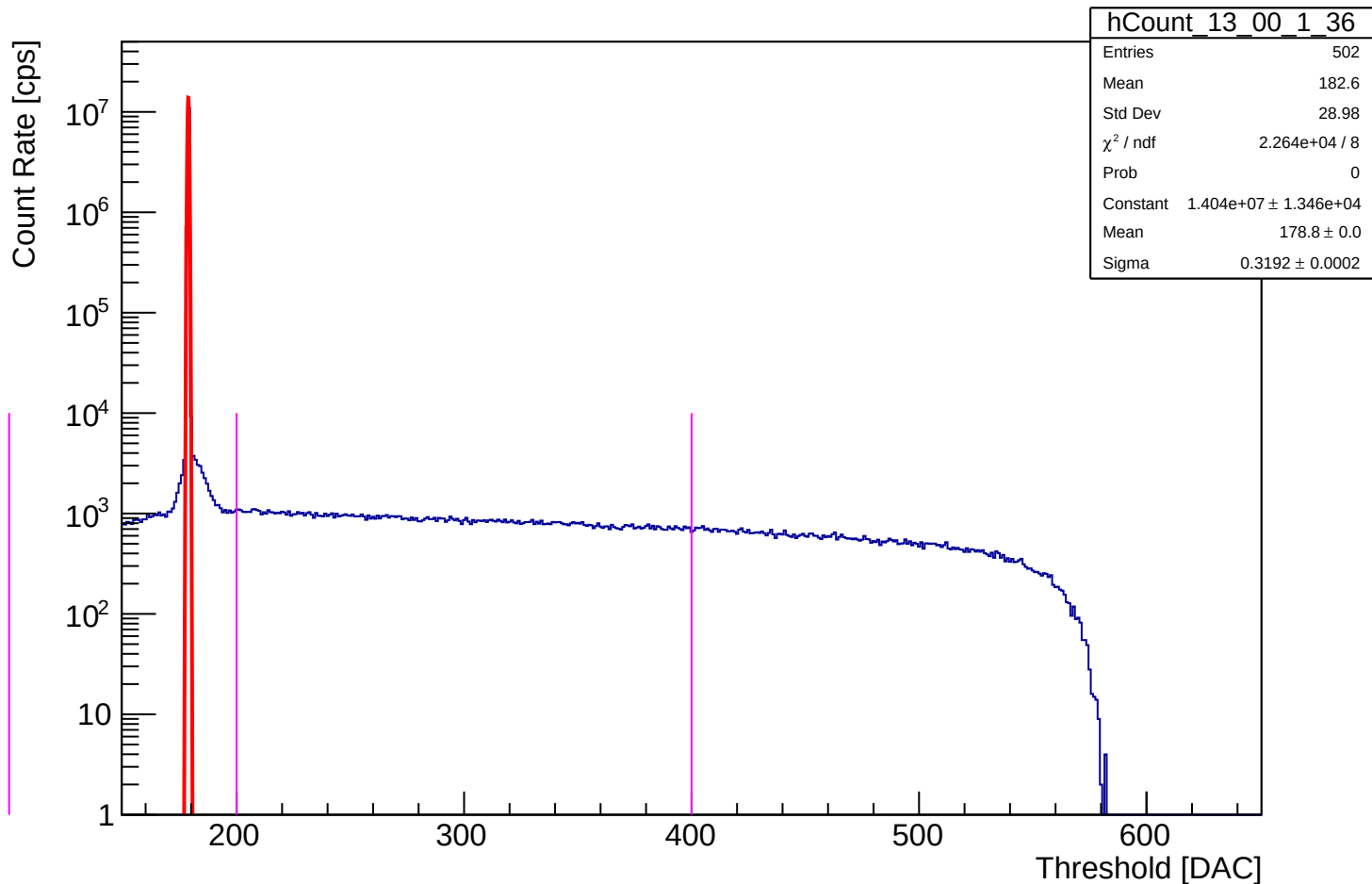
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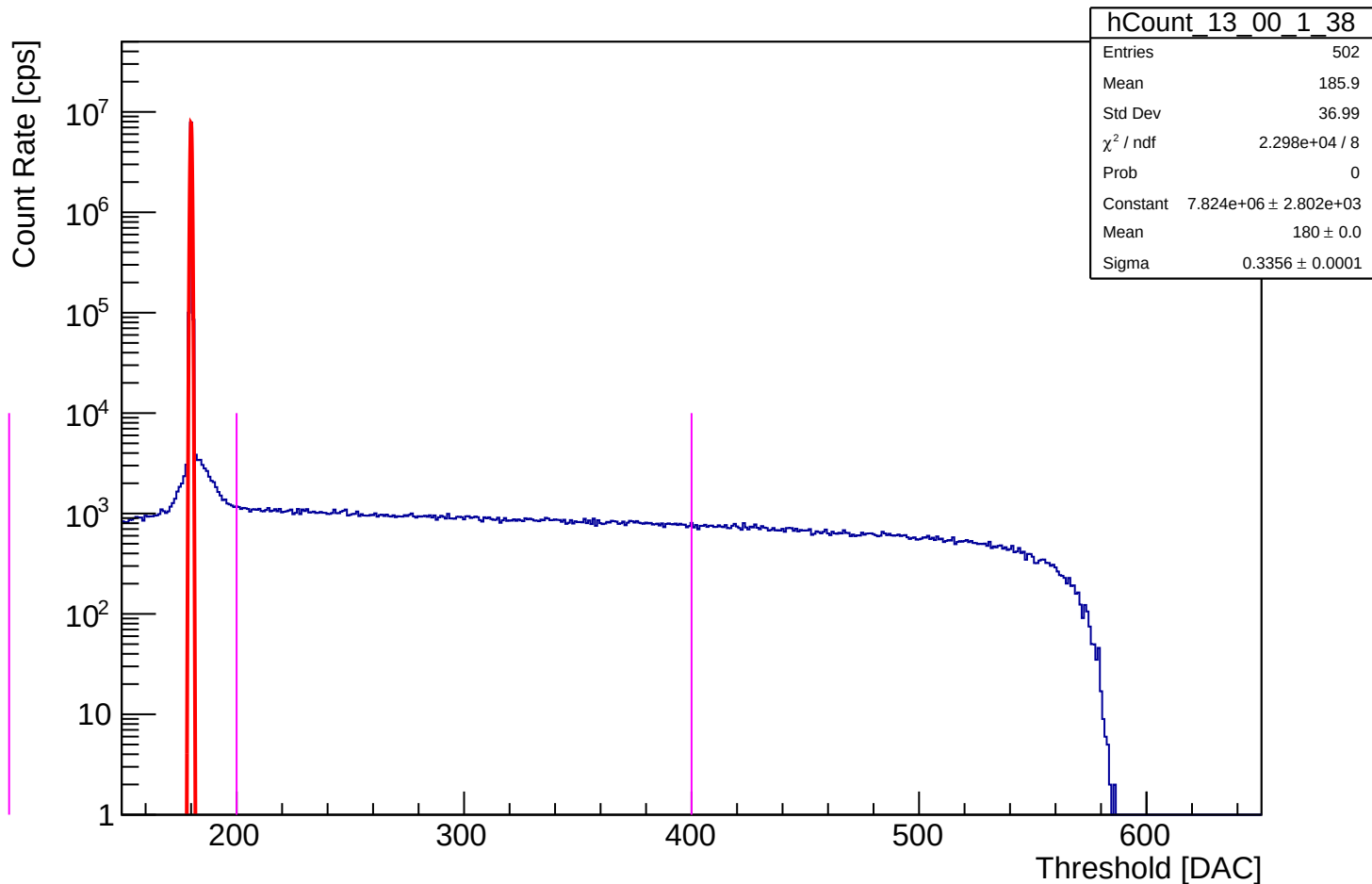
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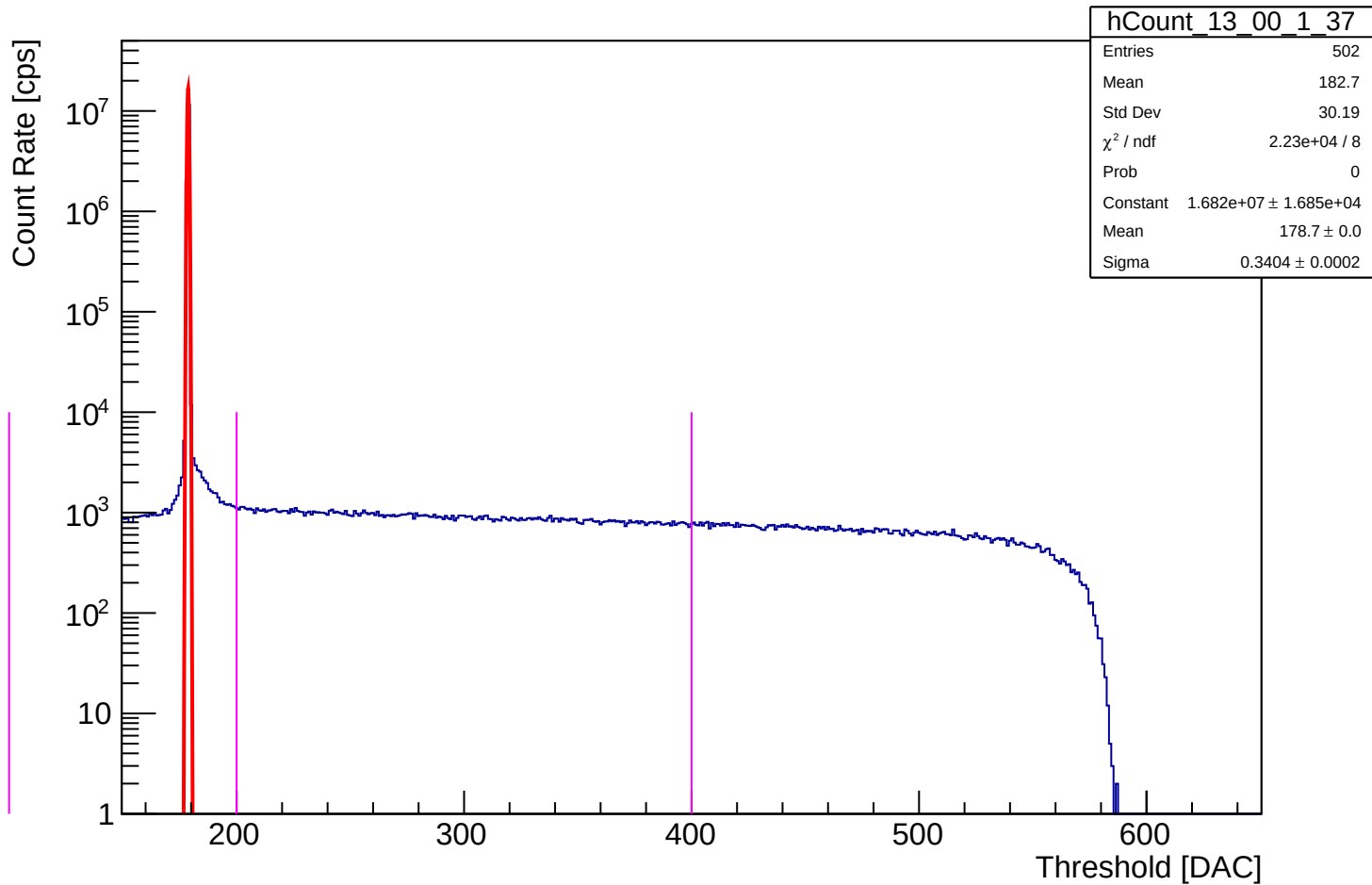
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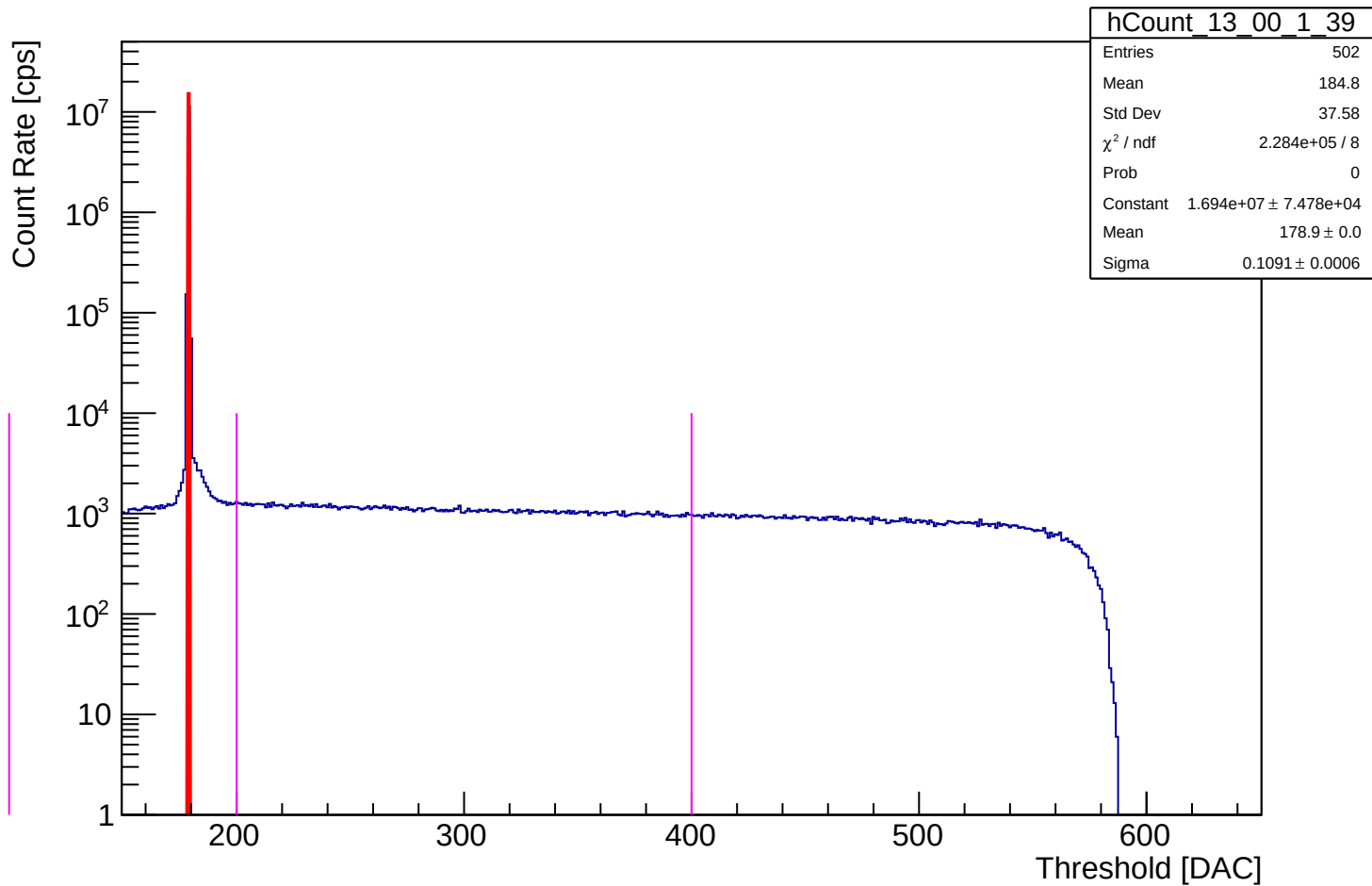
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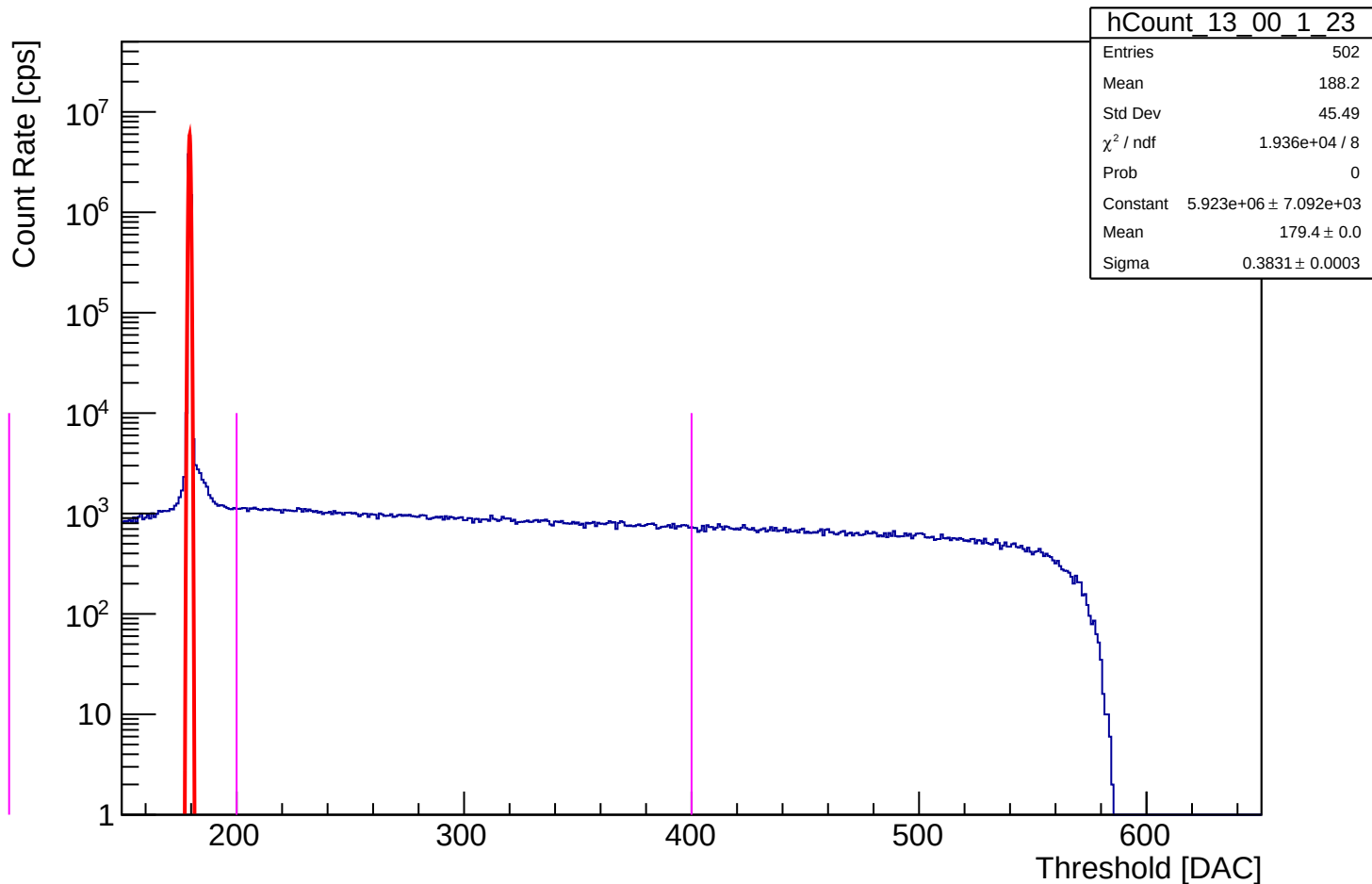
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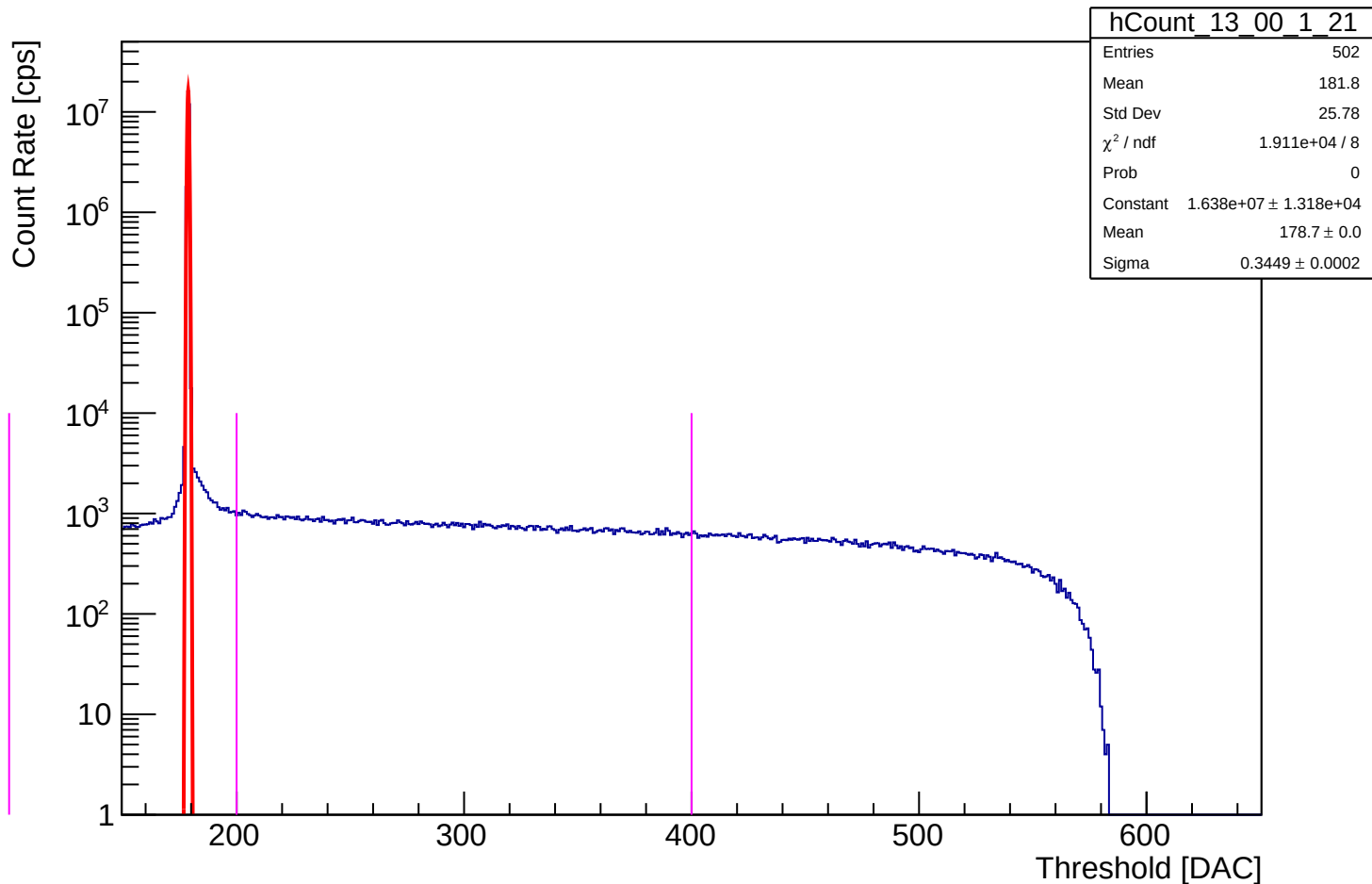
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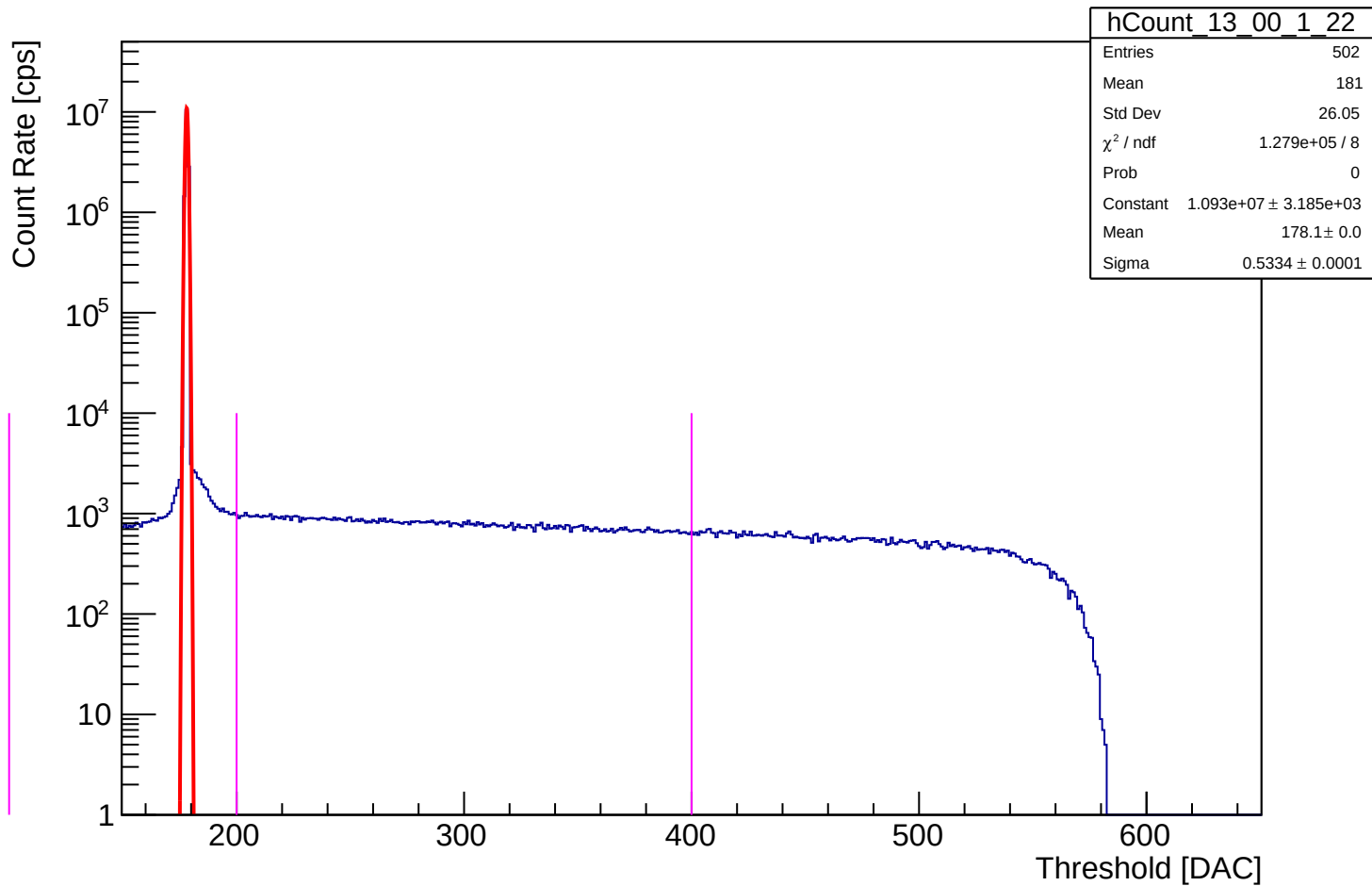
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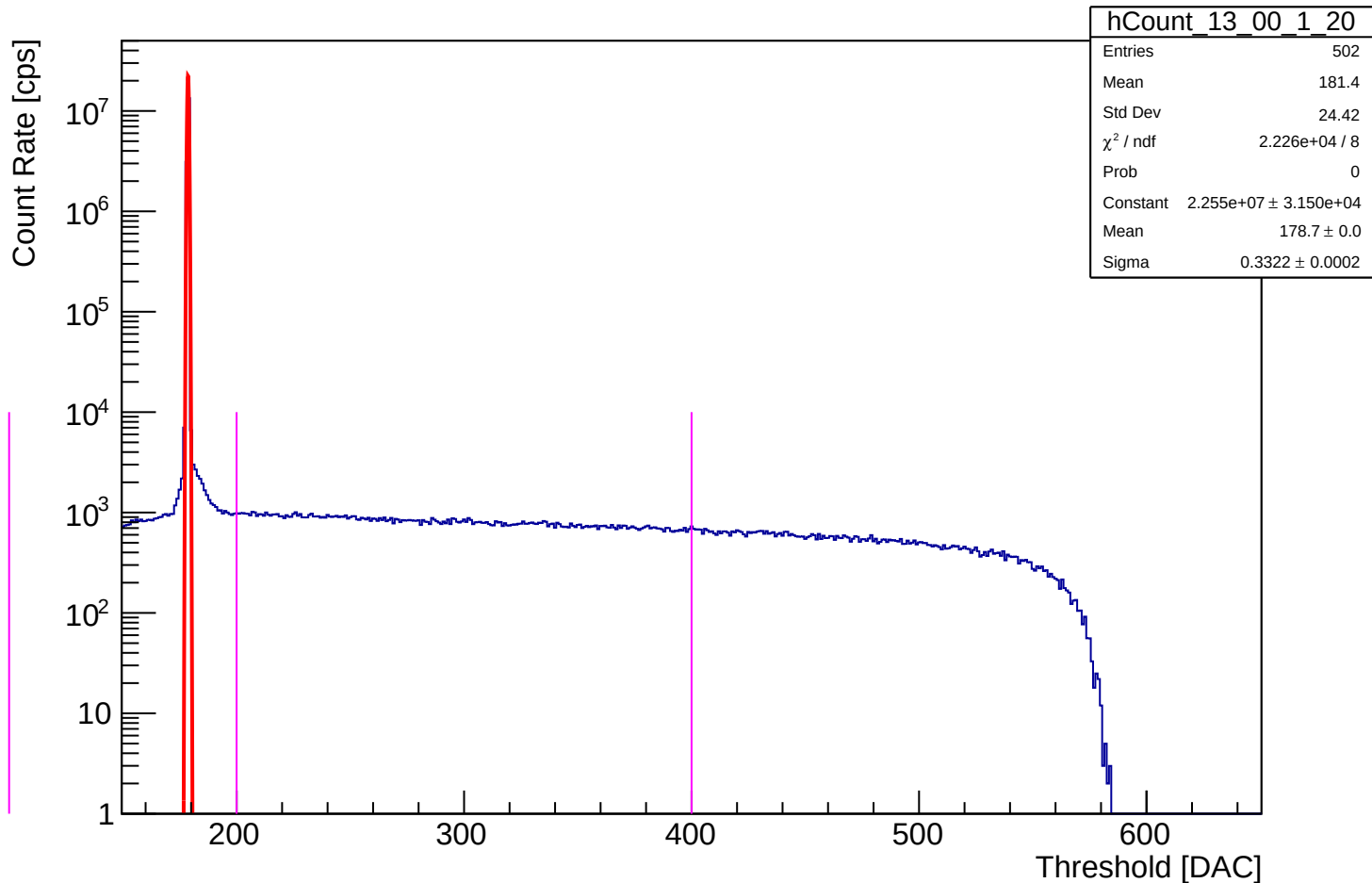
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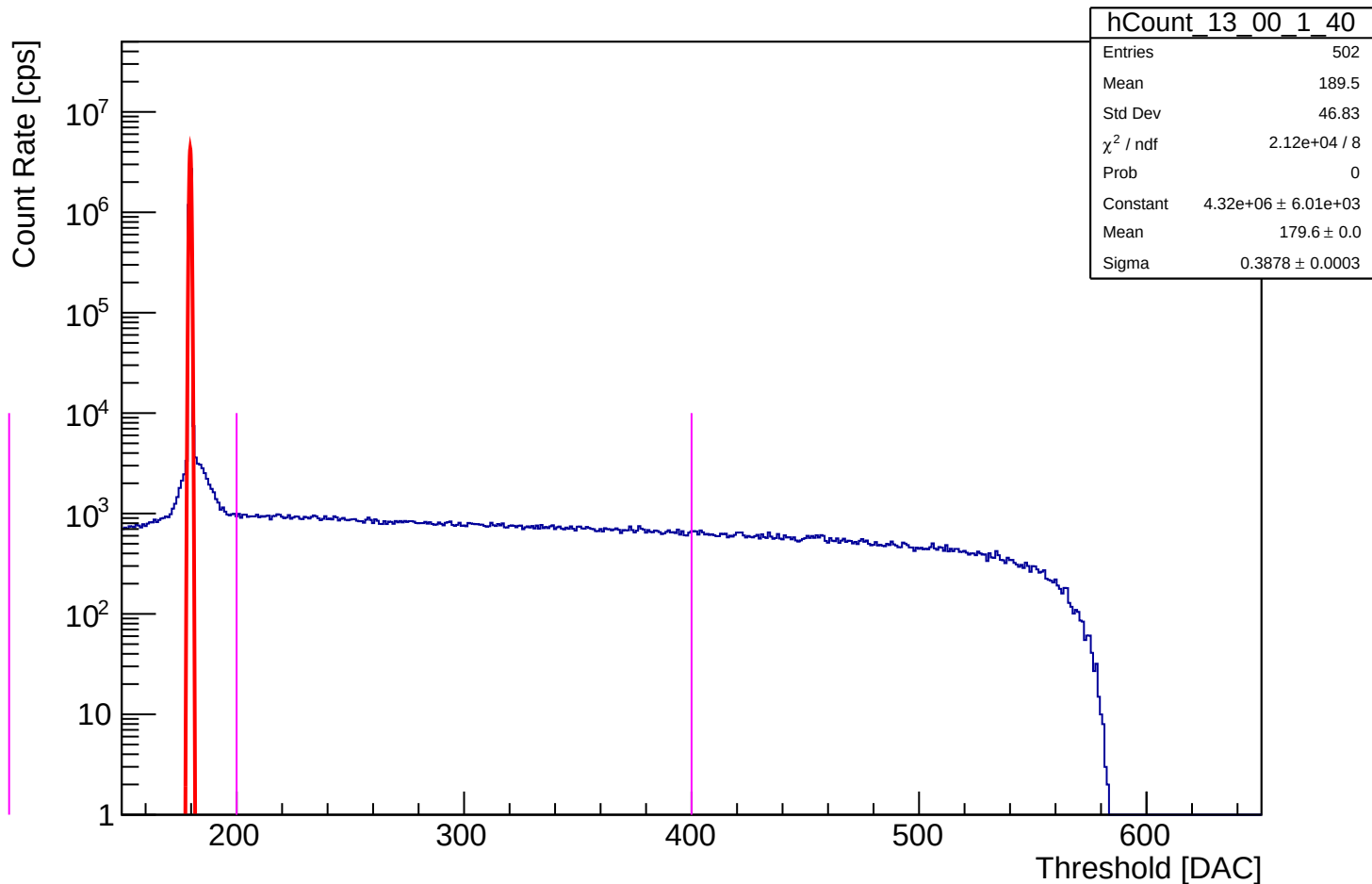
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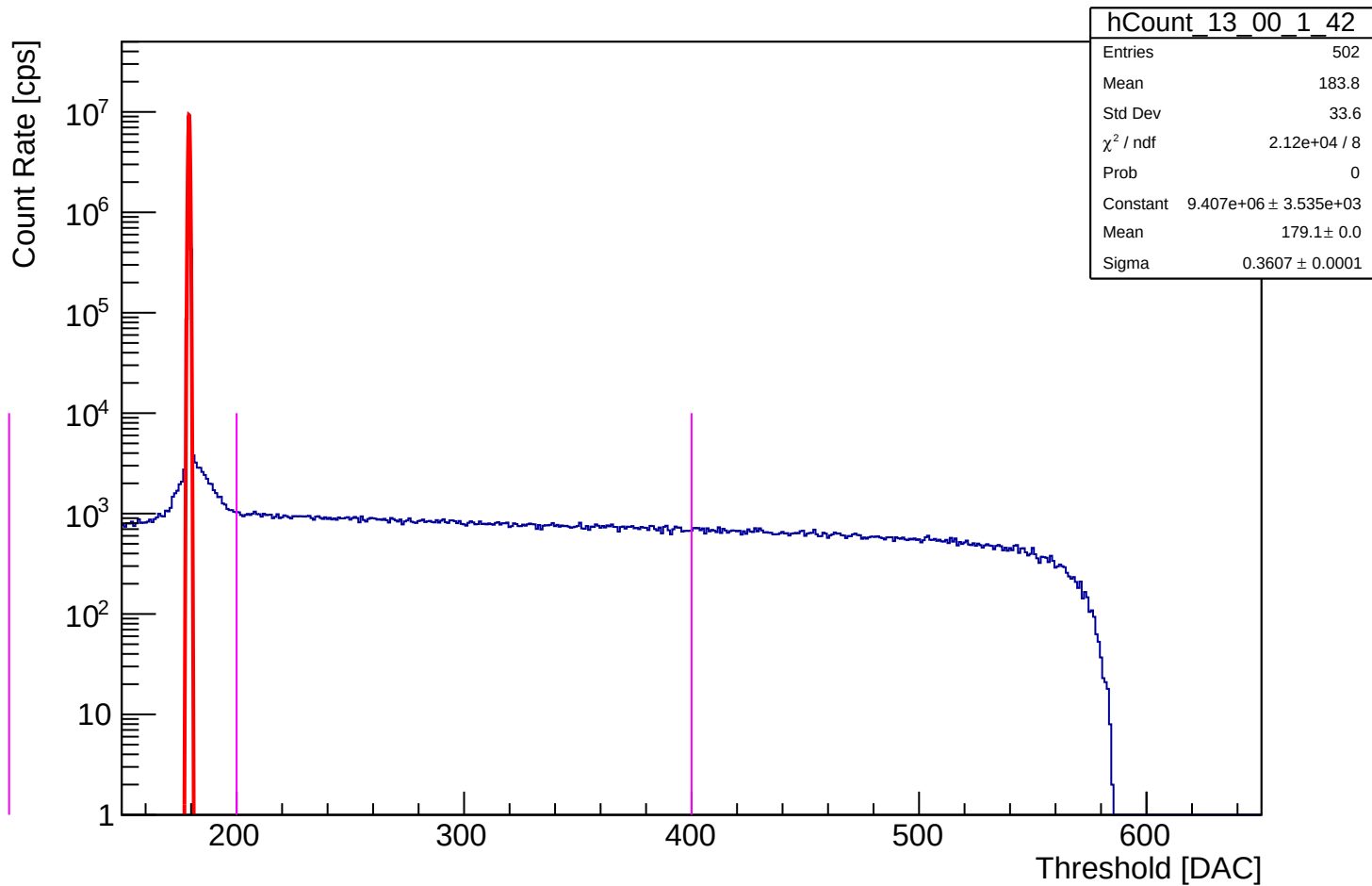
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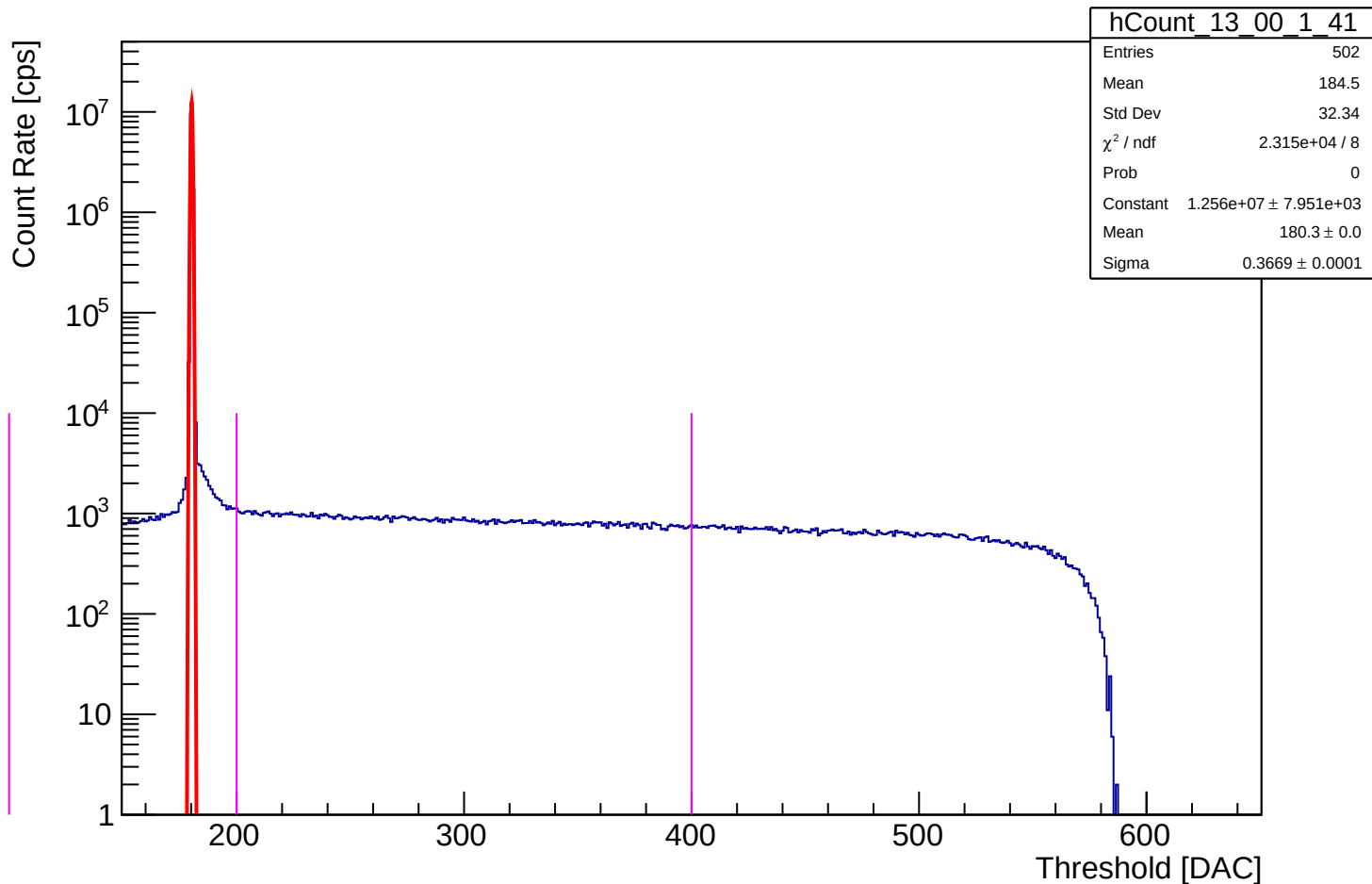
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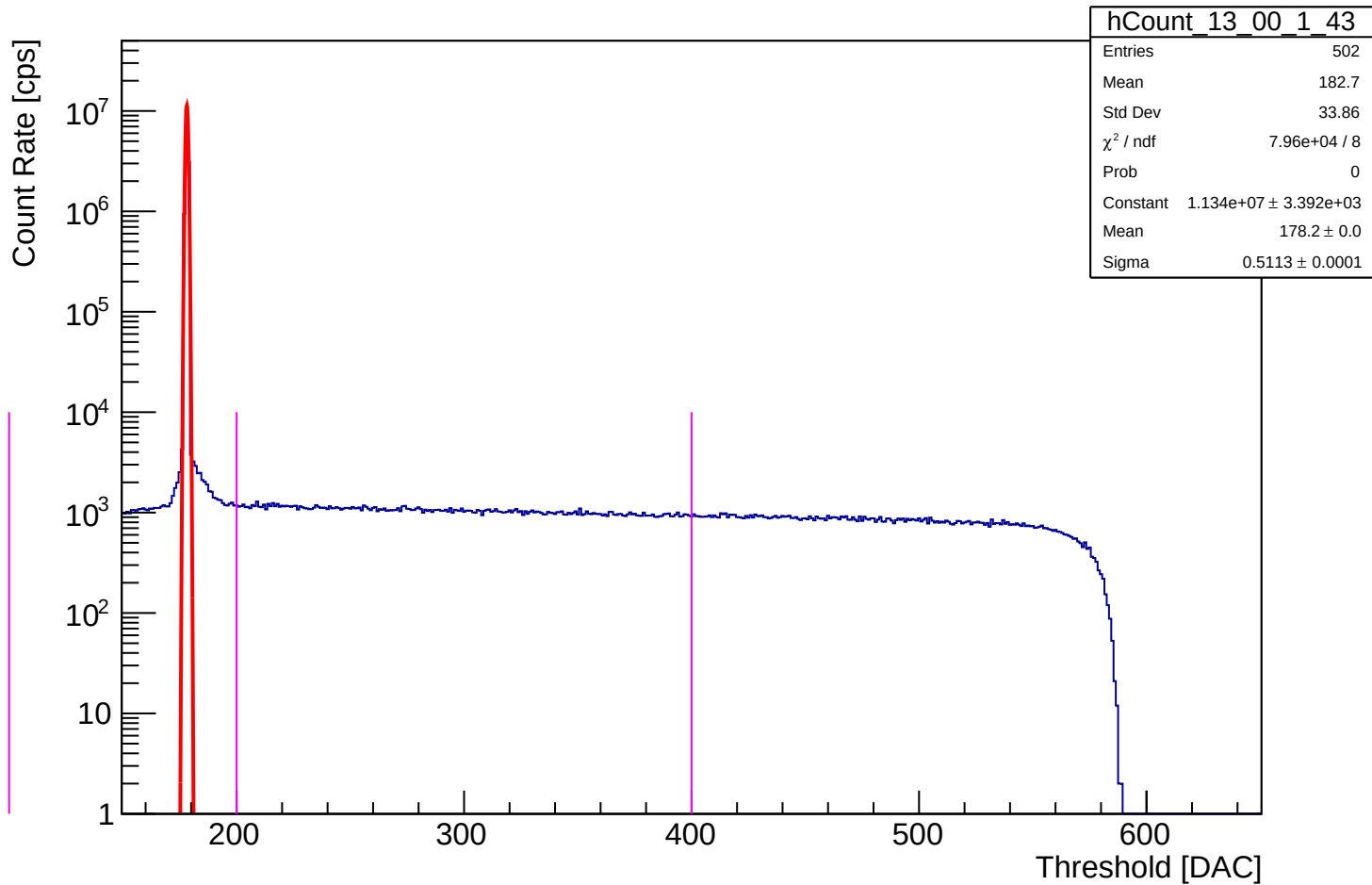
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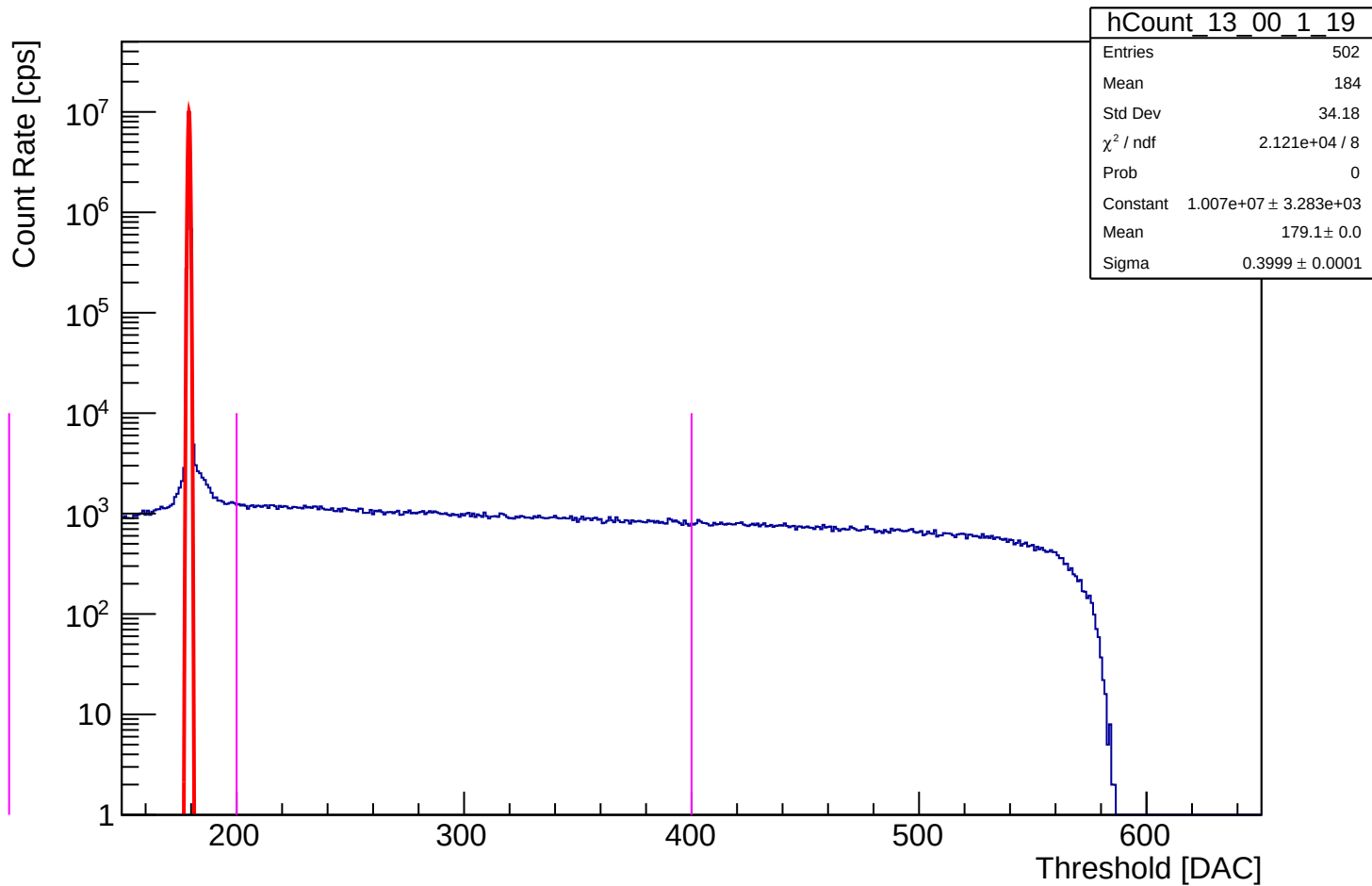
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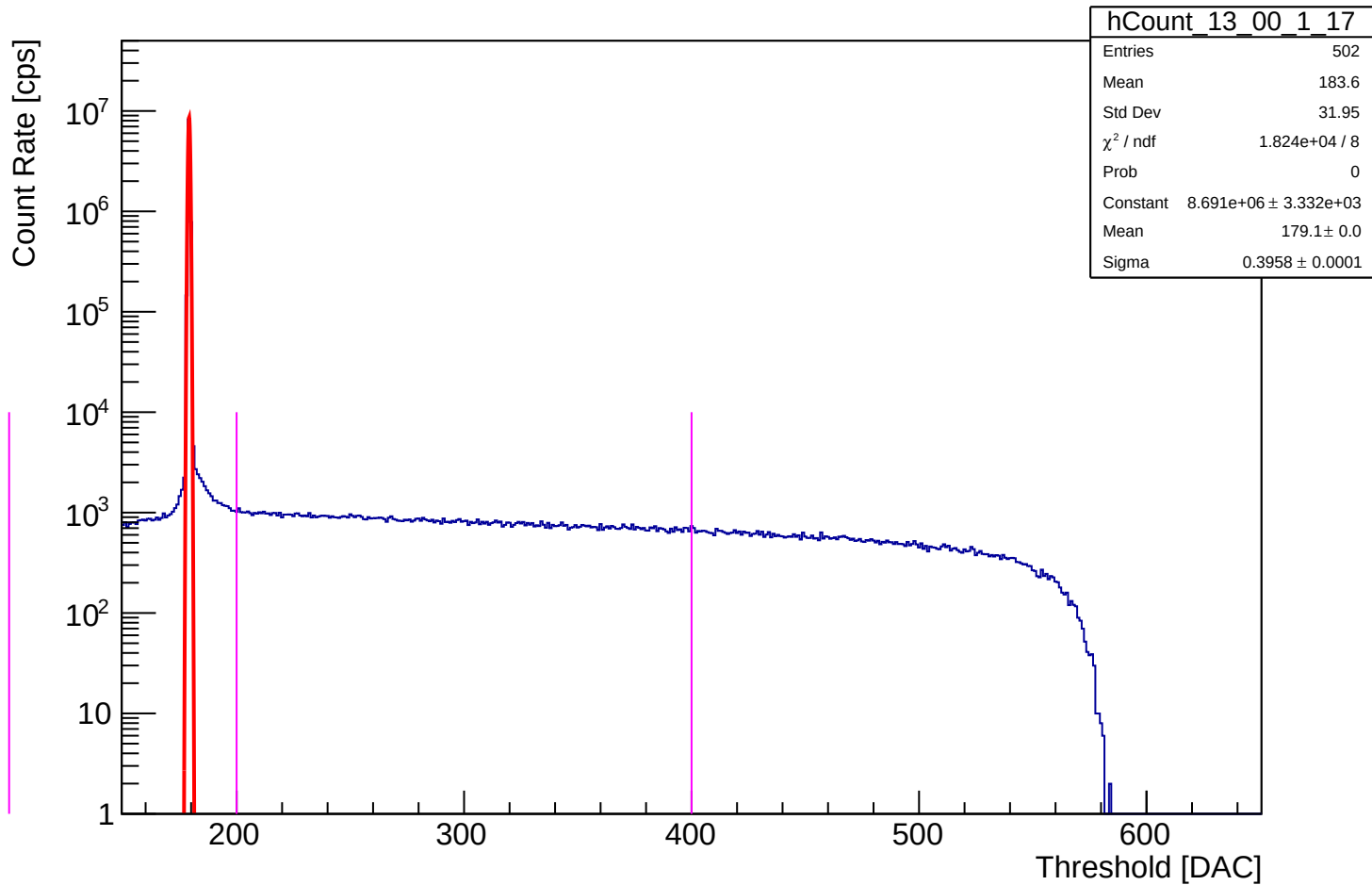
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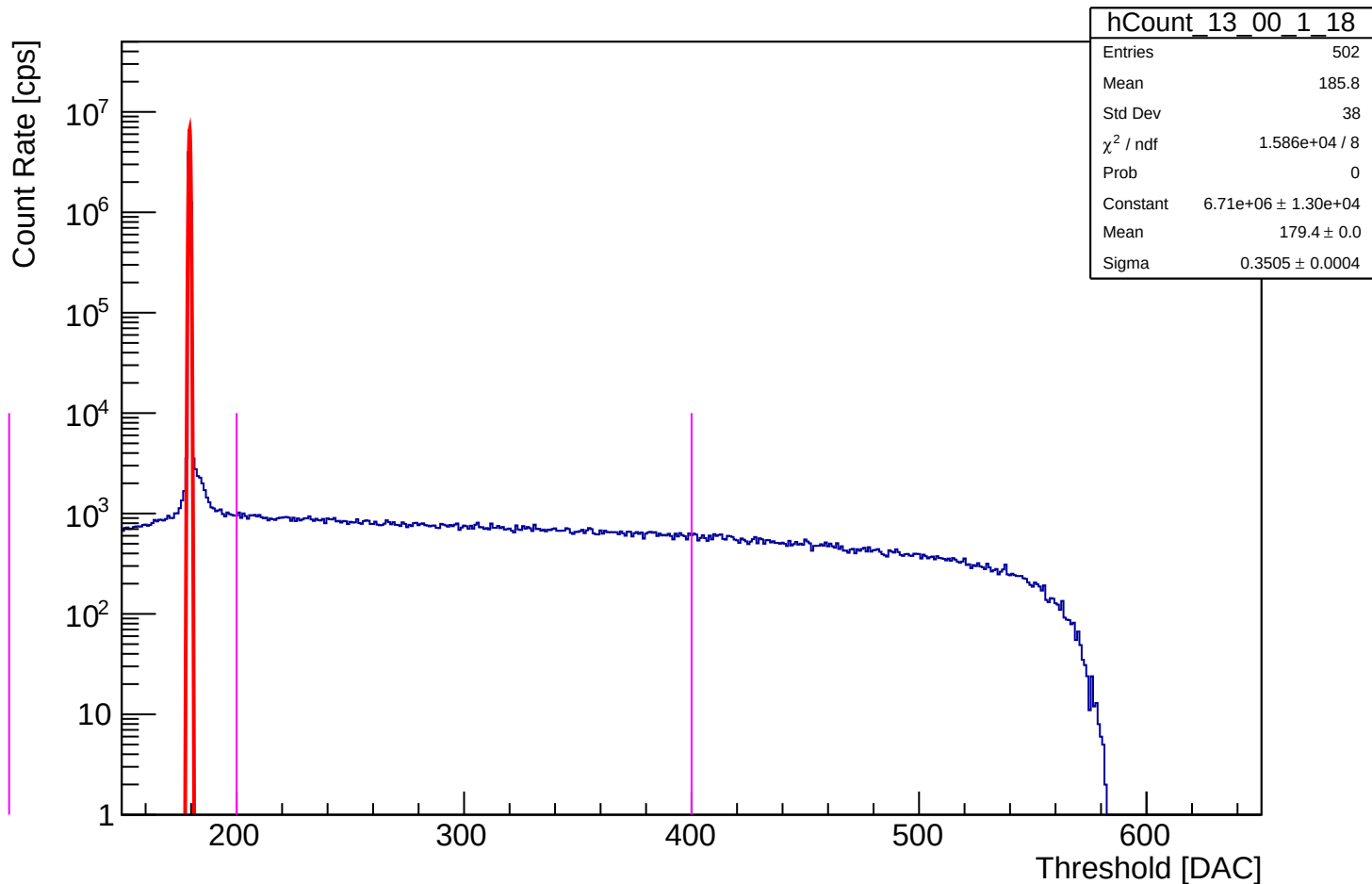
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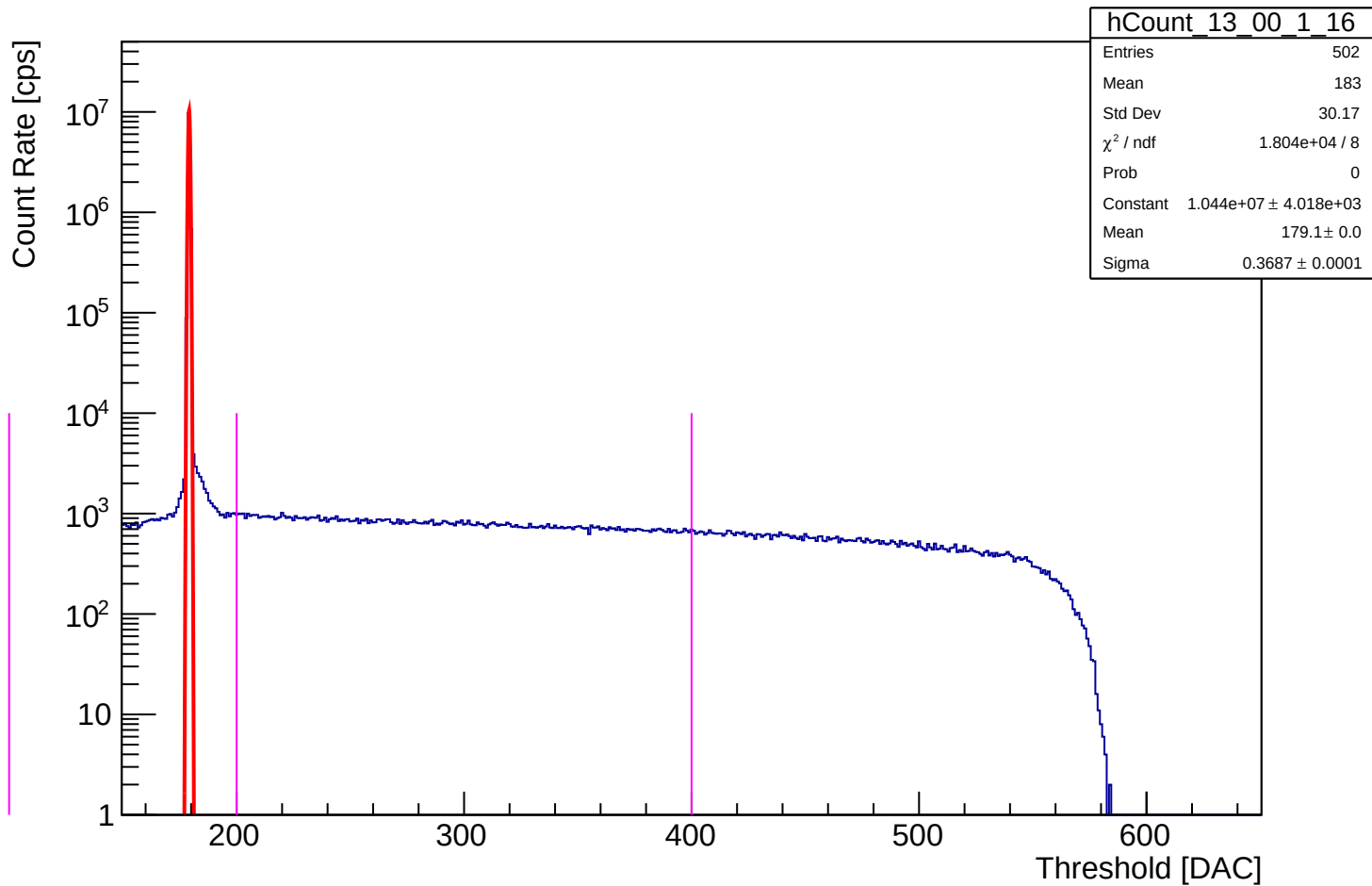
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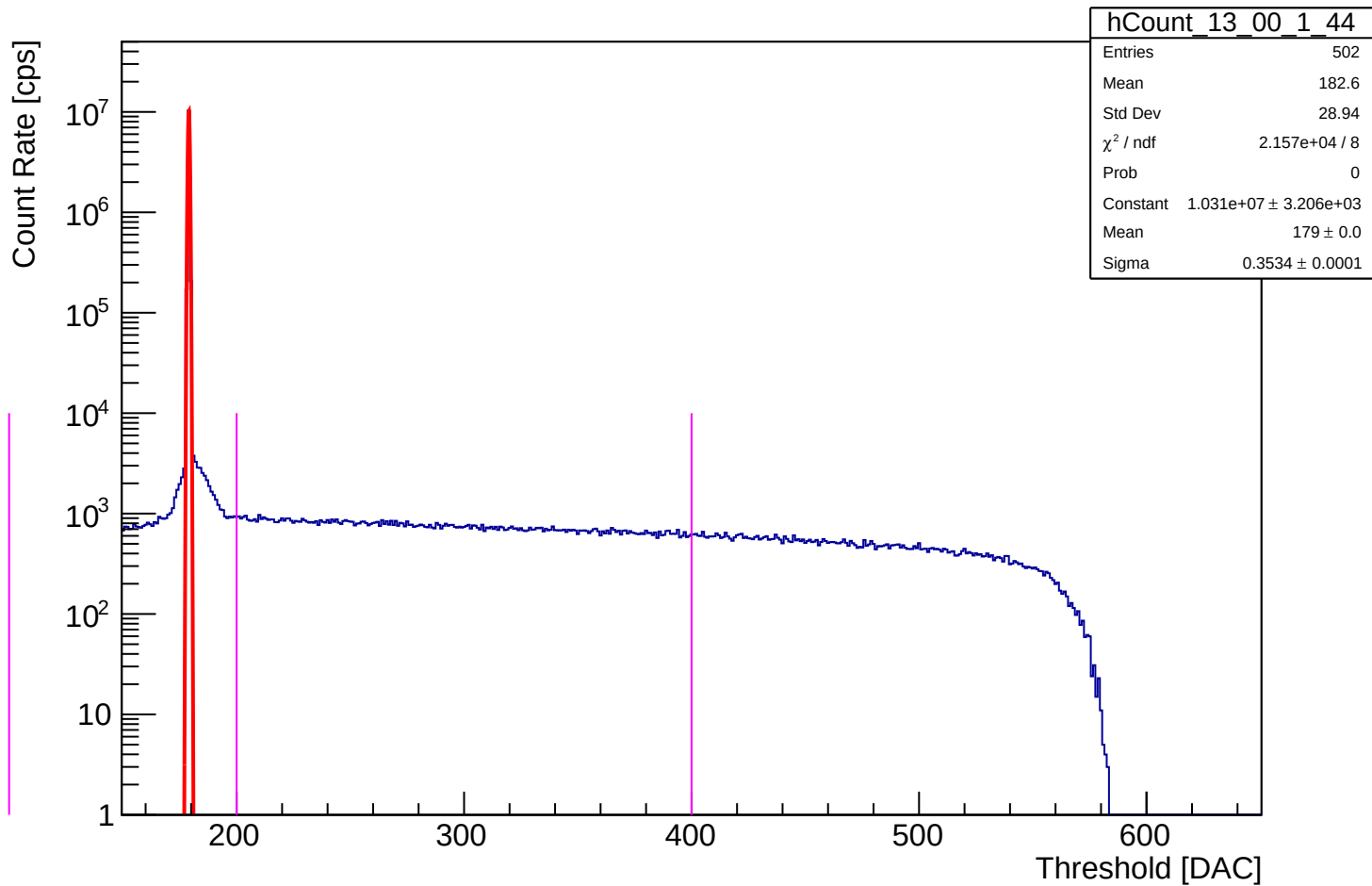
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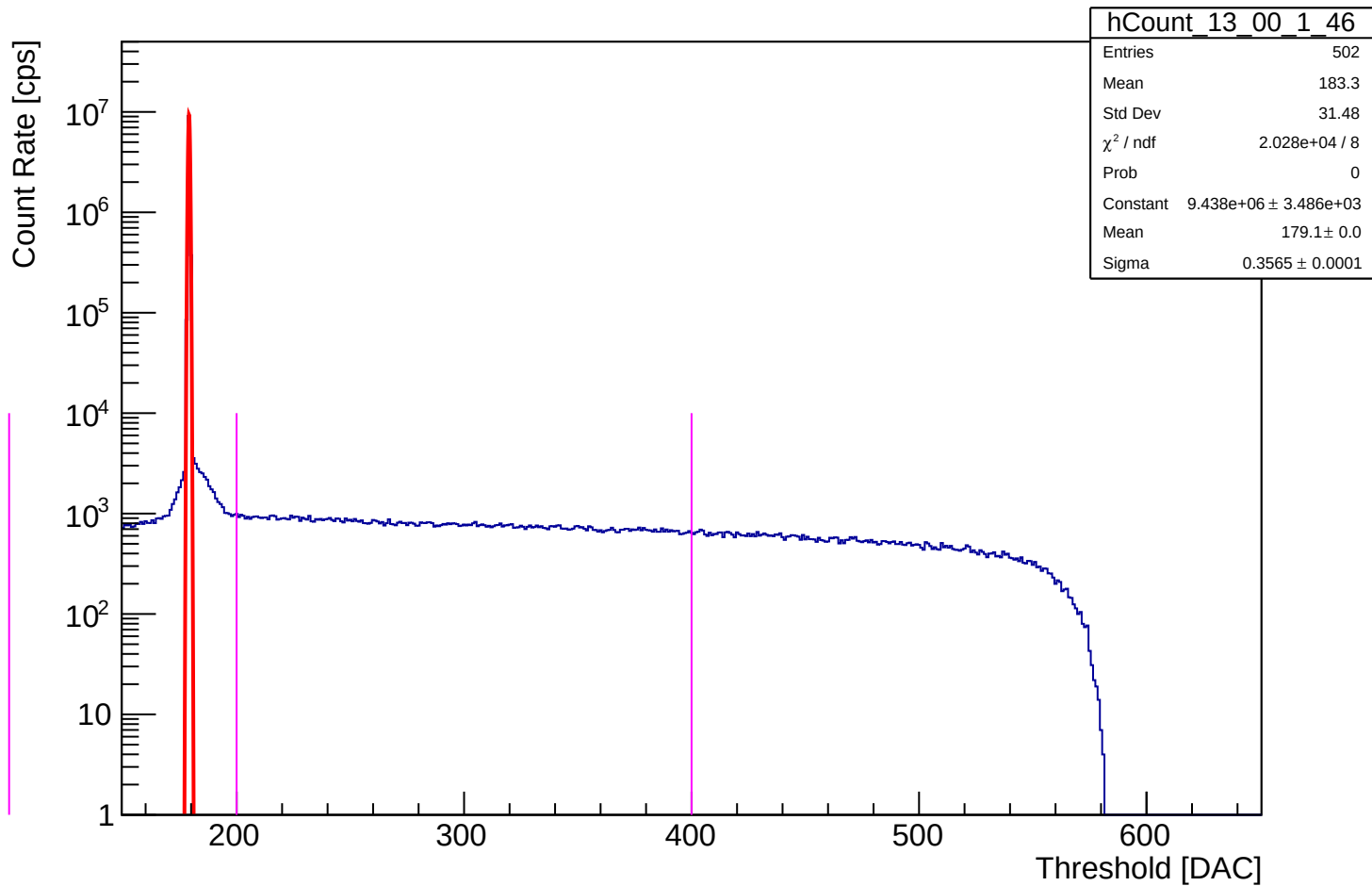
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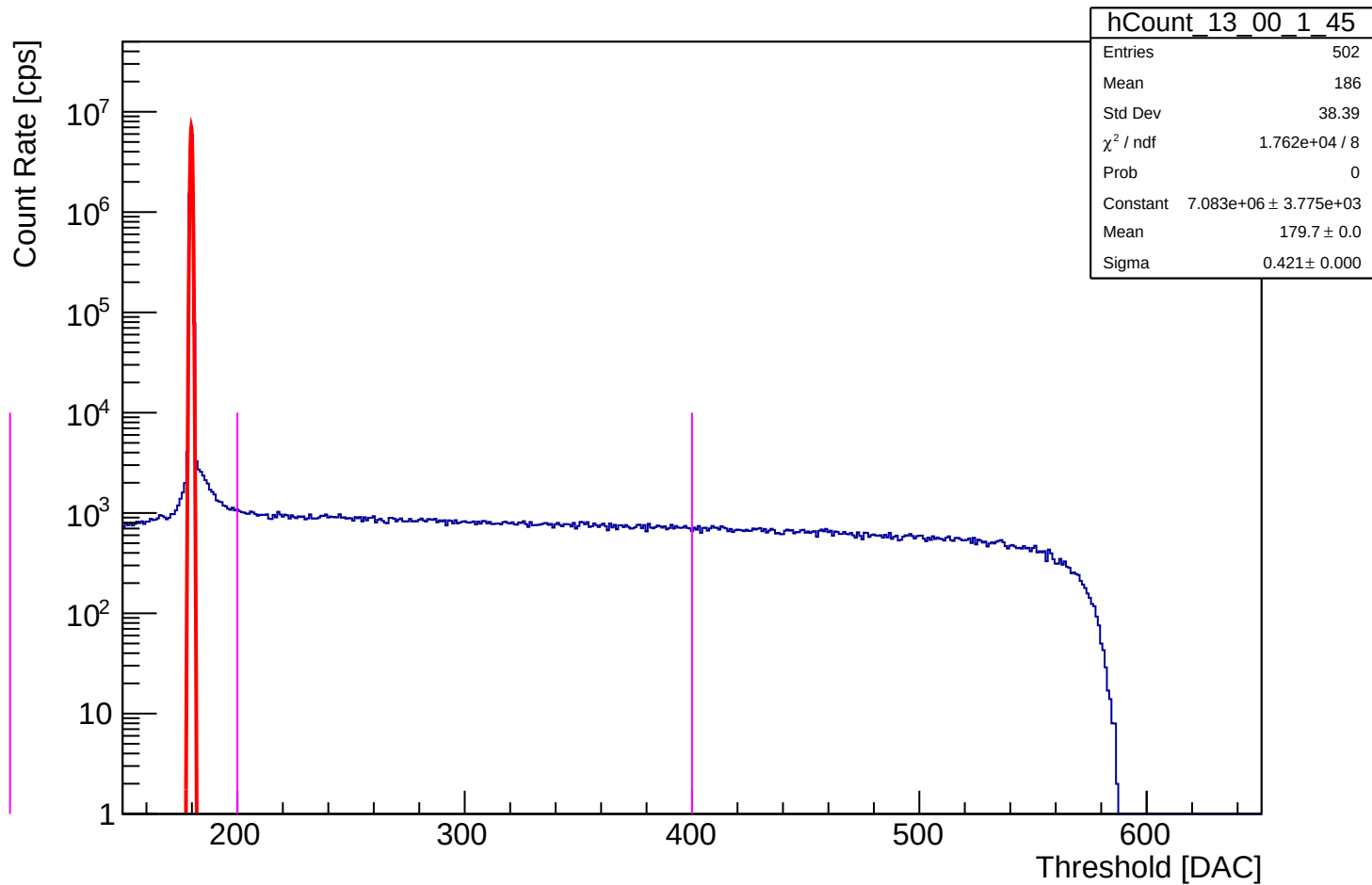
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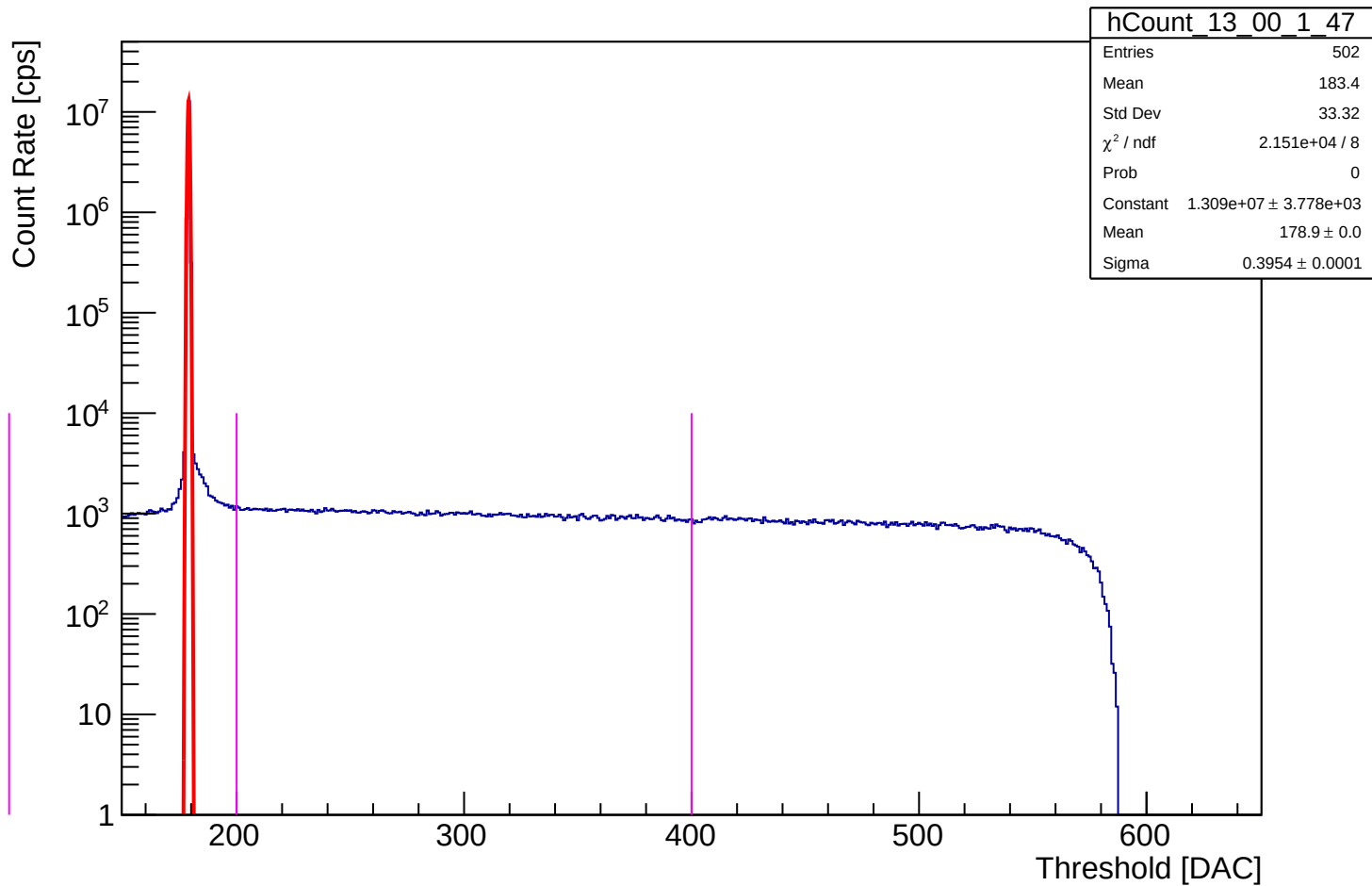
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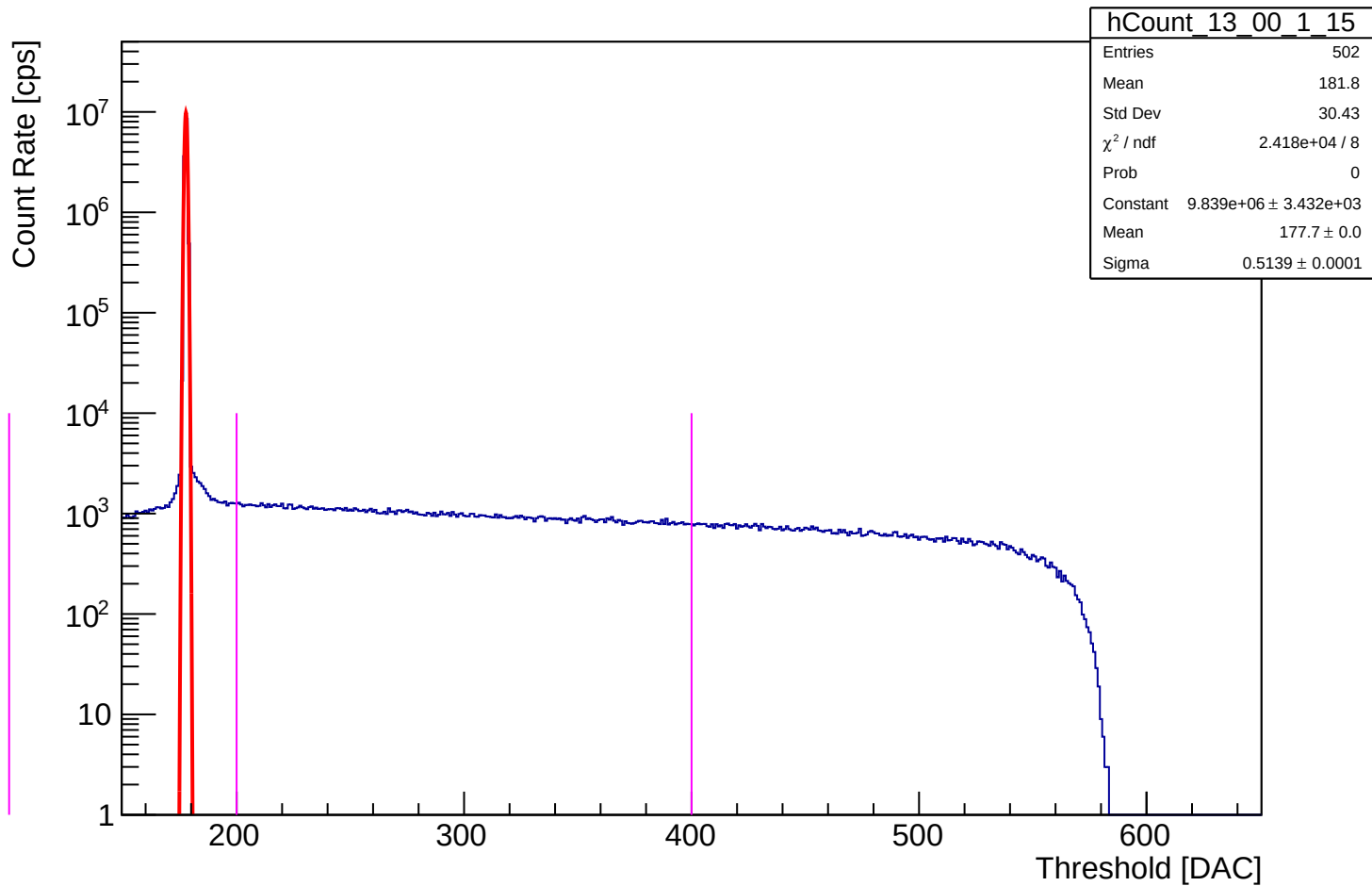
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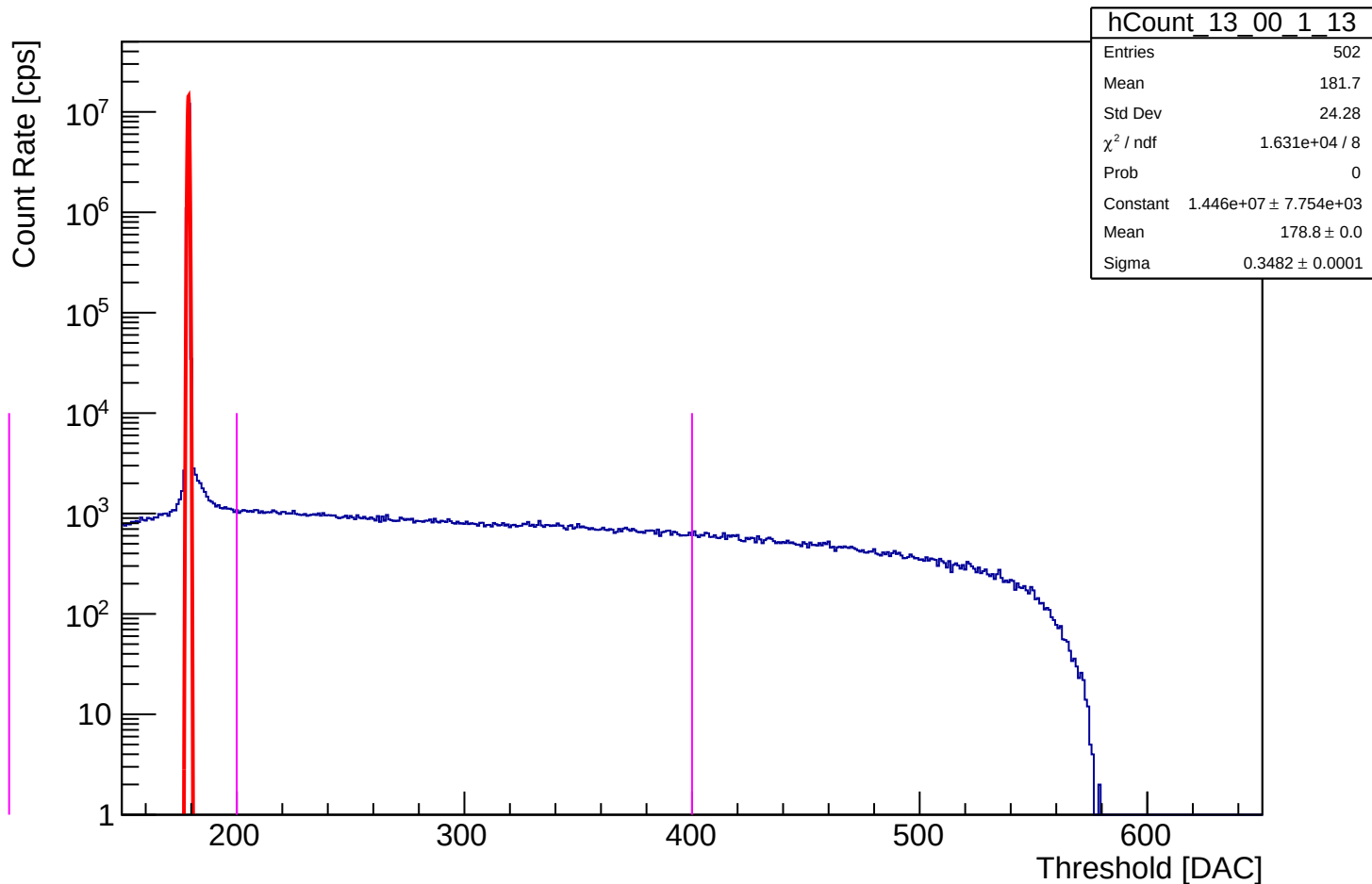
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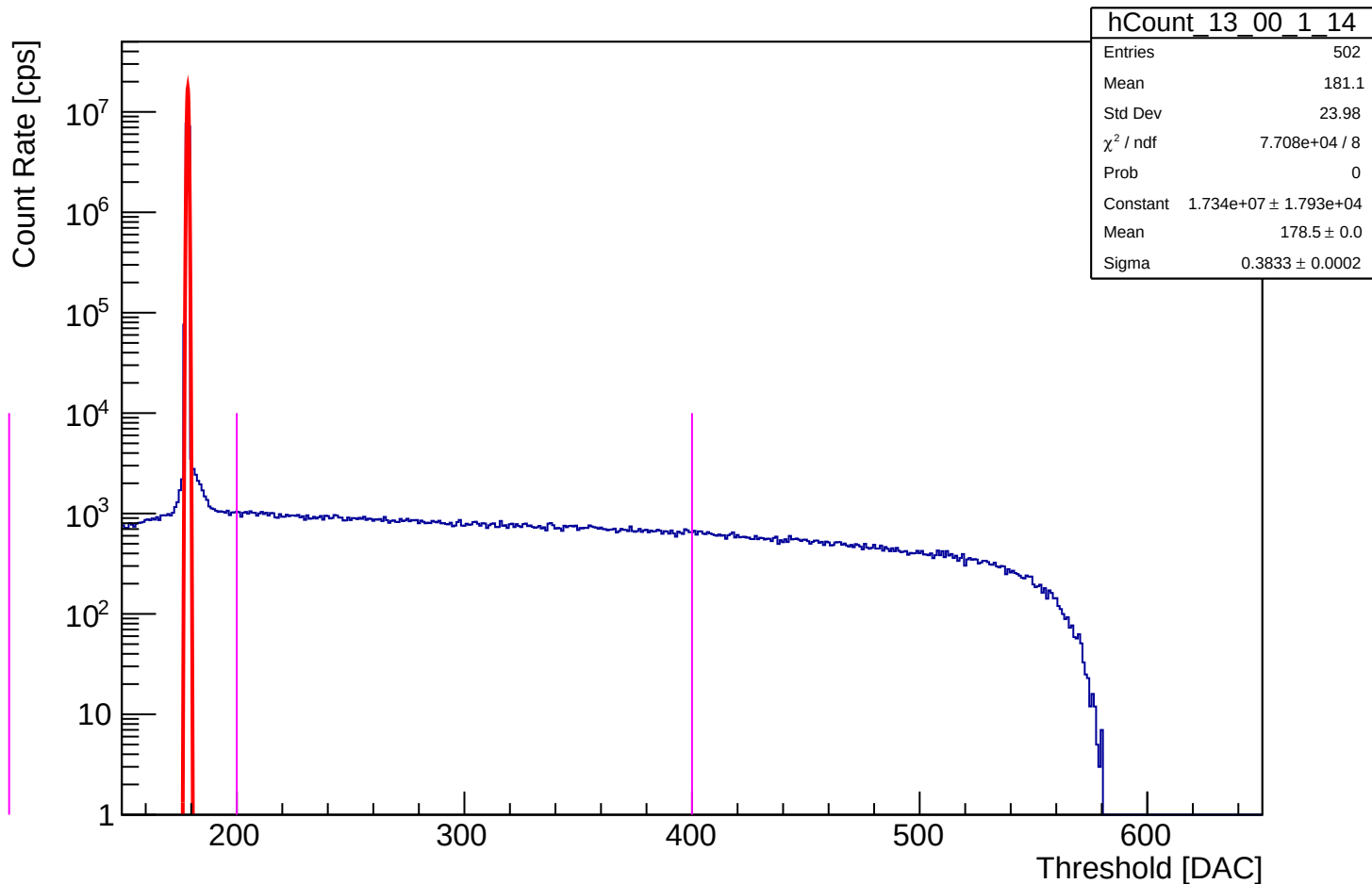
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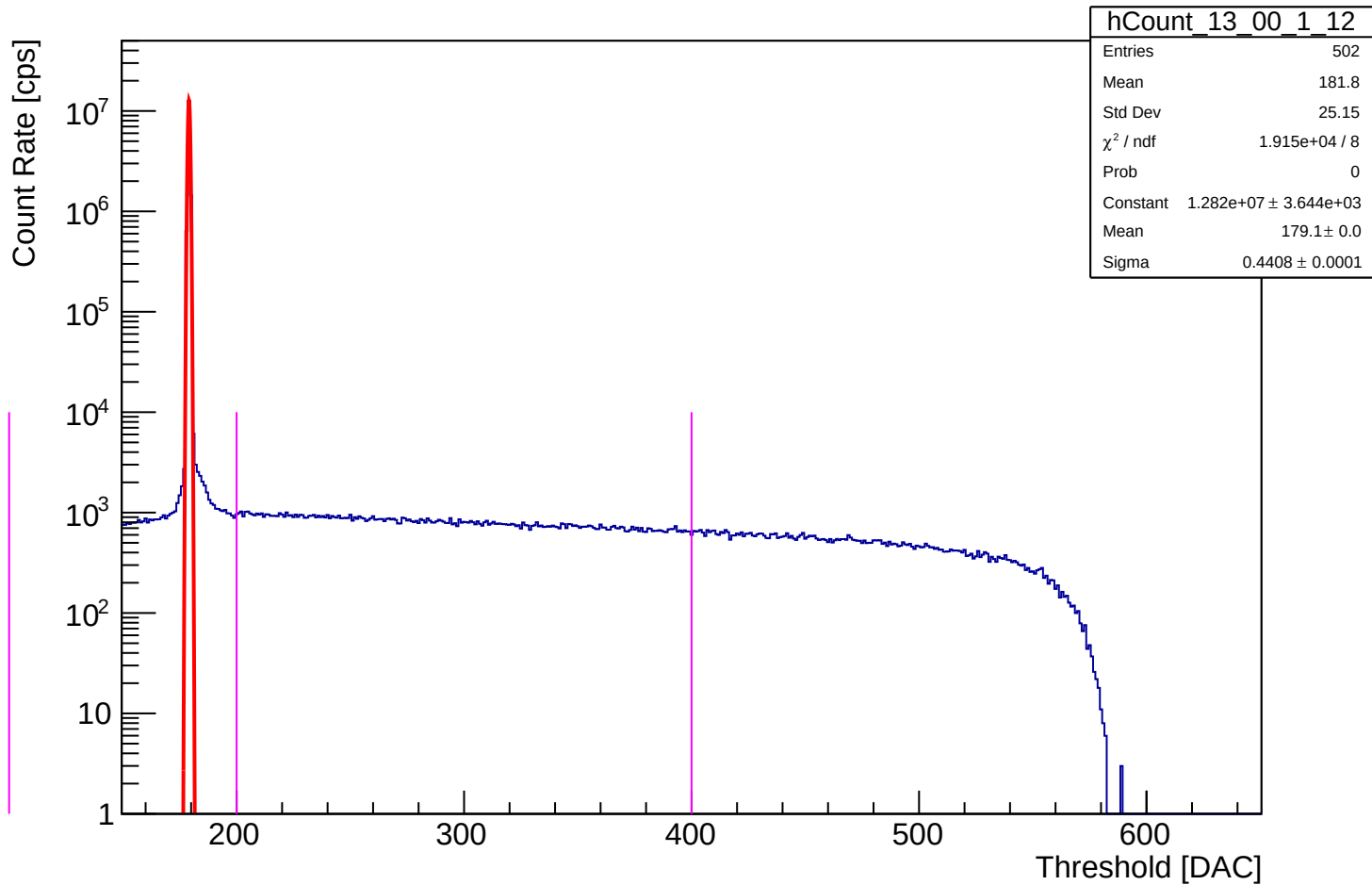
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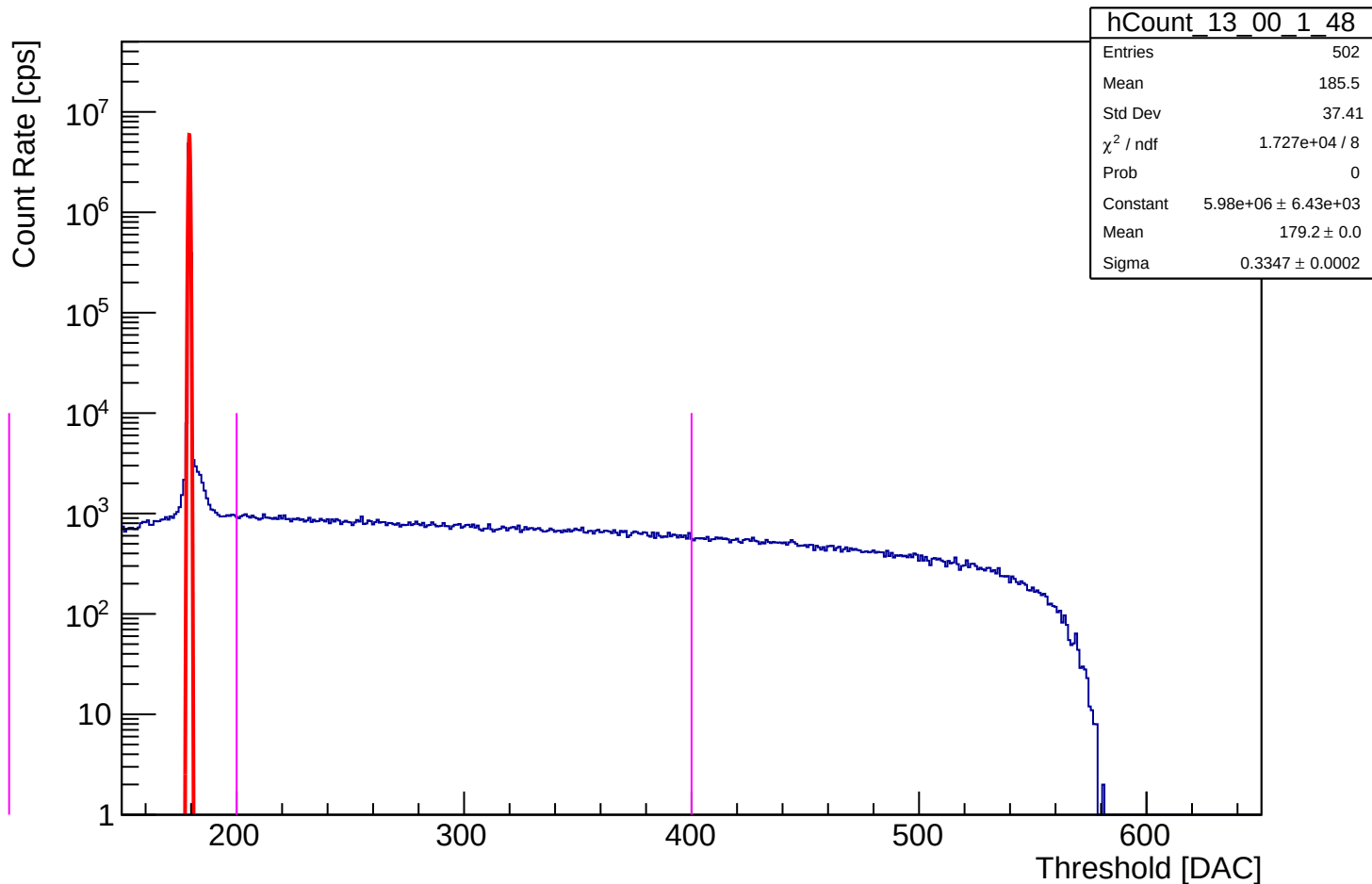
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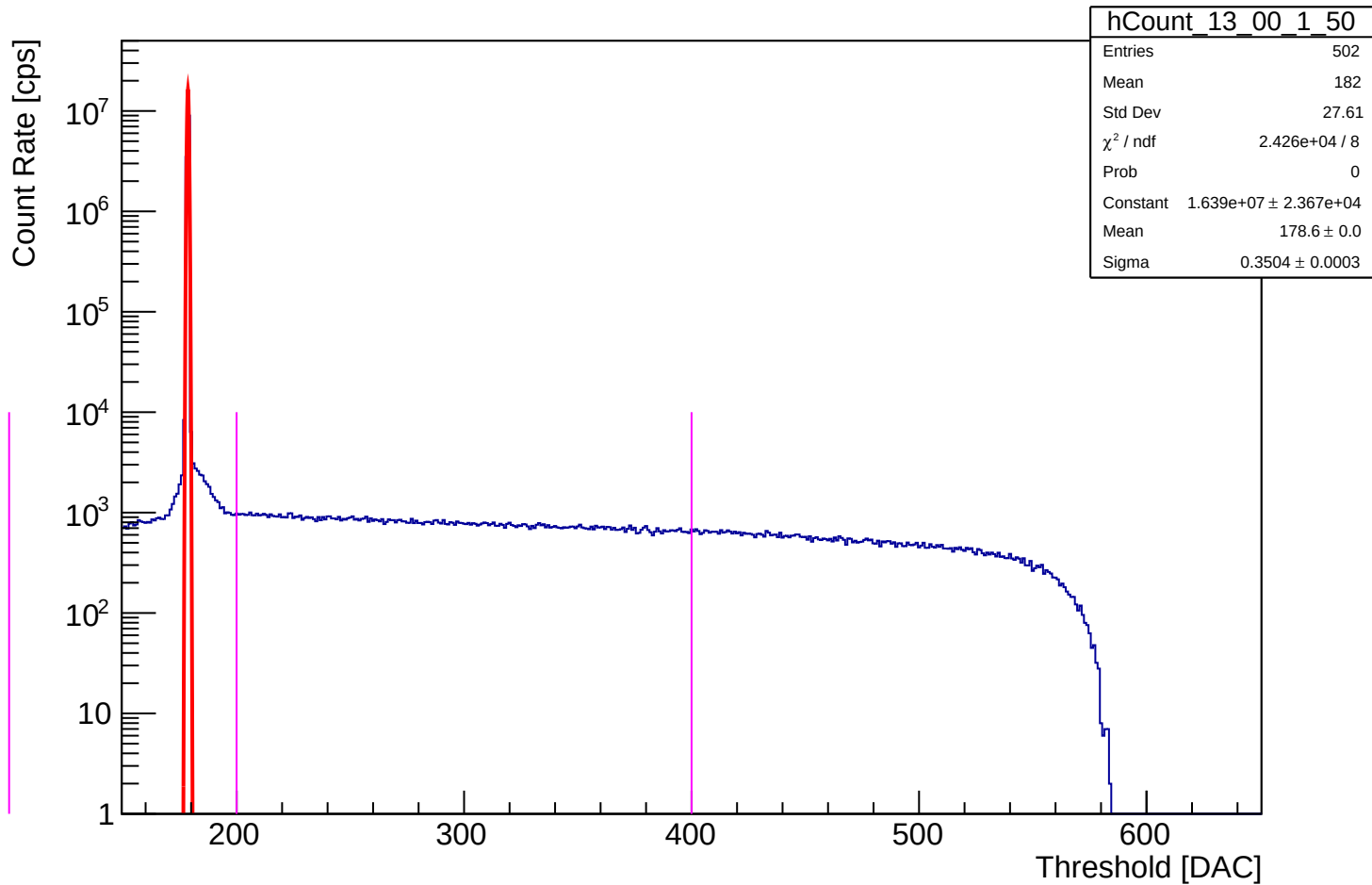
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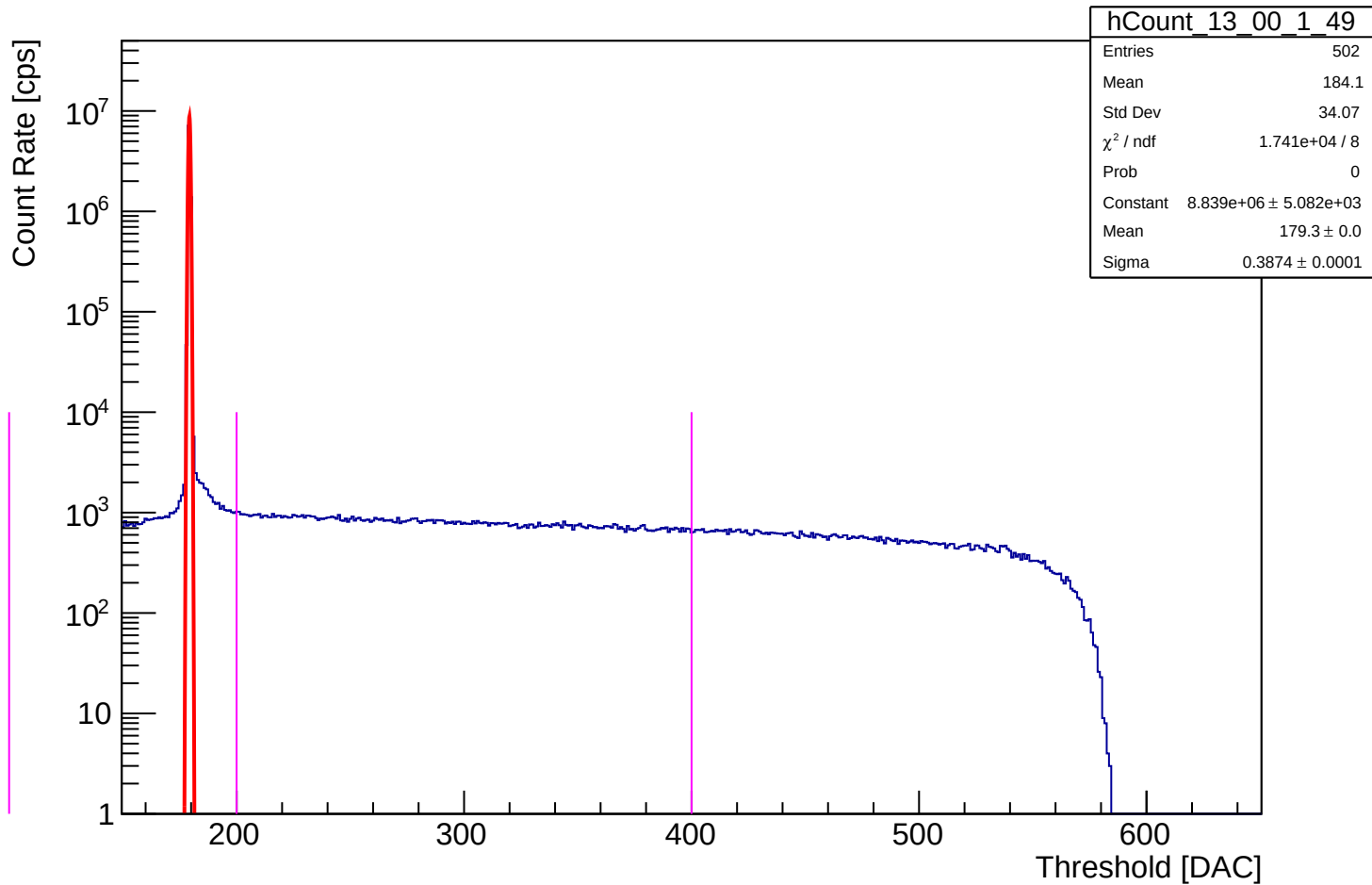
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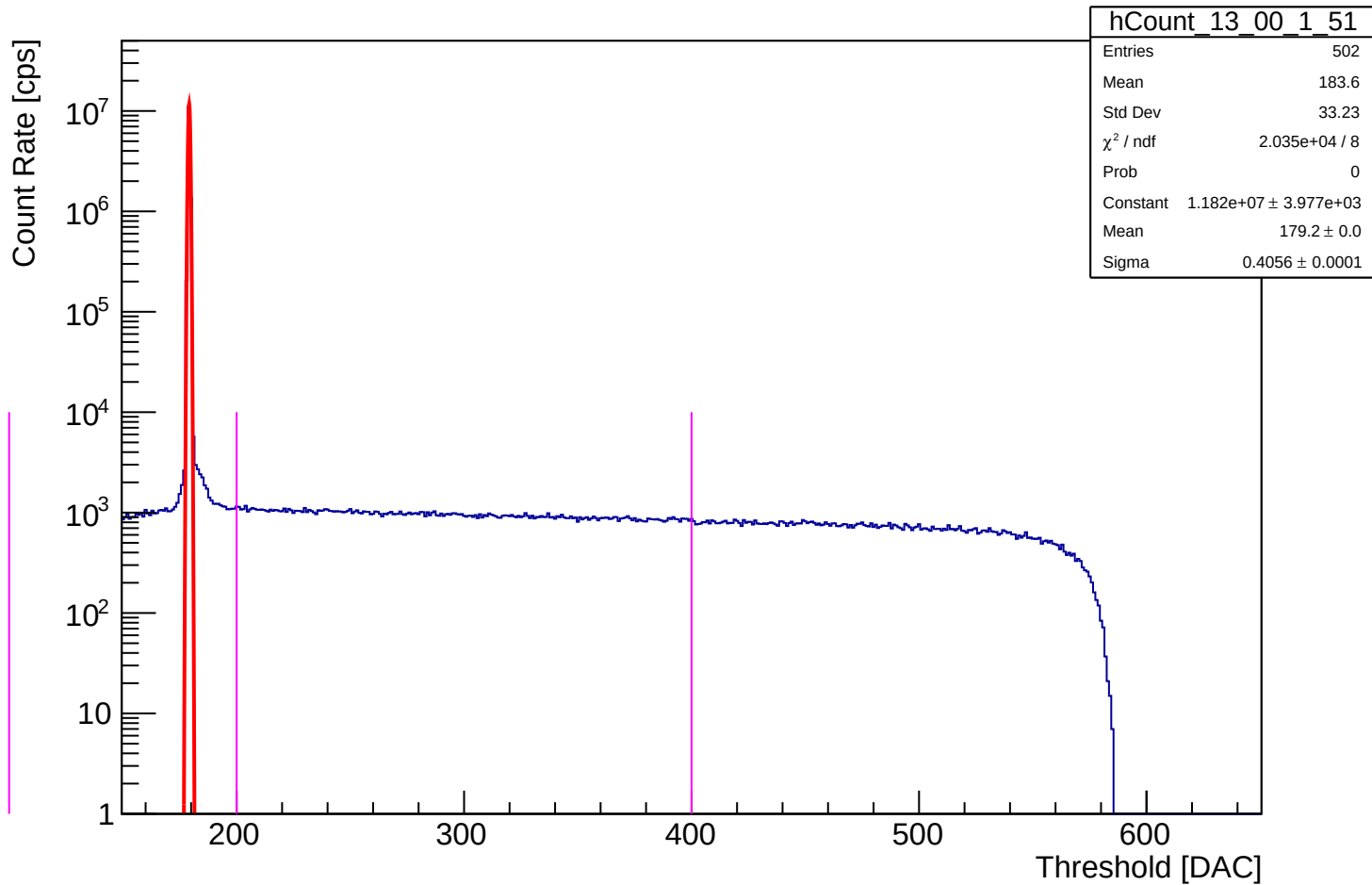
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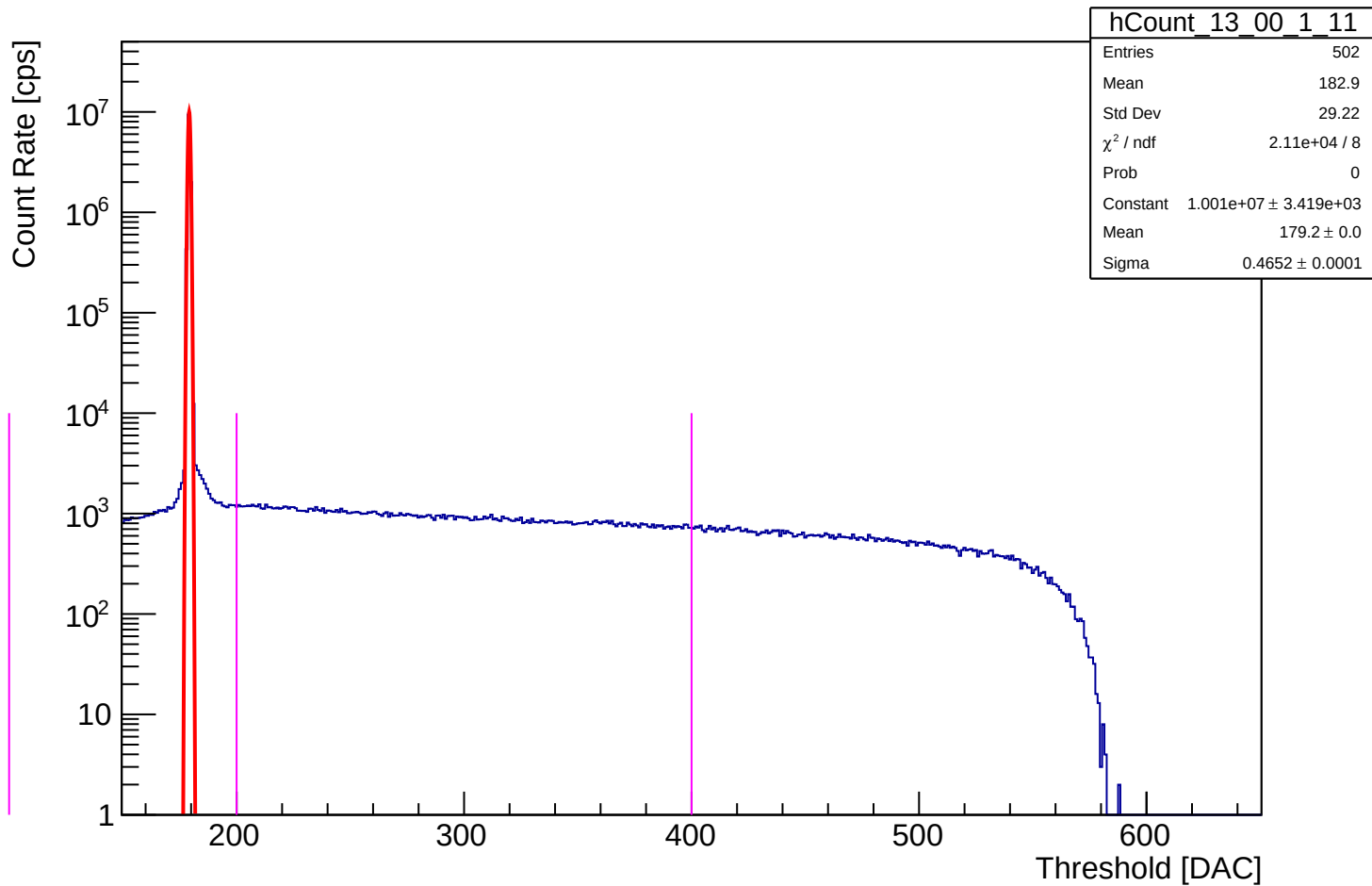
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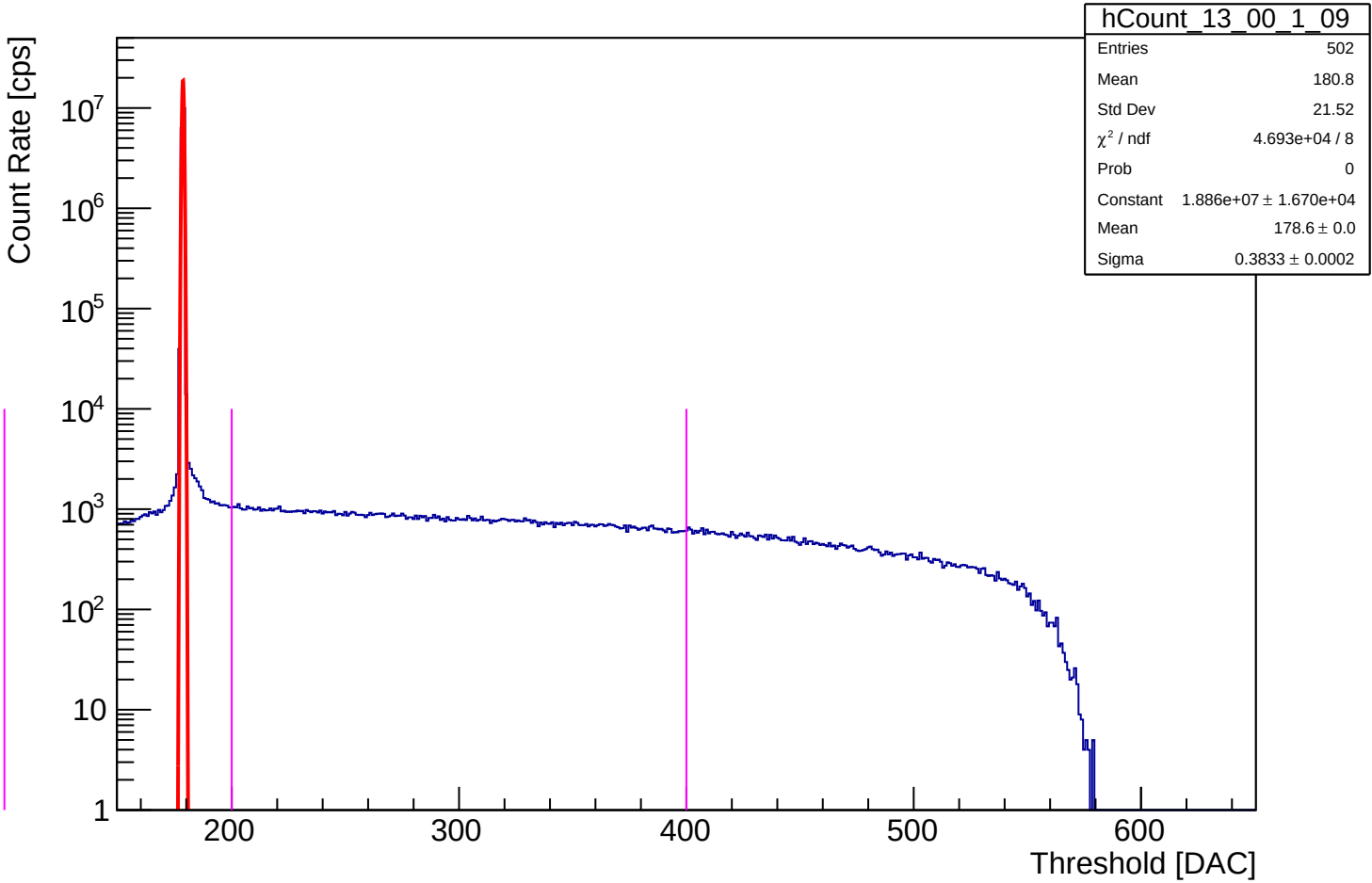


Row 1 Tile 1 Pmt 2 Pixel 40 Slot 13 Fiber 0 Asic 1 Channel 51

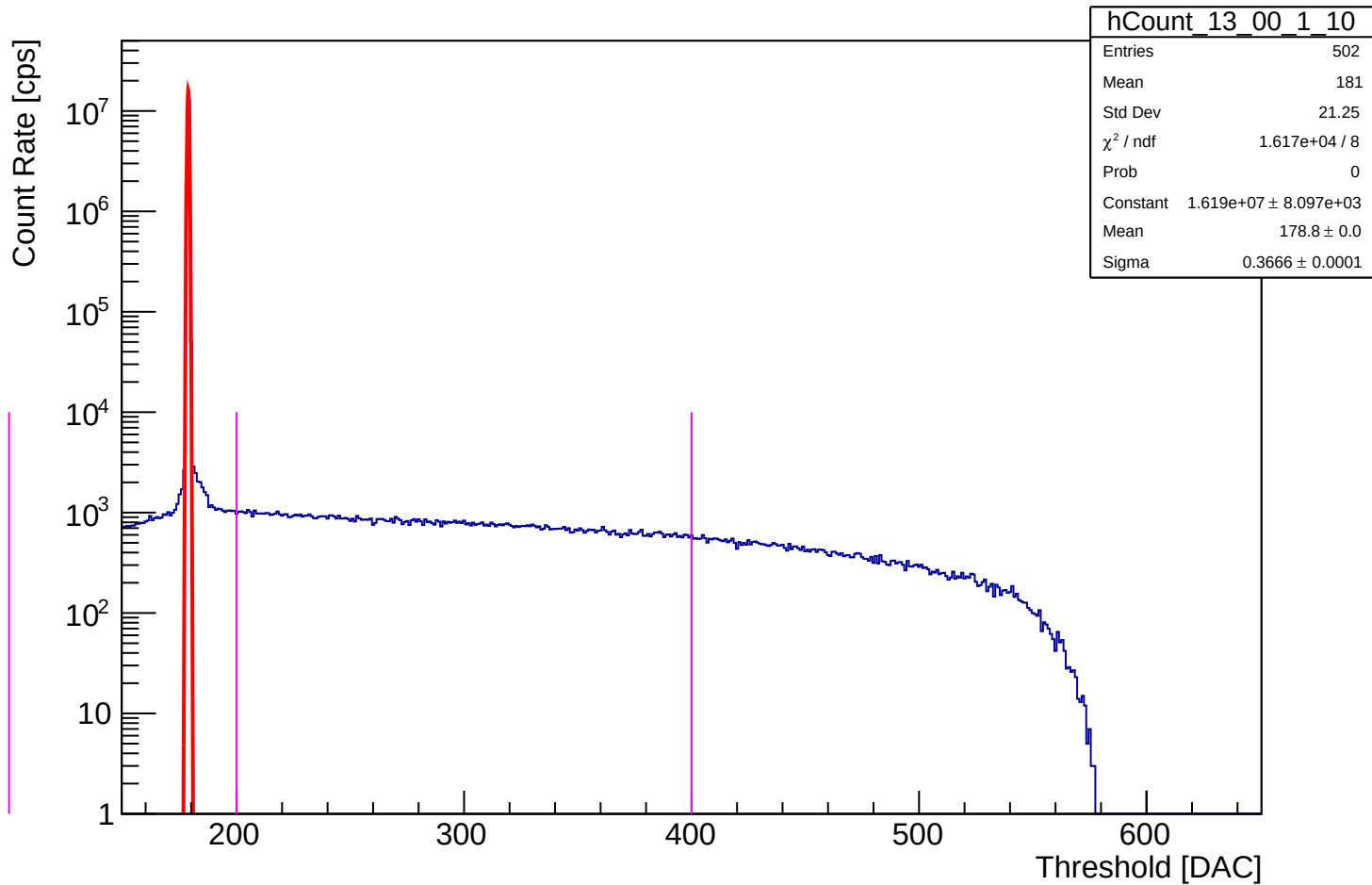


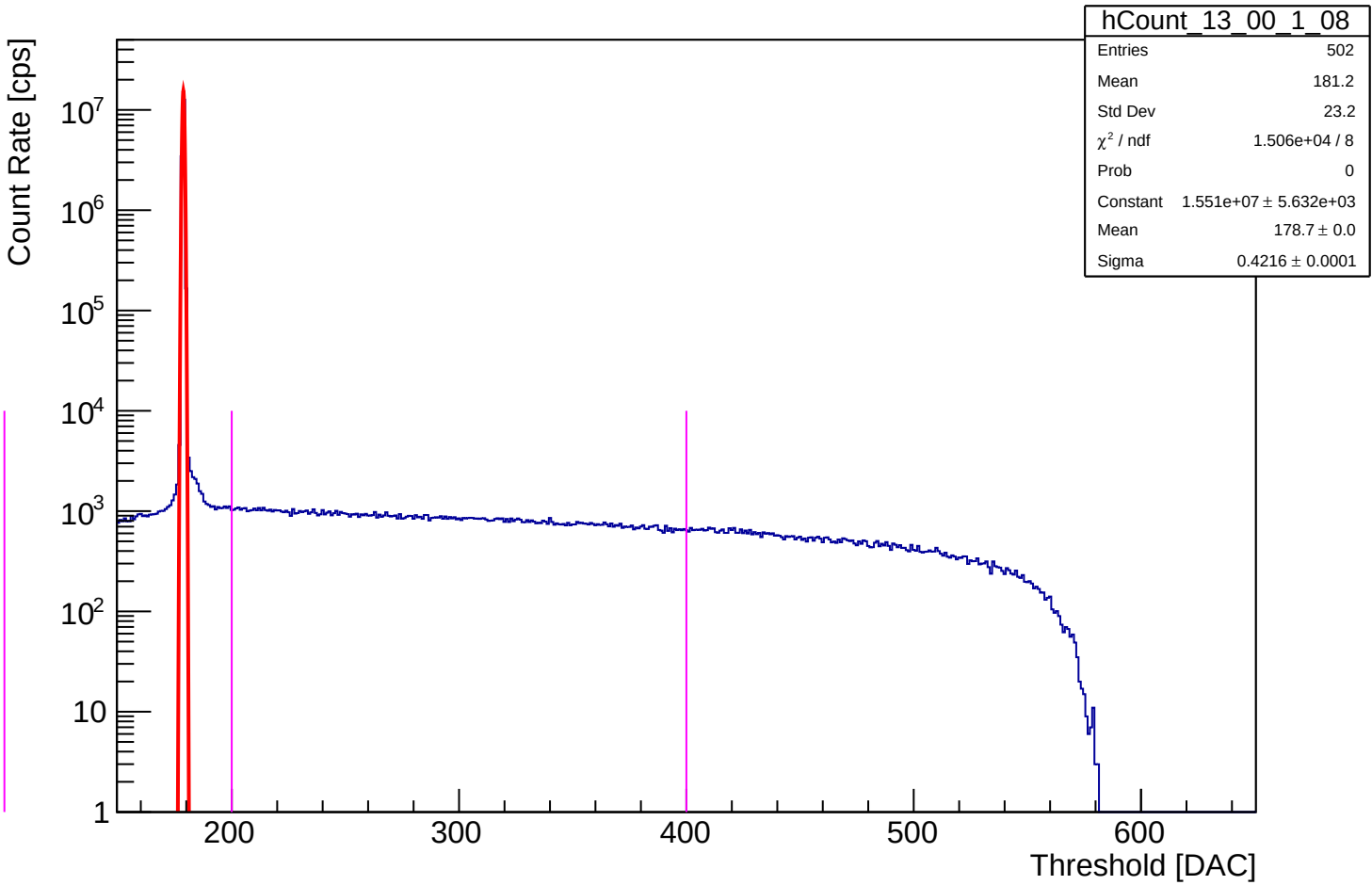
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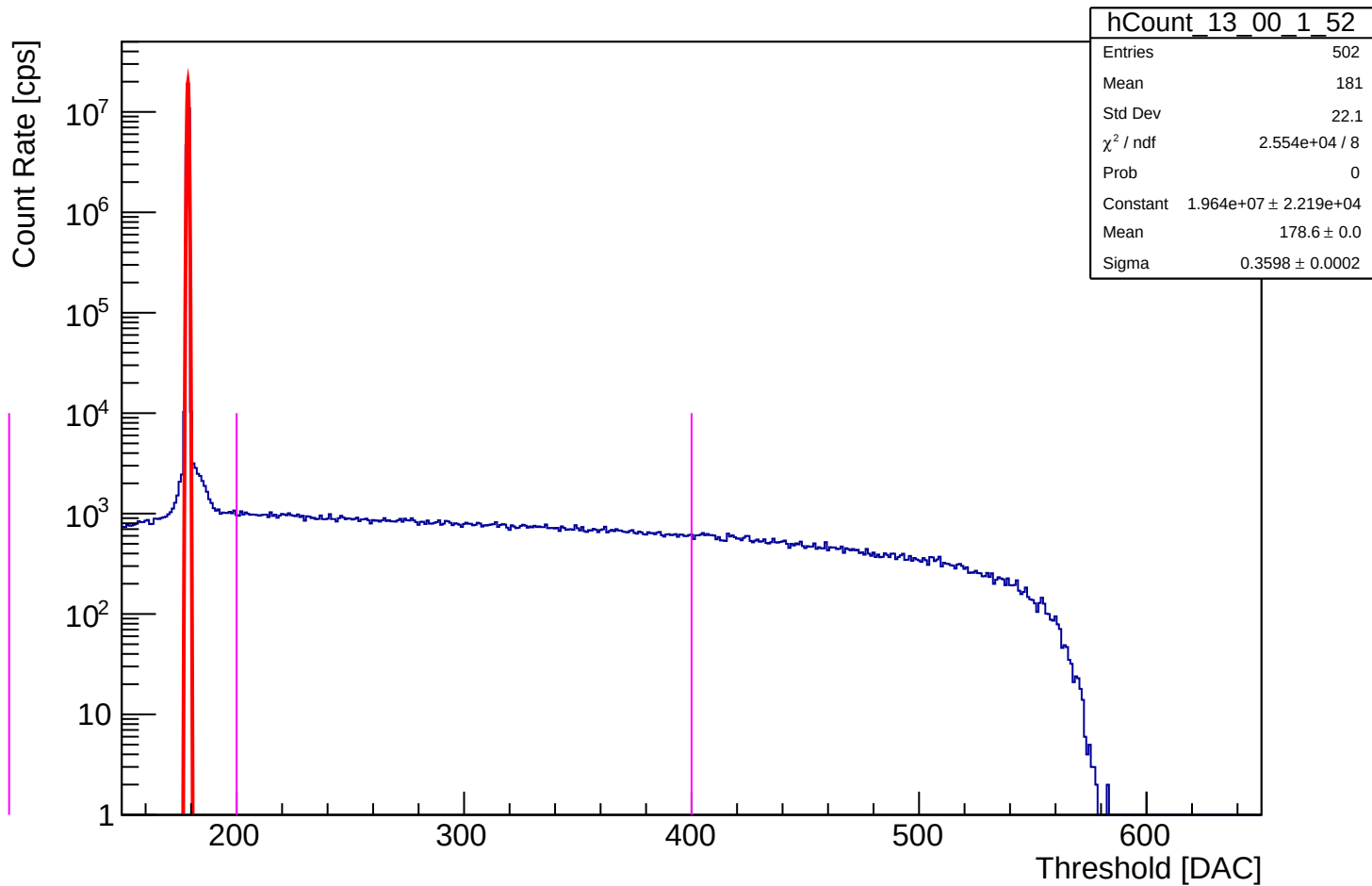


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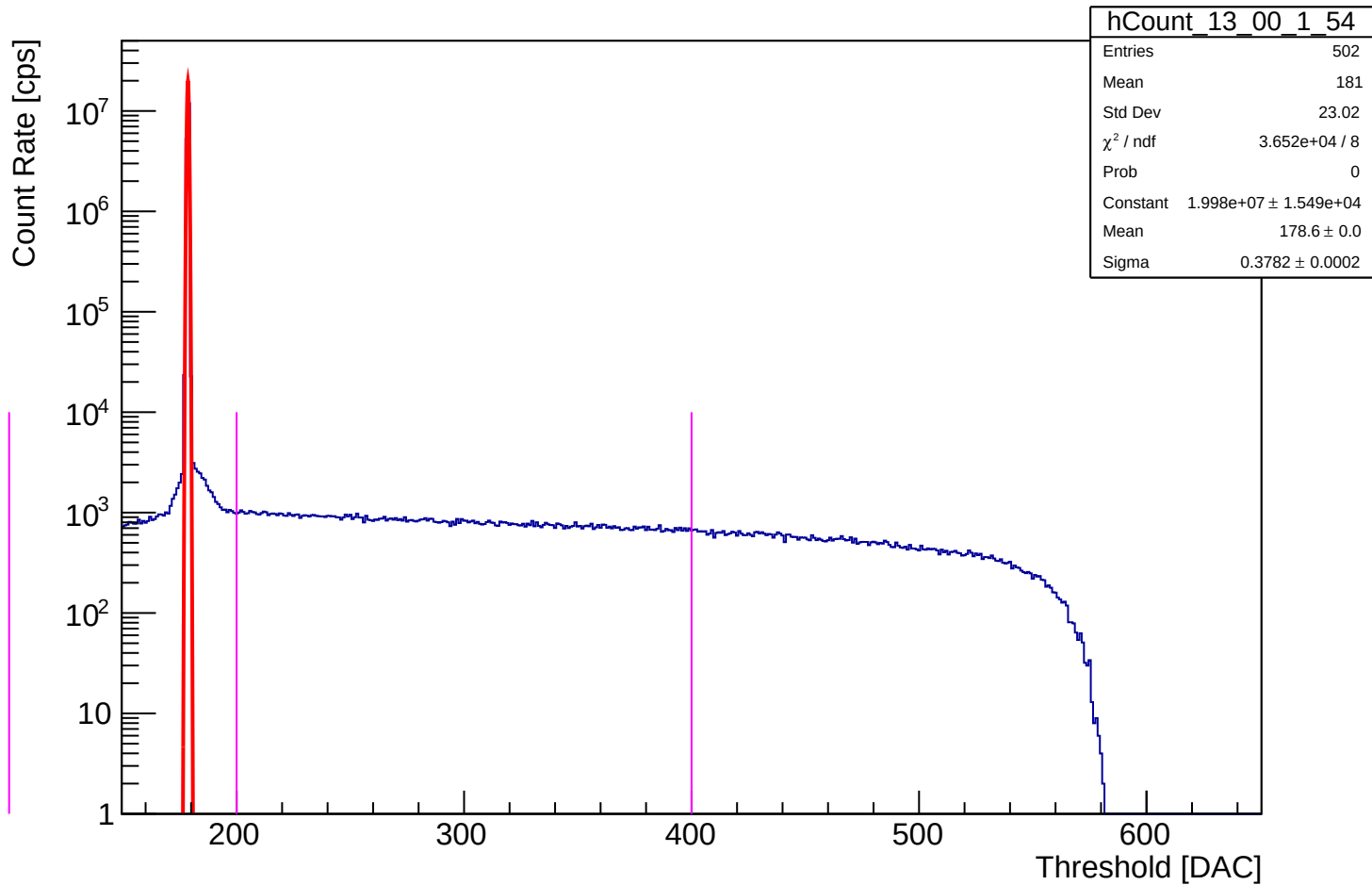




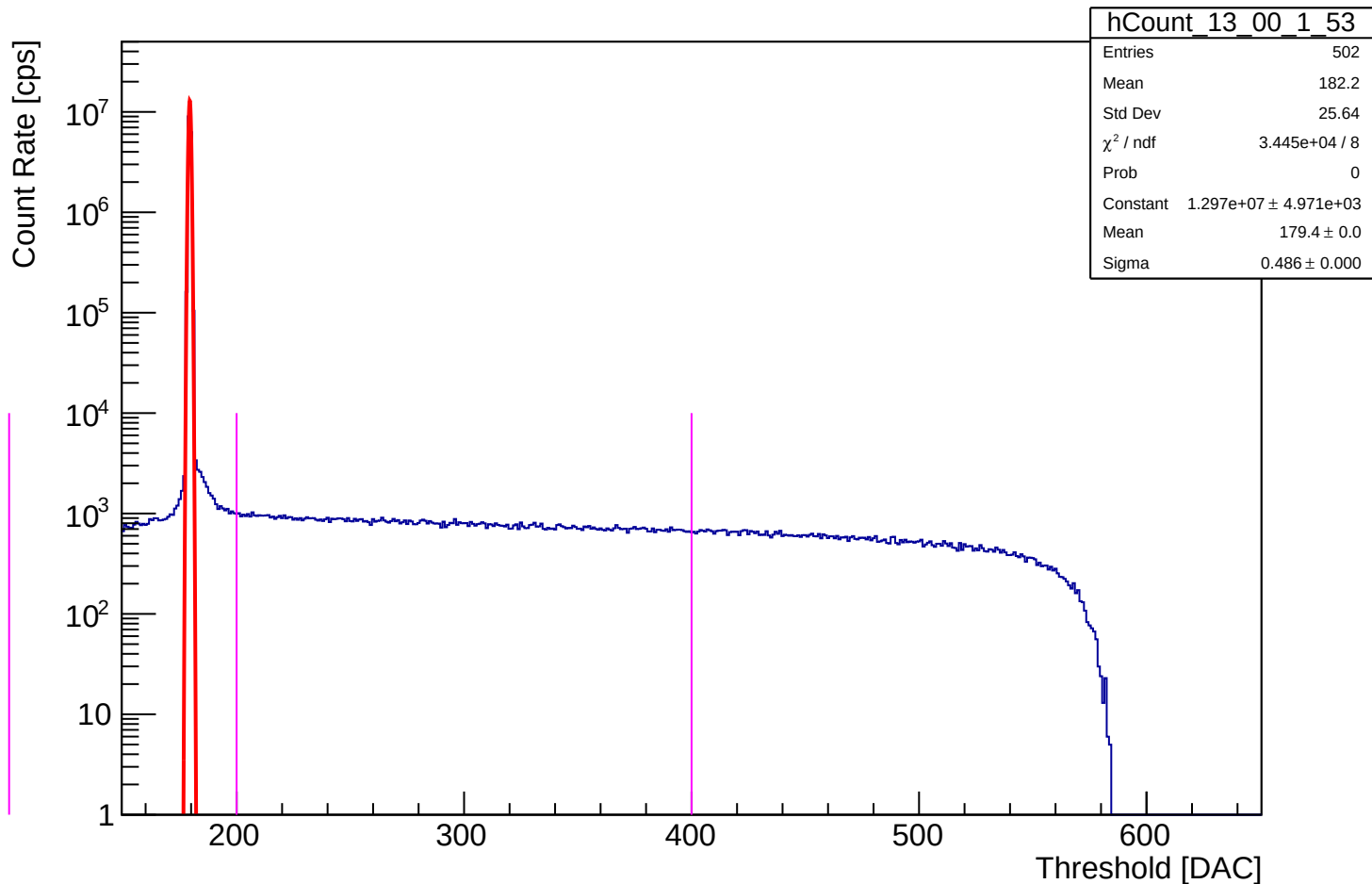
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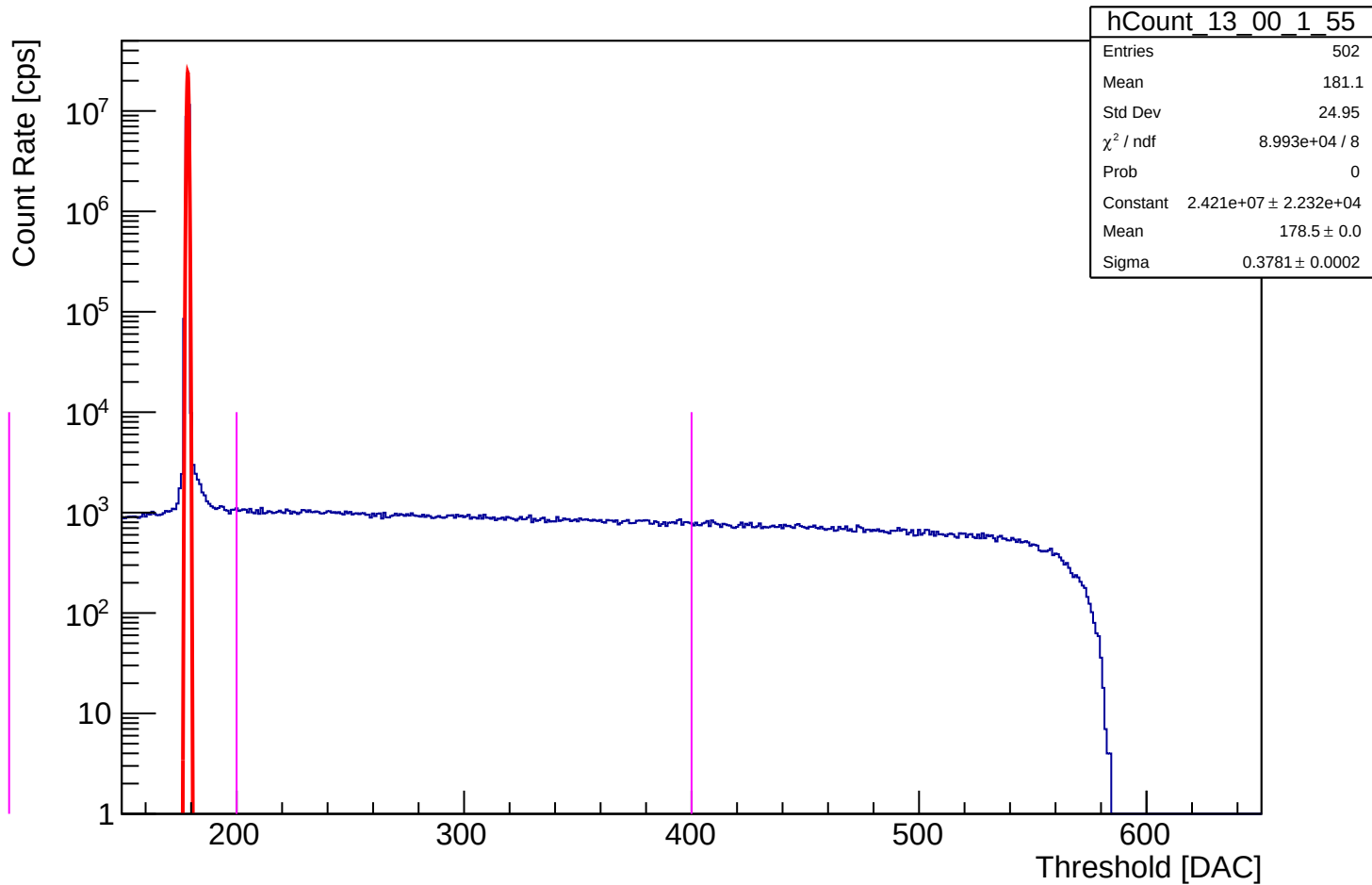
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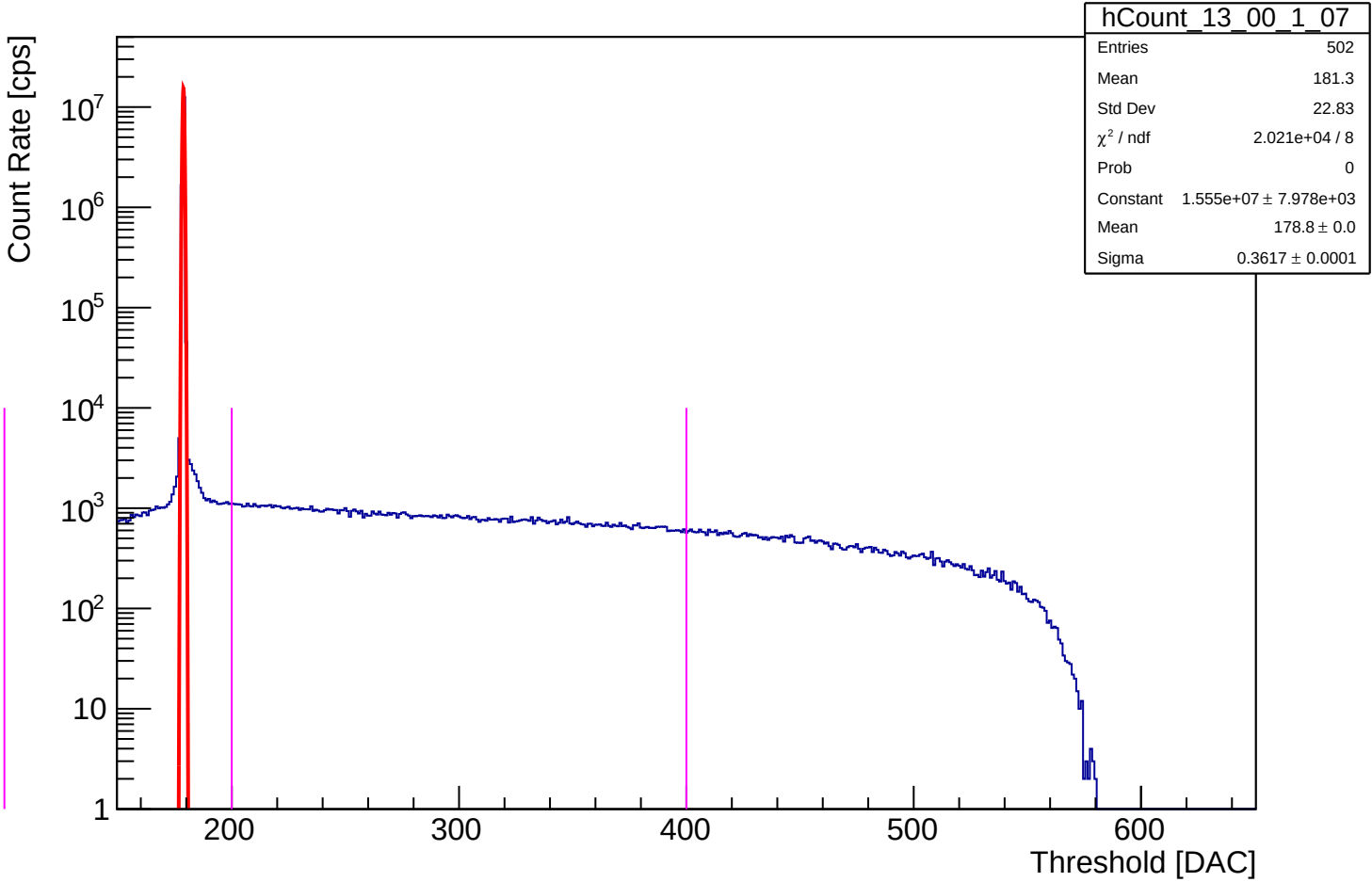


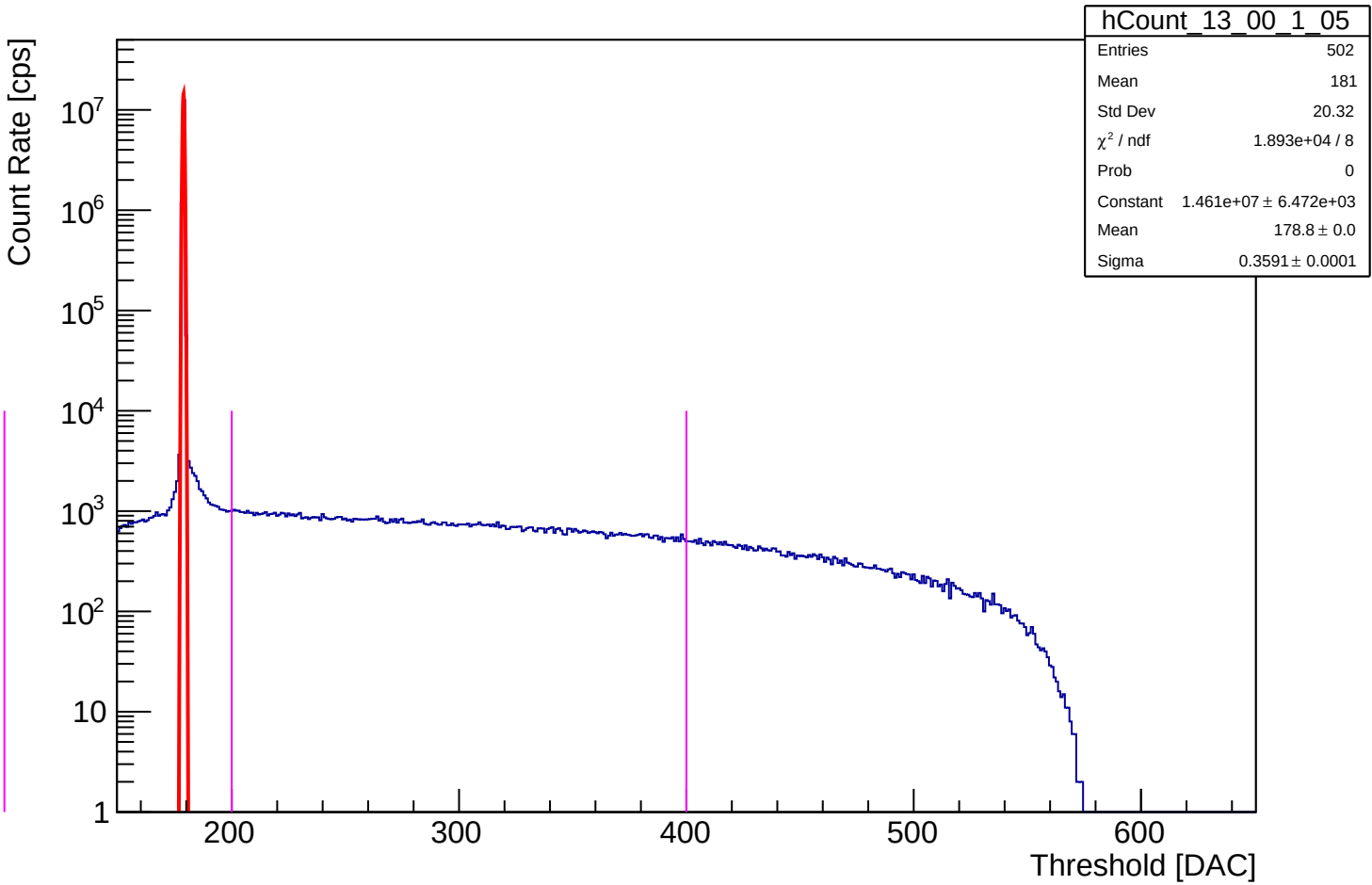
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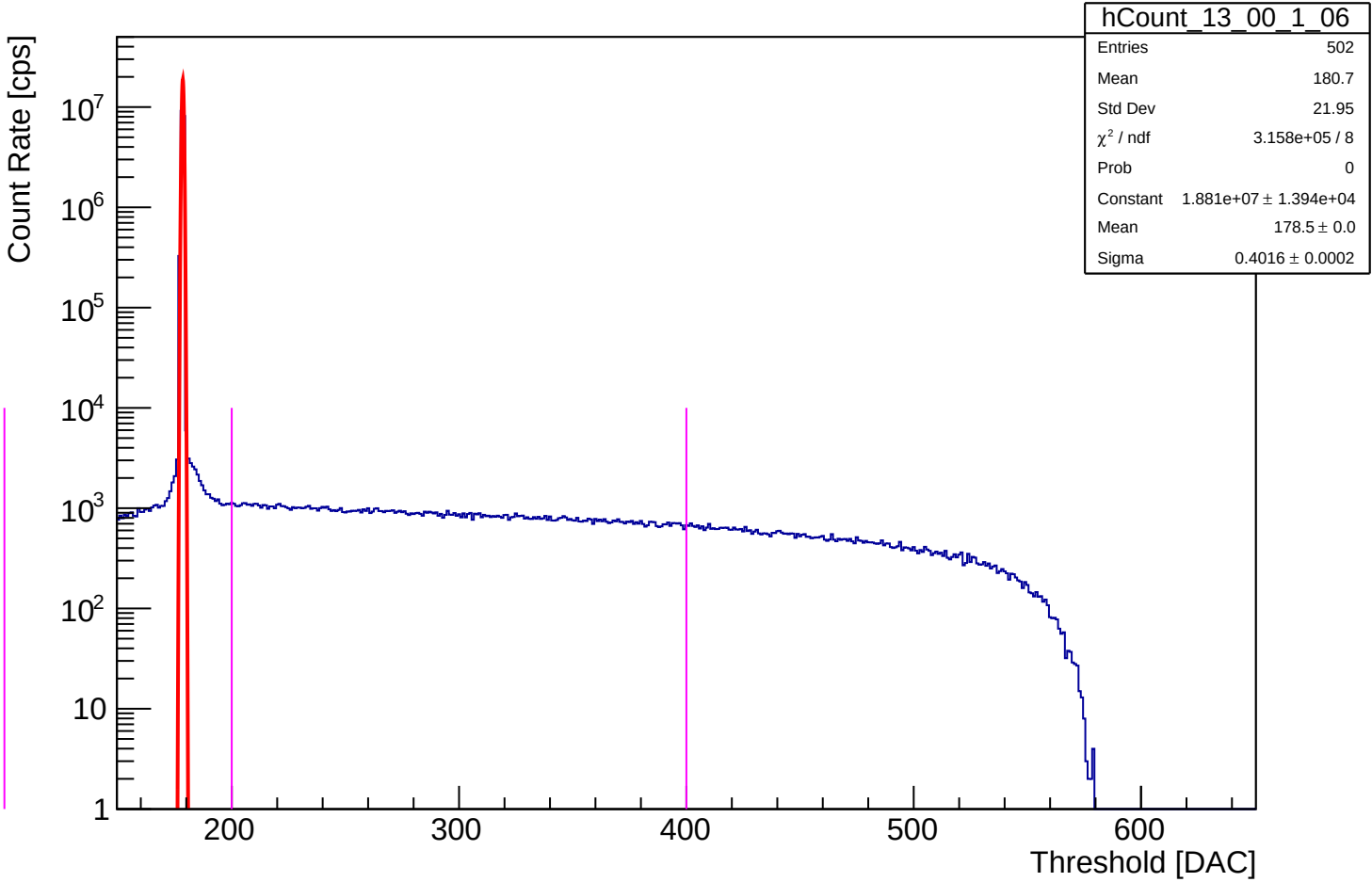


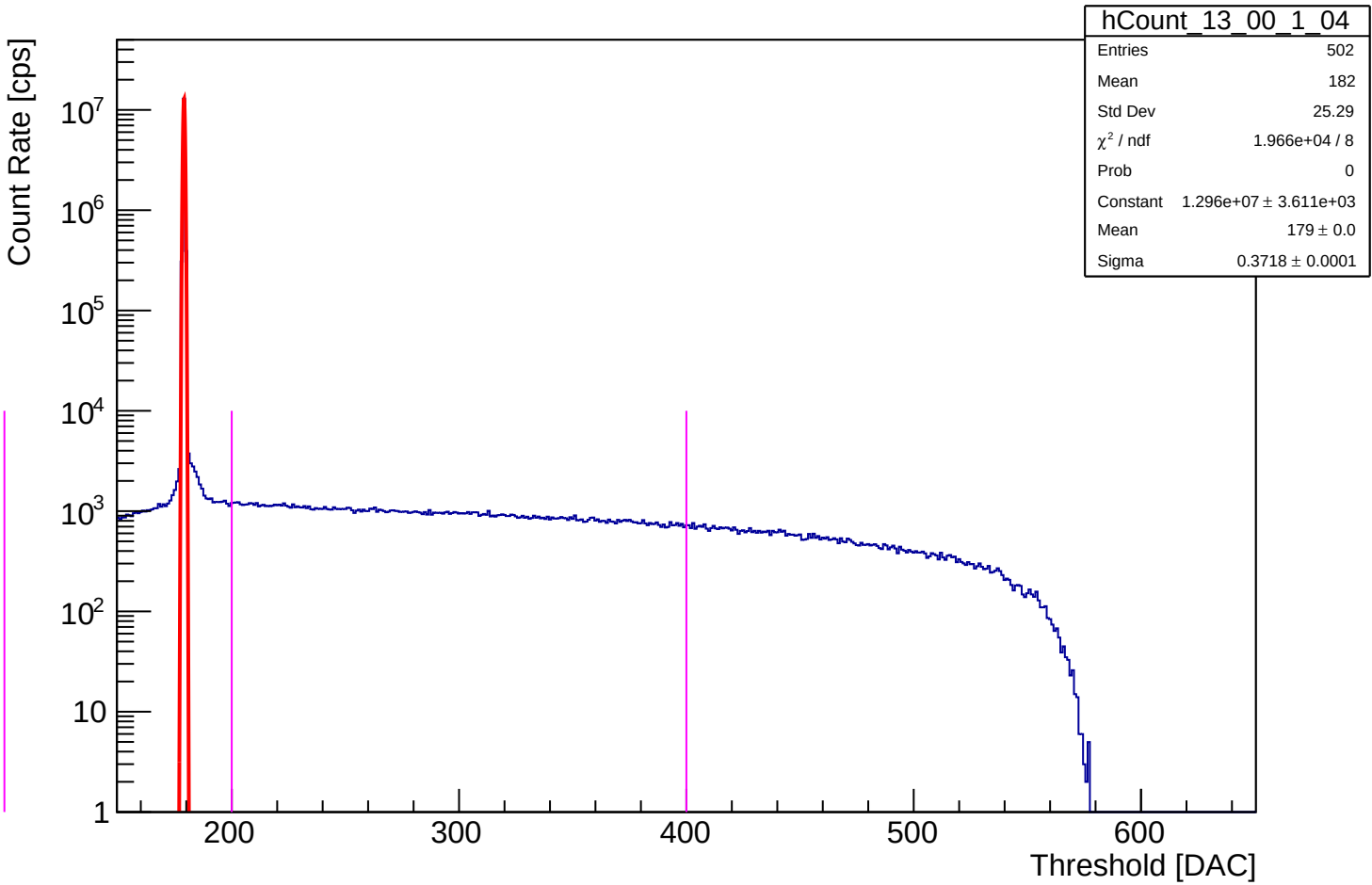
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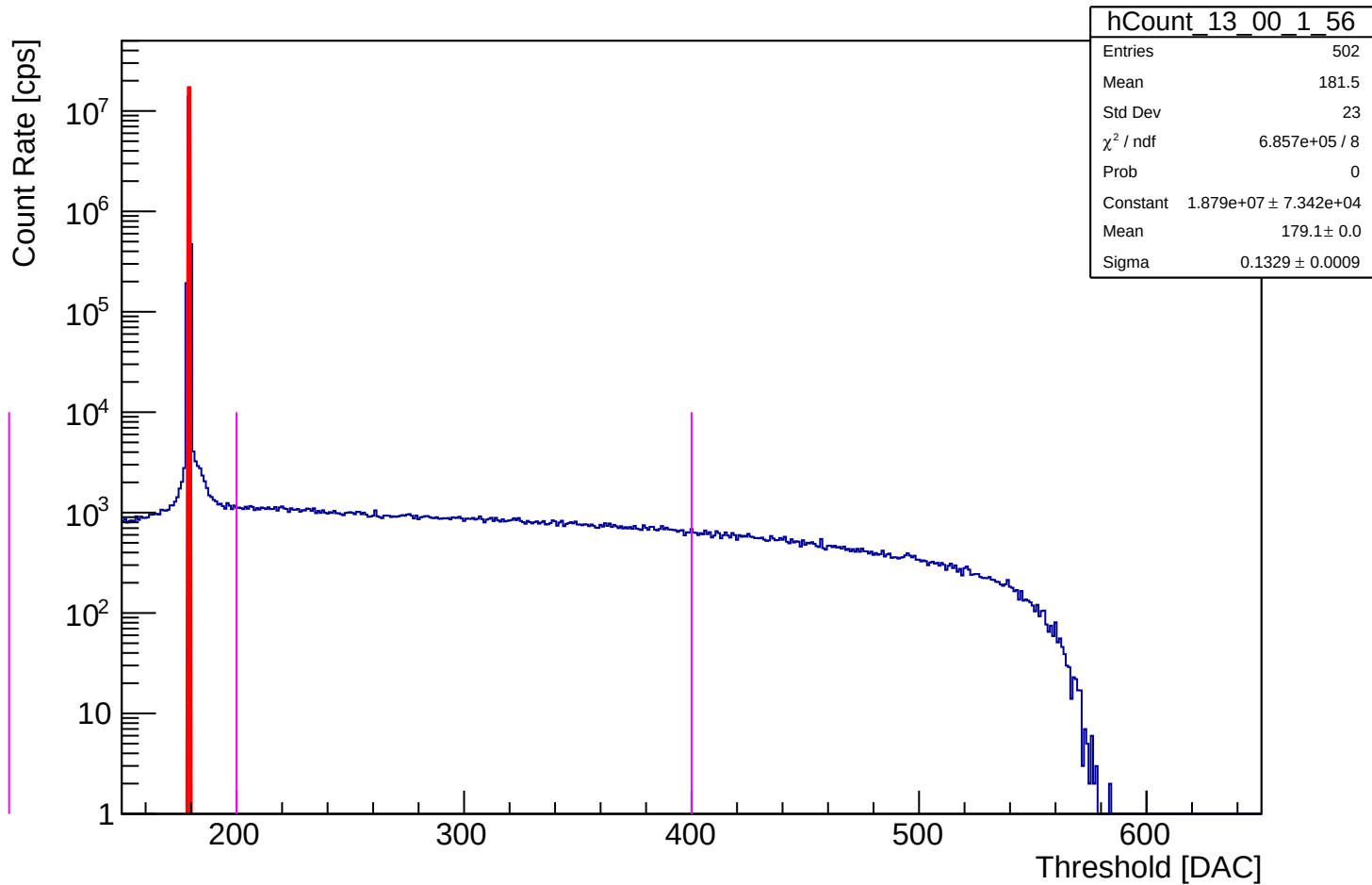




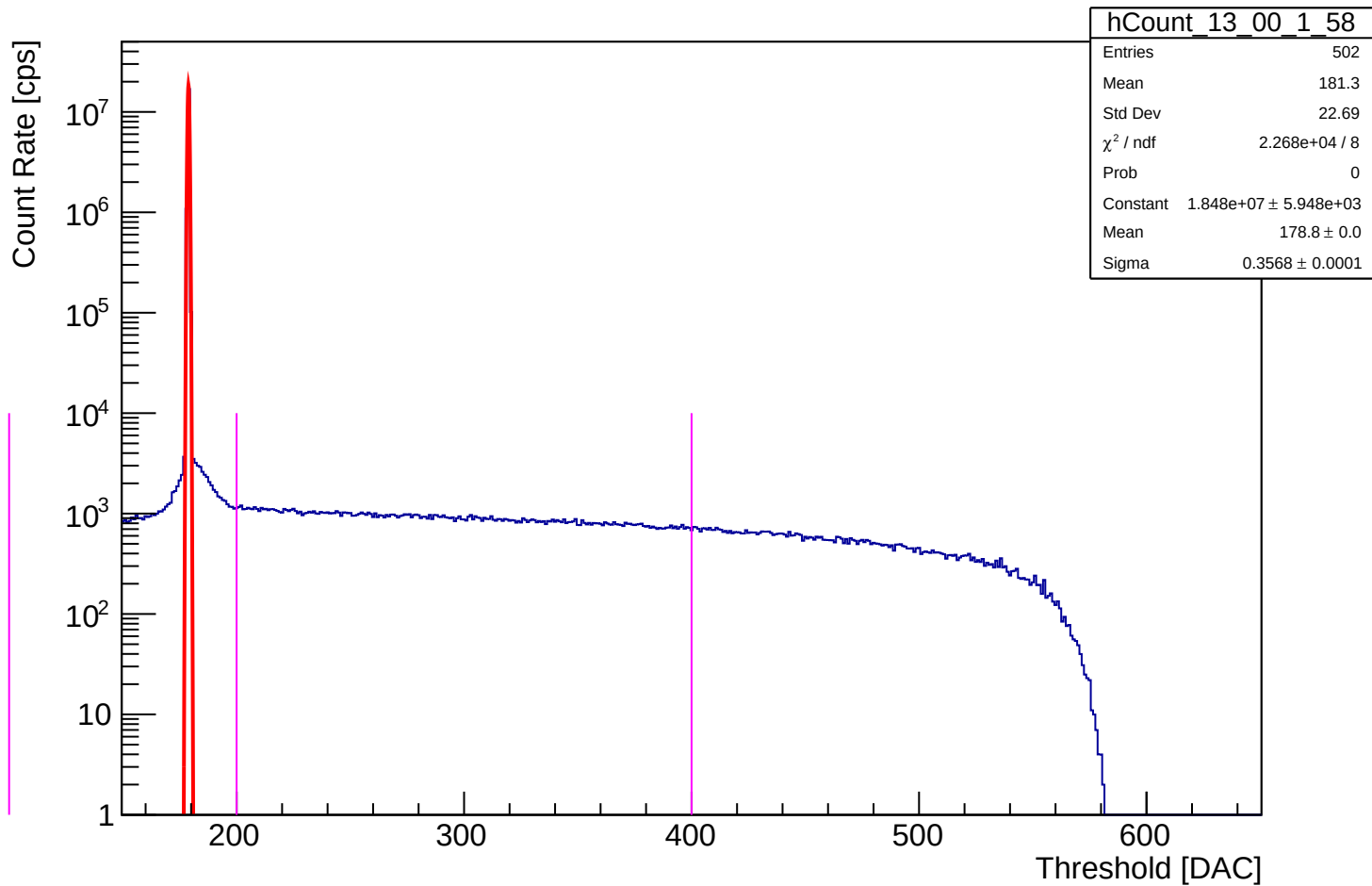




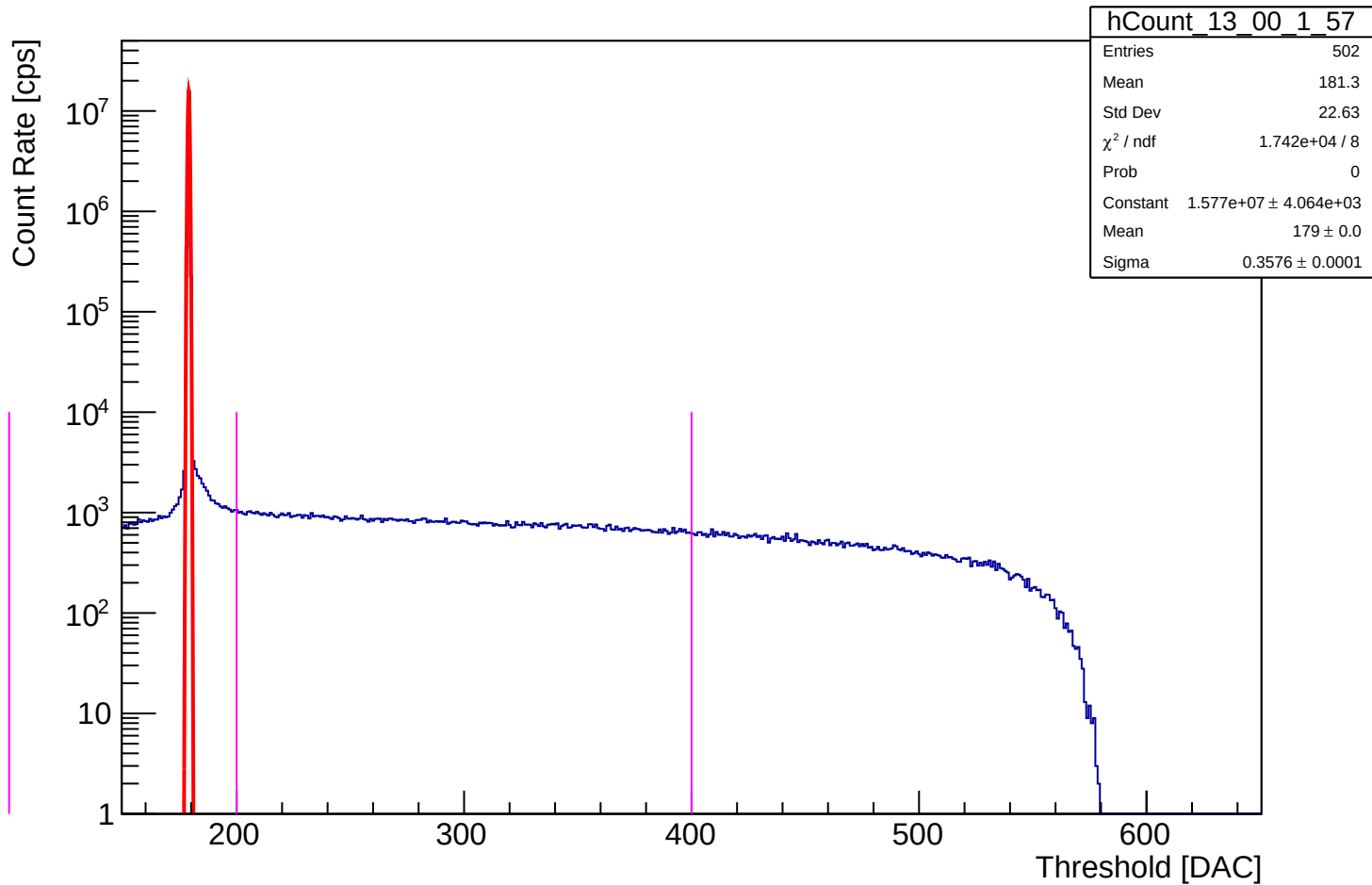
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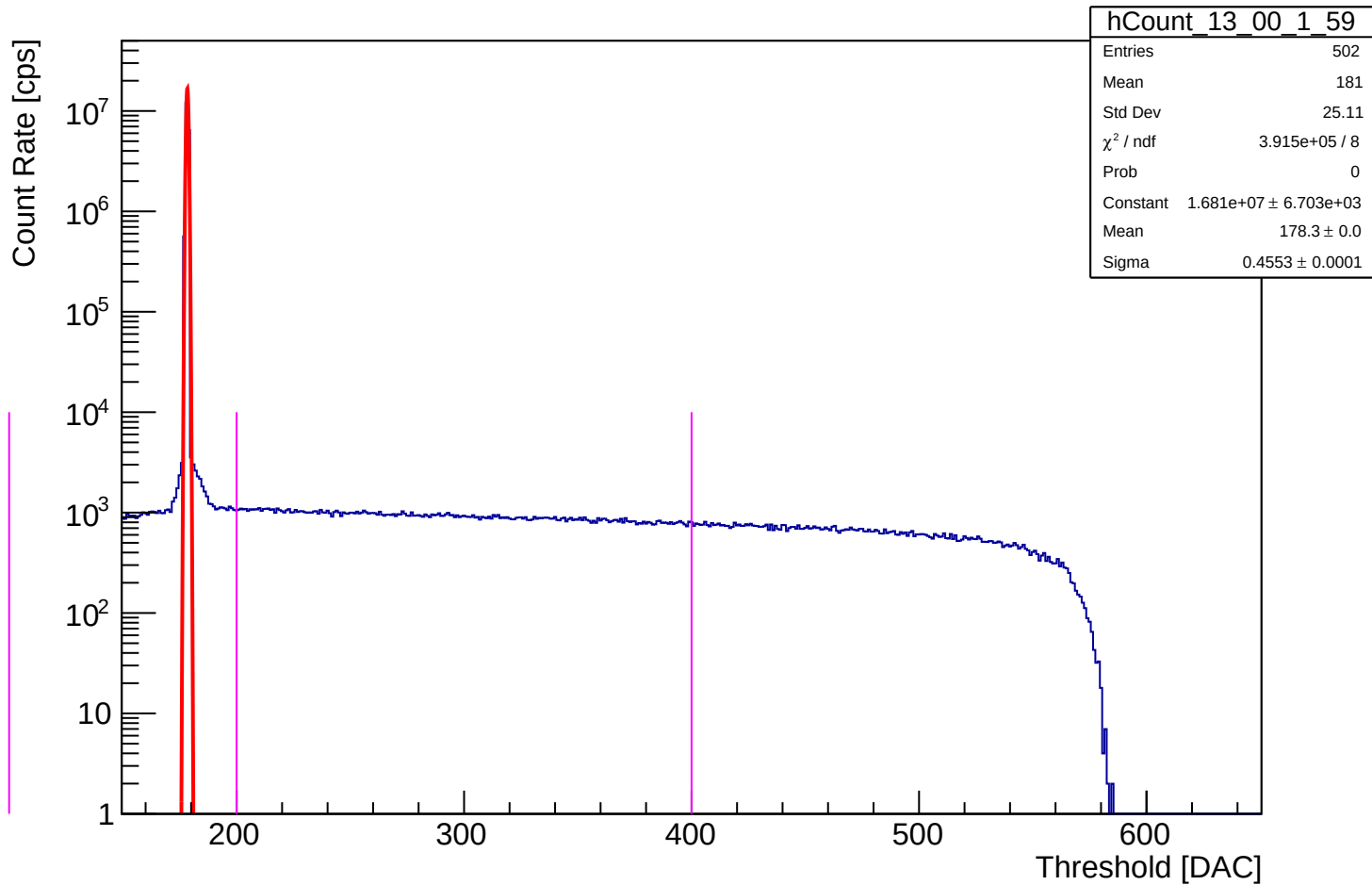
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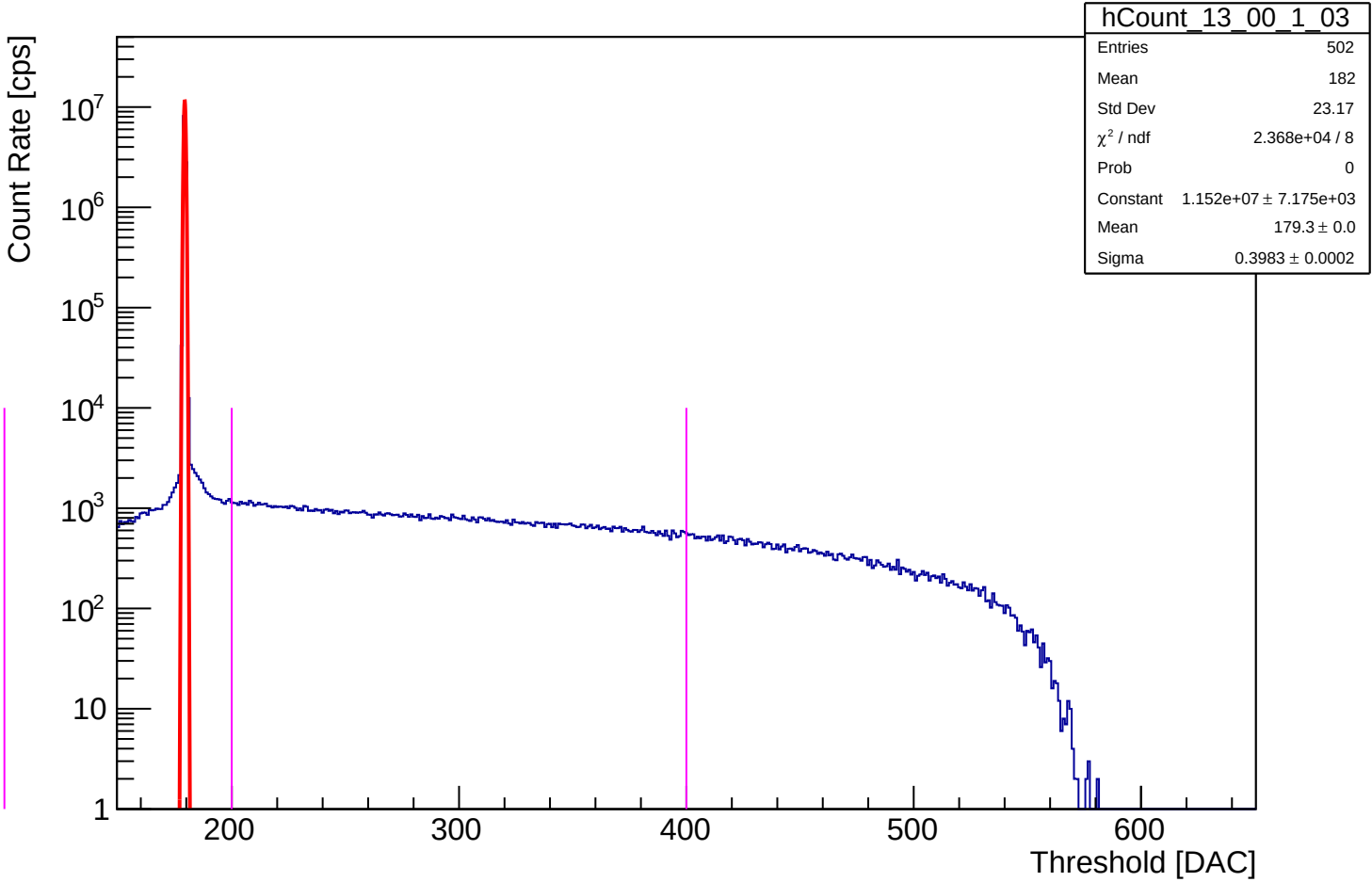


Row 1 Tile 1 Pmt 2 Pixel 55 Slot 13 Fiber 0 Asic 1 Channel 57

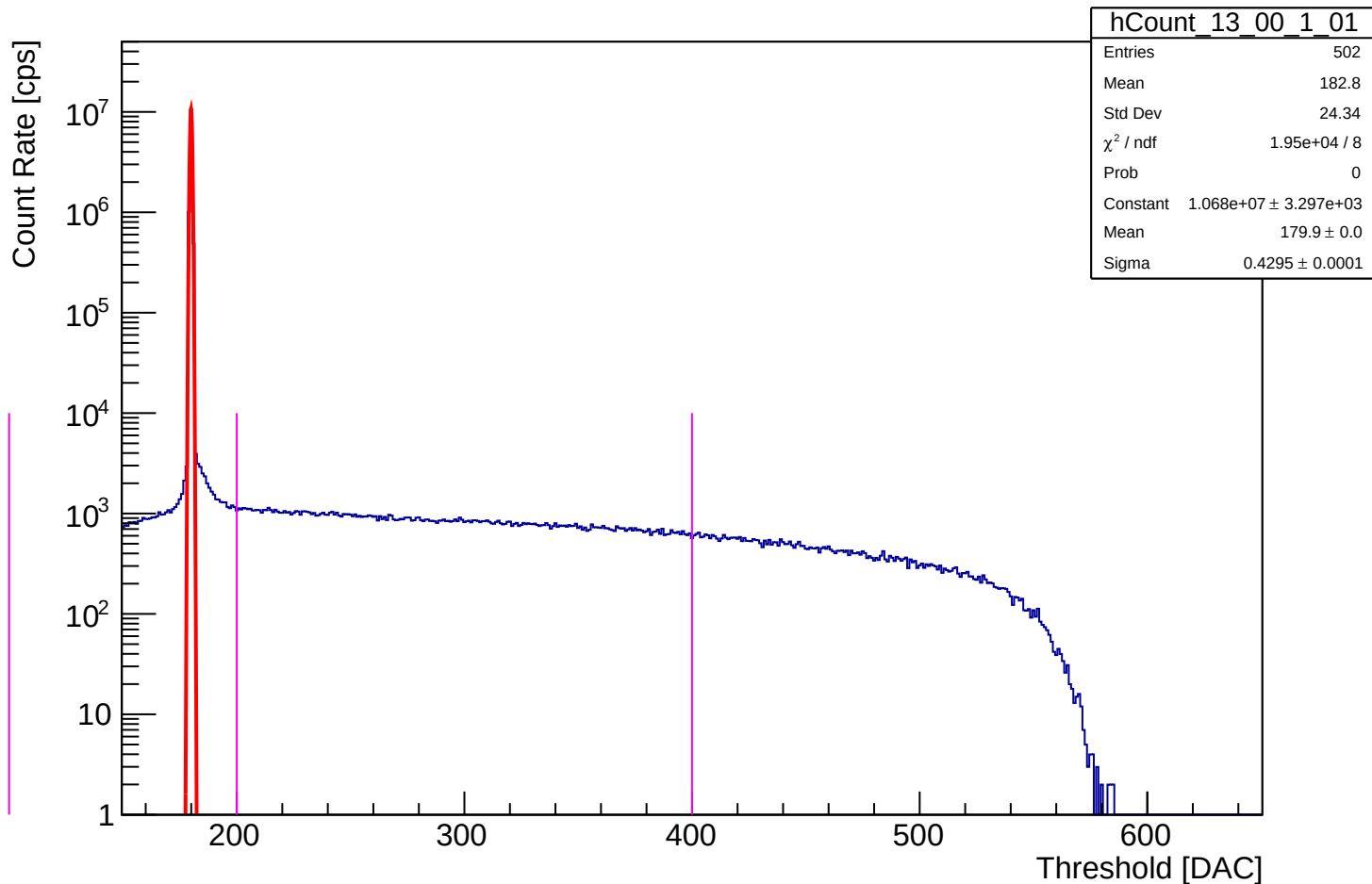


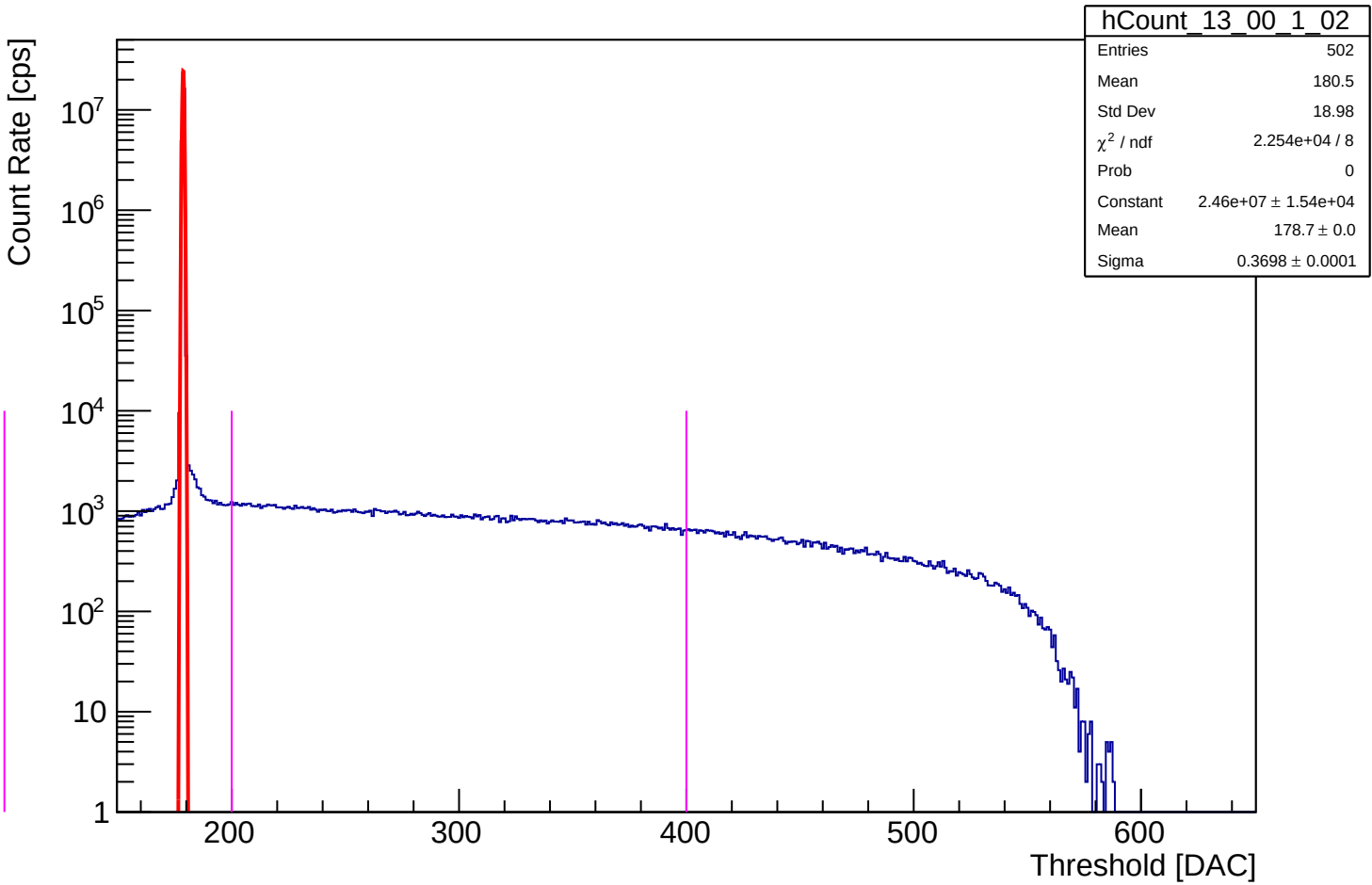
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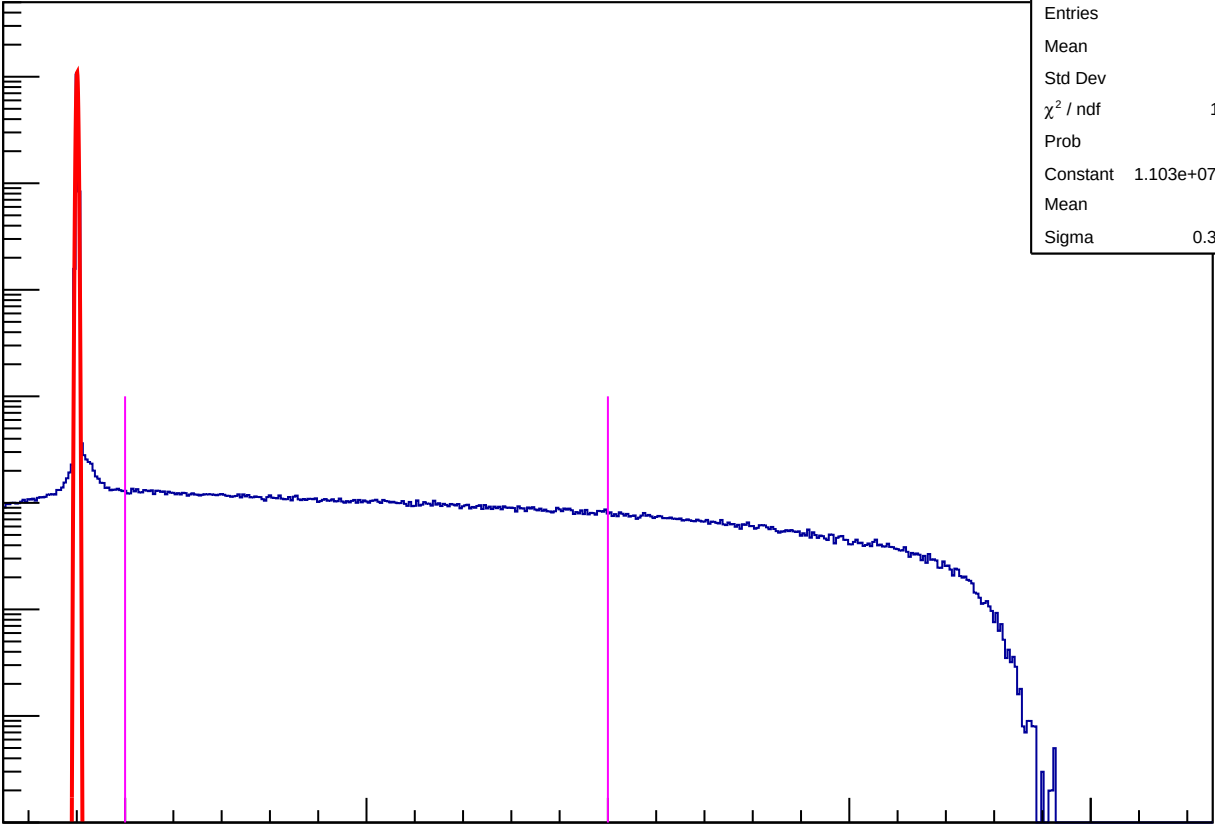
Row 1 Tile 1 Pmt 2 Pixel 58 Slot 13 Fiber 0 Asic 1 Channel 1





Count Rate [cps]

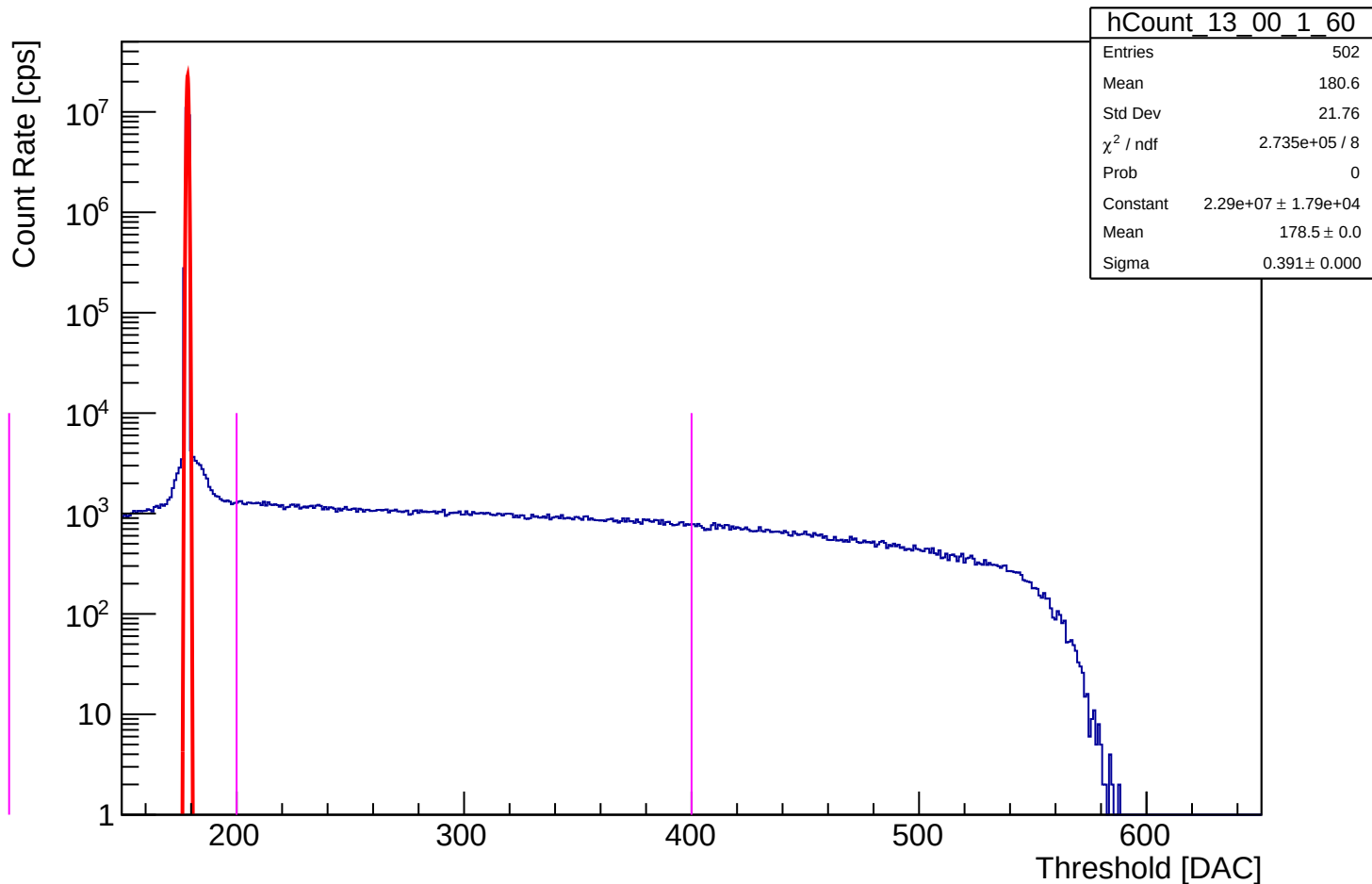
10^7
 10^6
 10^5
 10^4
 10^3
 10^2
 10
 1



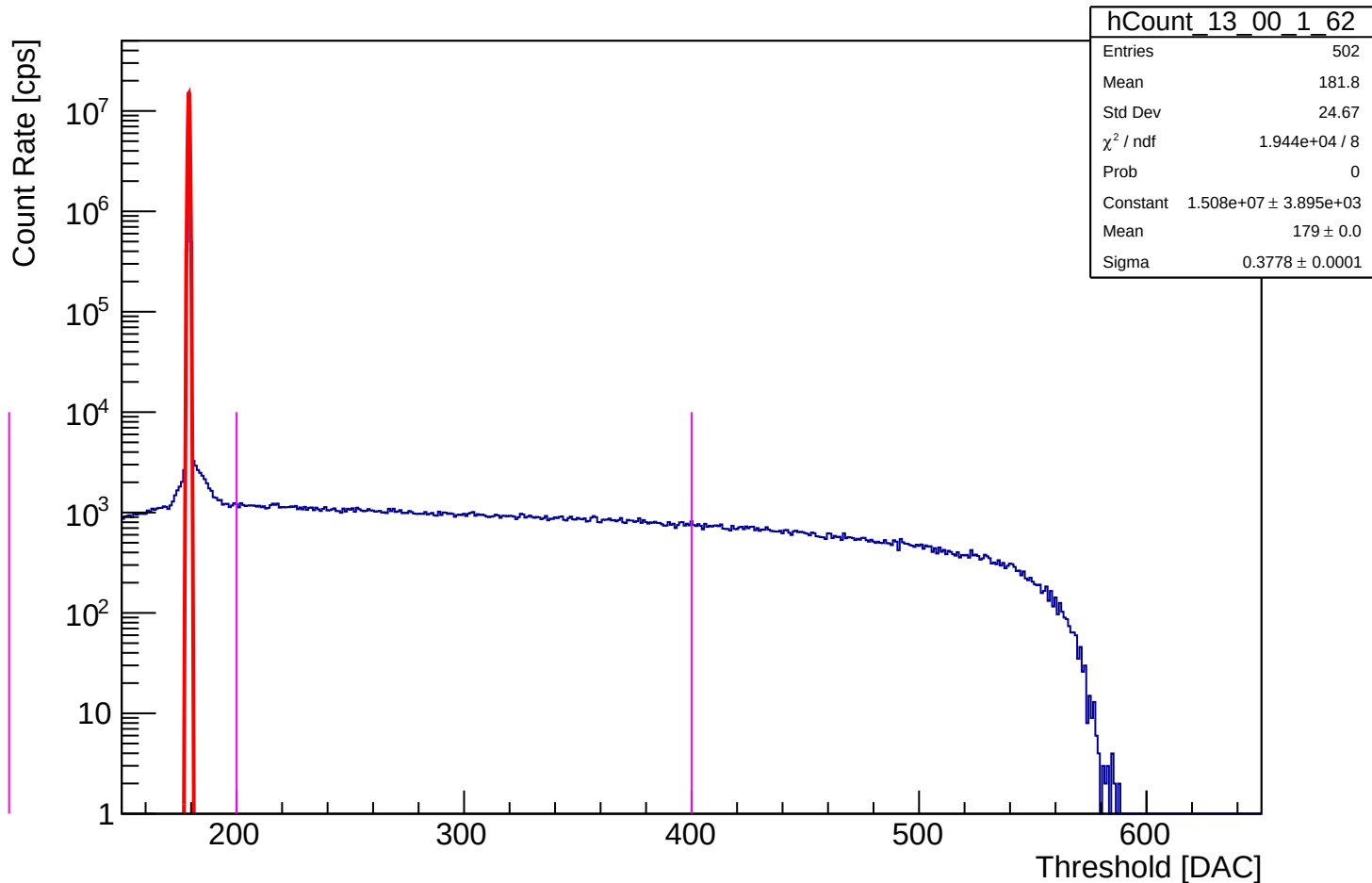
Threshold [DAC]

hCount_13_00_1_00	
Entries	502
Mean	184
Std Dev	28.82
χ^2 / ndf	1.872e+04 / 8
Prob	0
Constant	$1.103e+07 \pm 3.785e+03$
Mean	180.1 ± 0.0
Sigma	0.3856 ± 0.0001

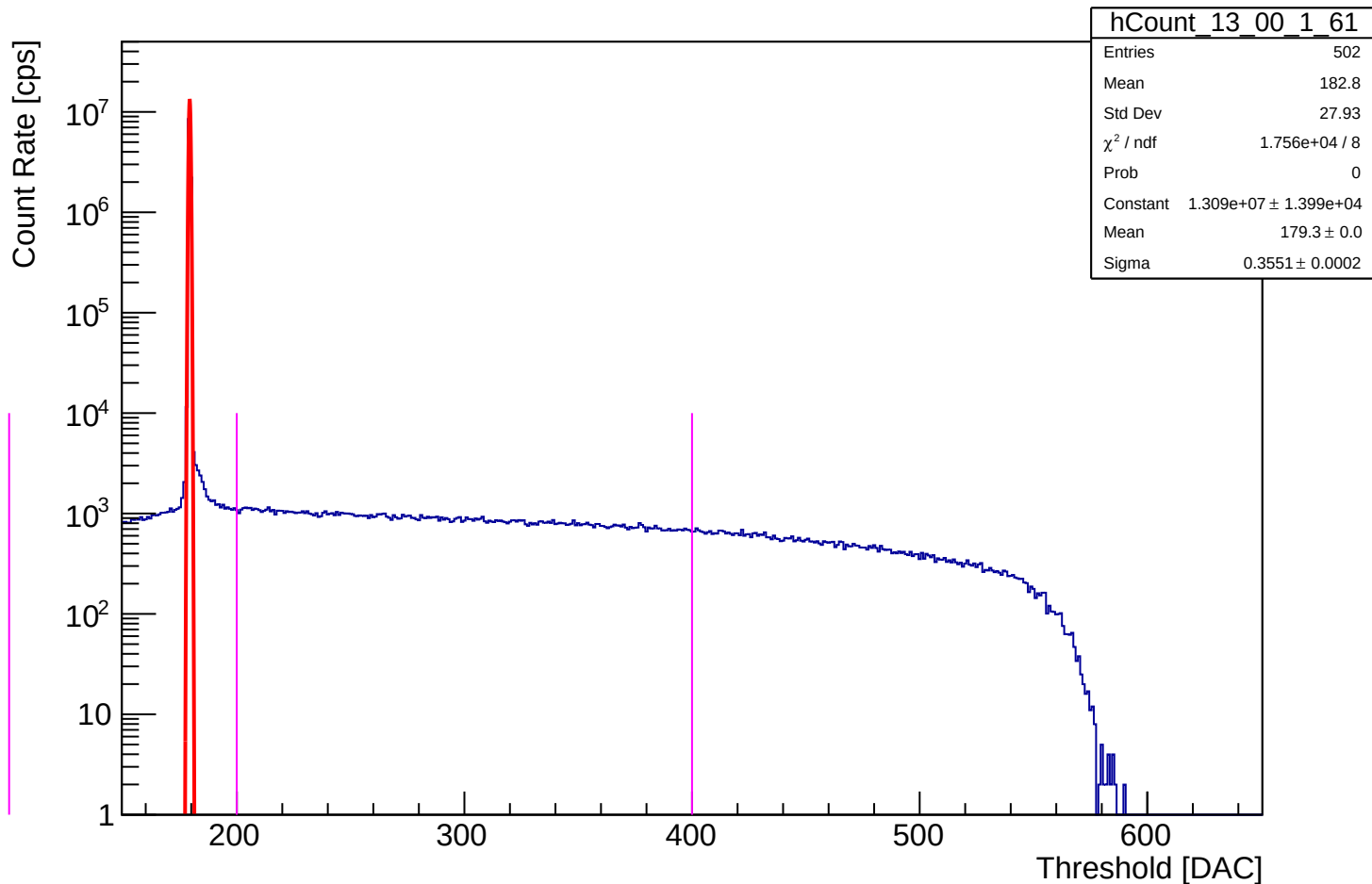
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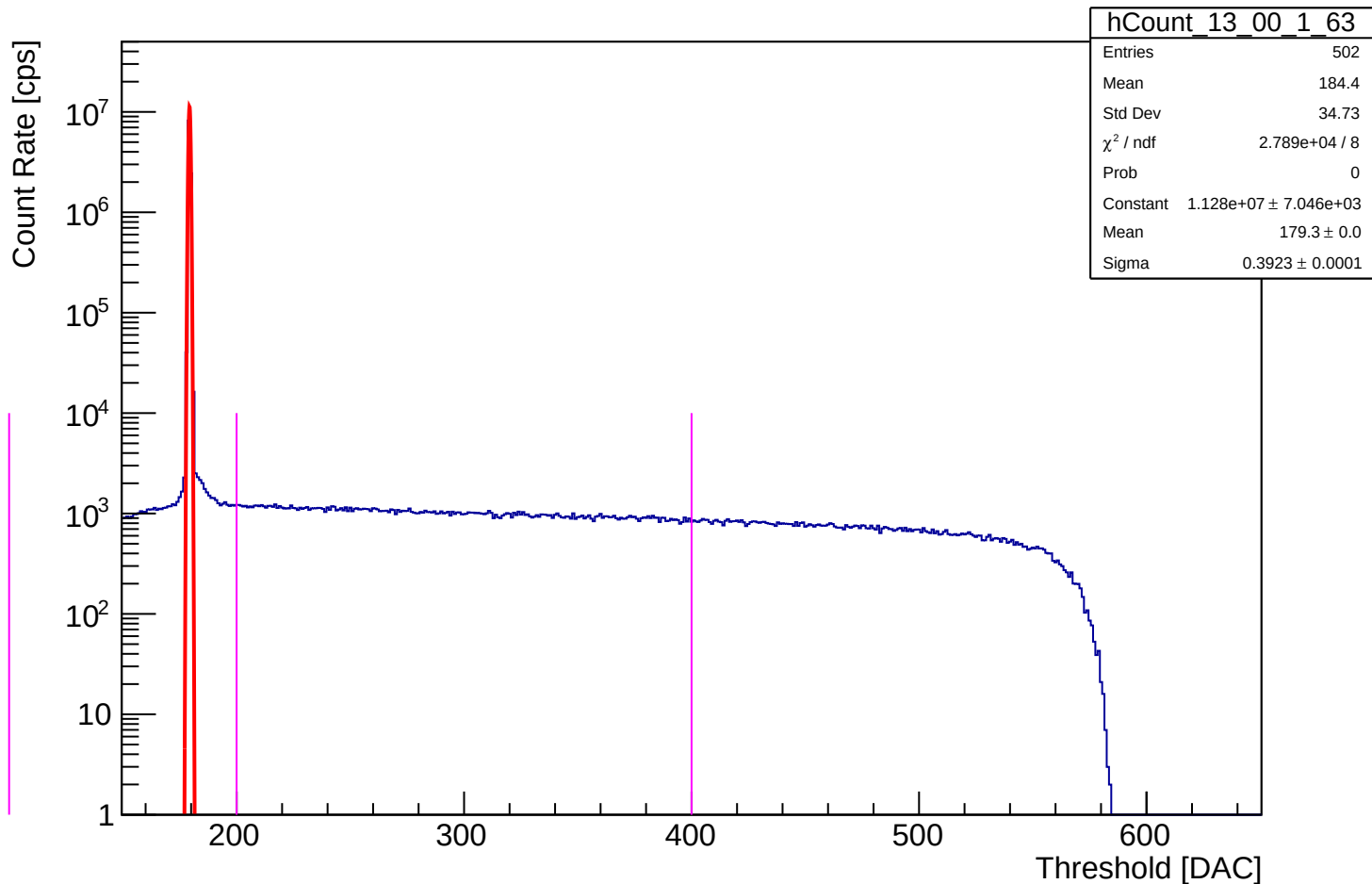
Row 1 Tile 1 Pmt 2 Pixel 62 Slot 13 Fiber 0 Asic 1 Channel 62



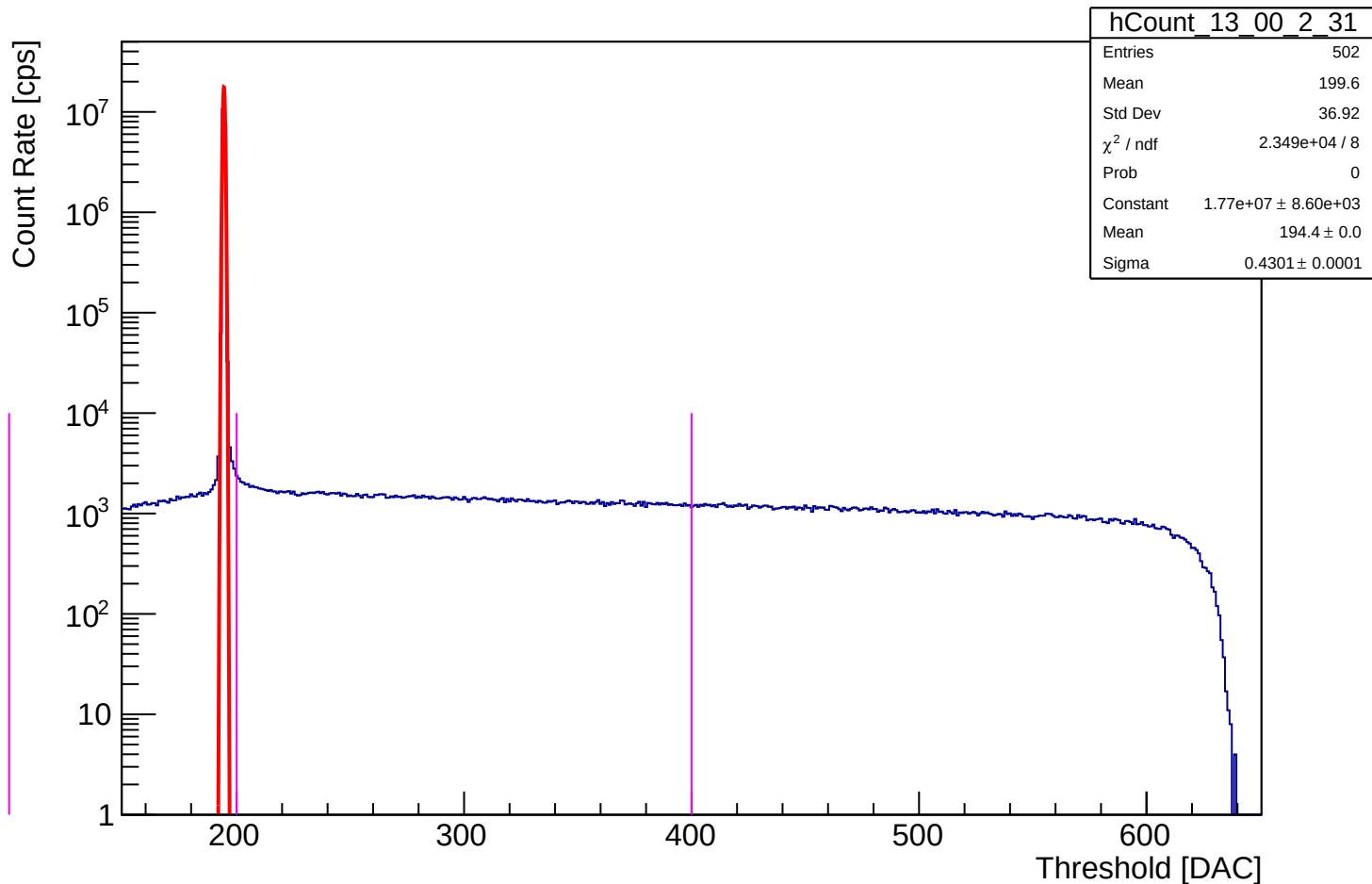
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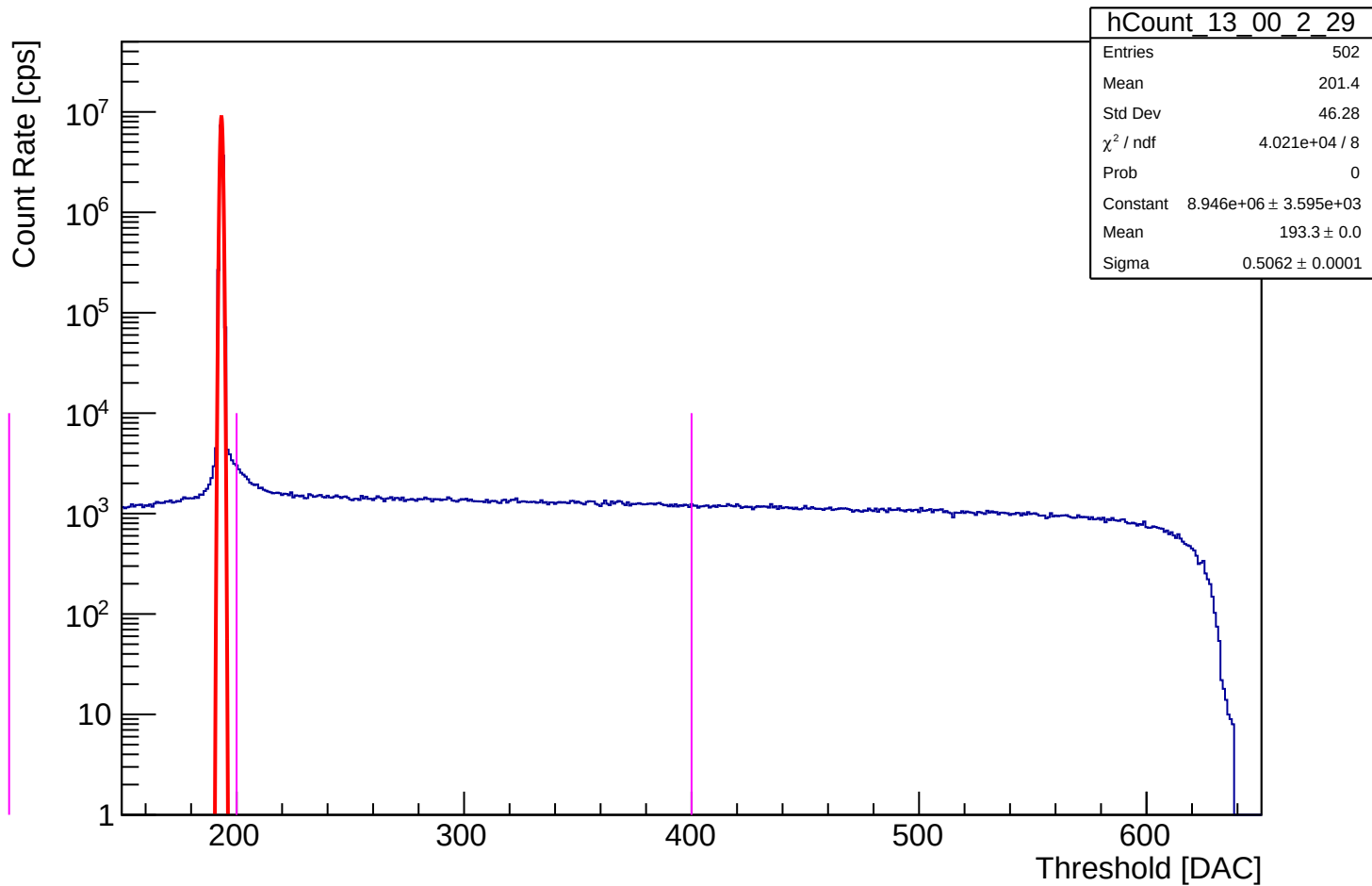
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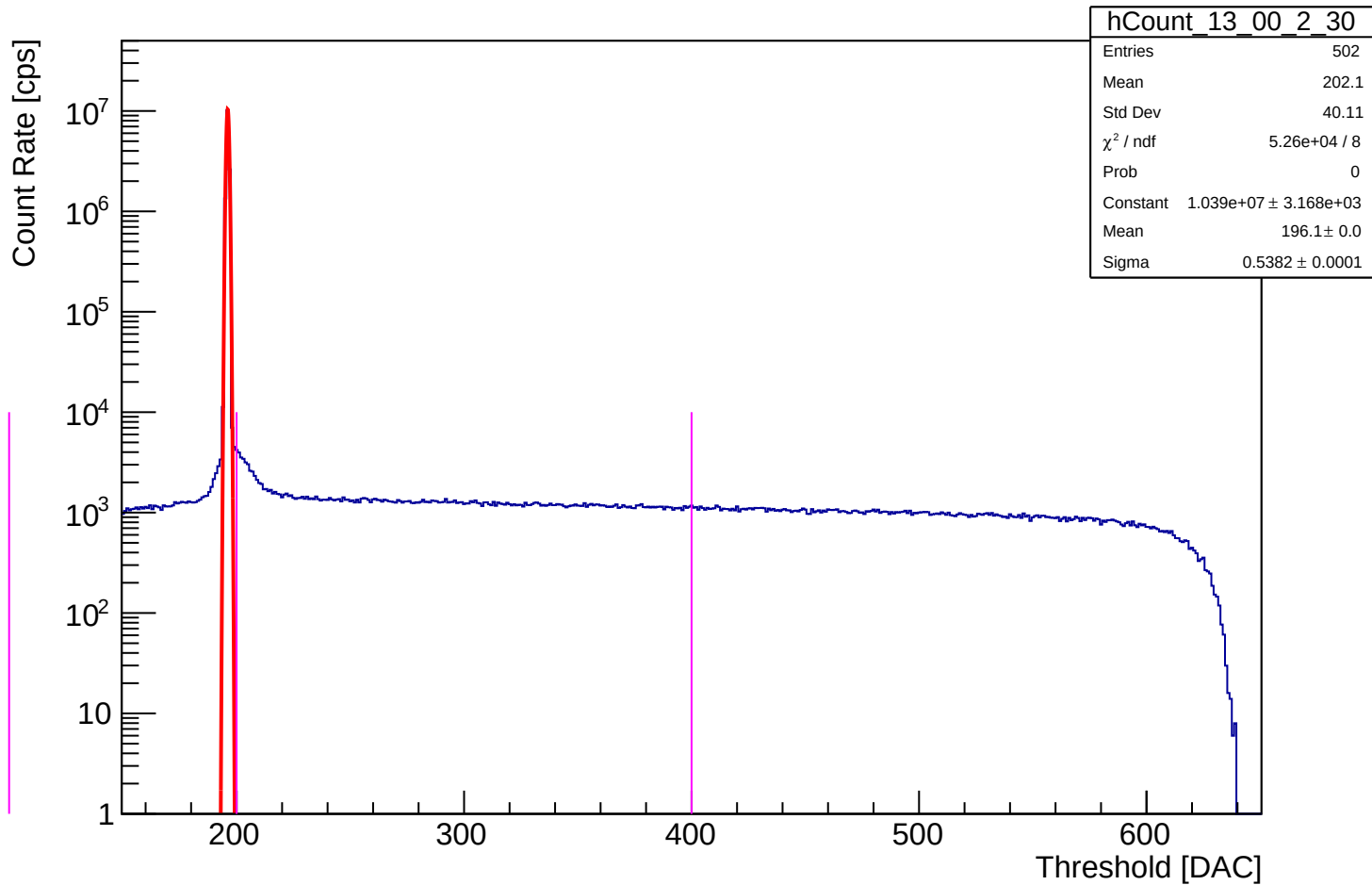
Row 1 Tile 1 Pmt 3 Pixel 1 Slot 13 Fiber 0 Asic 2 Channel 31



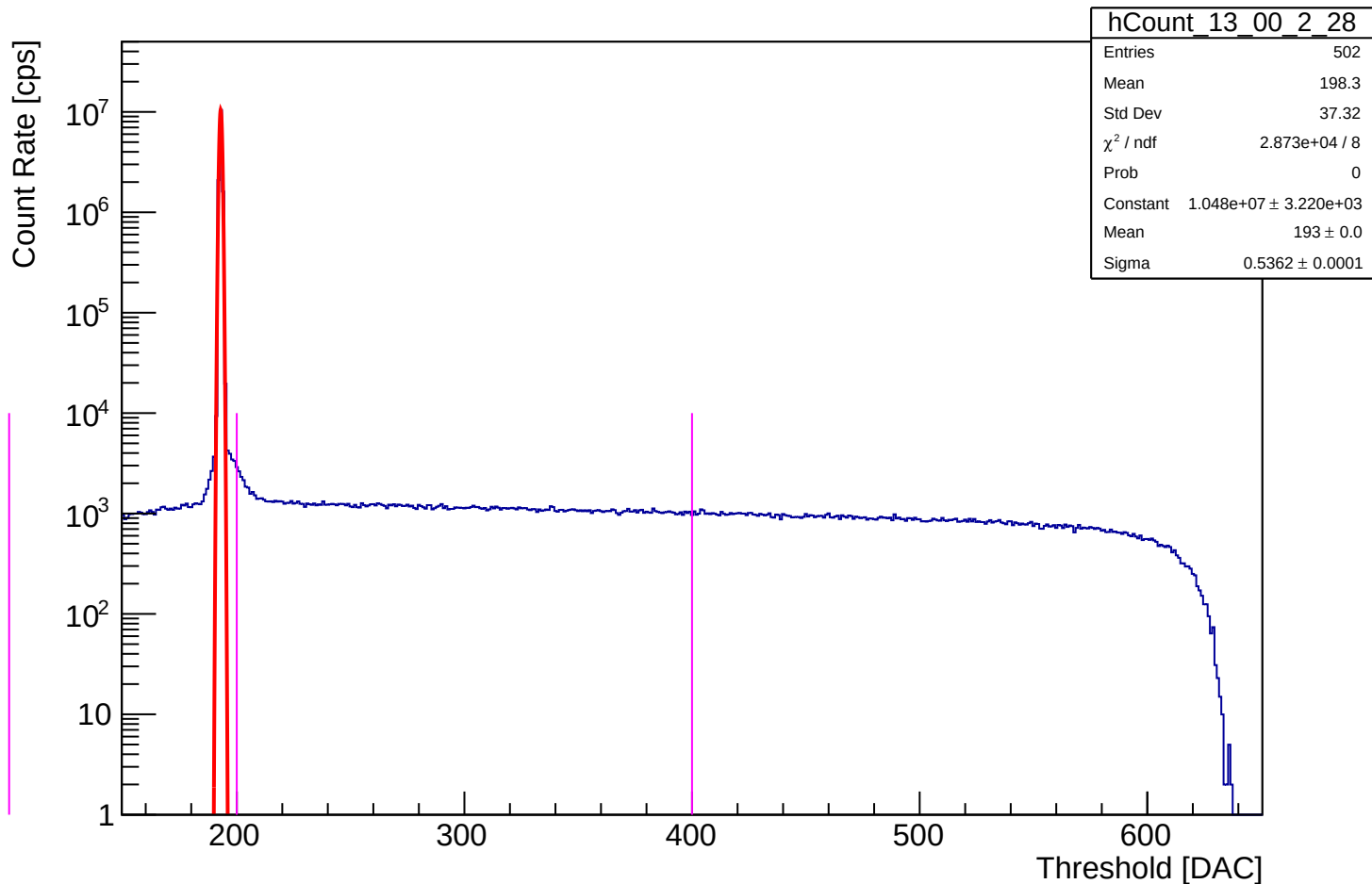
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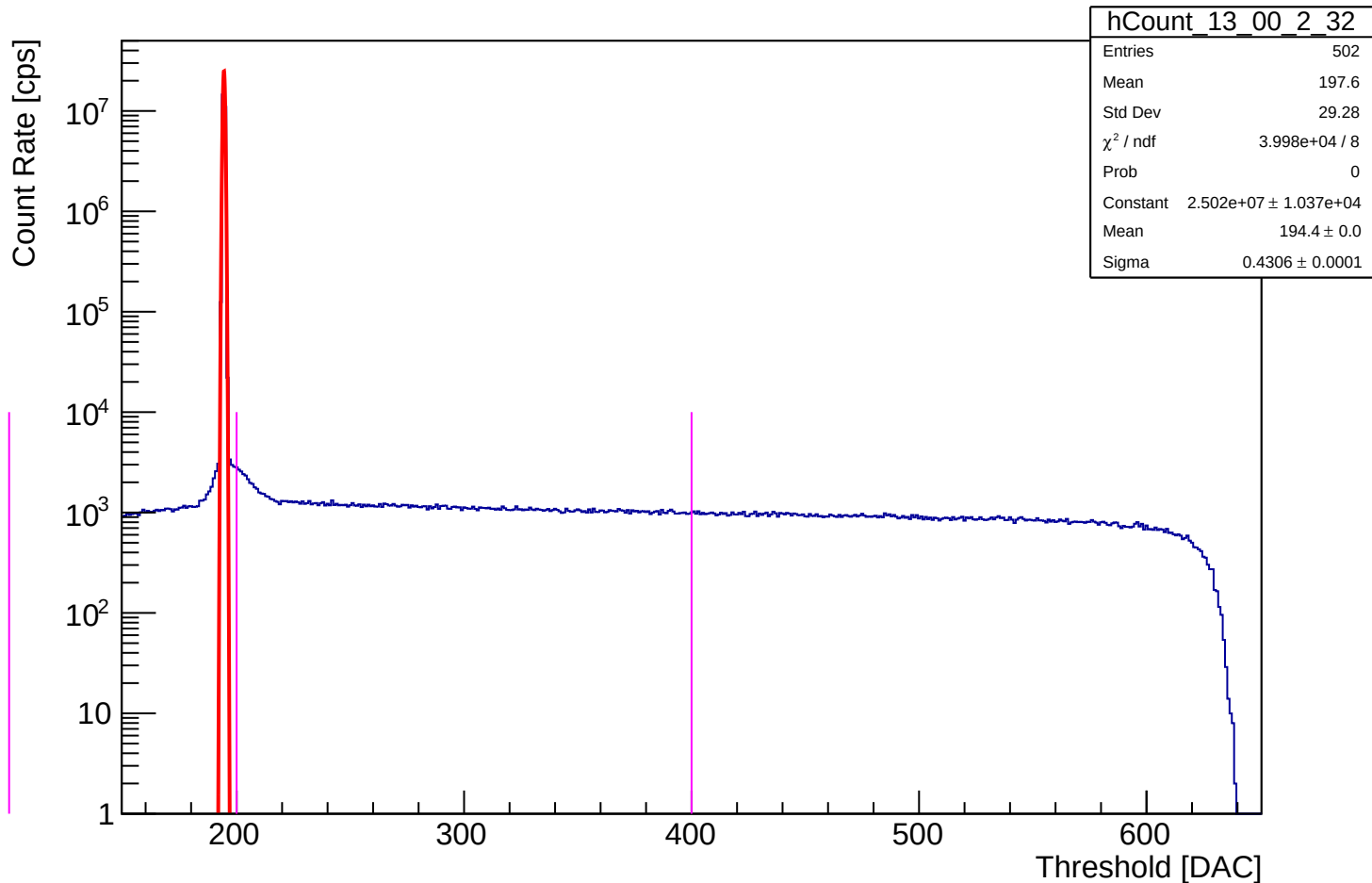
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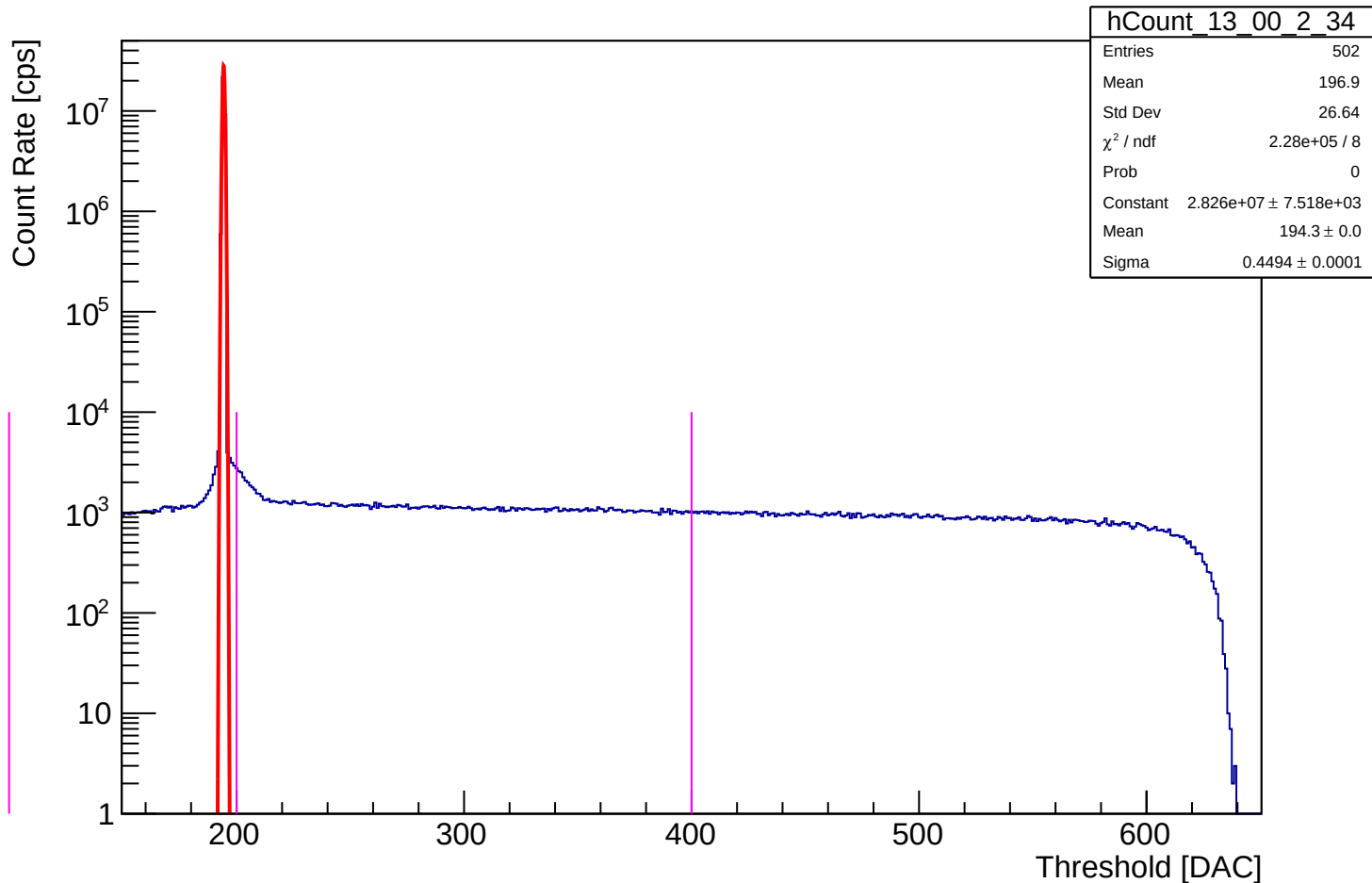
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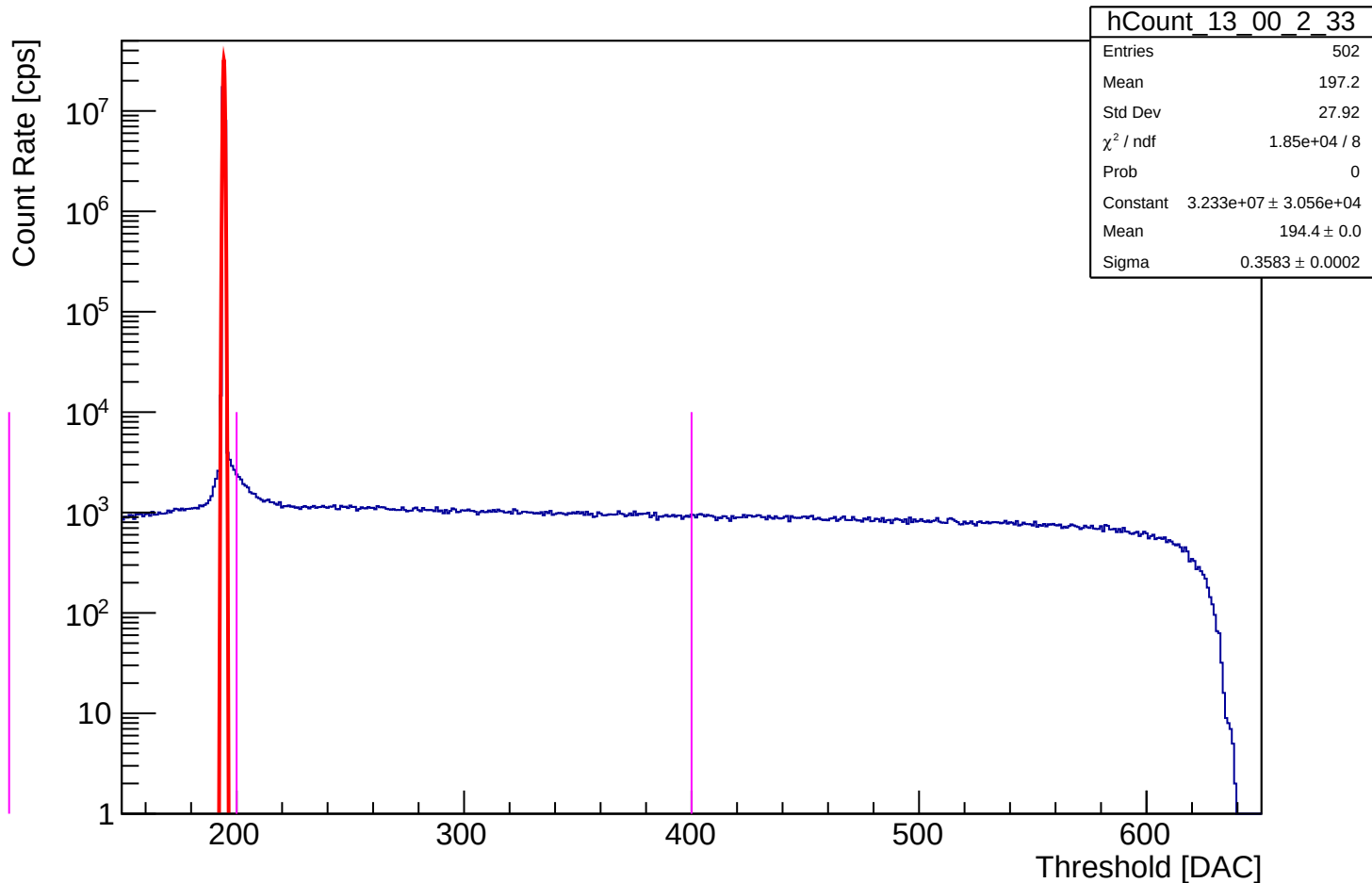
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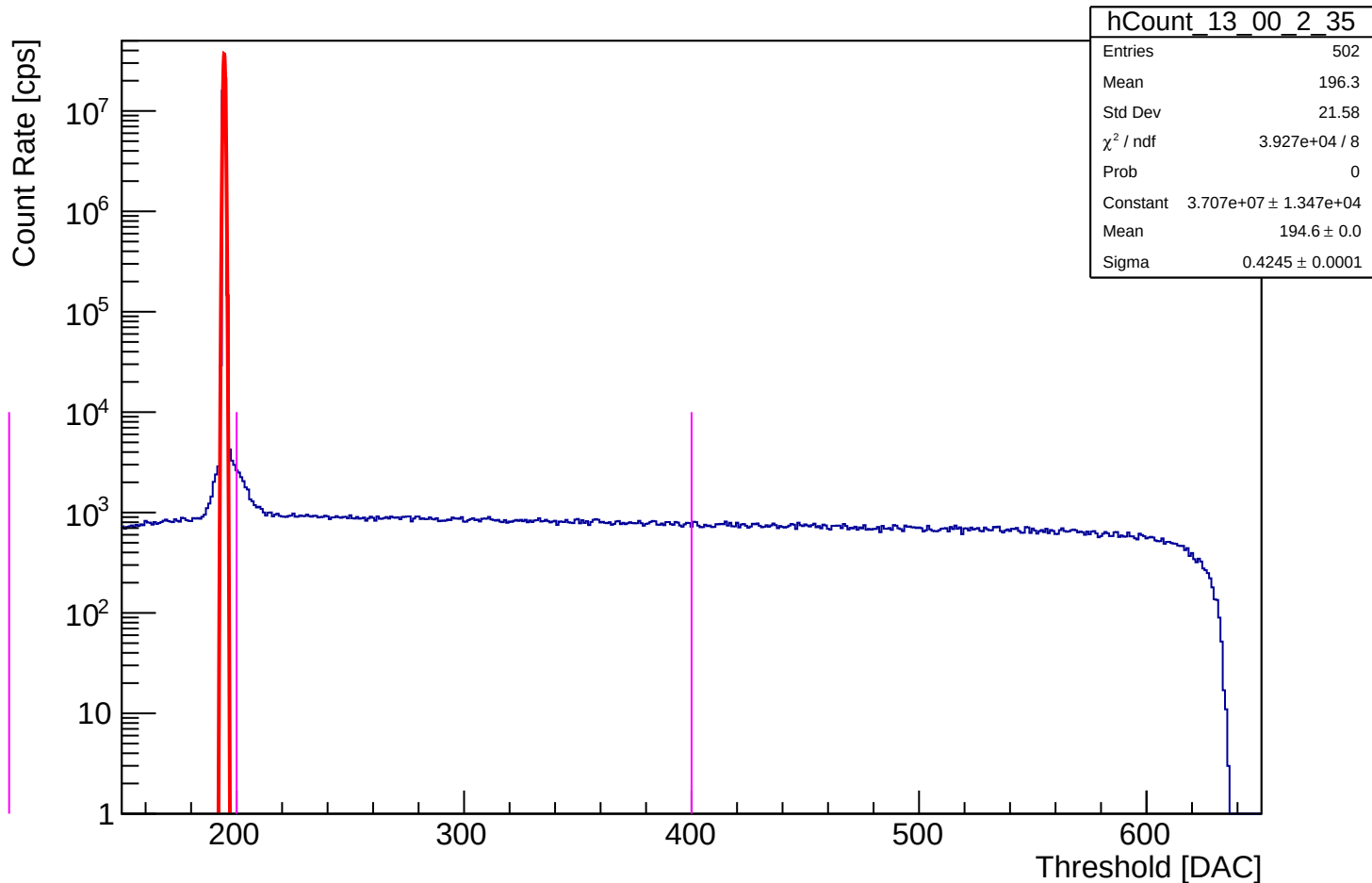
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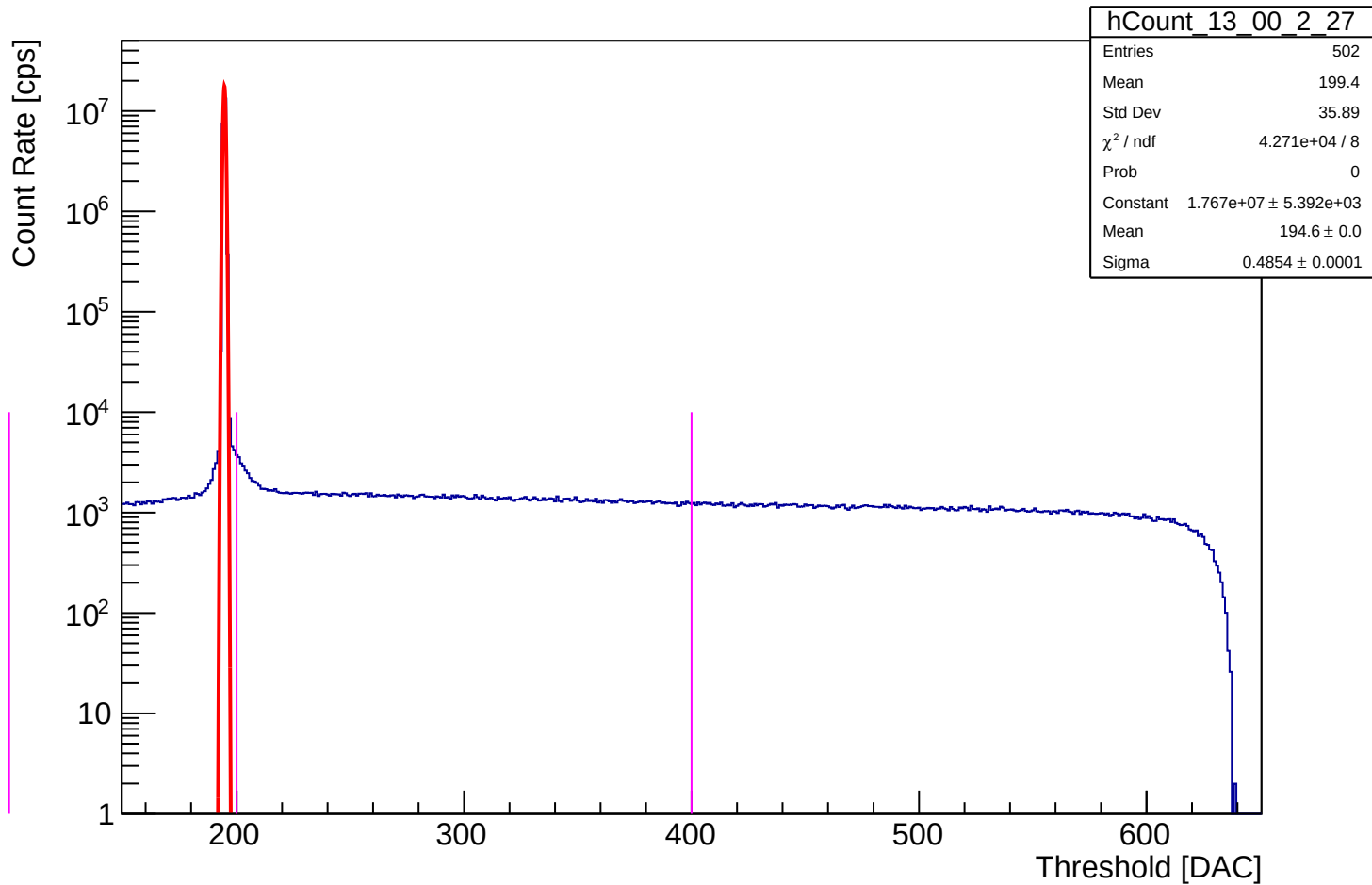
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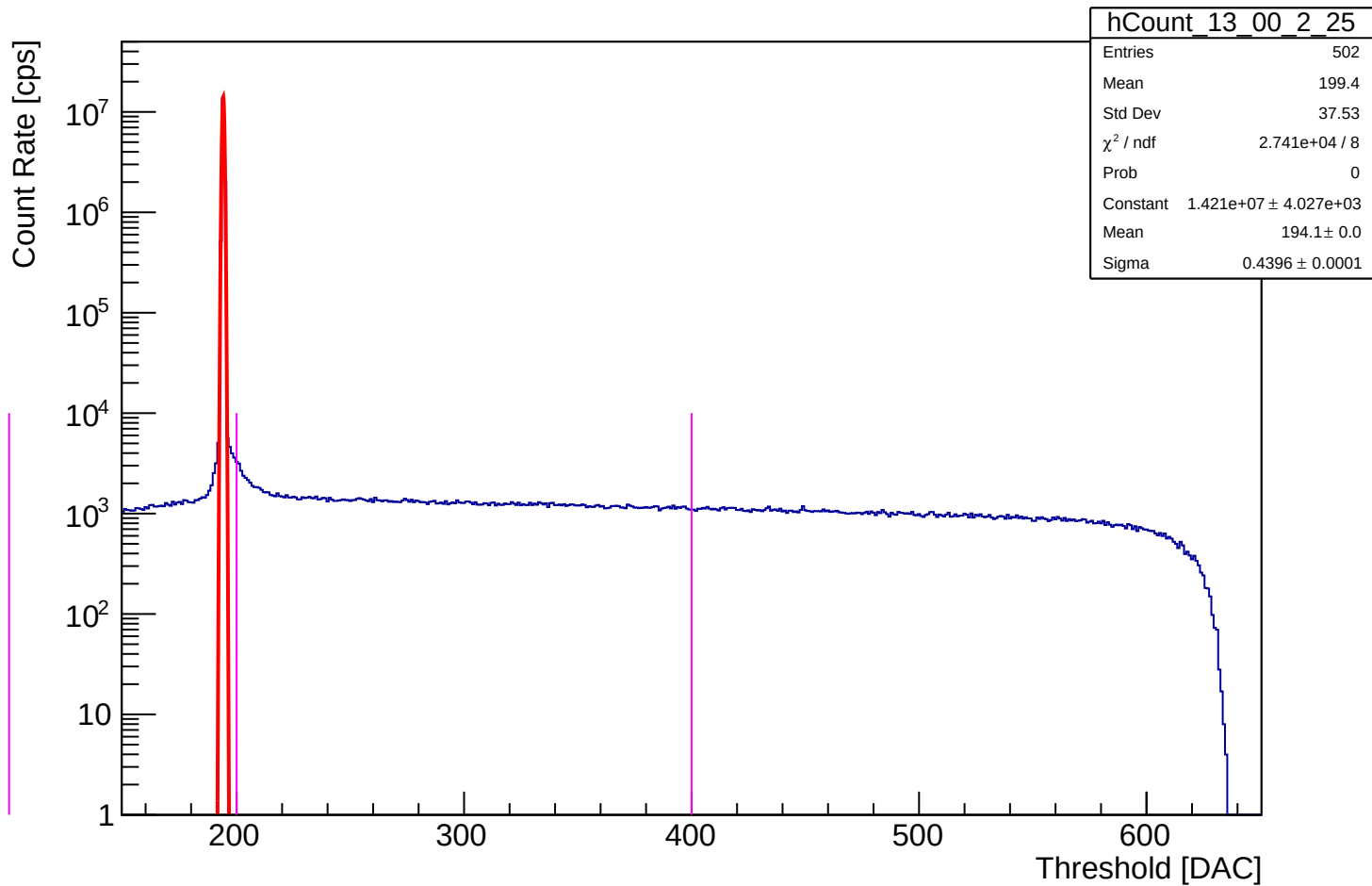
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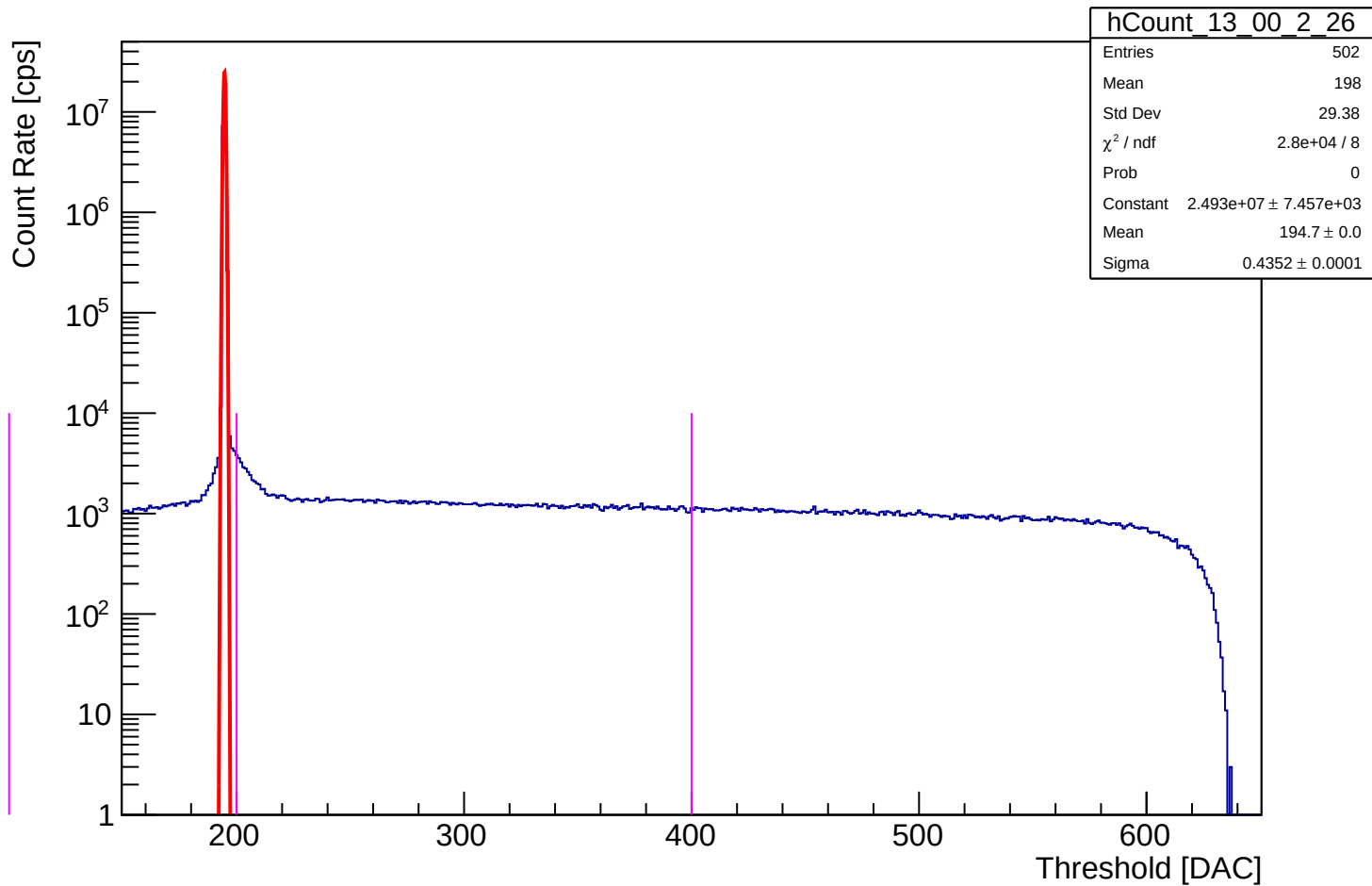
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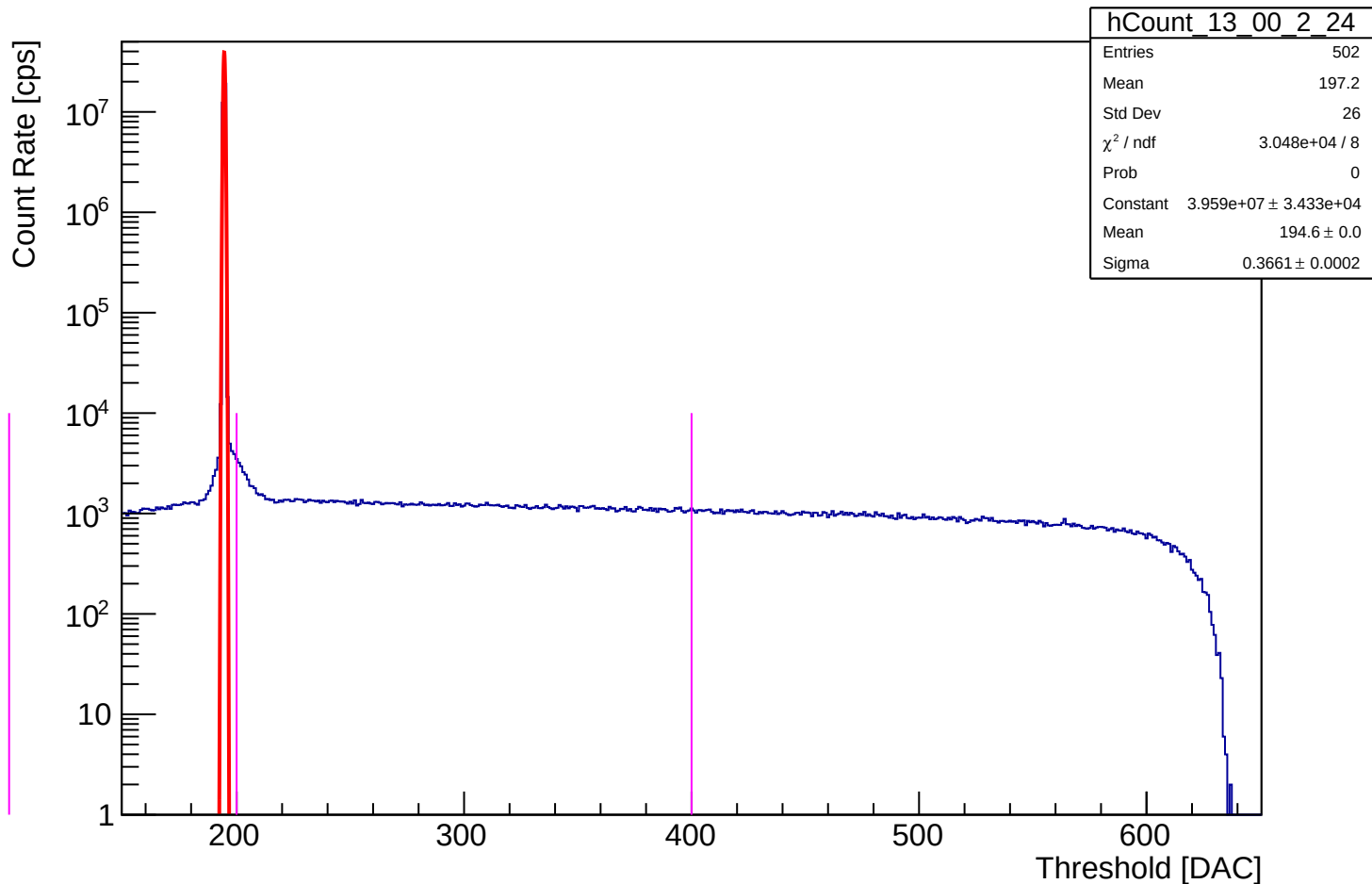
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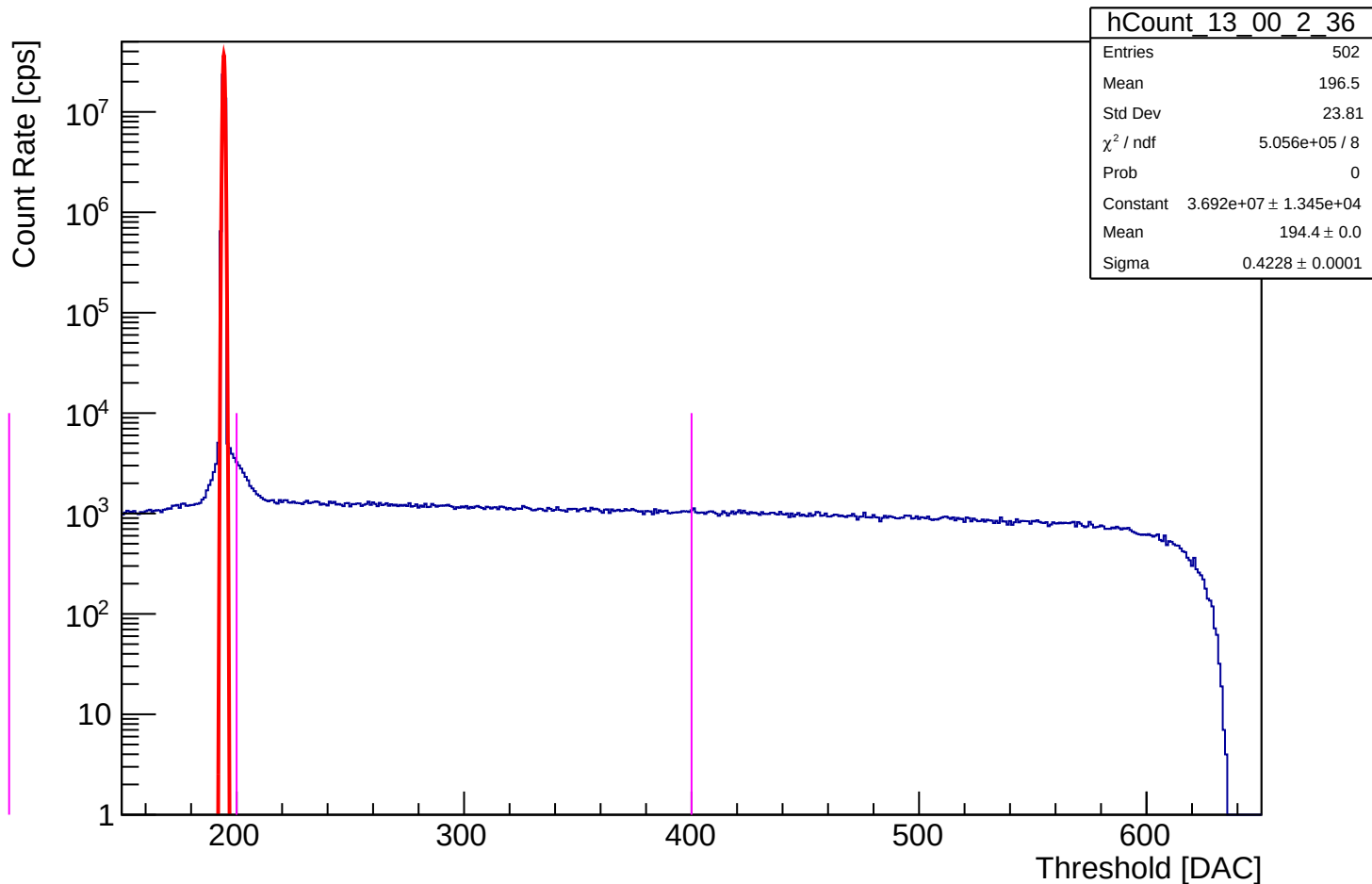
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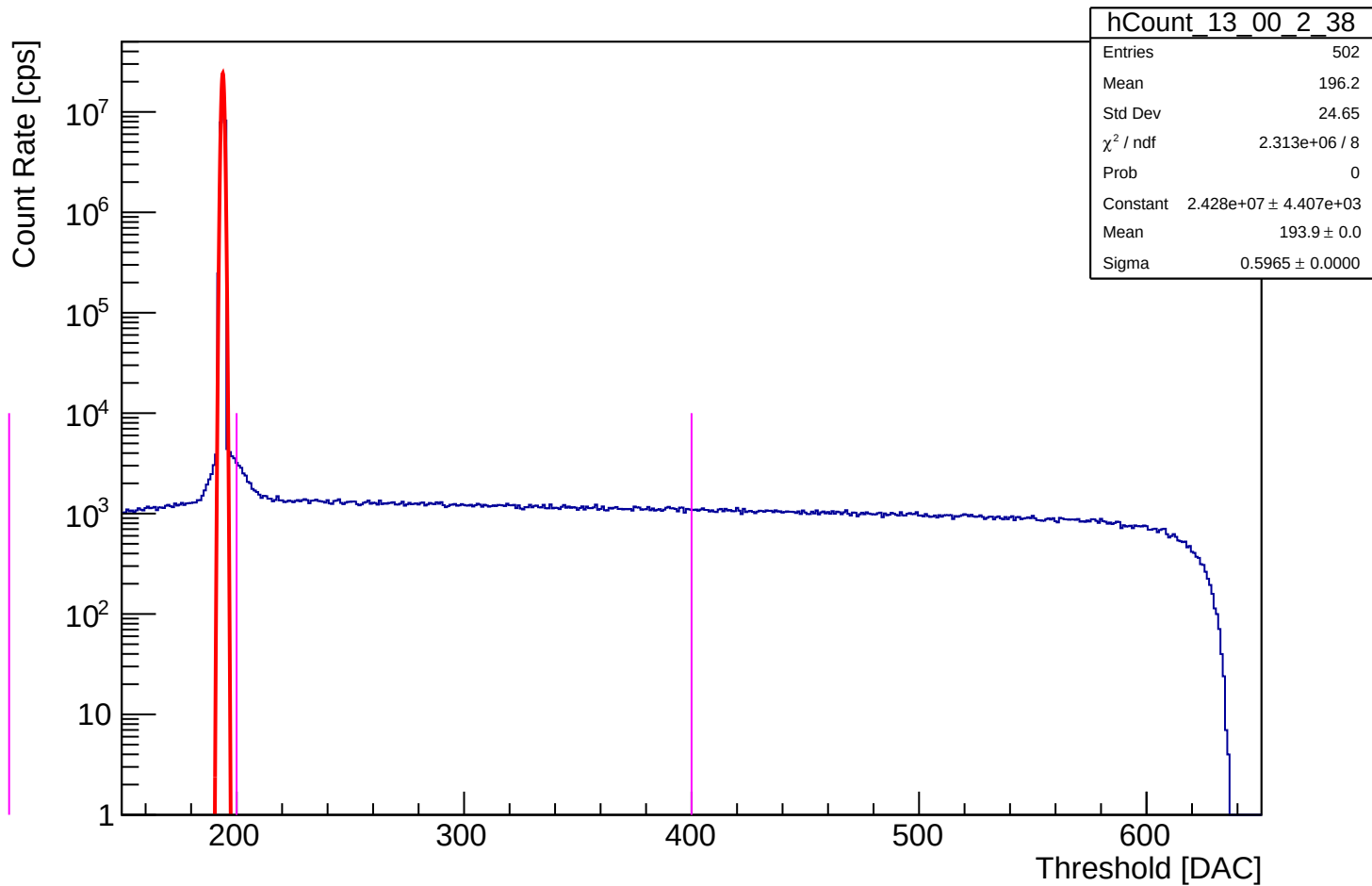
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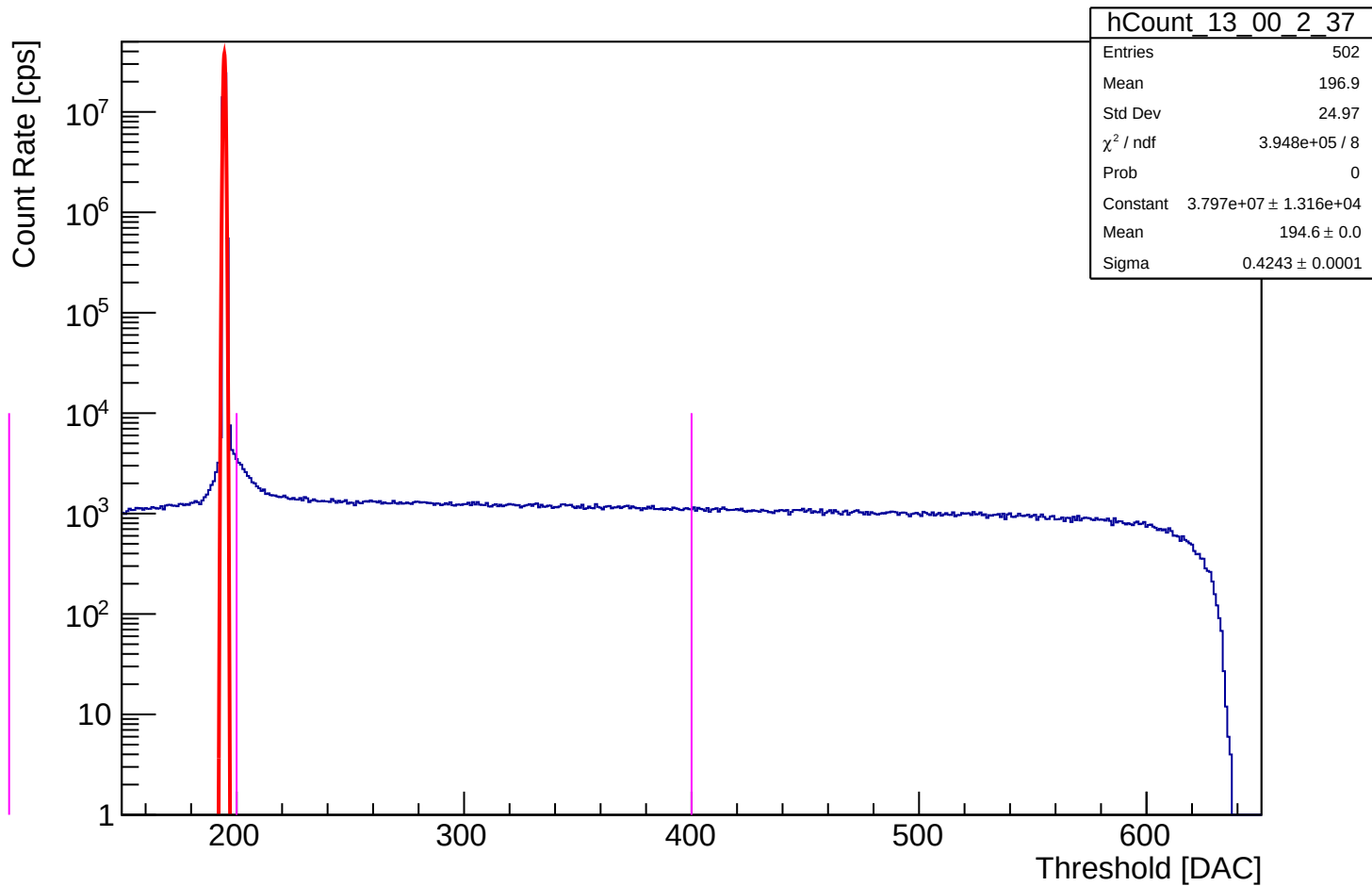
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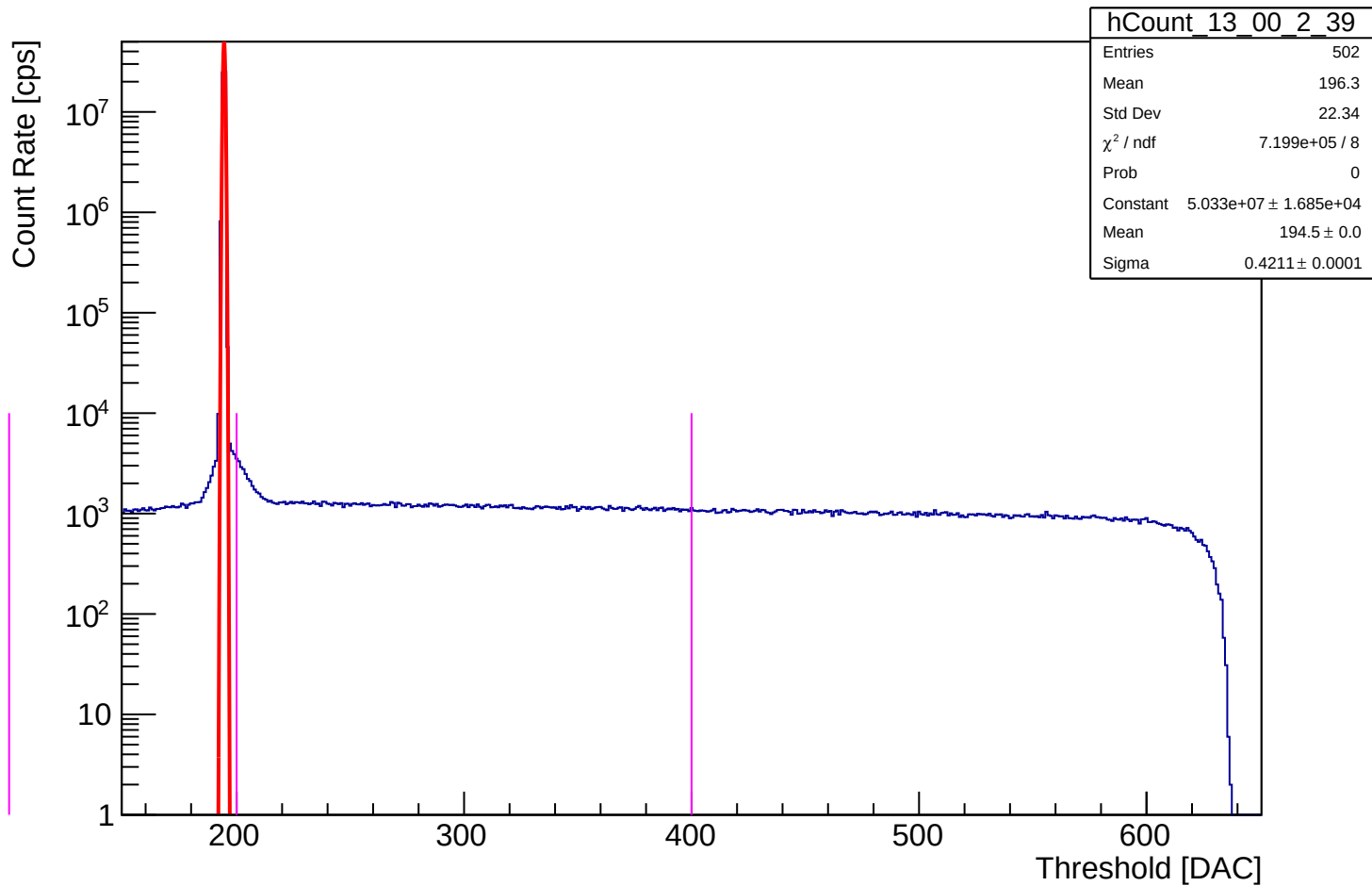
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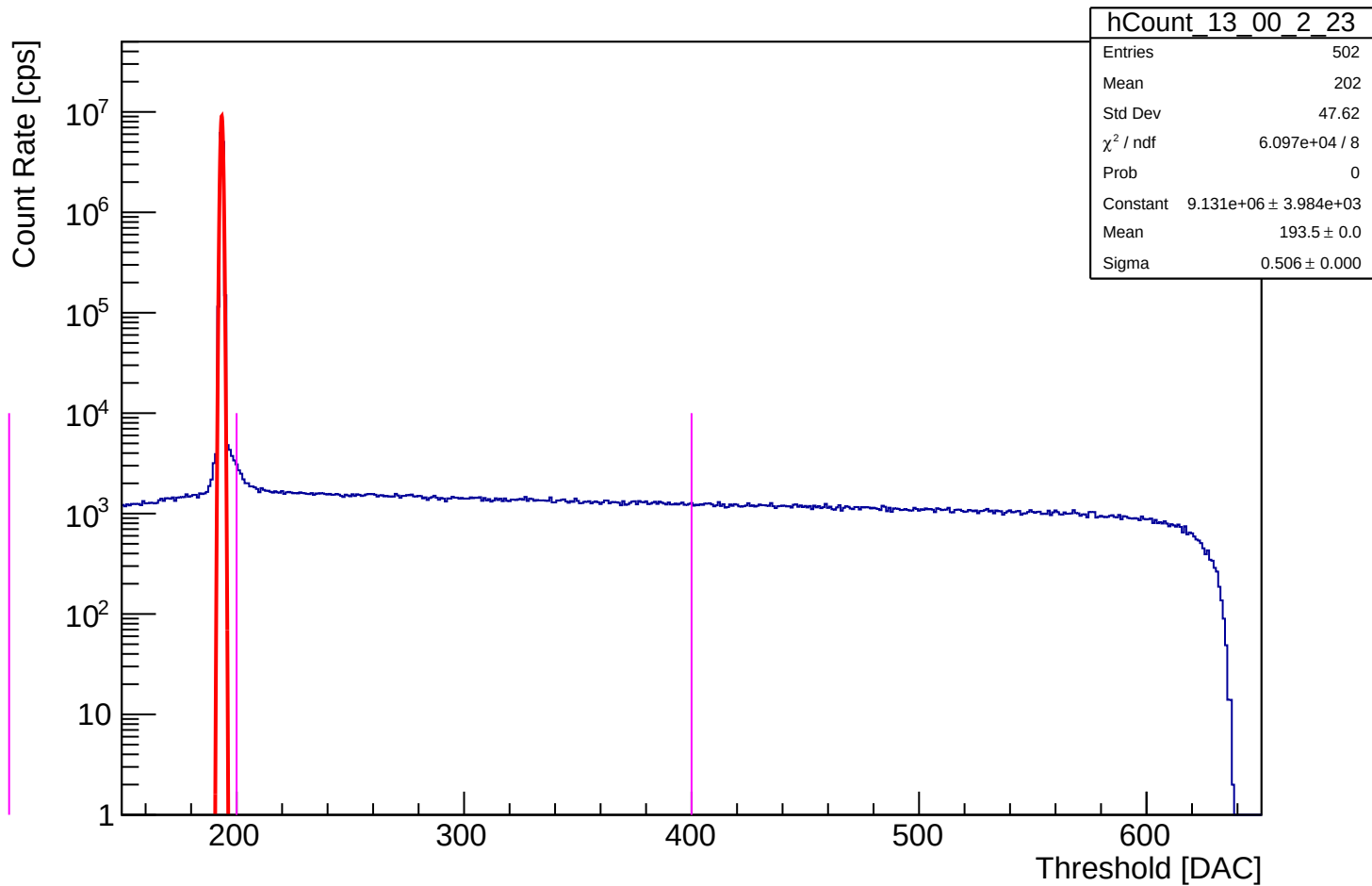
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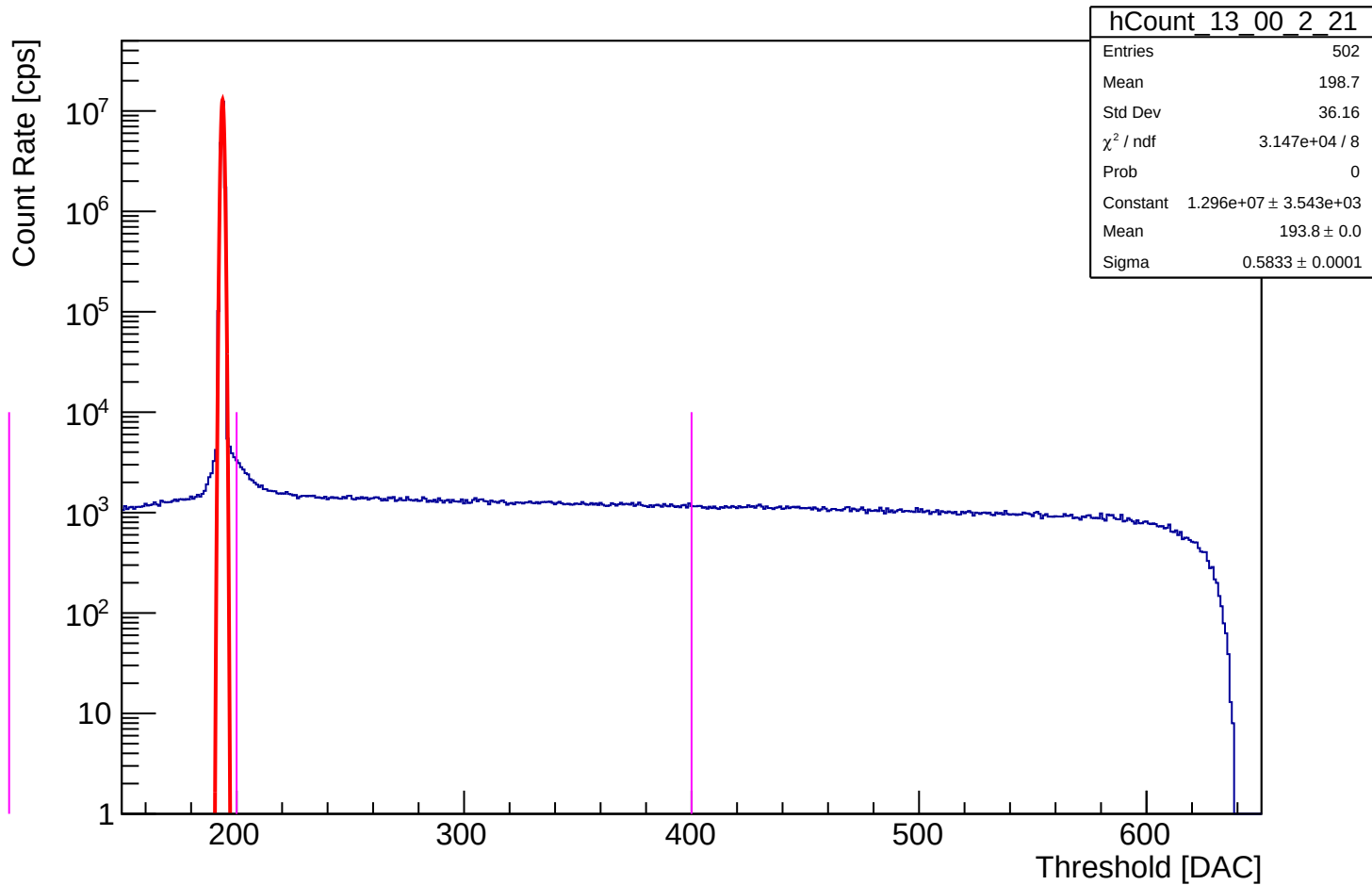
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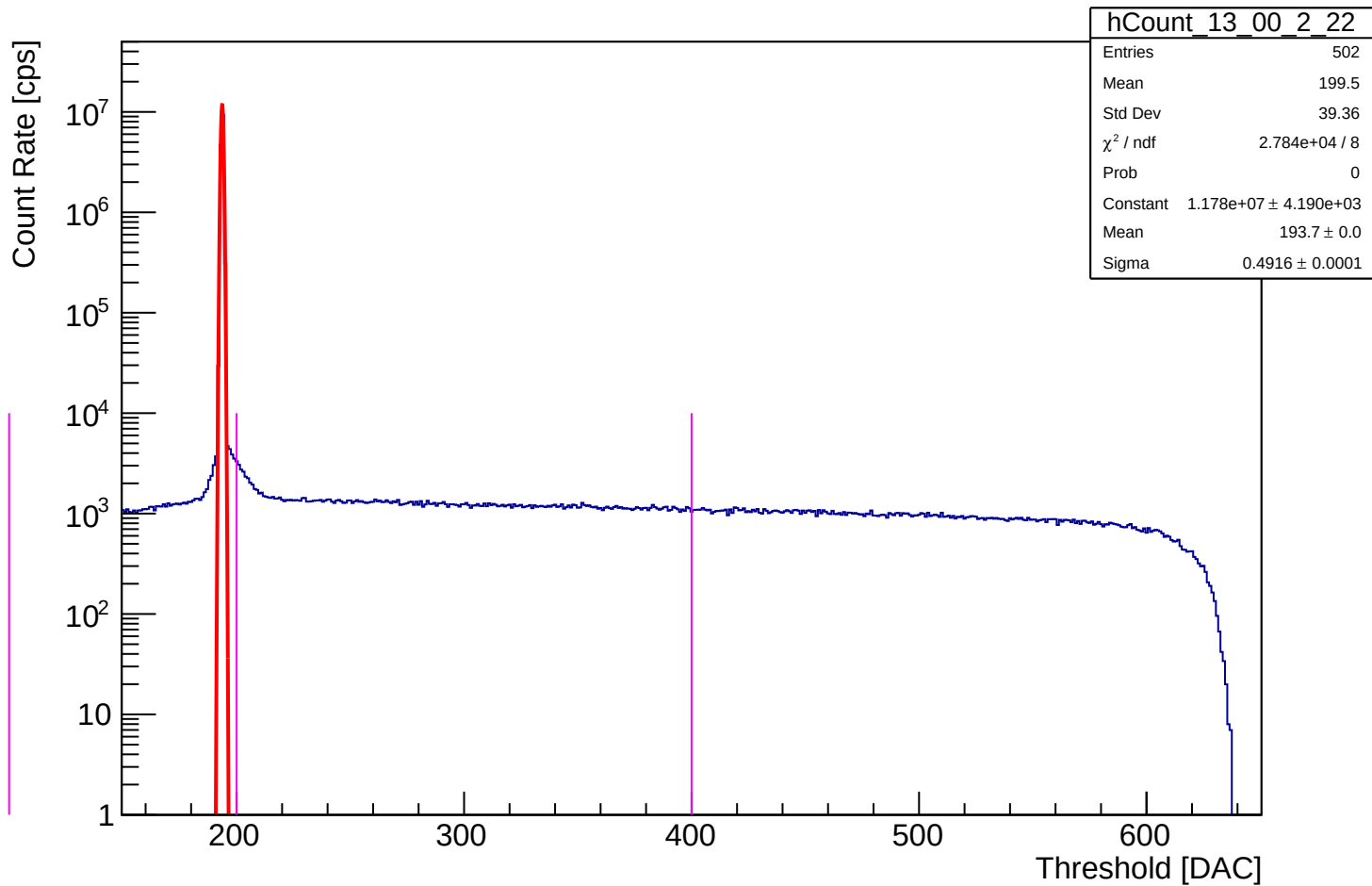
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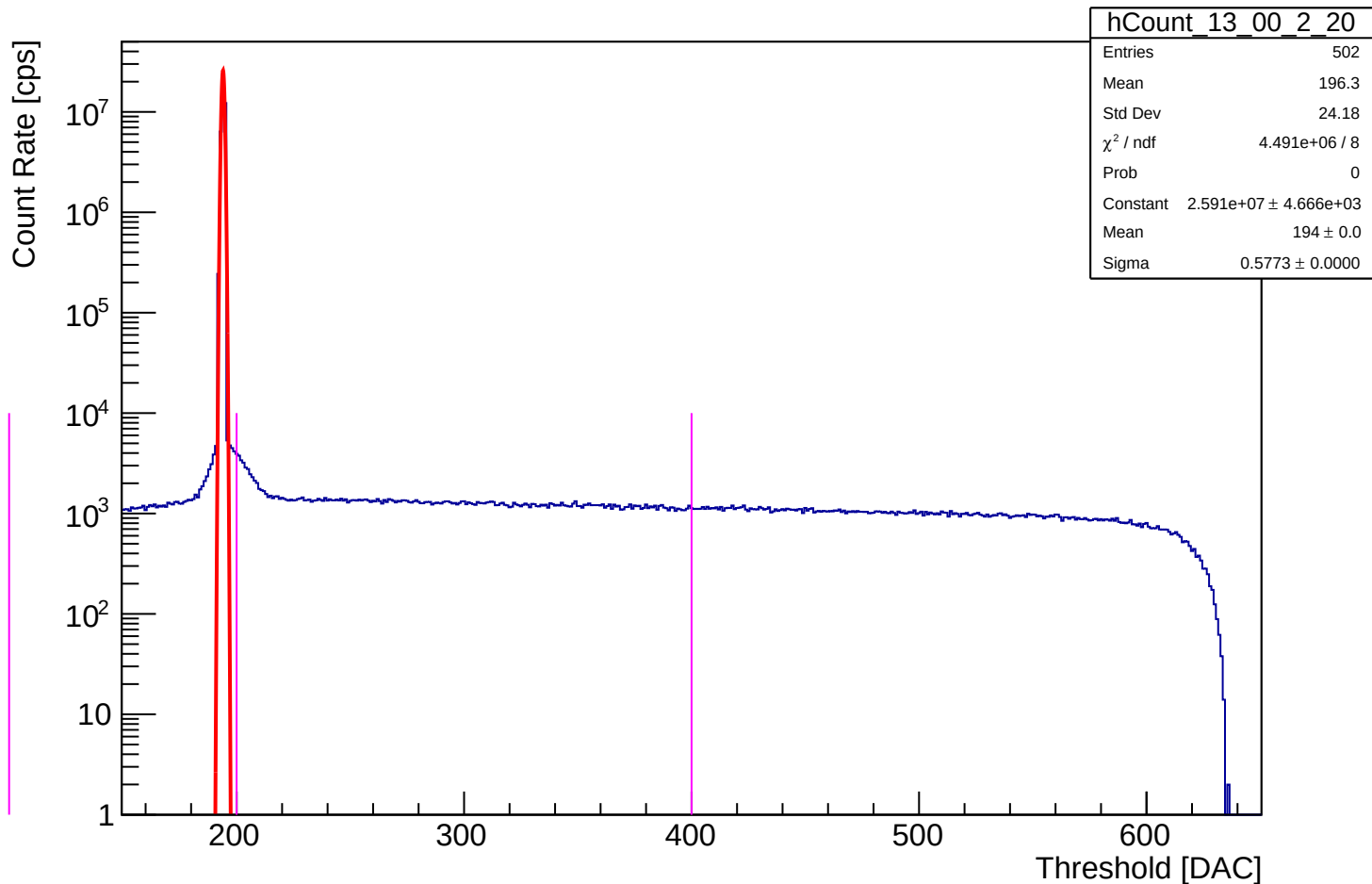
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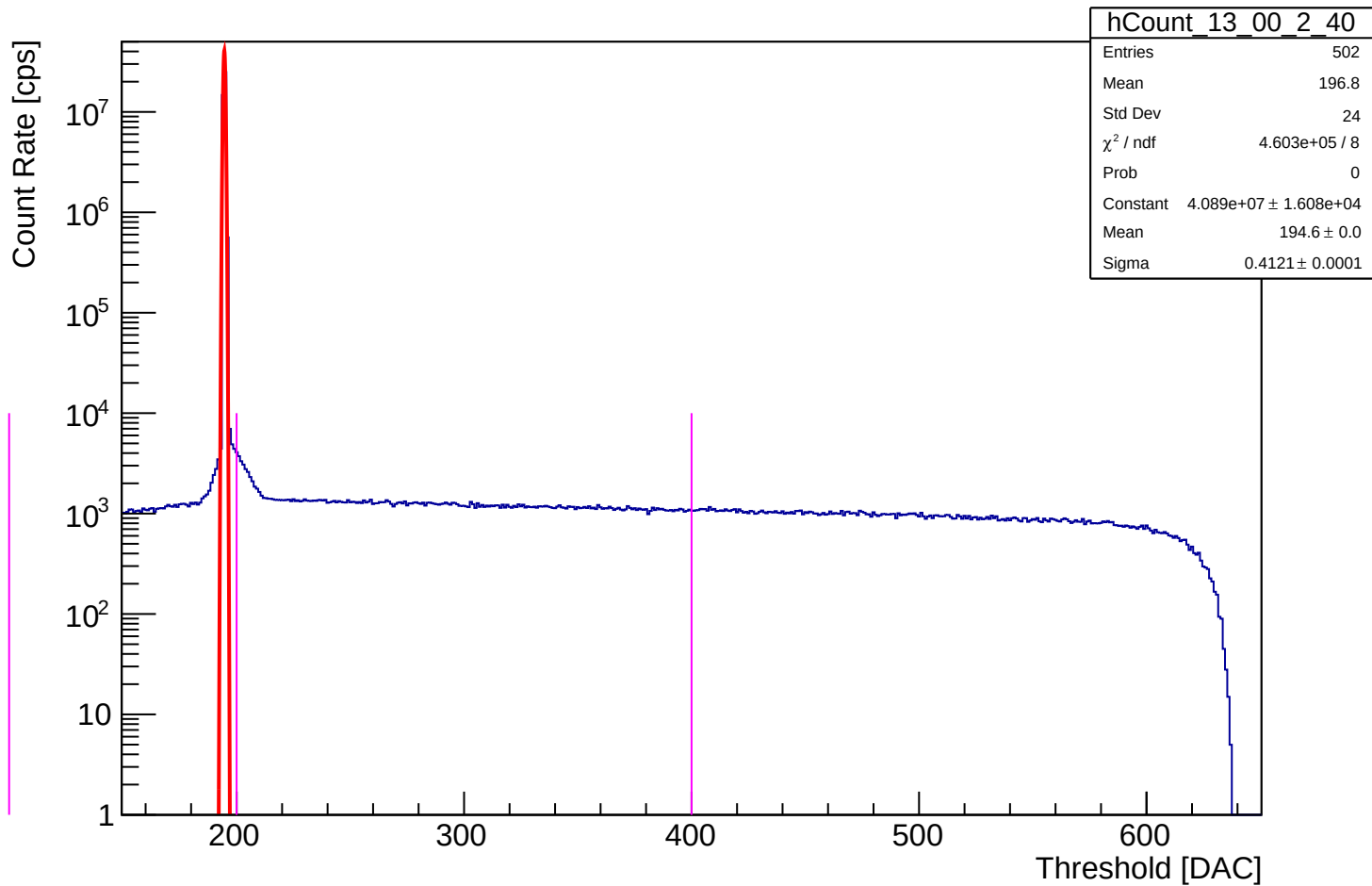
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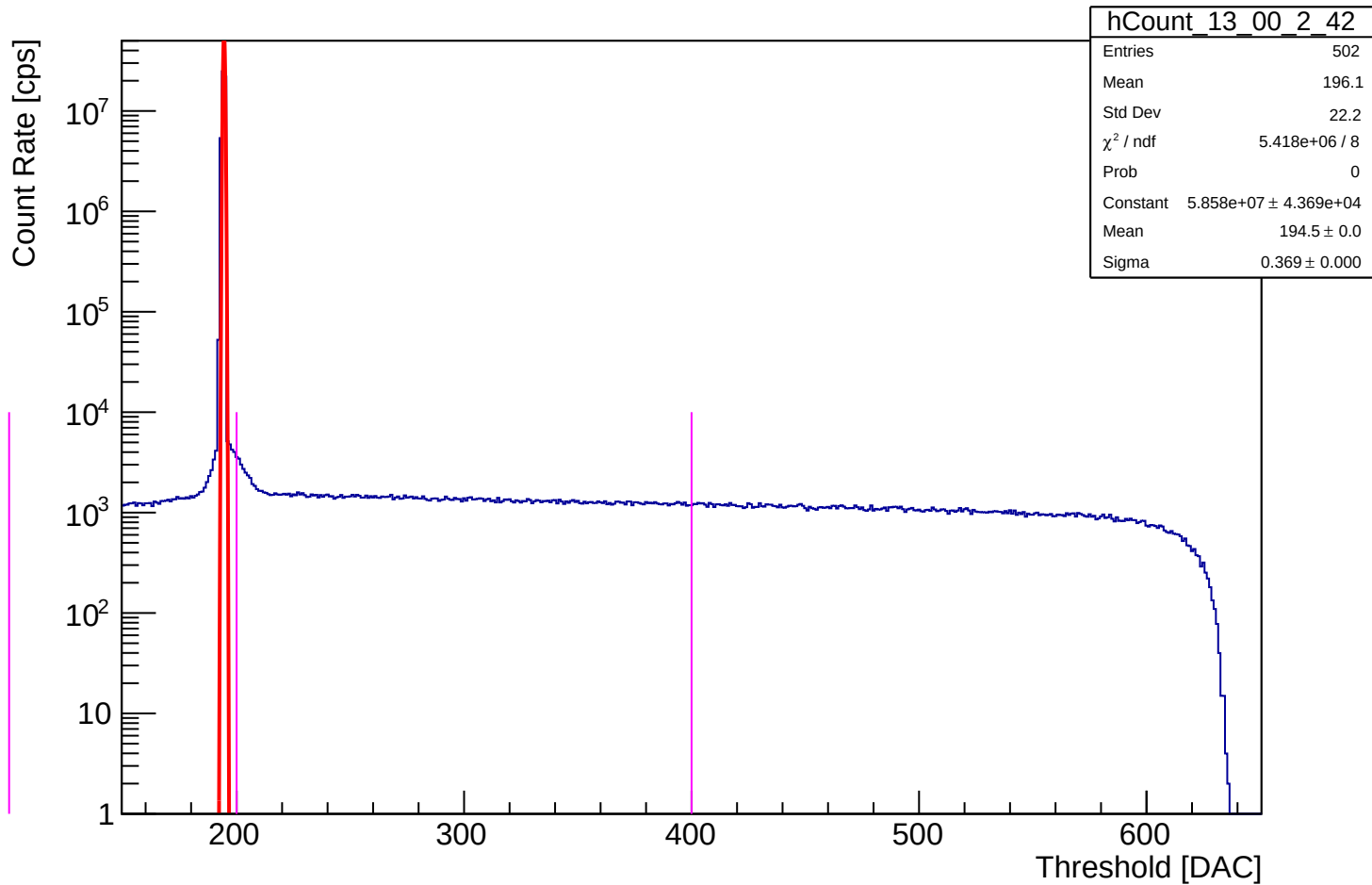
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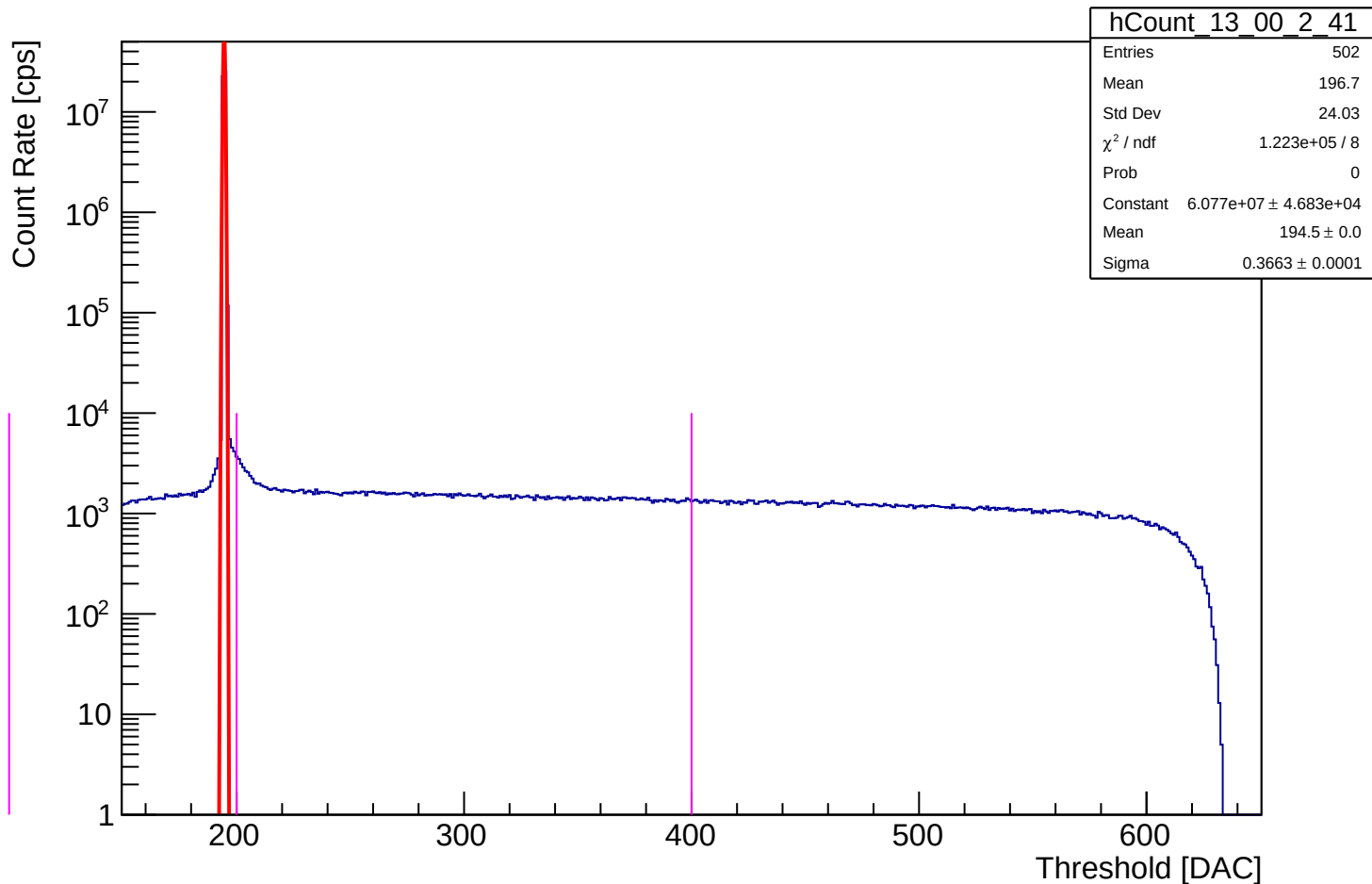
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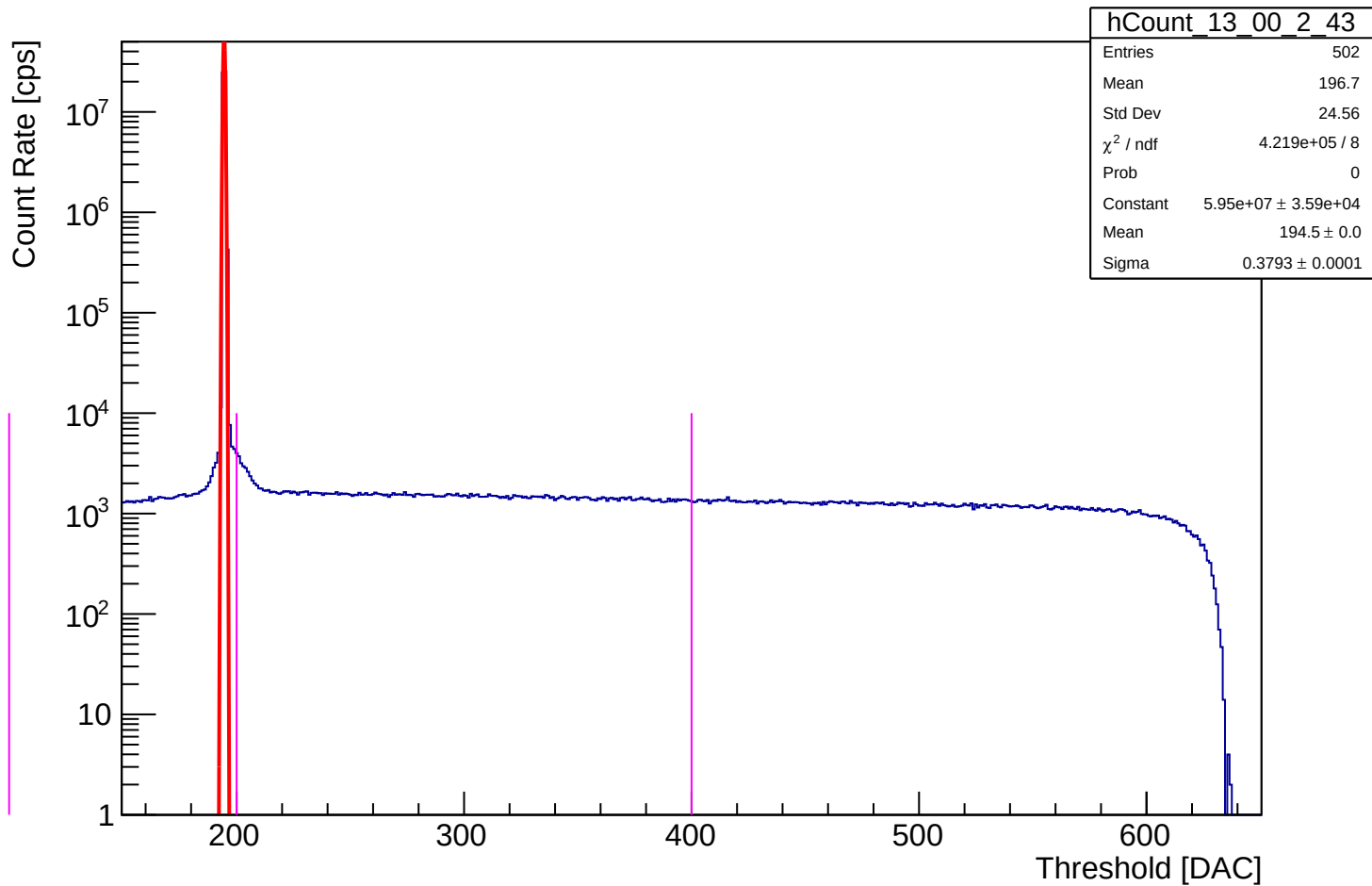
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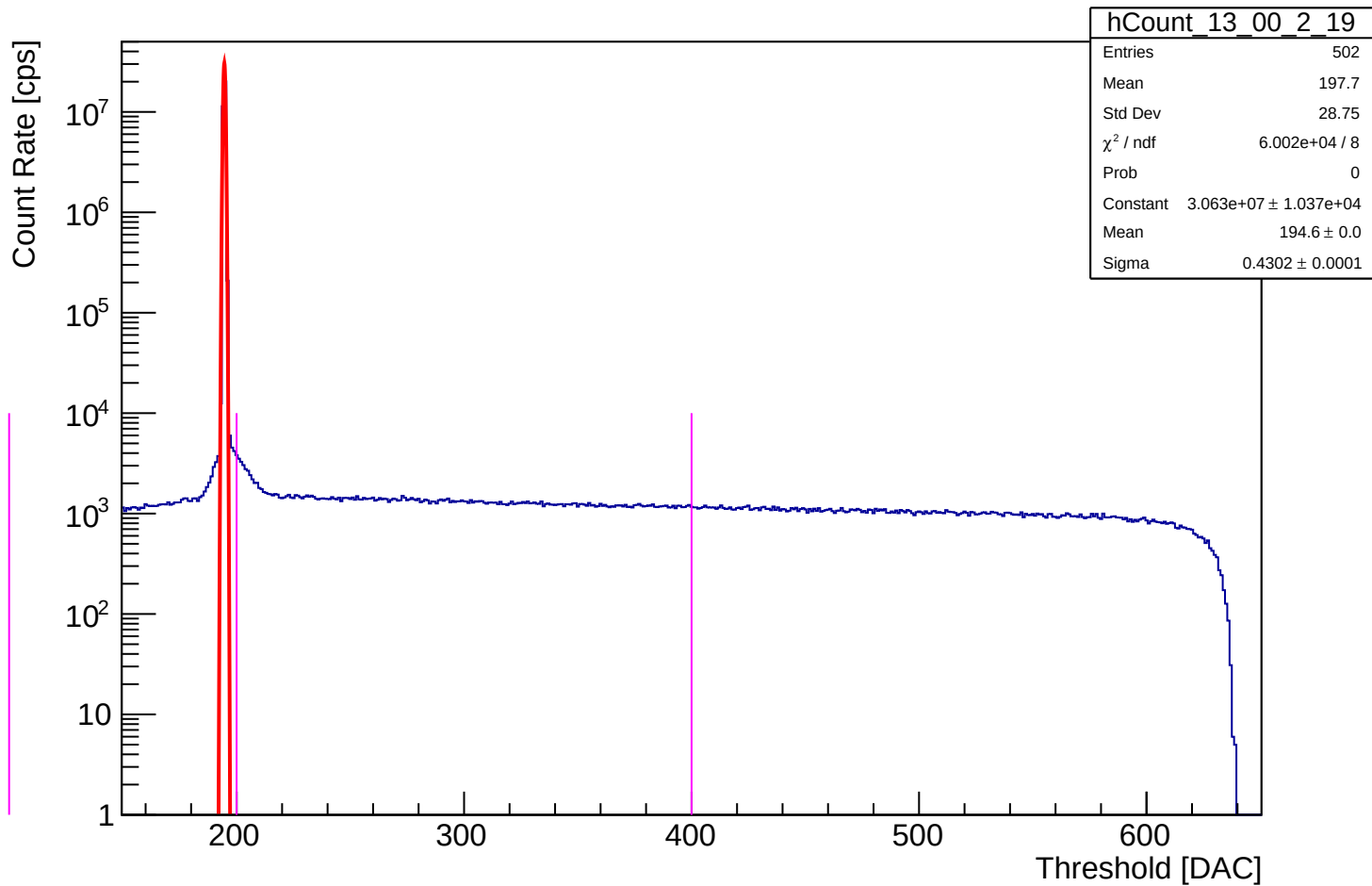
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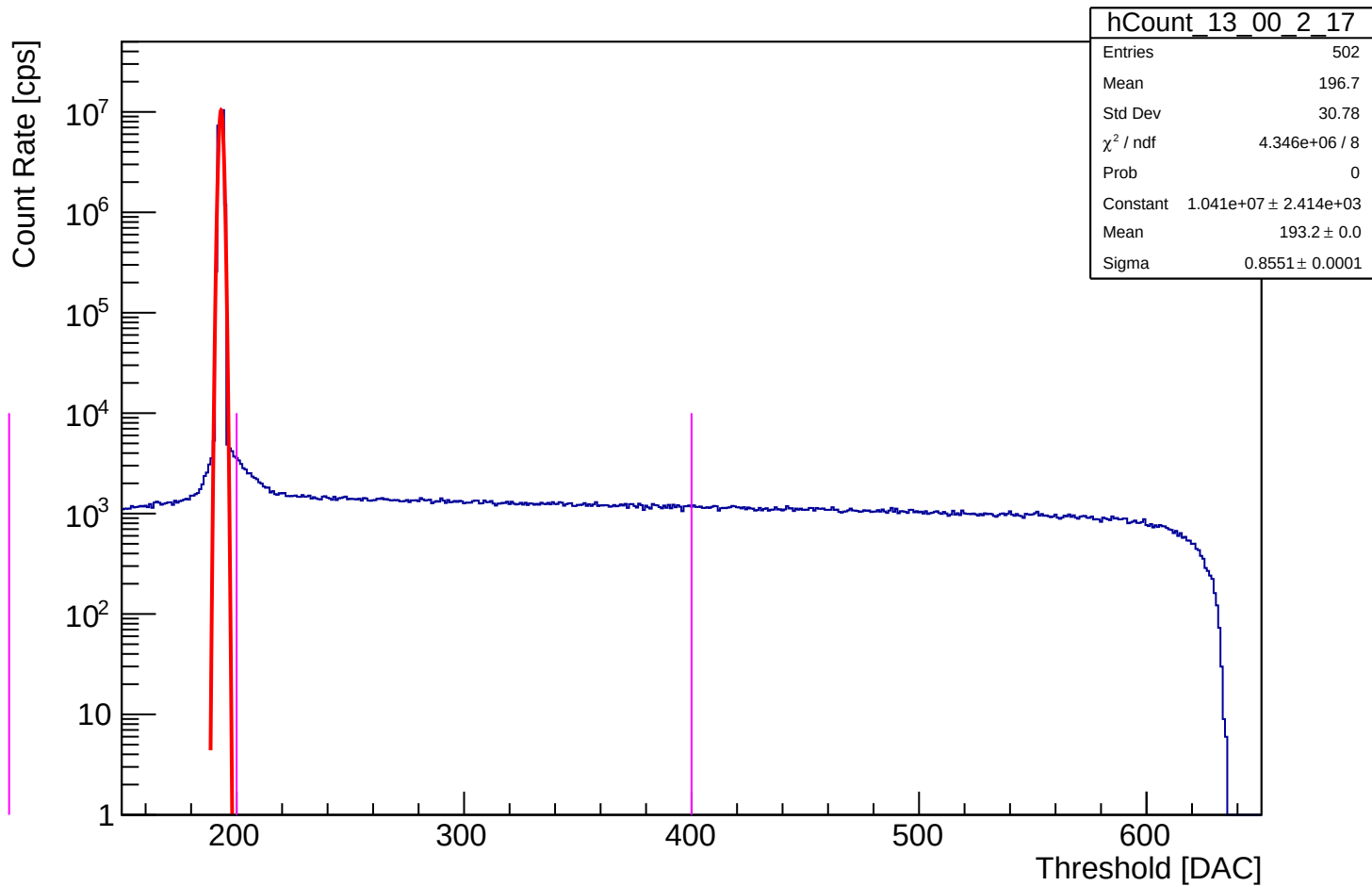
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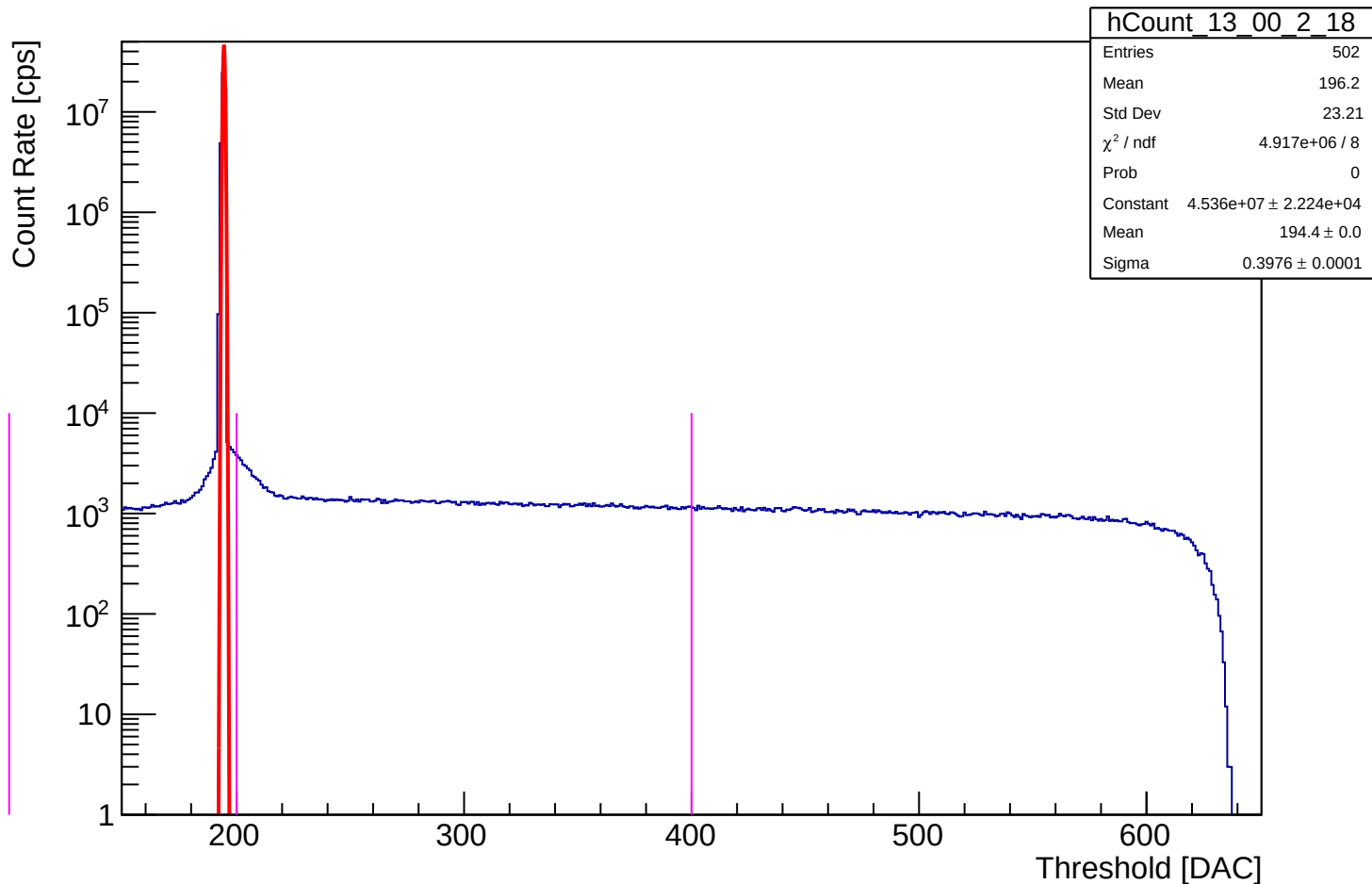
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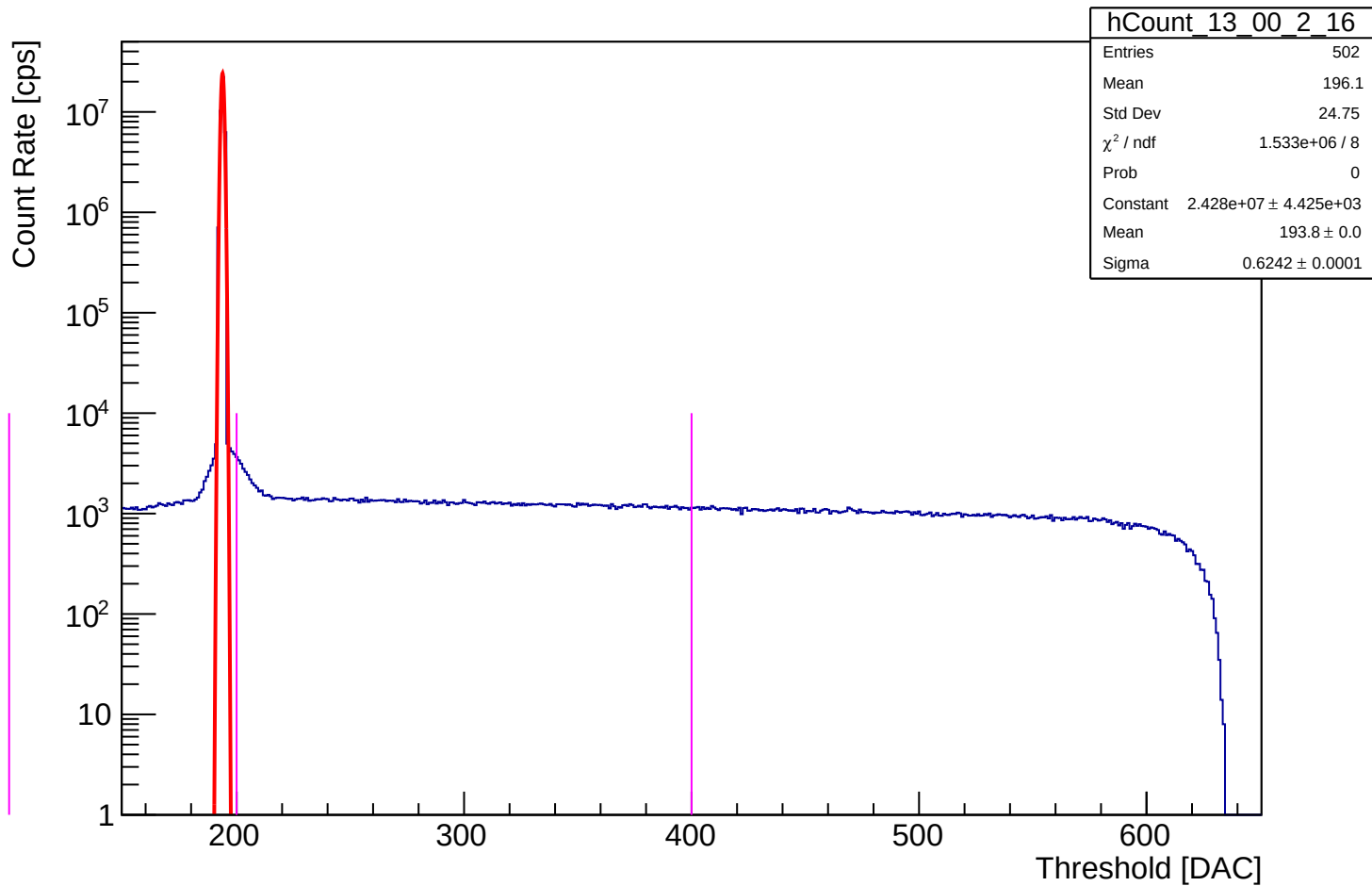
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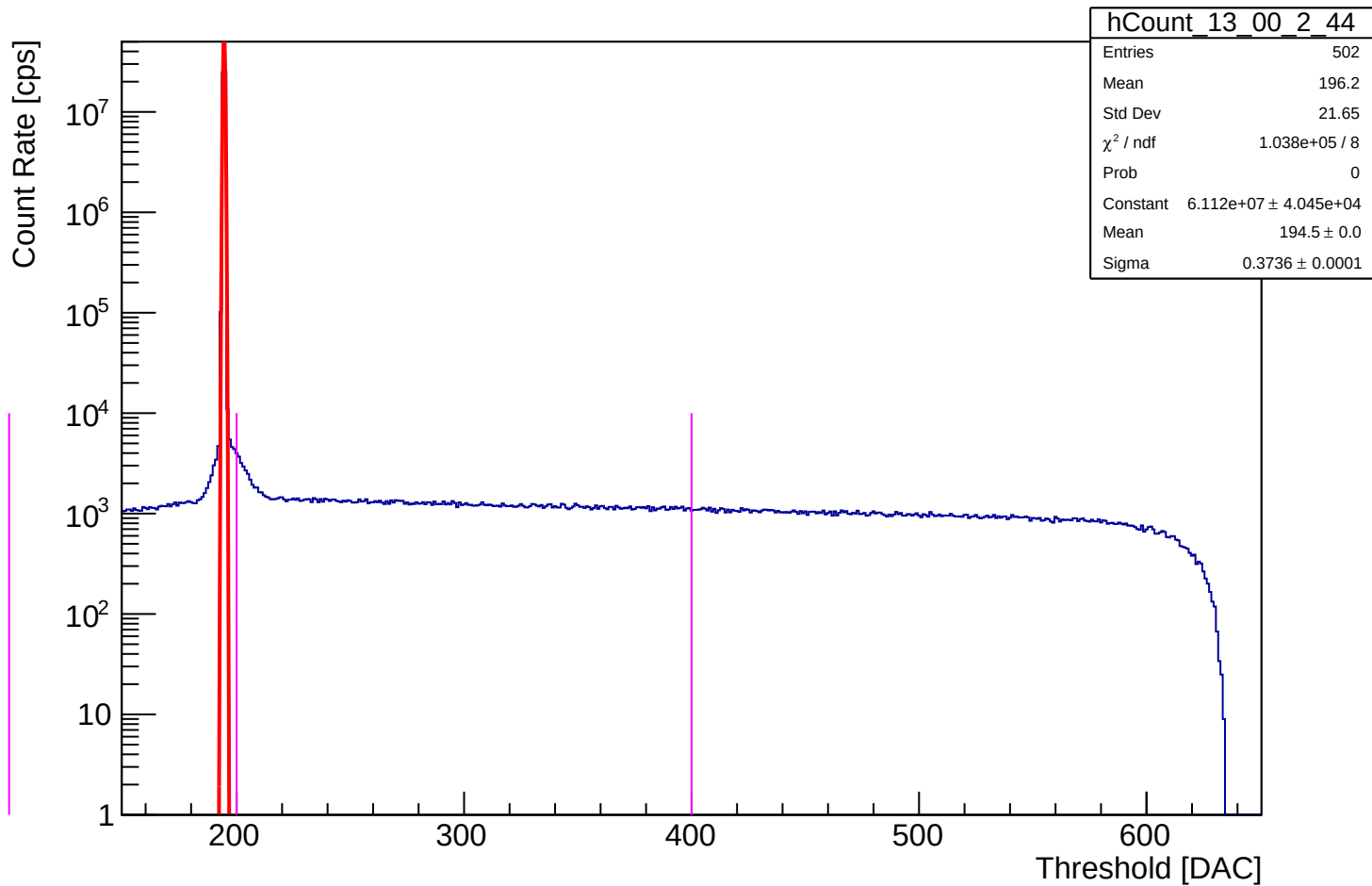
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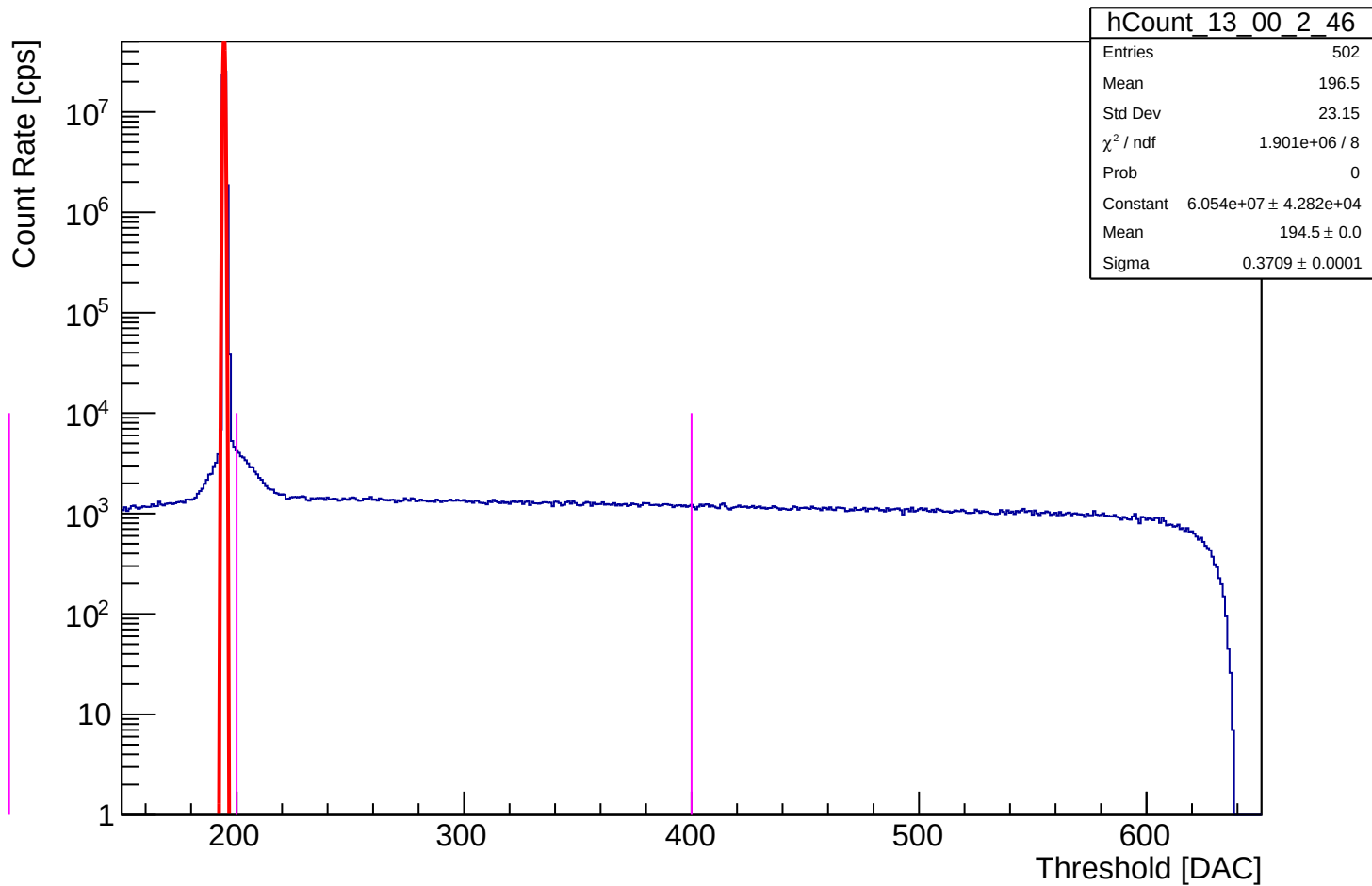
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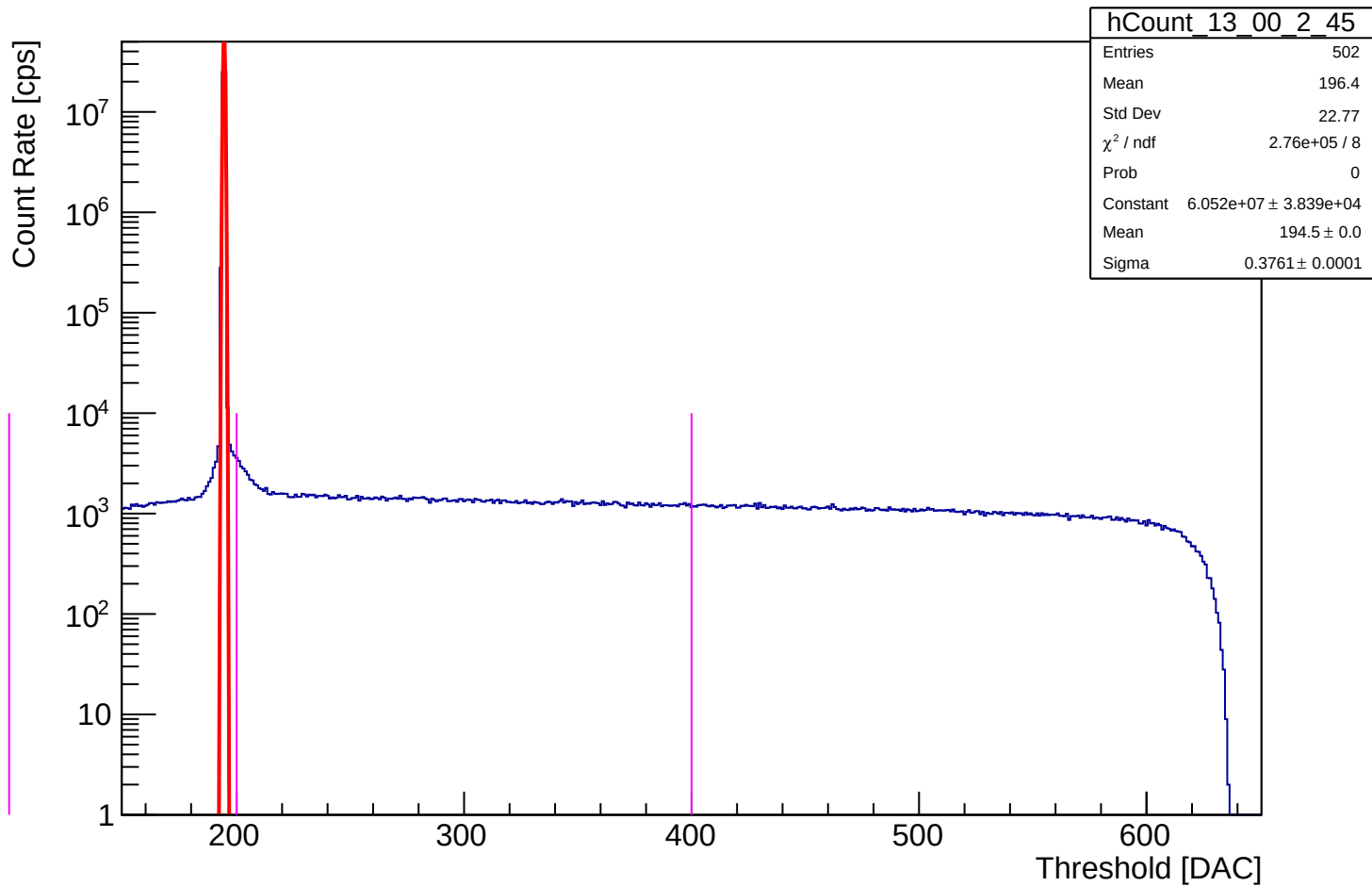
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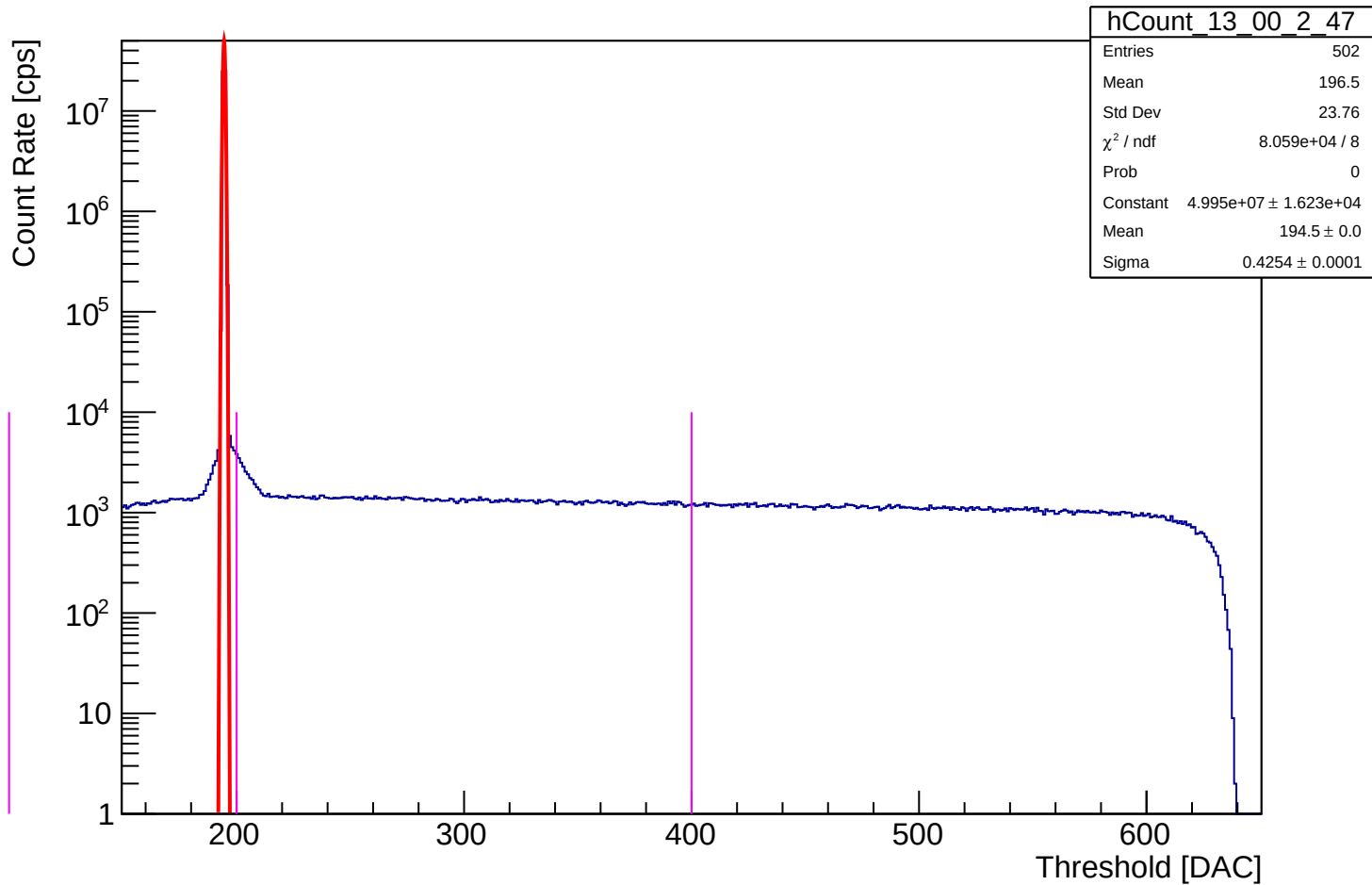
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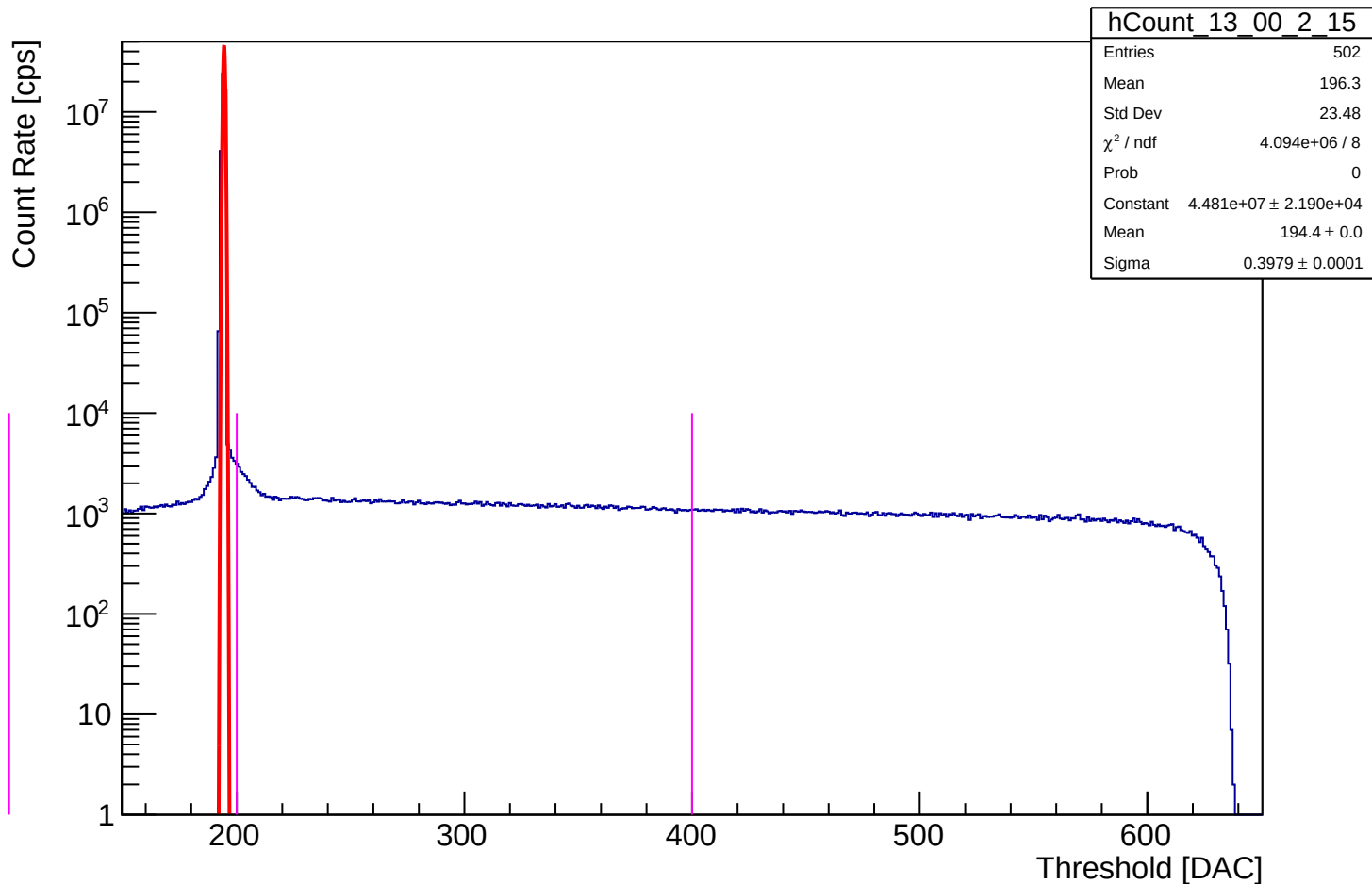
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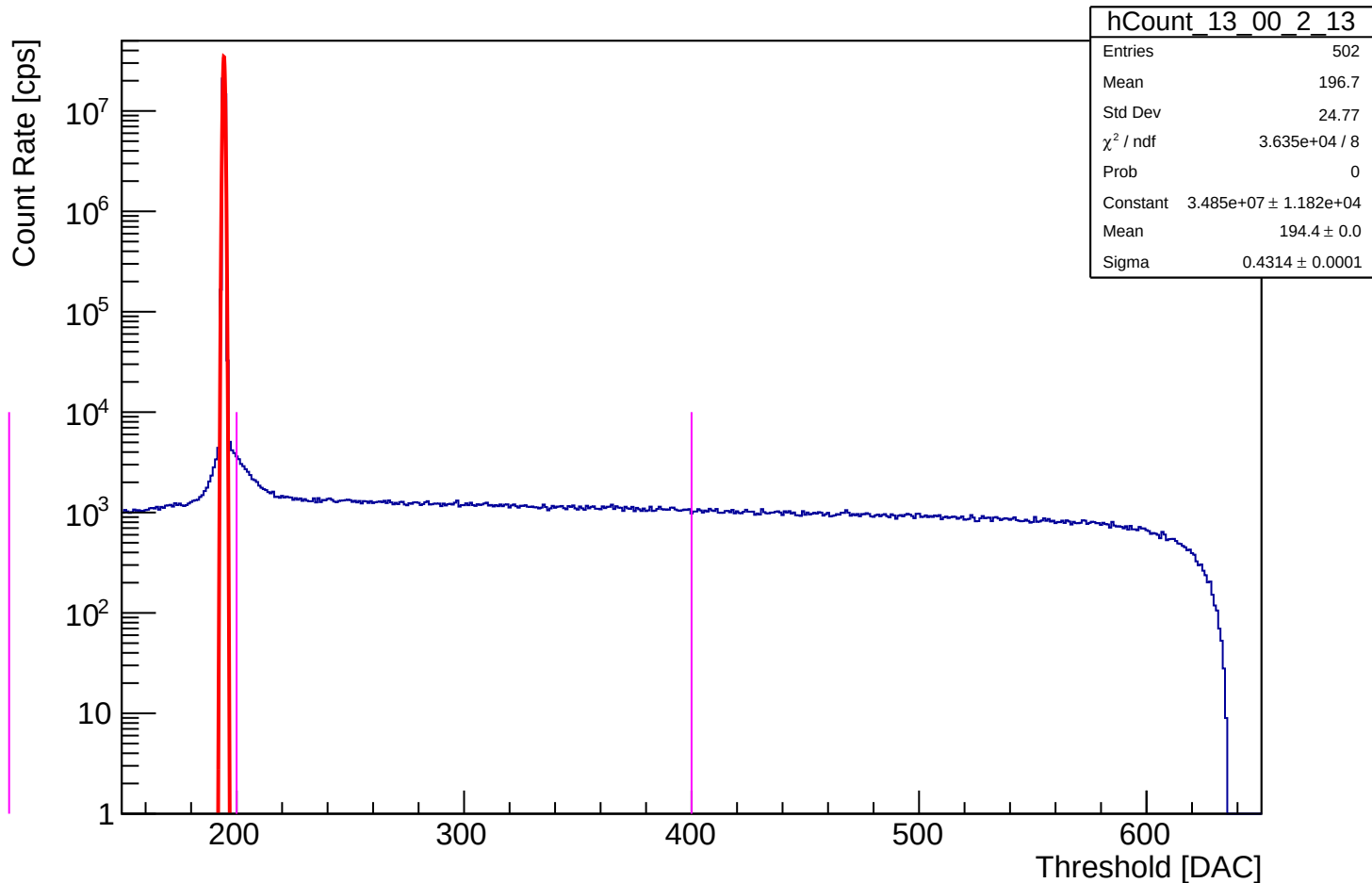
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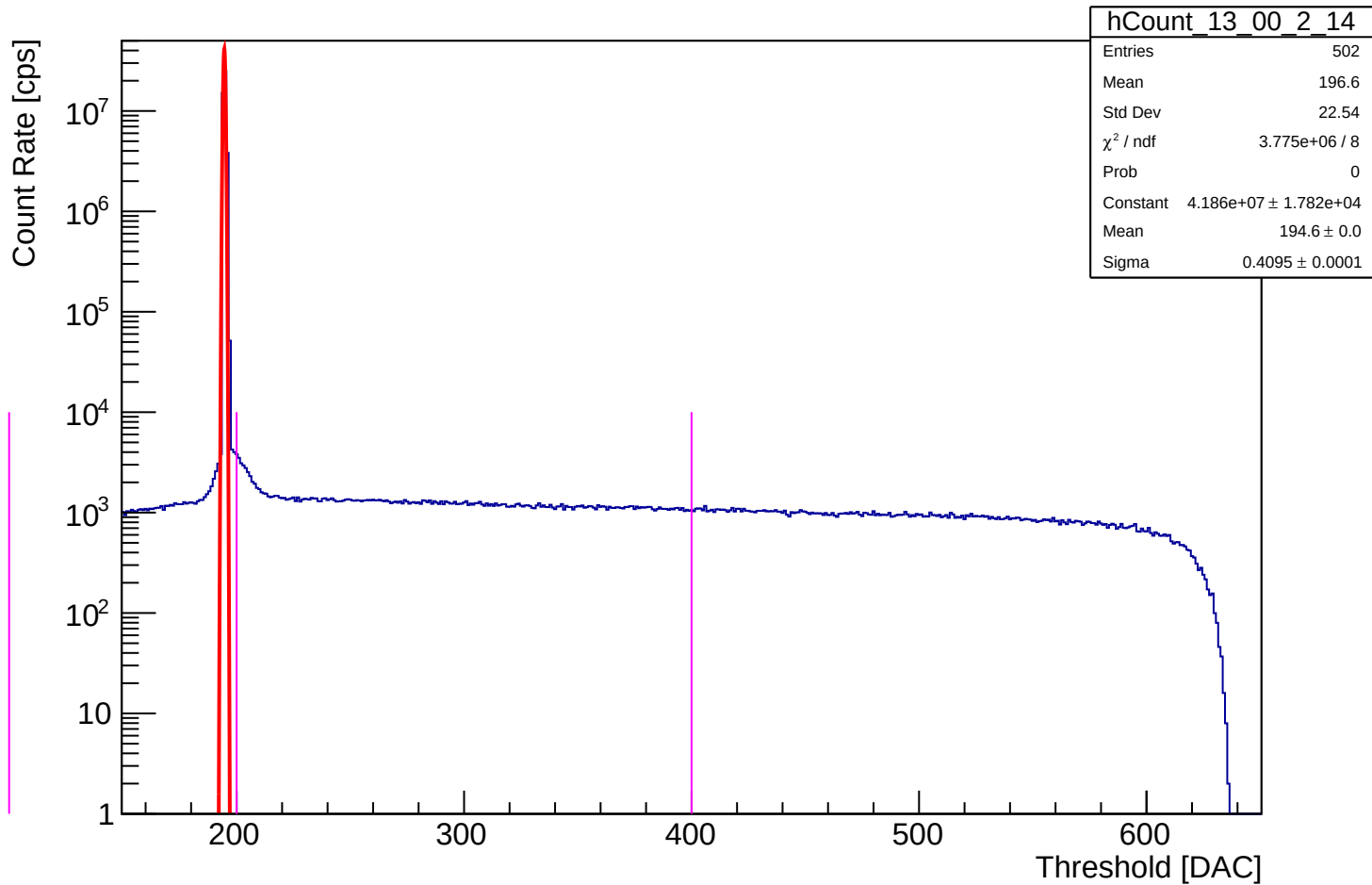
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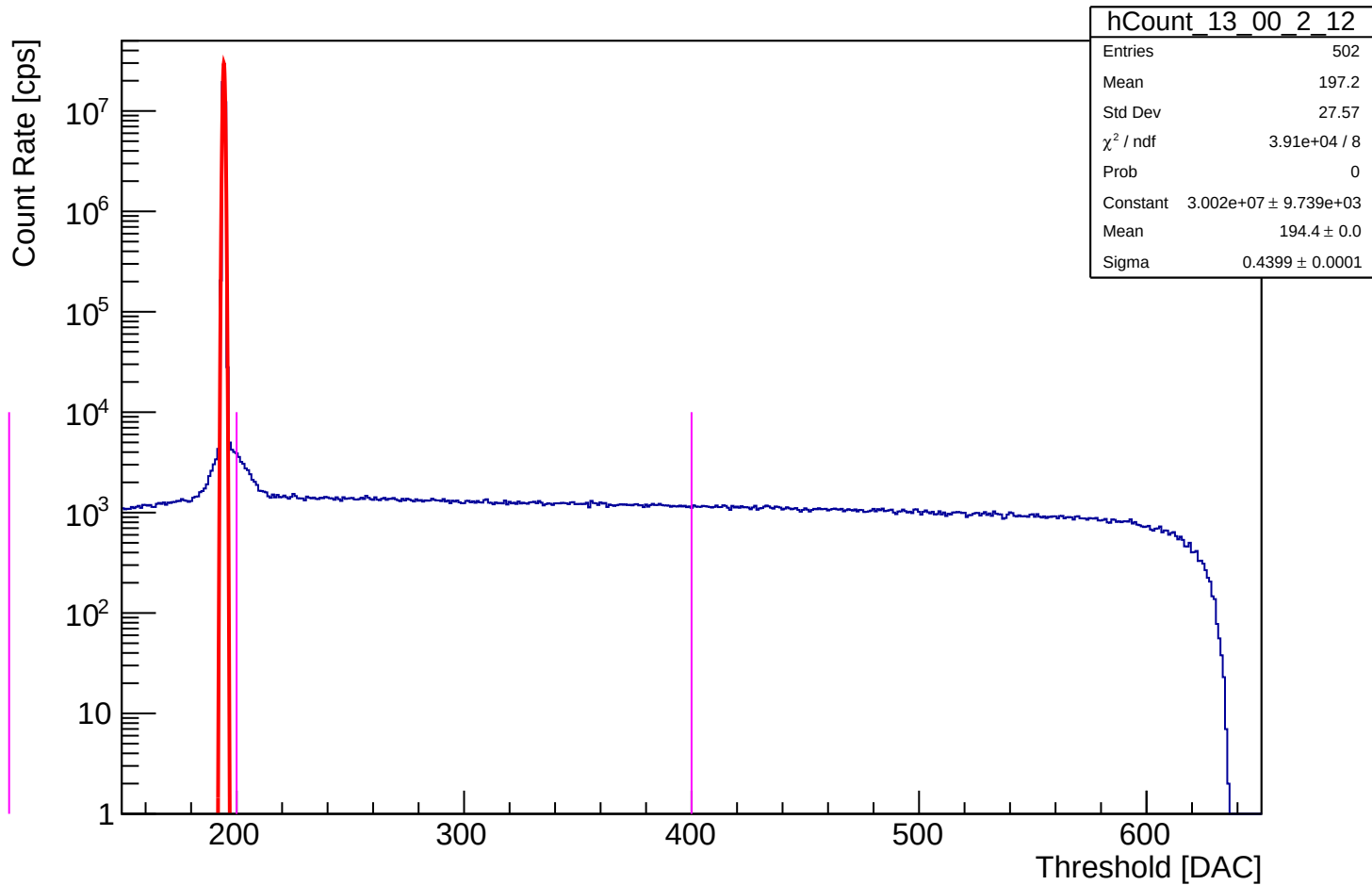
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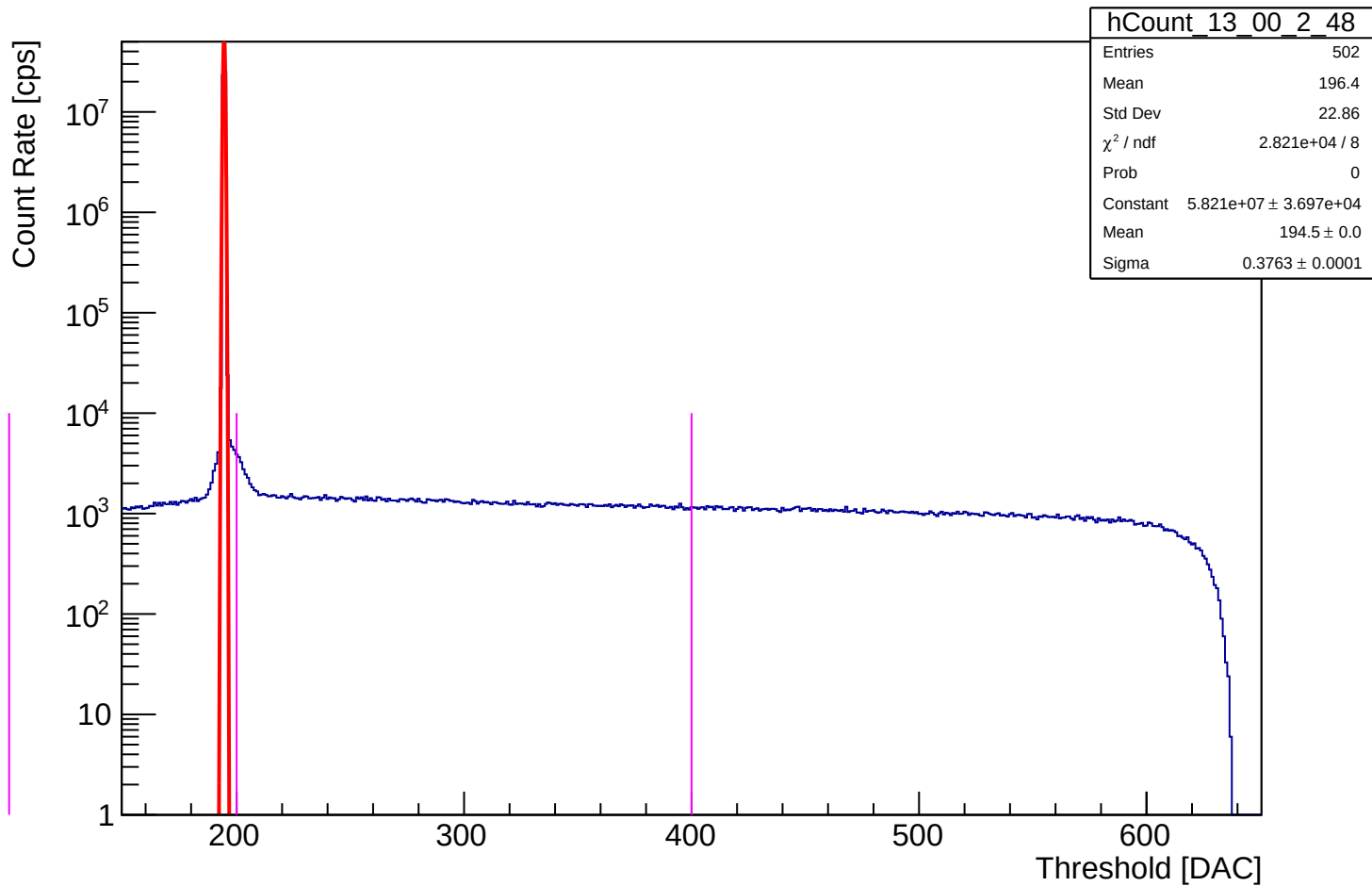
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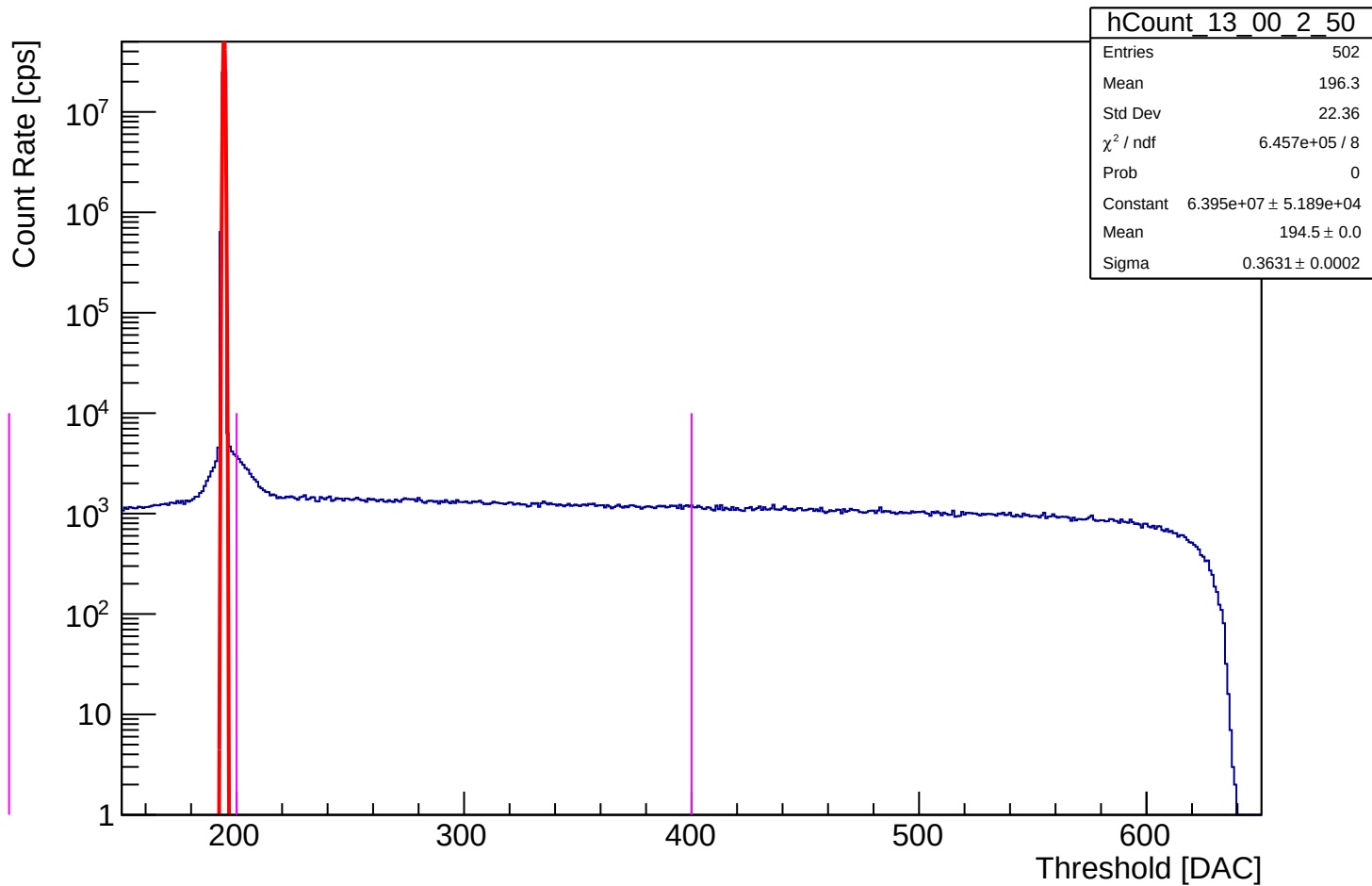
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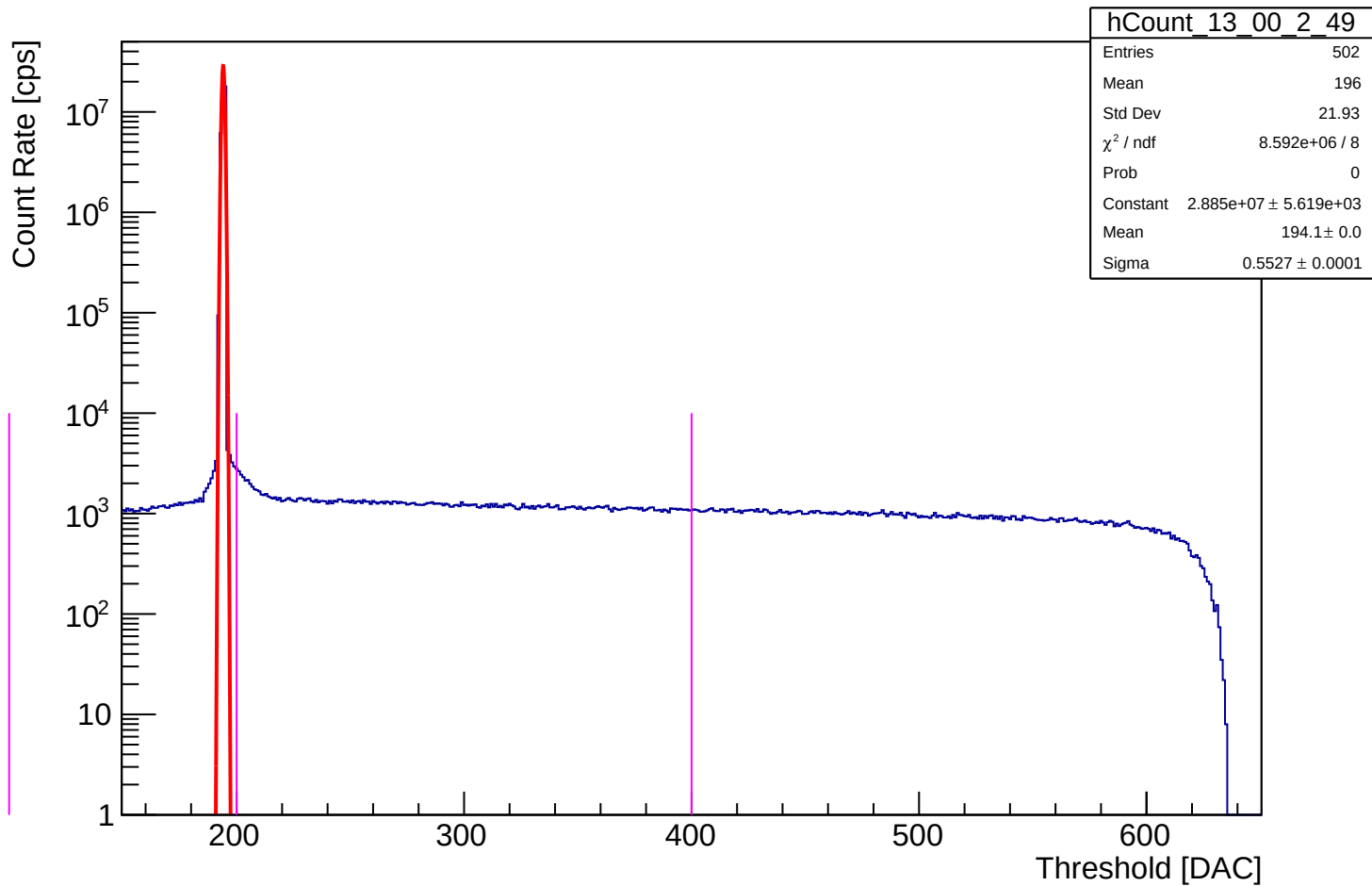
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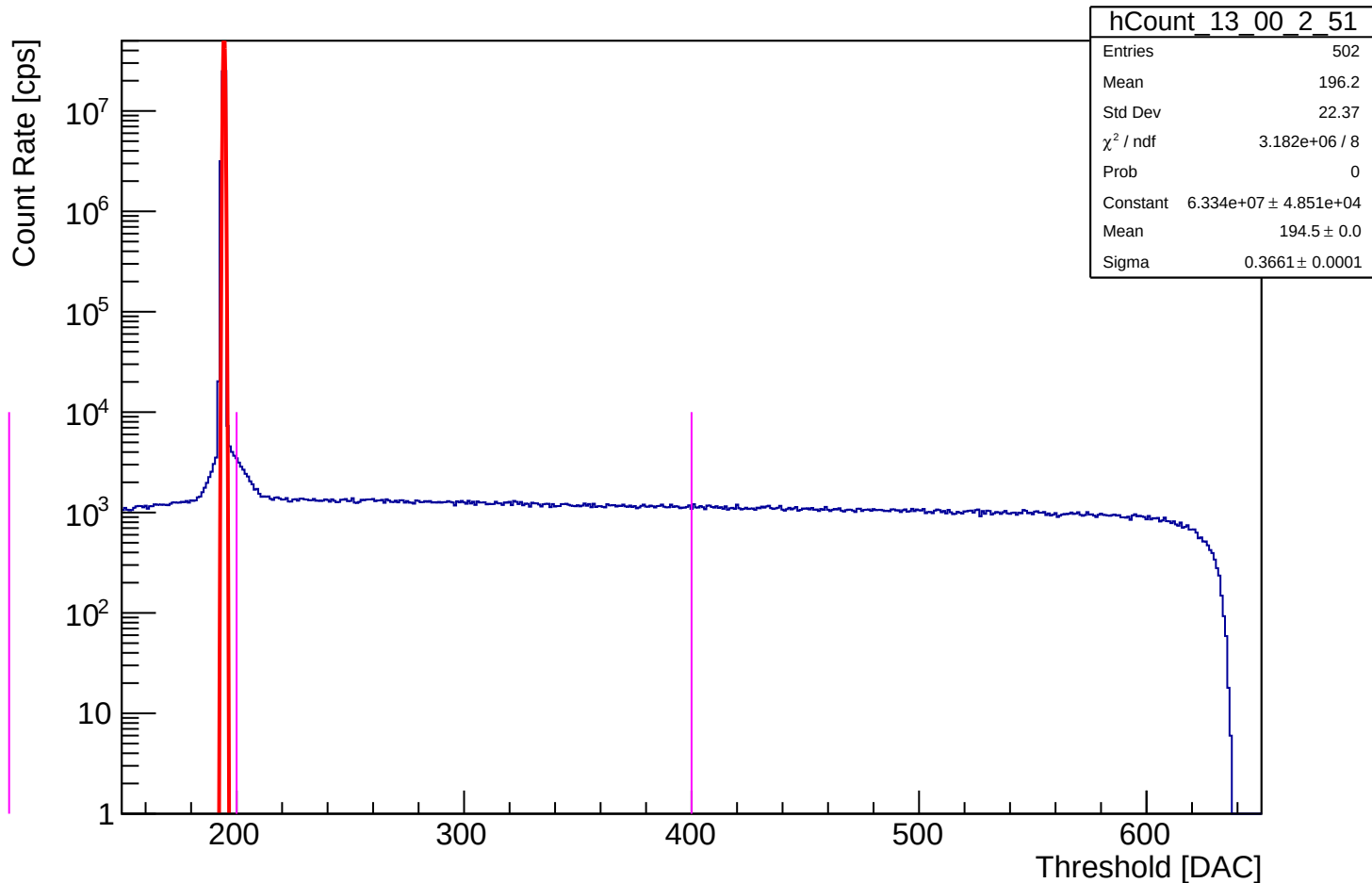
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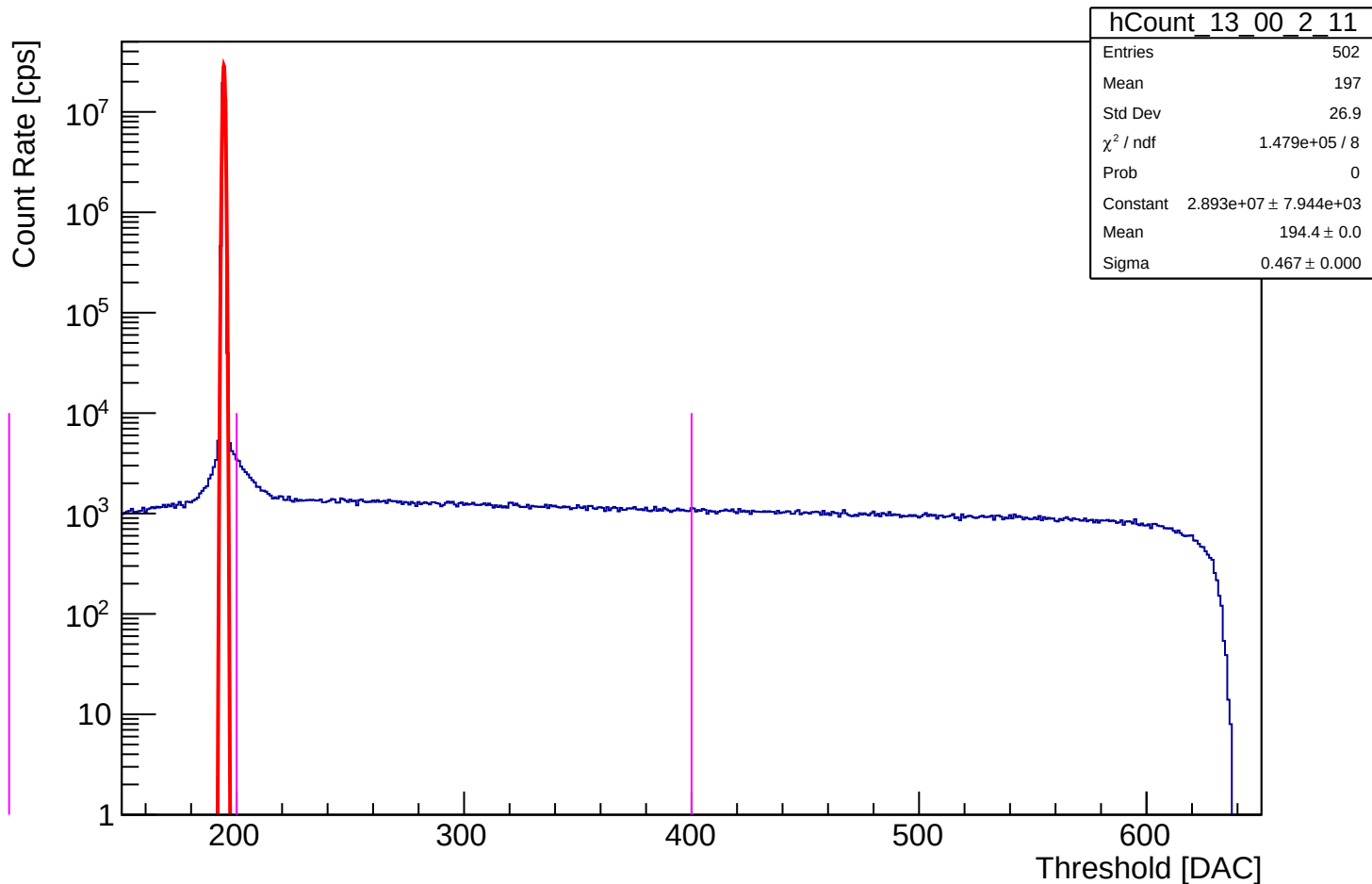
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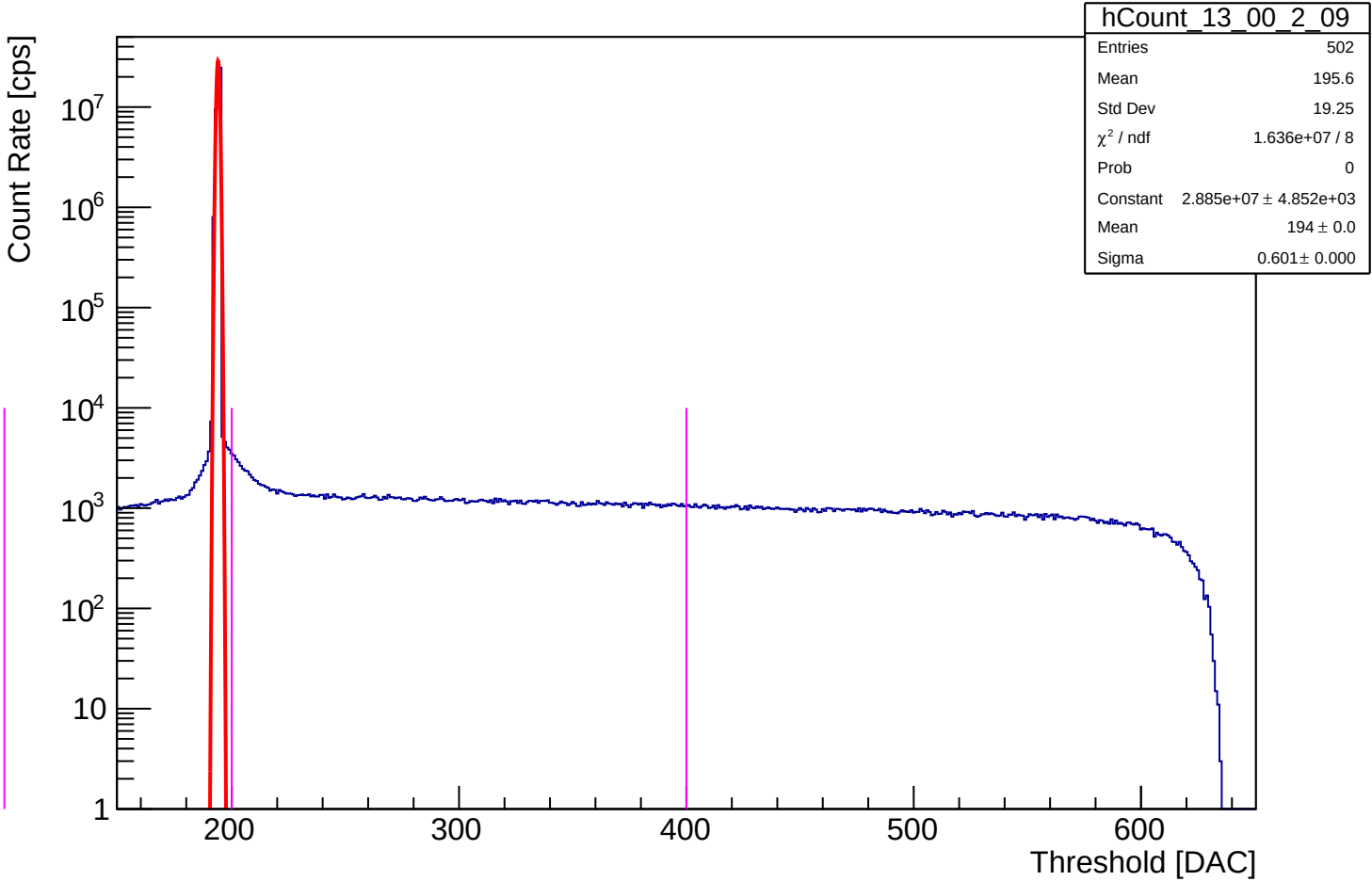


Row 1 Tile 1 Pmt 3 Pixel 40 Slot 13 Fiber 0 Asic 2 Channel 51

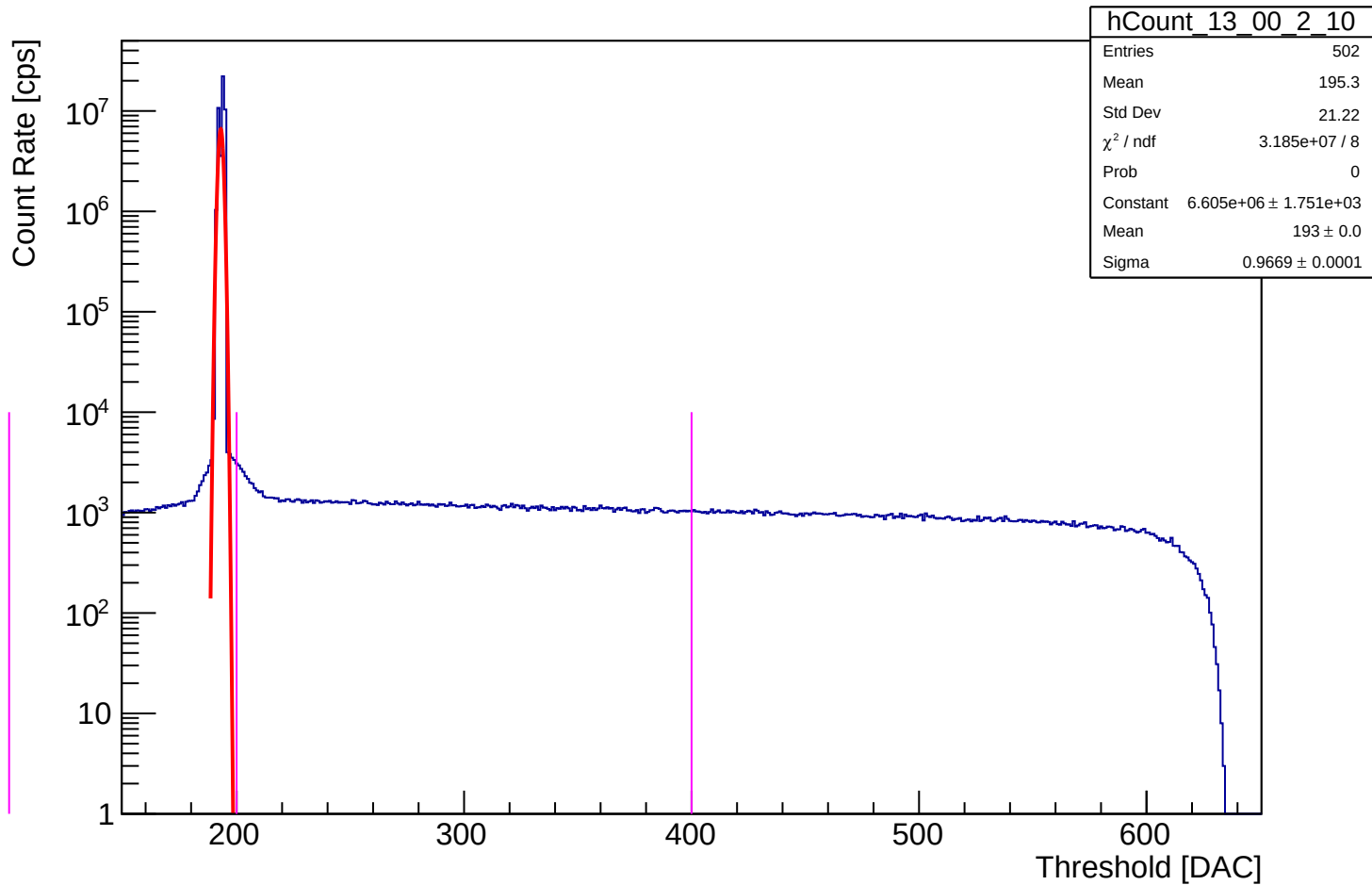


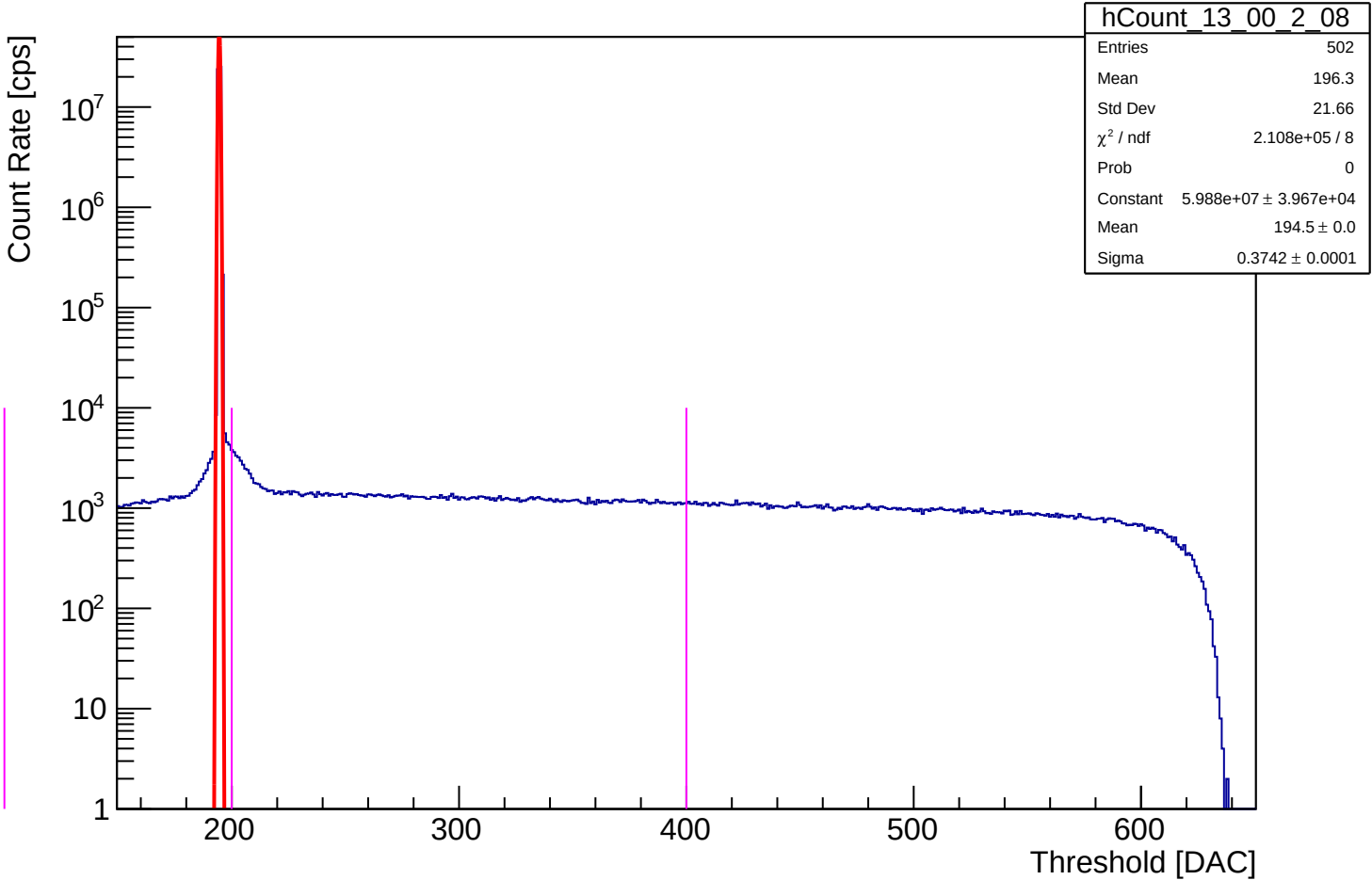
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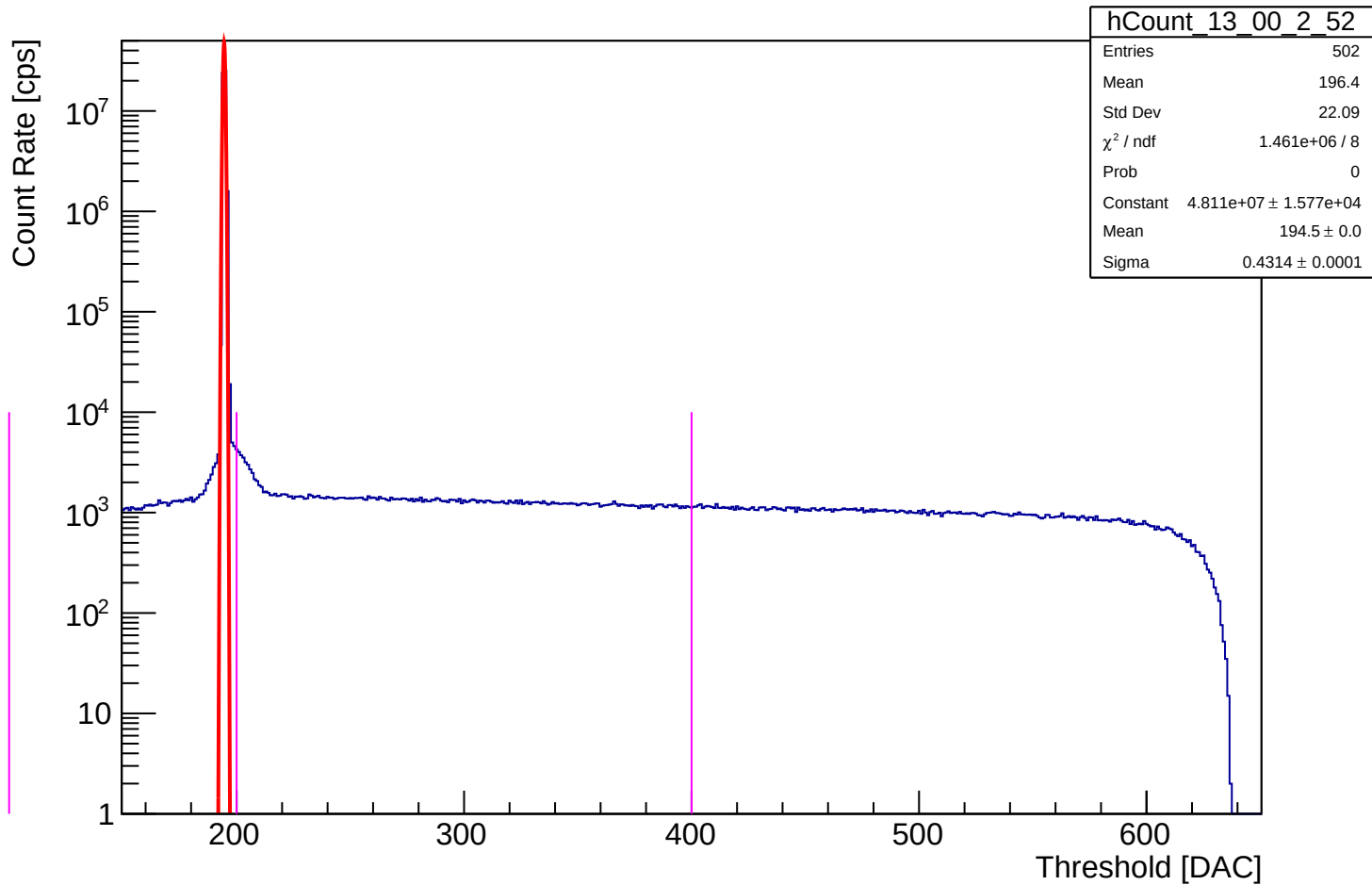


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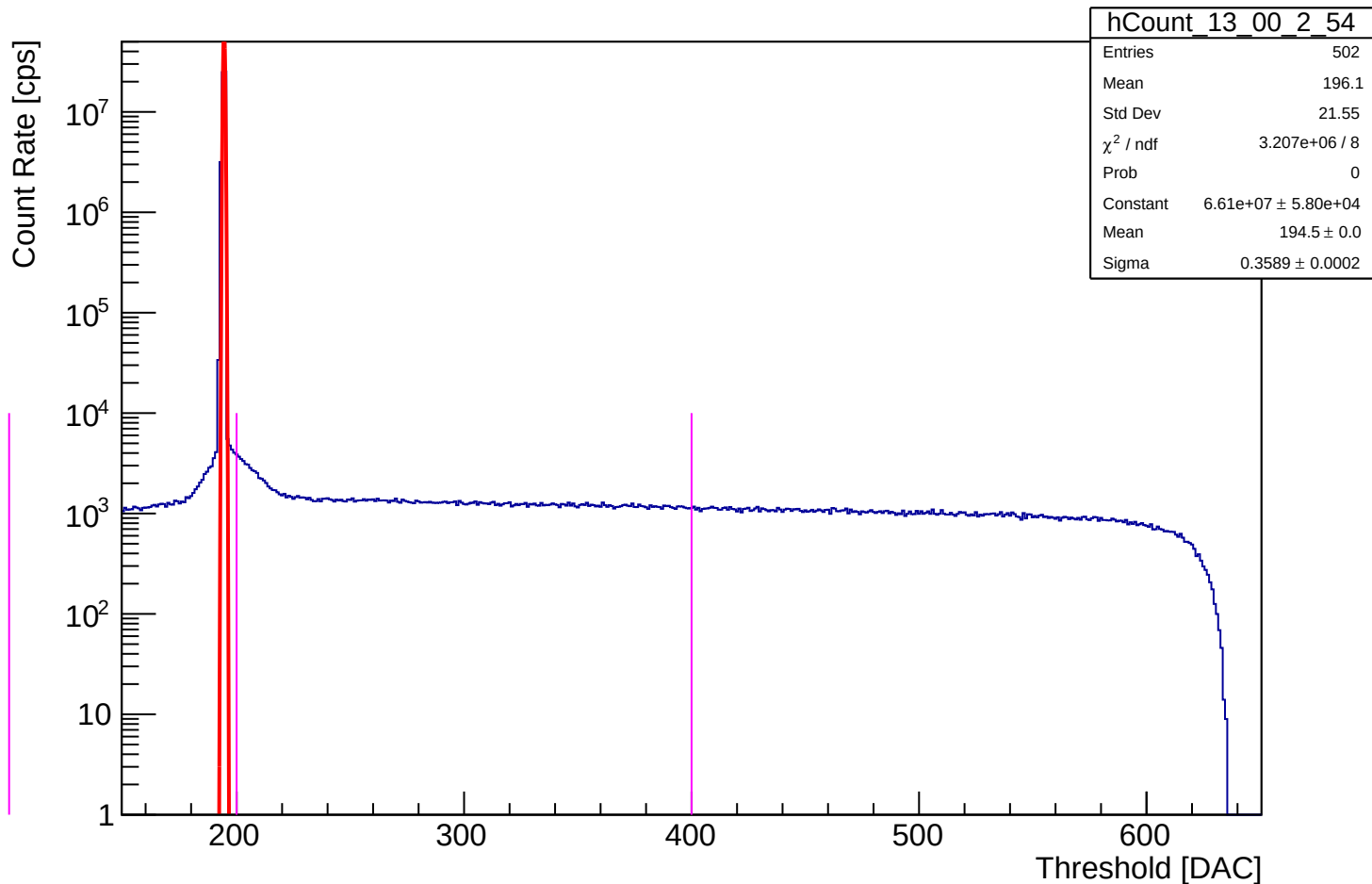




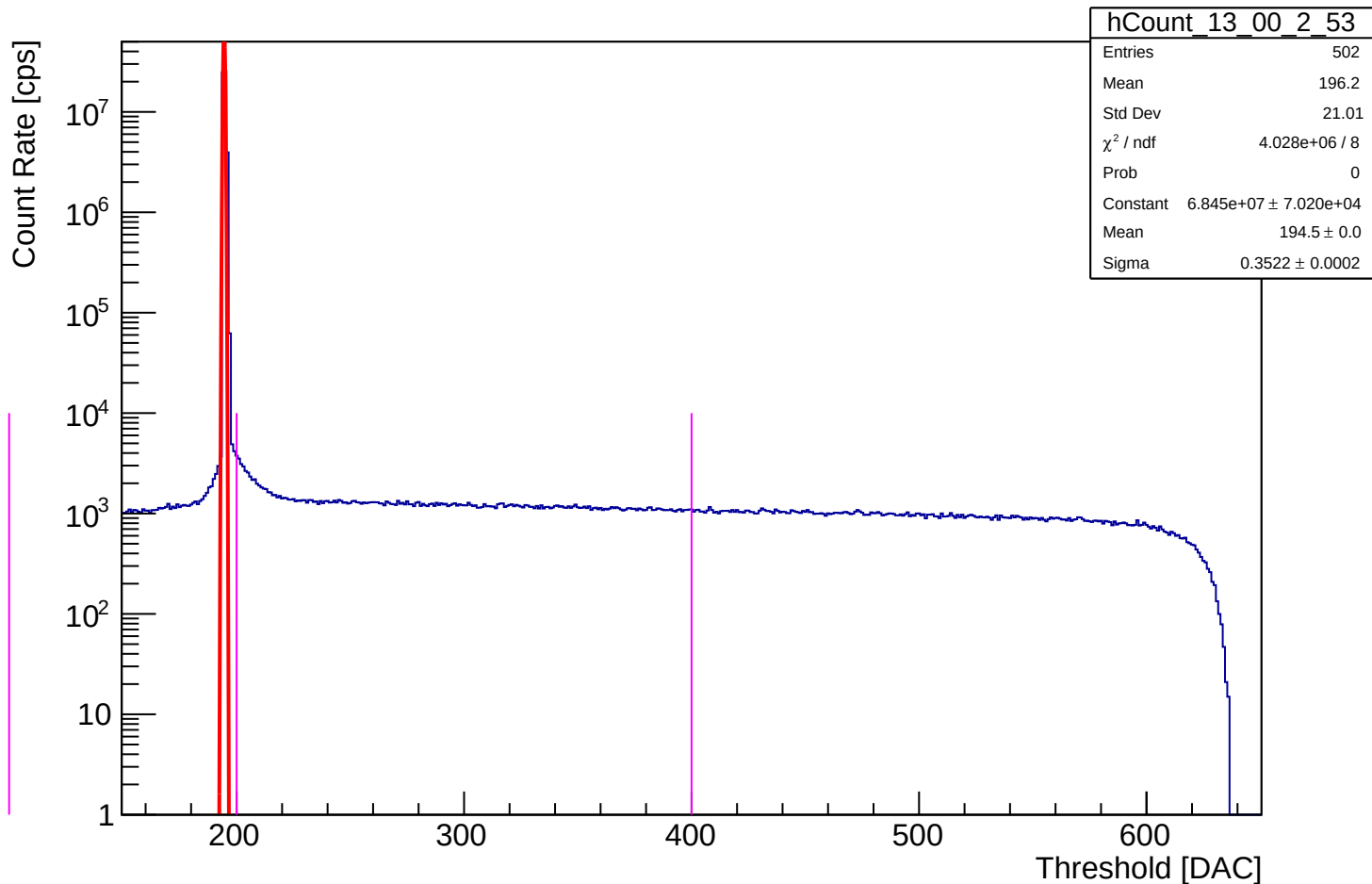
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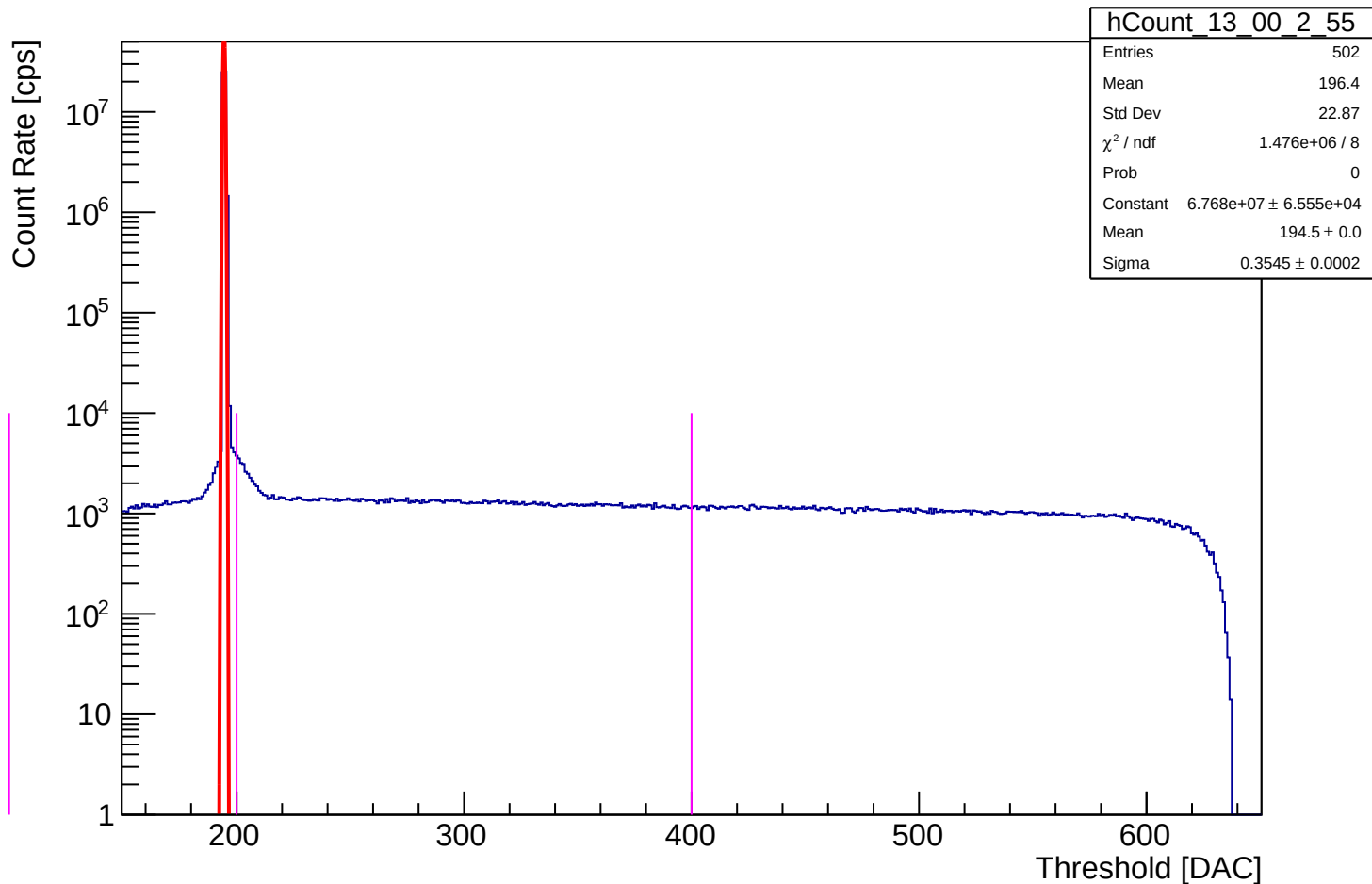
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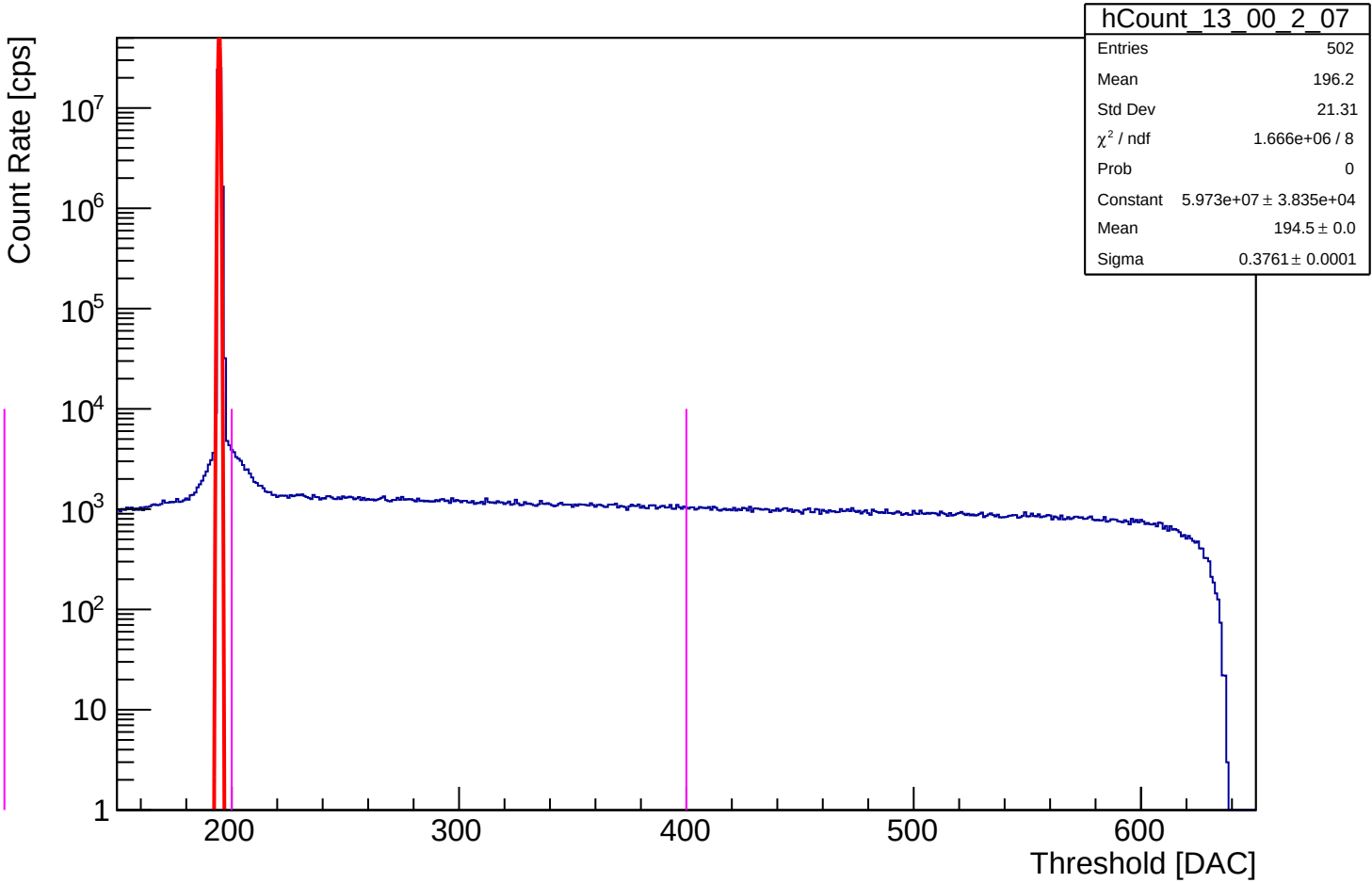


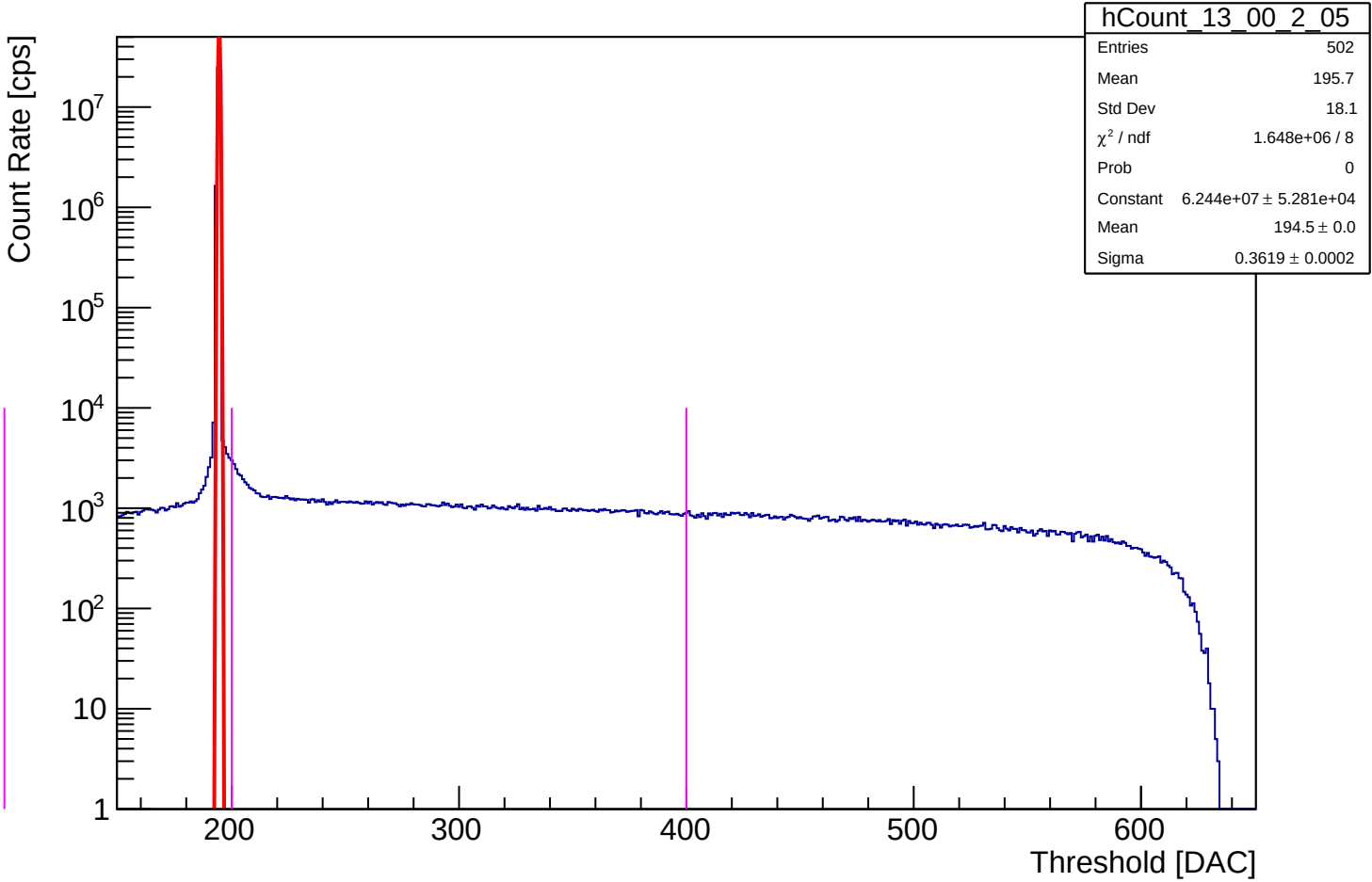
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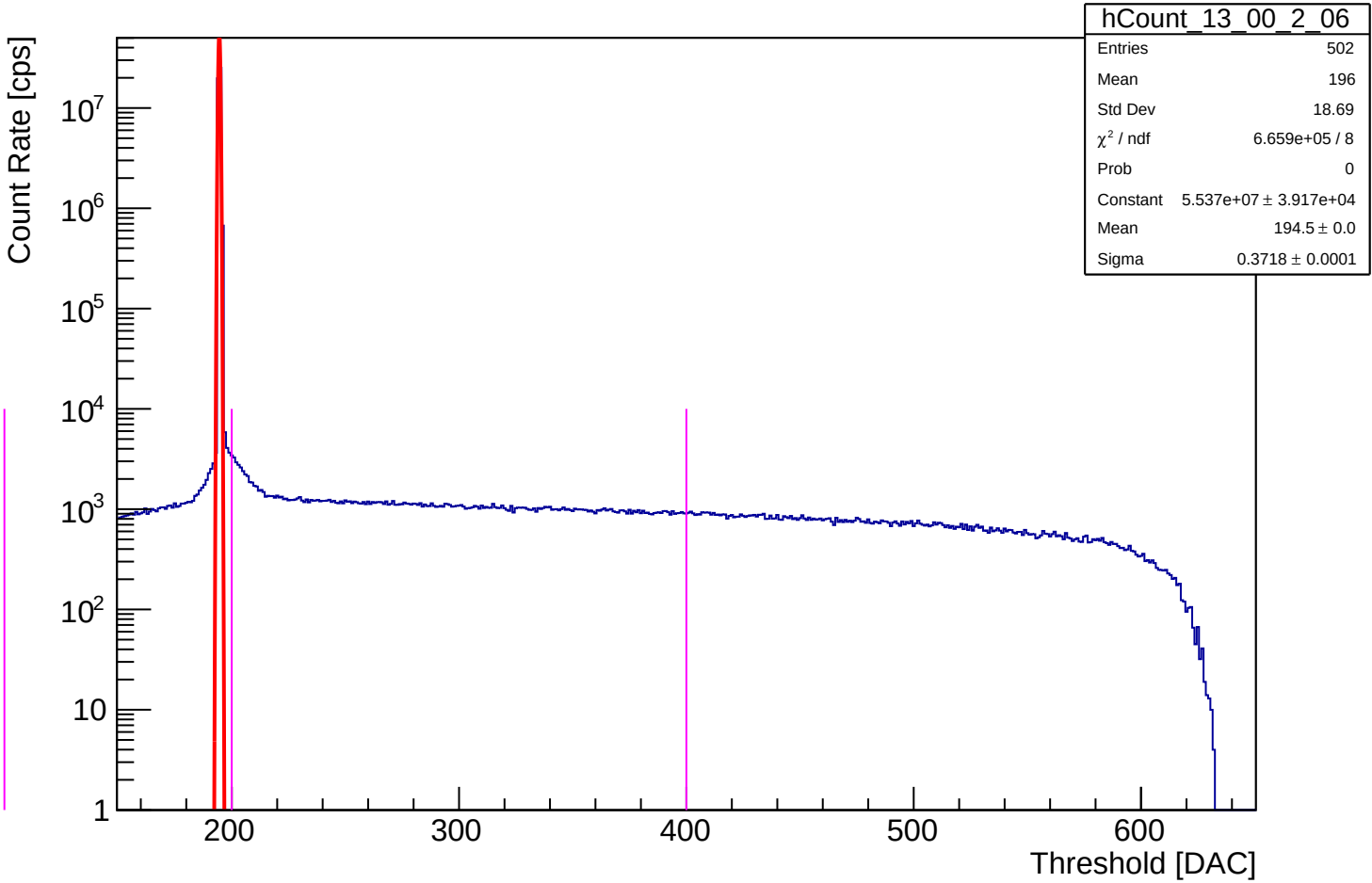


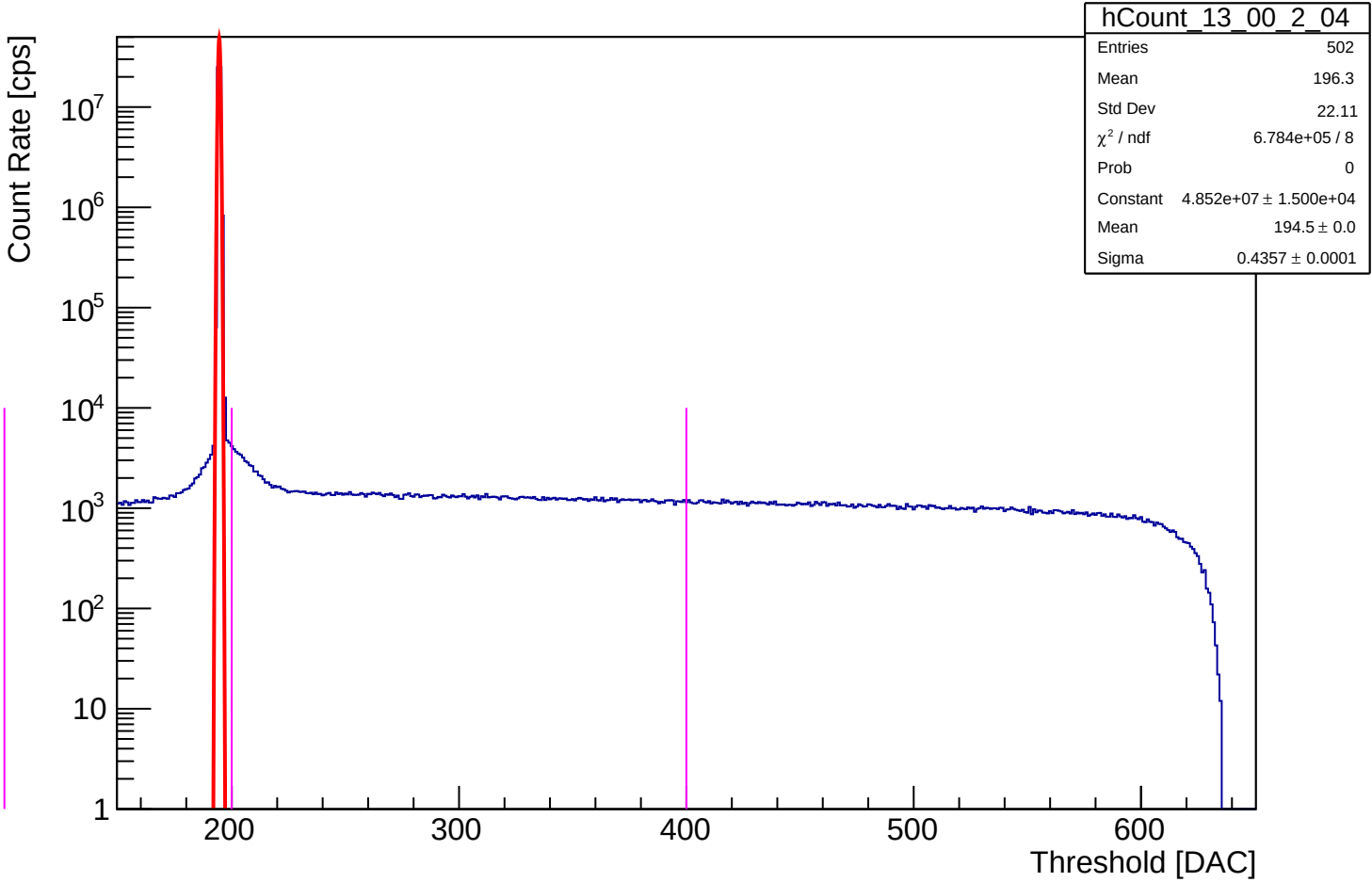
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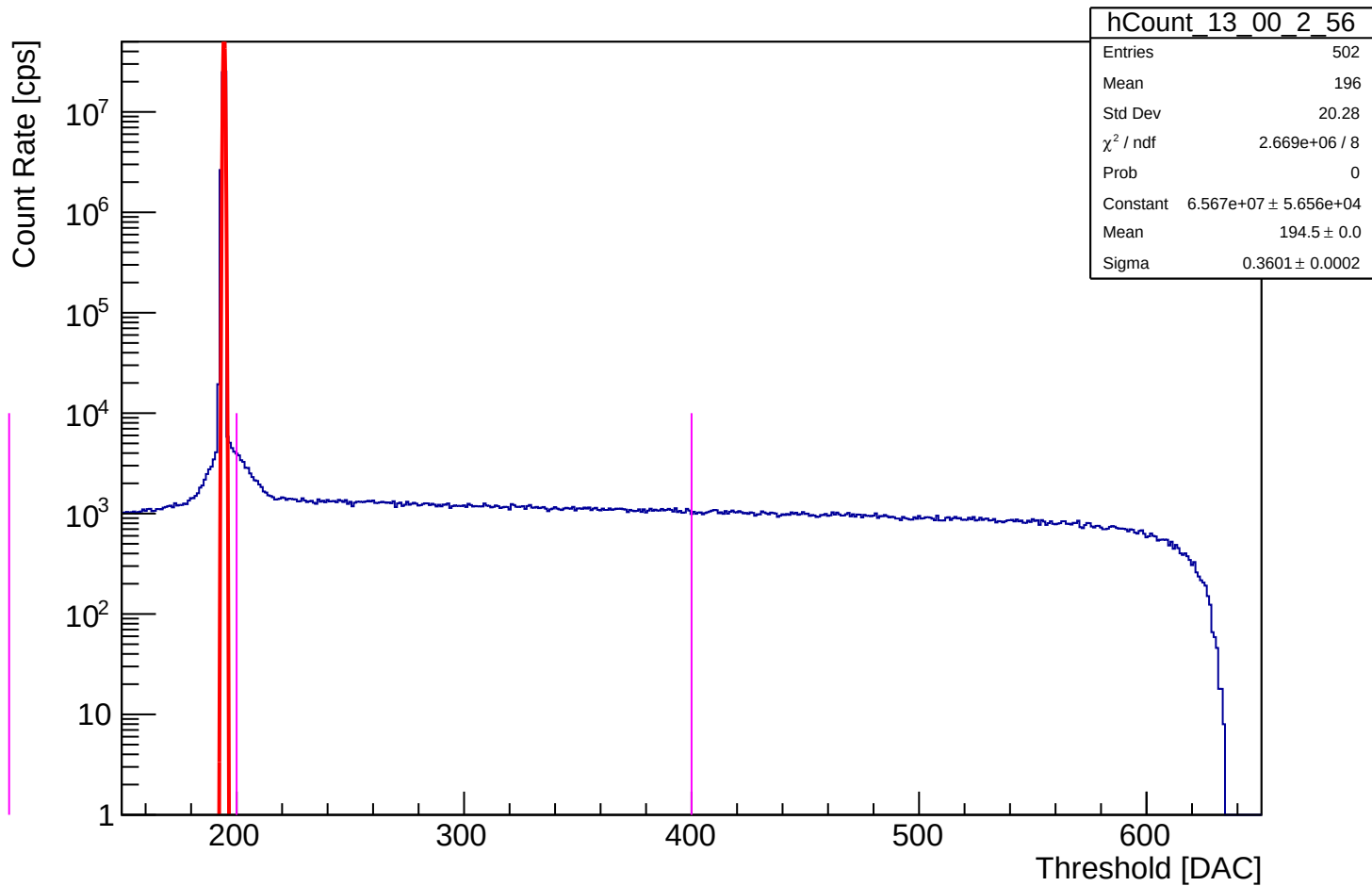




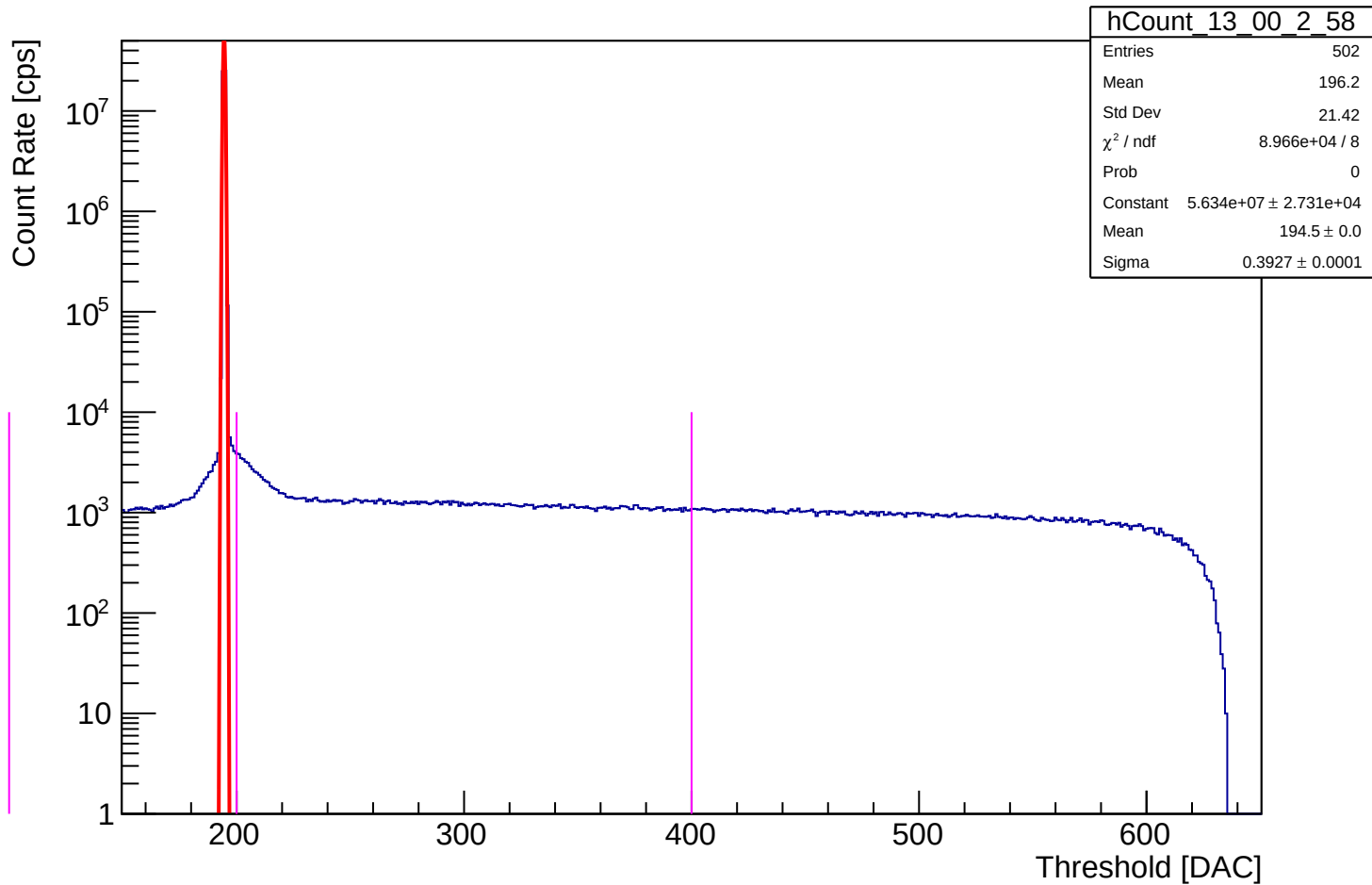




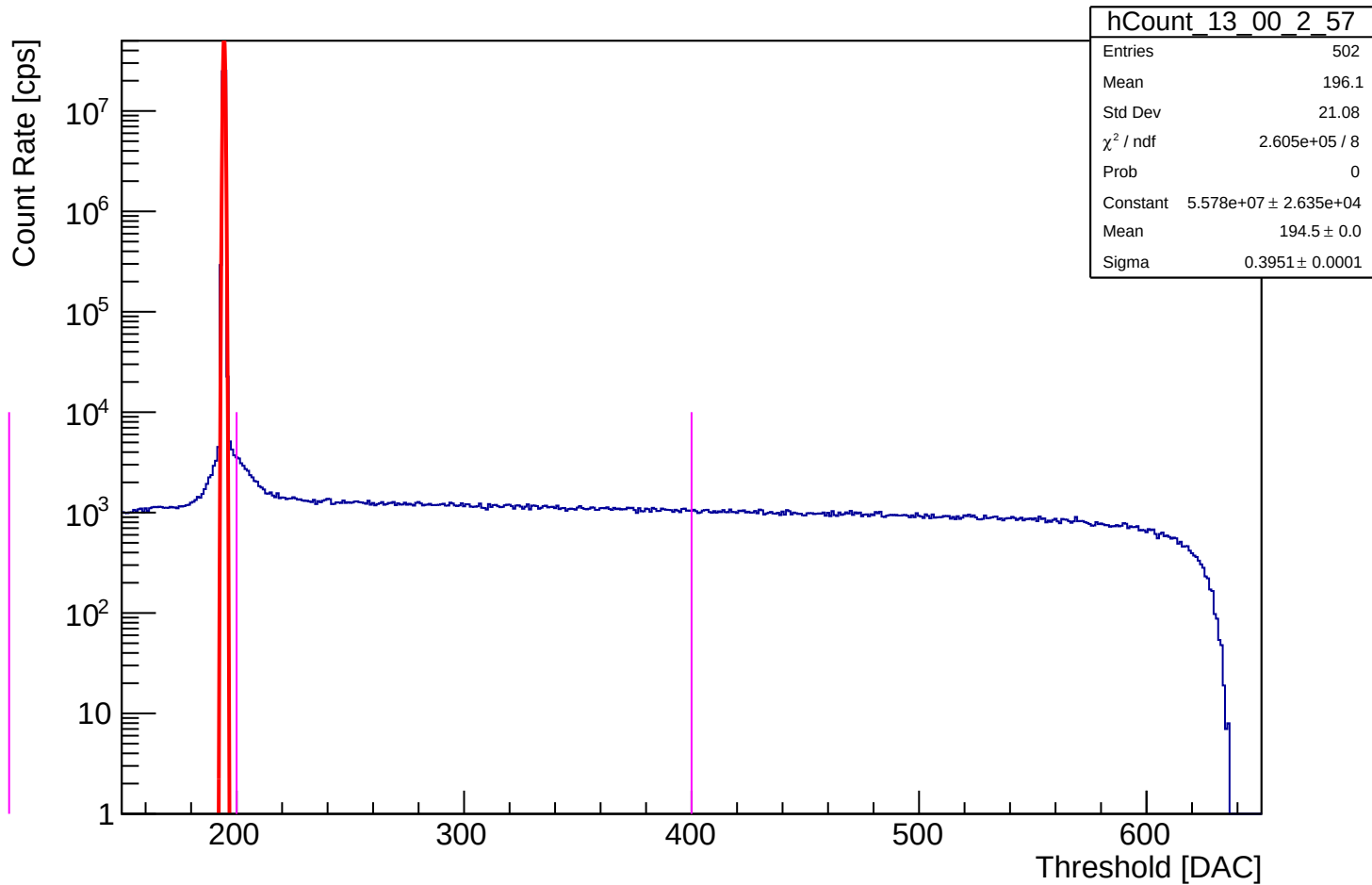
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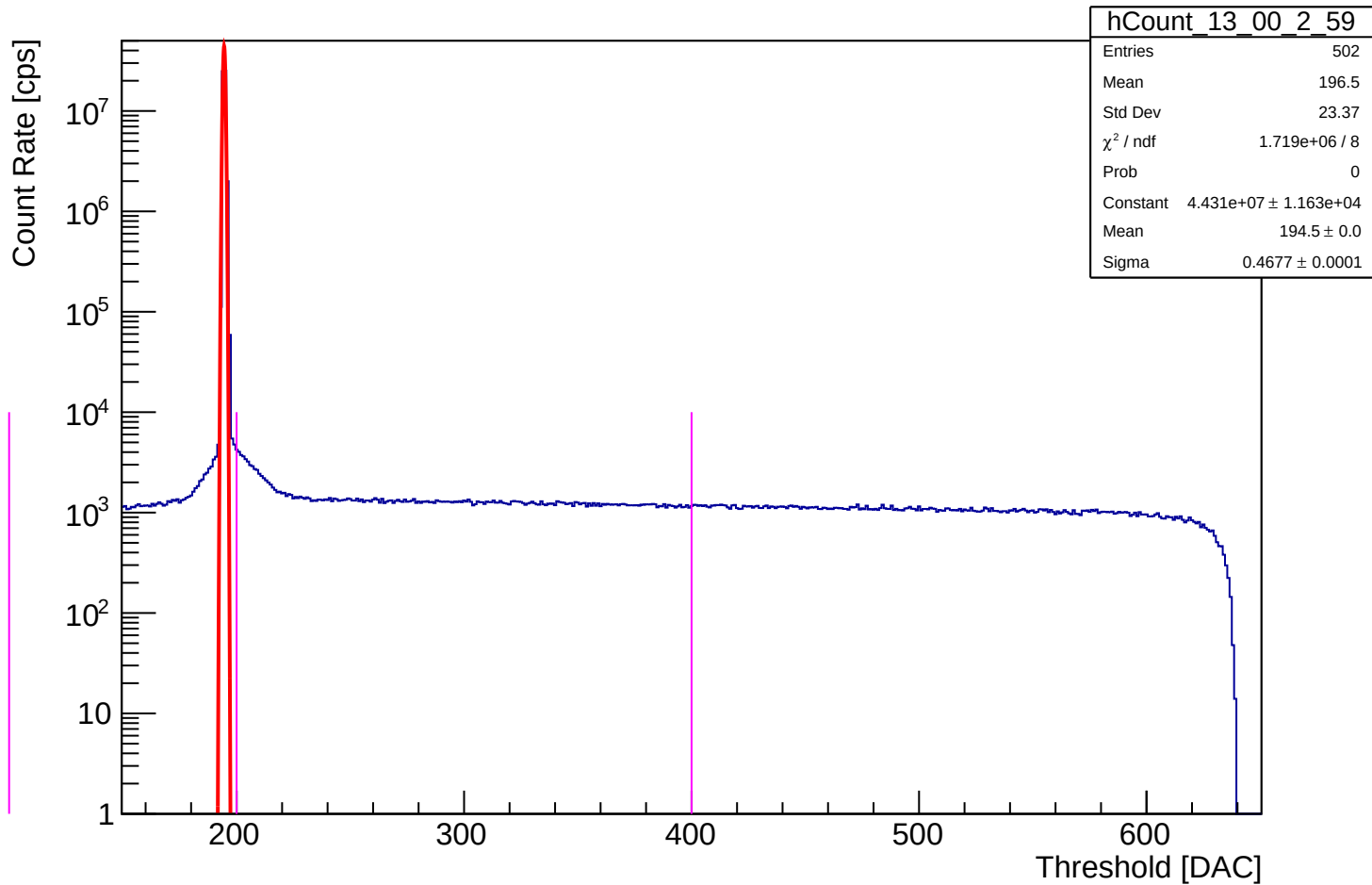
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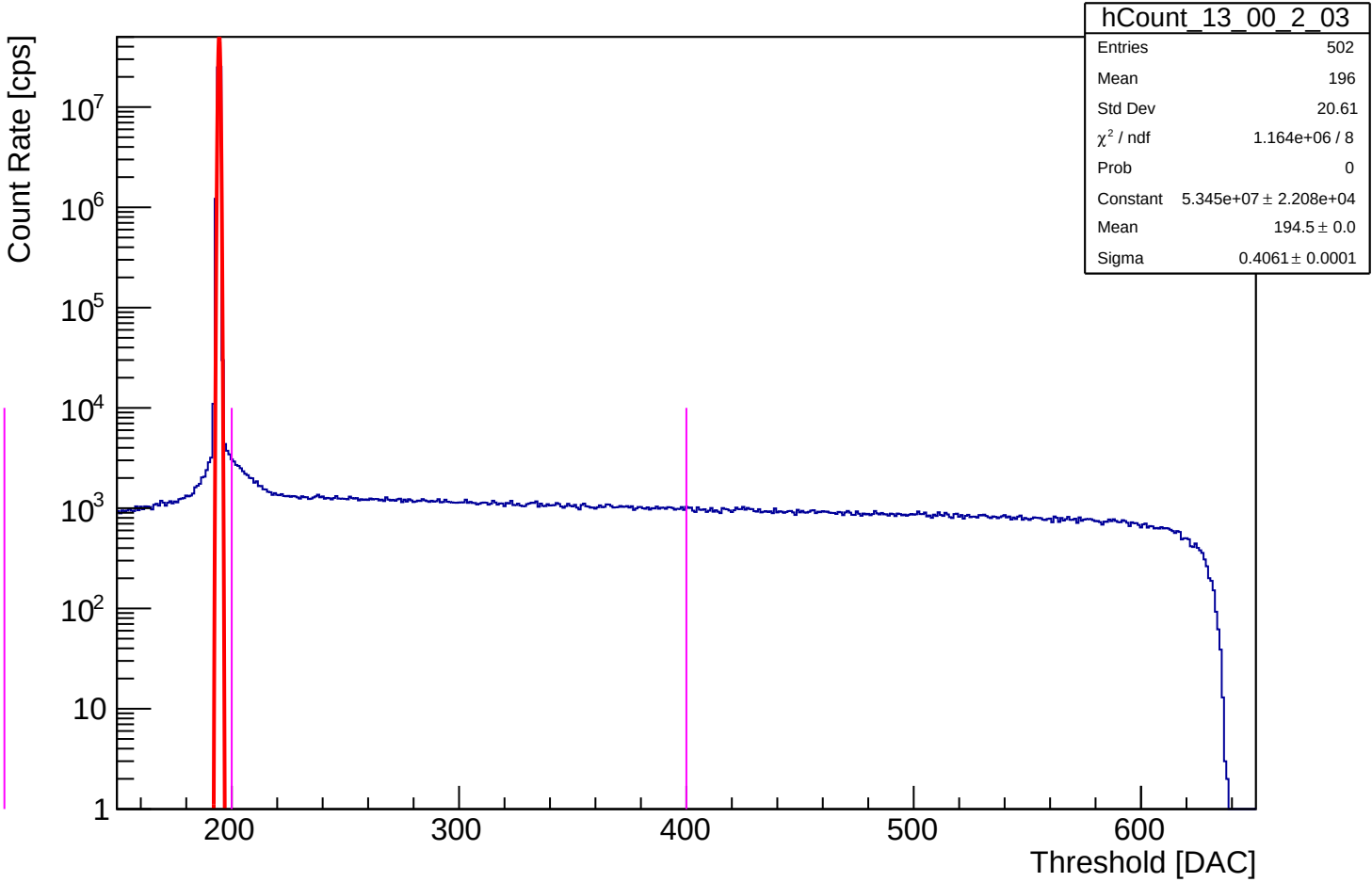


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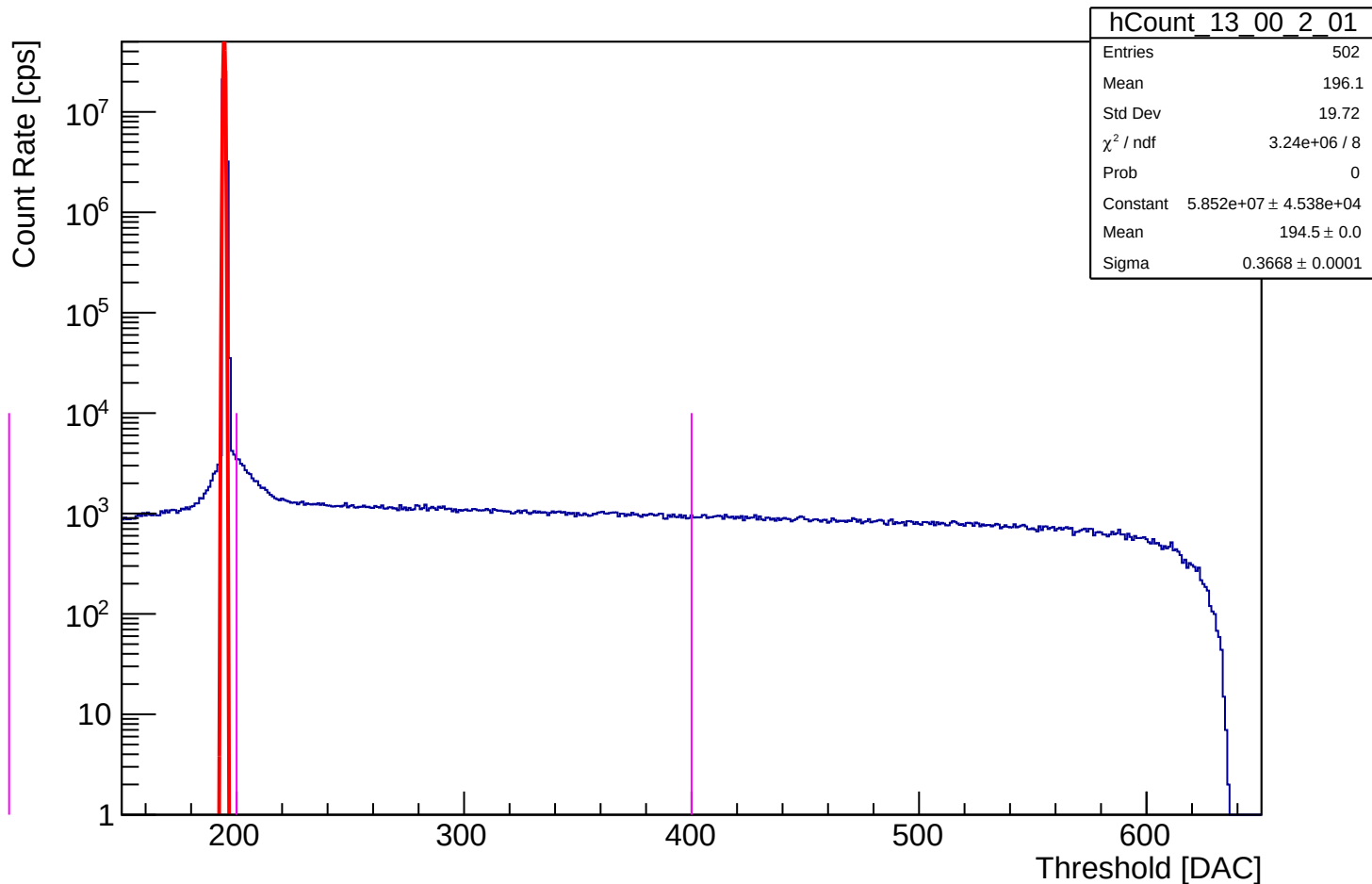


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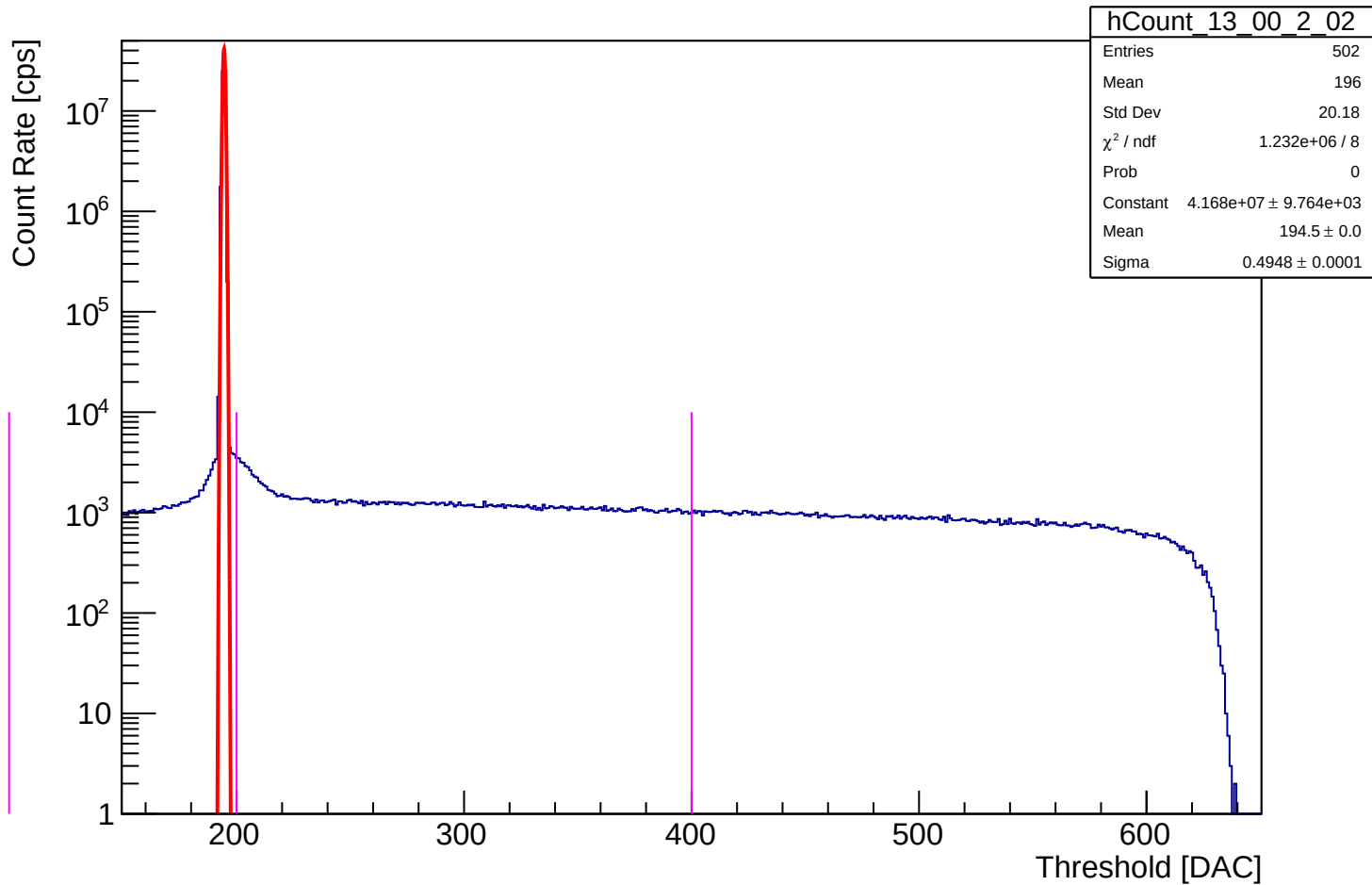


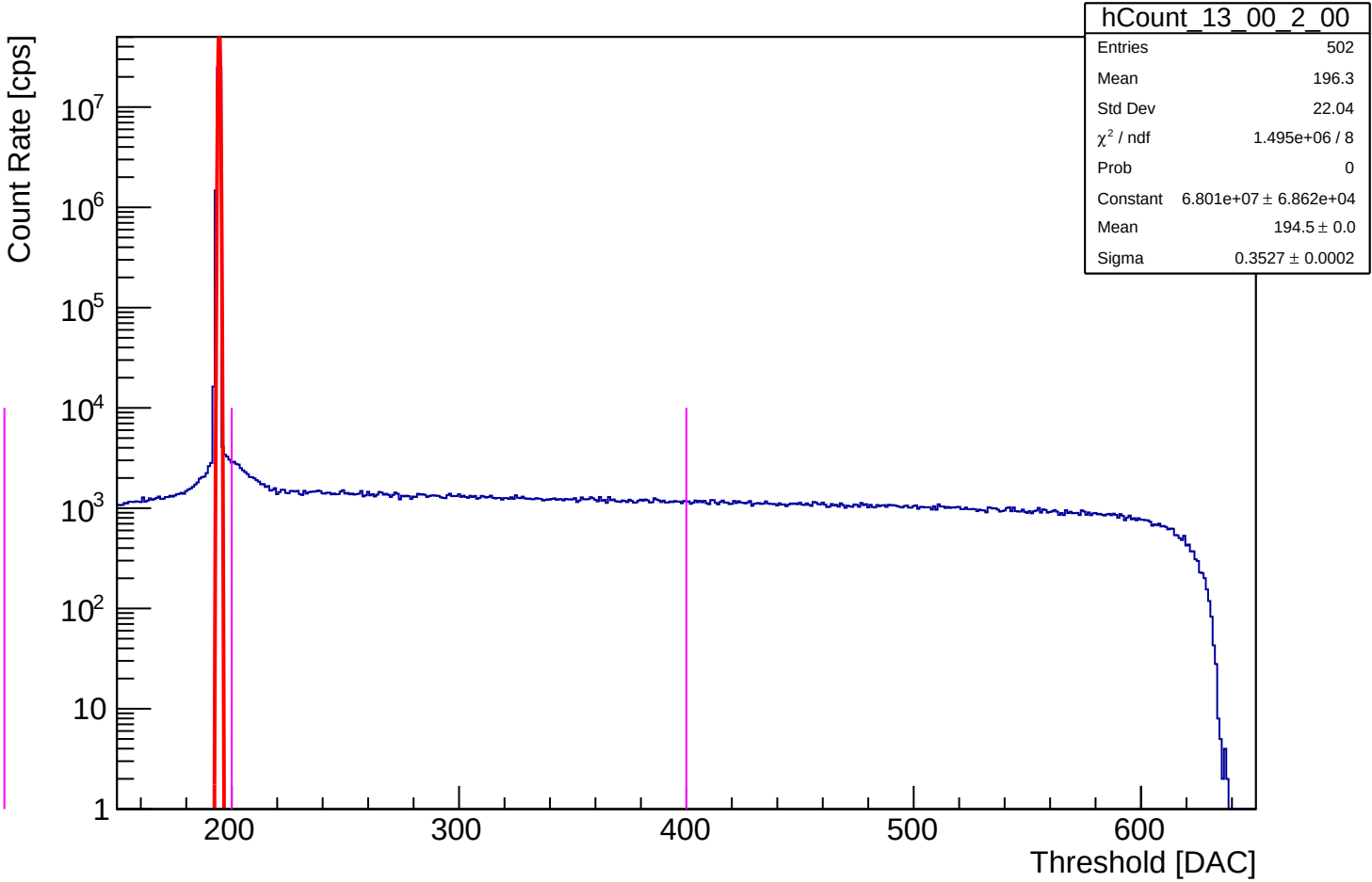


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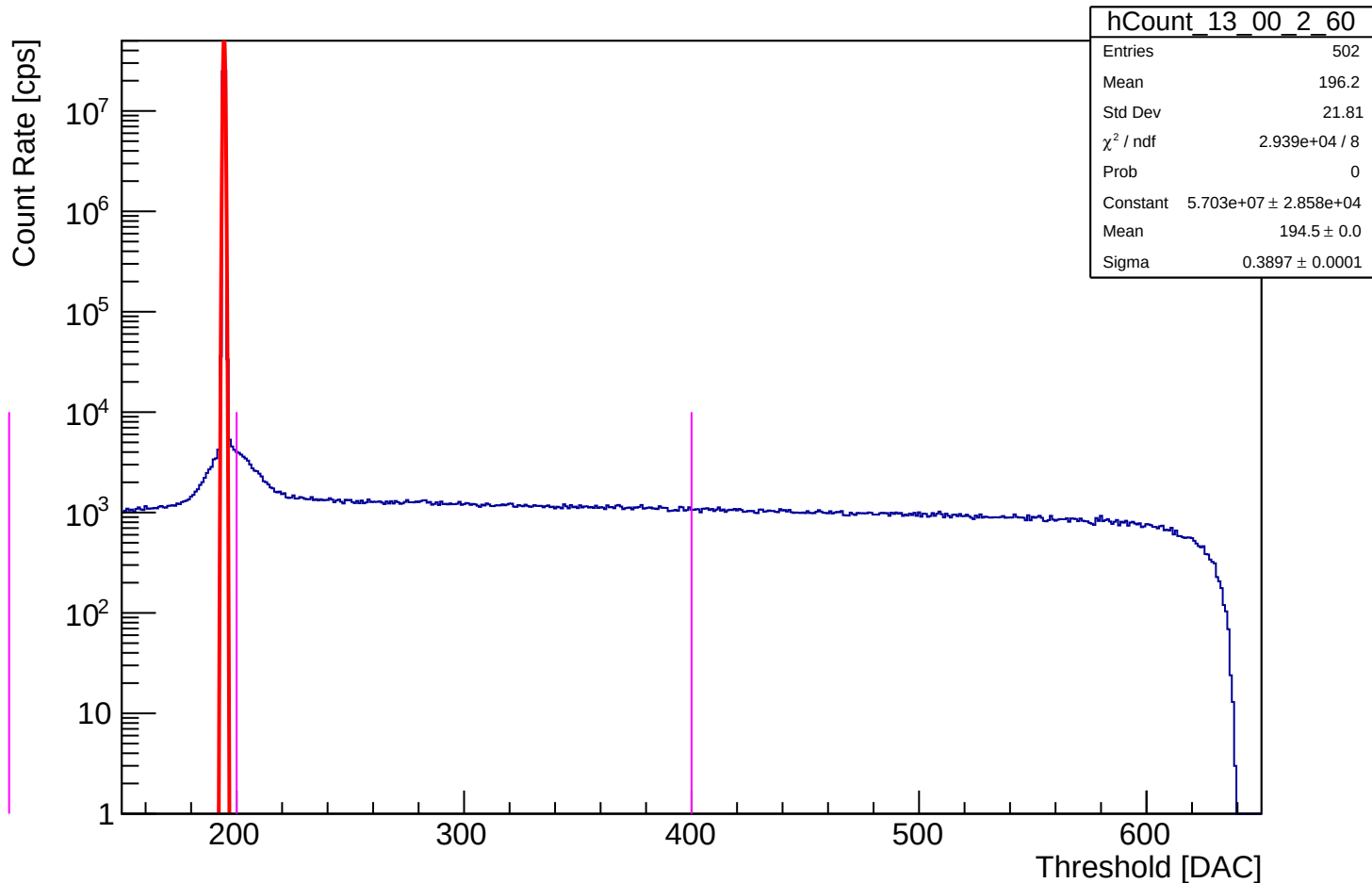


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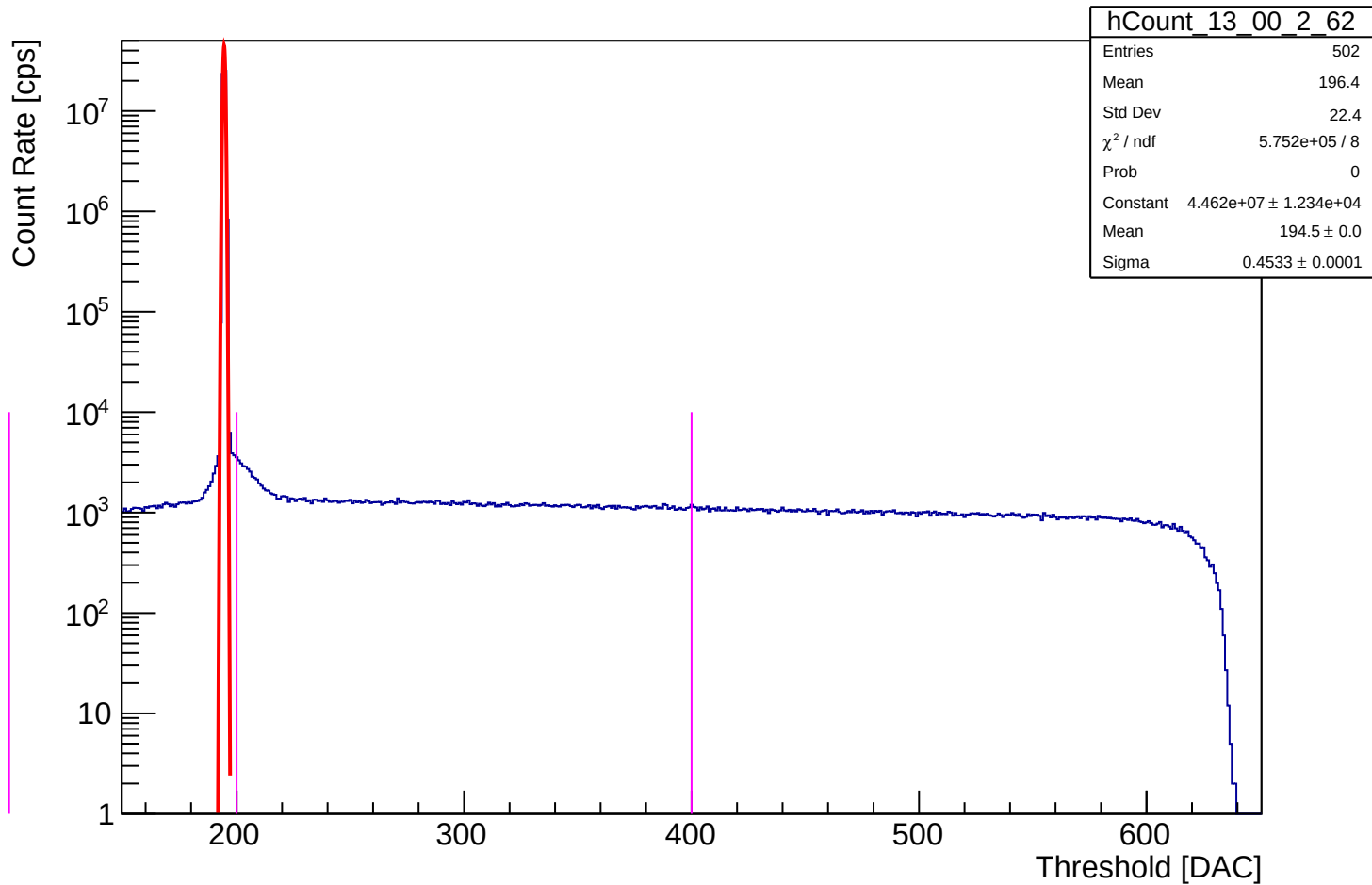




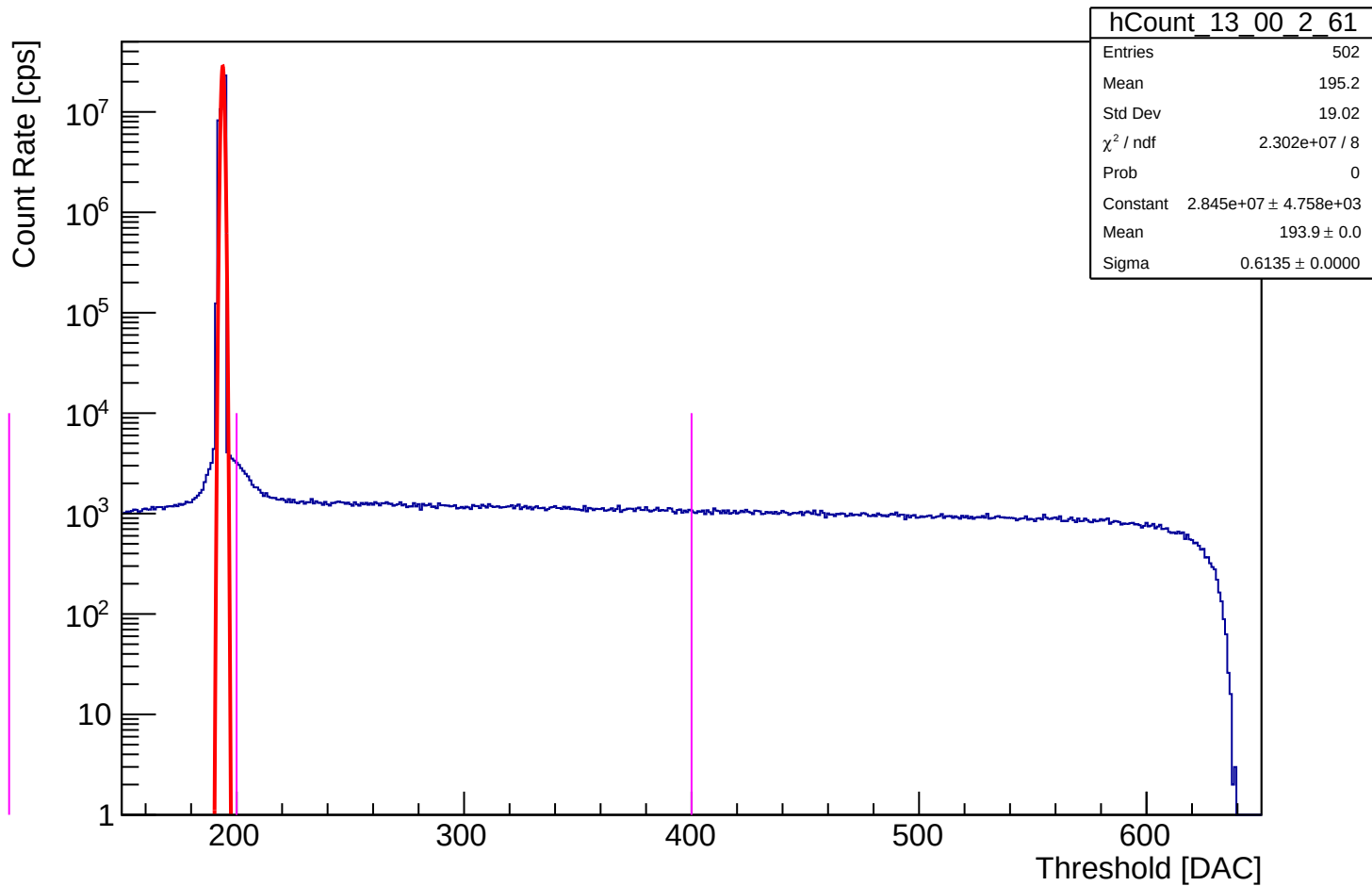
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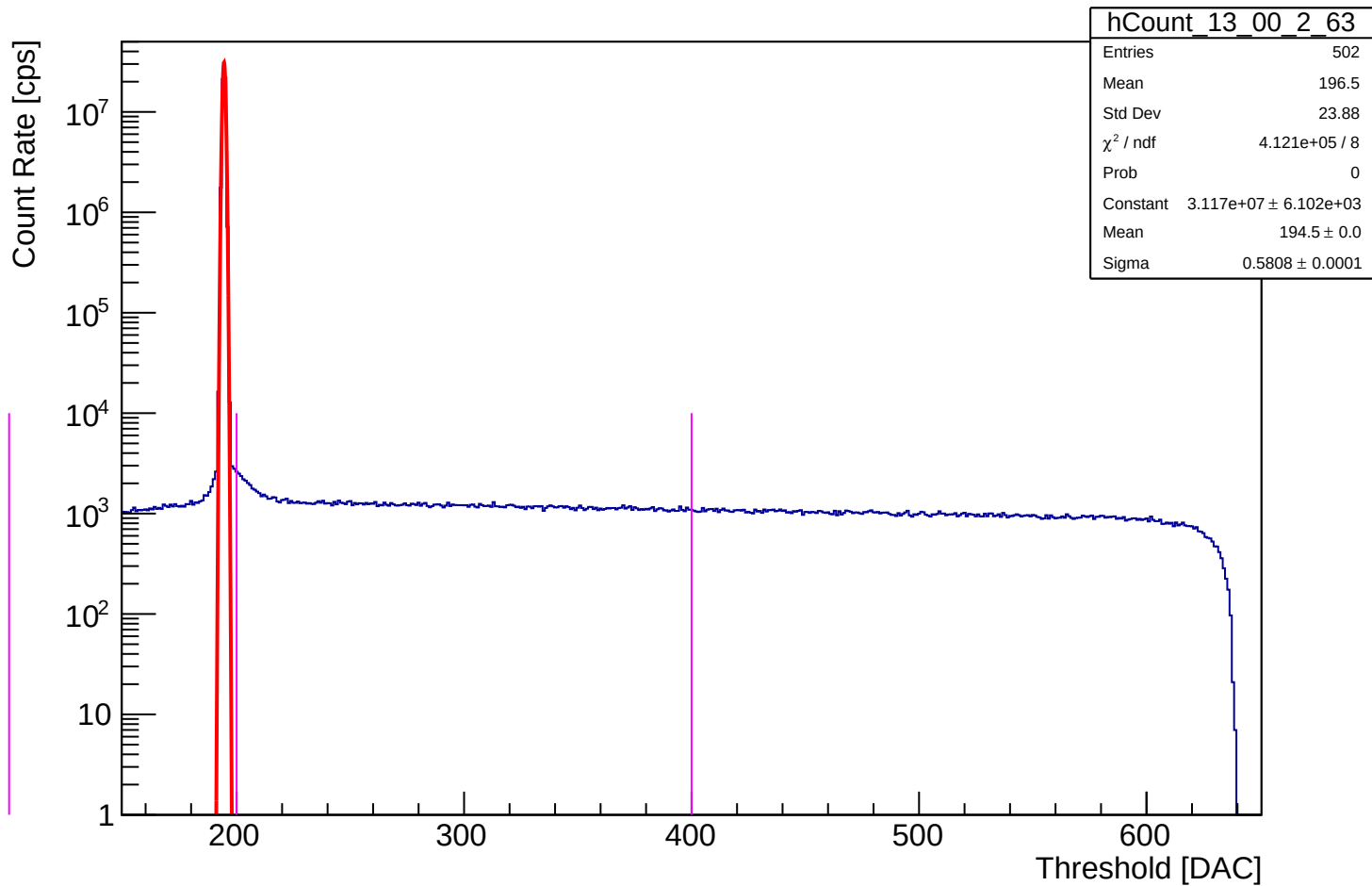
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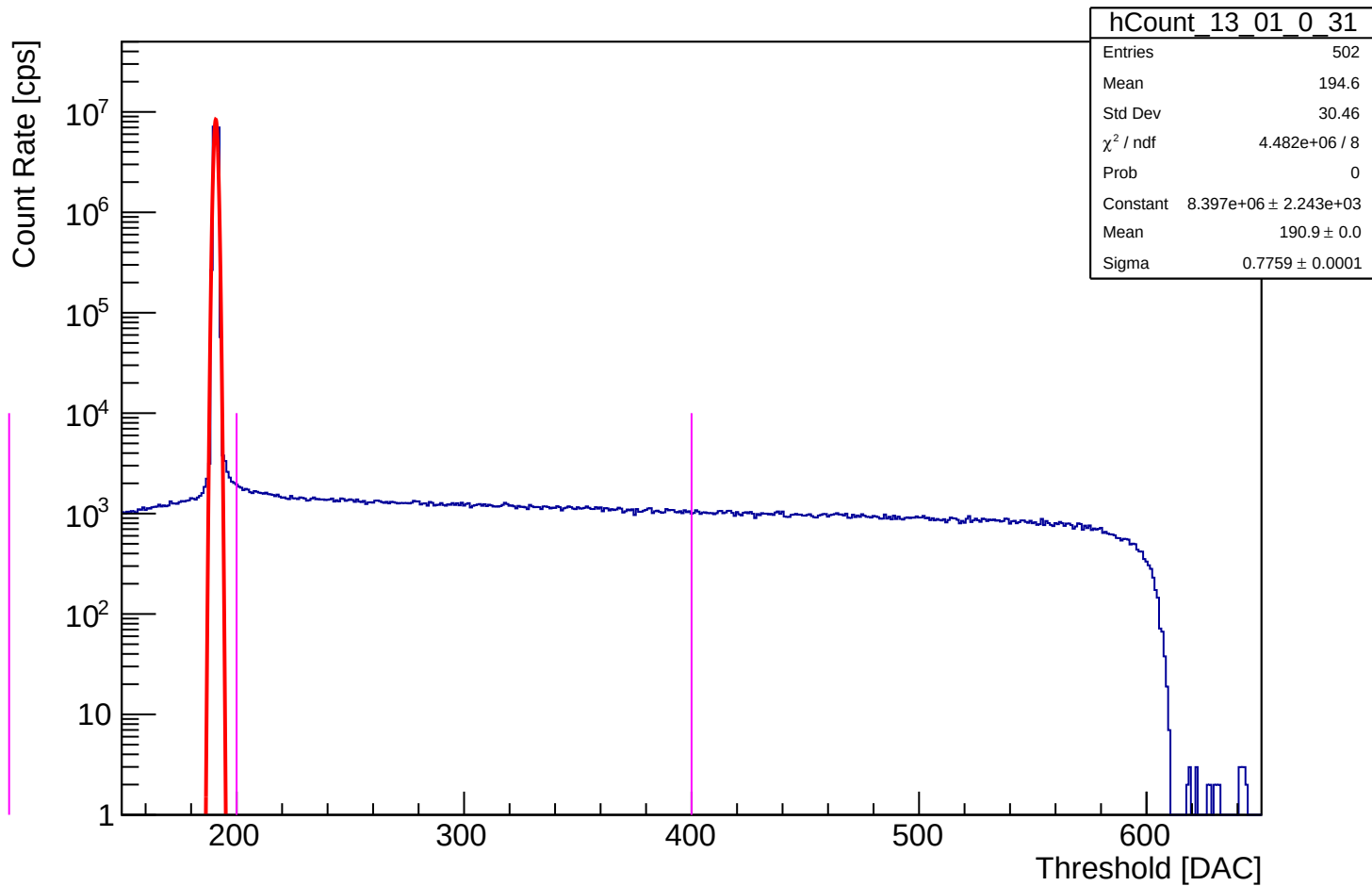
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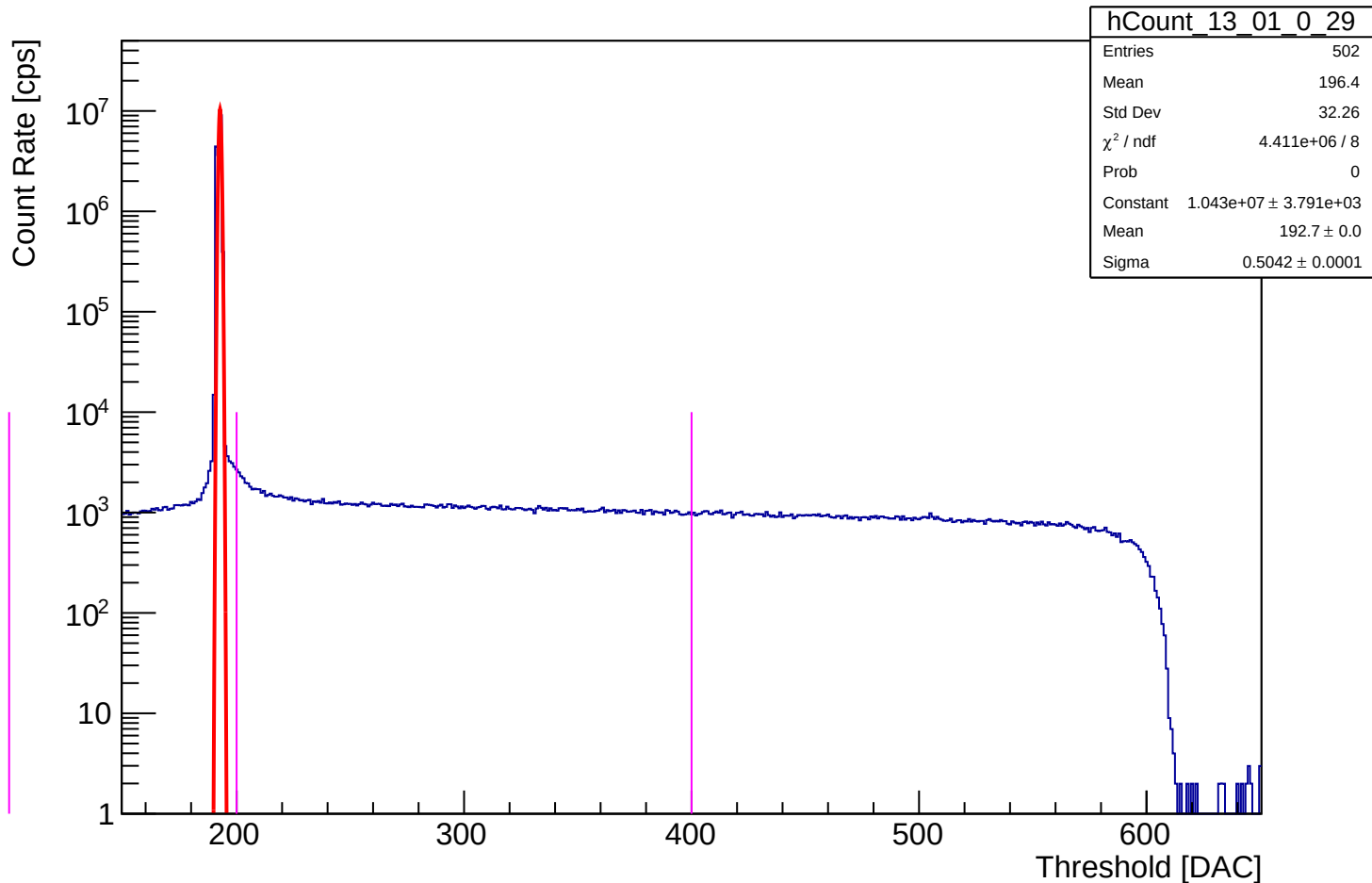
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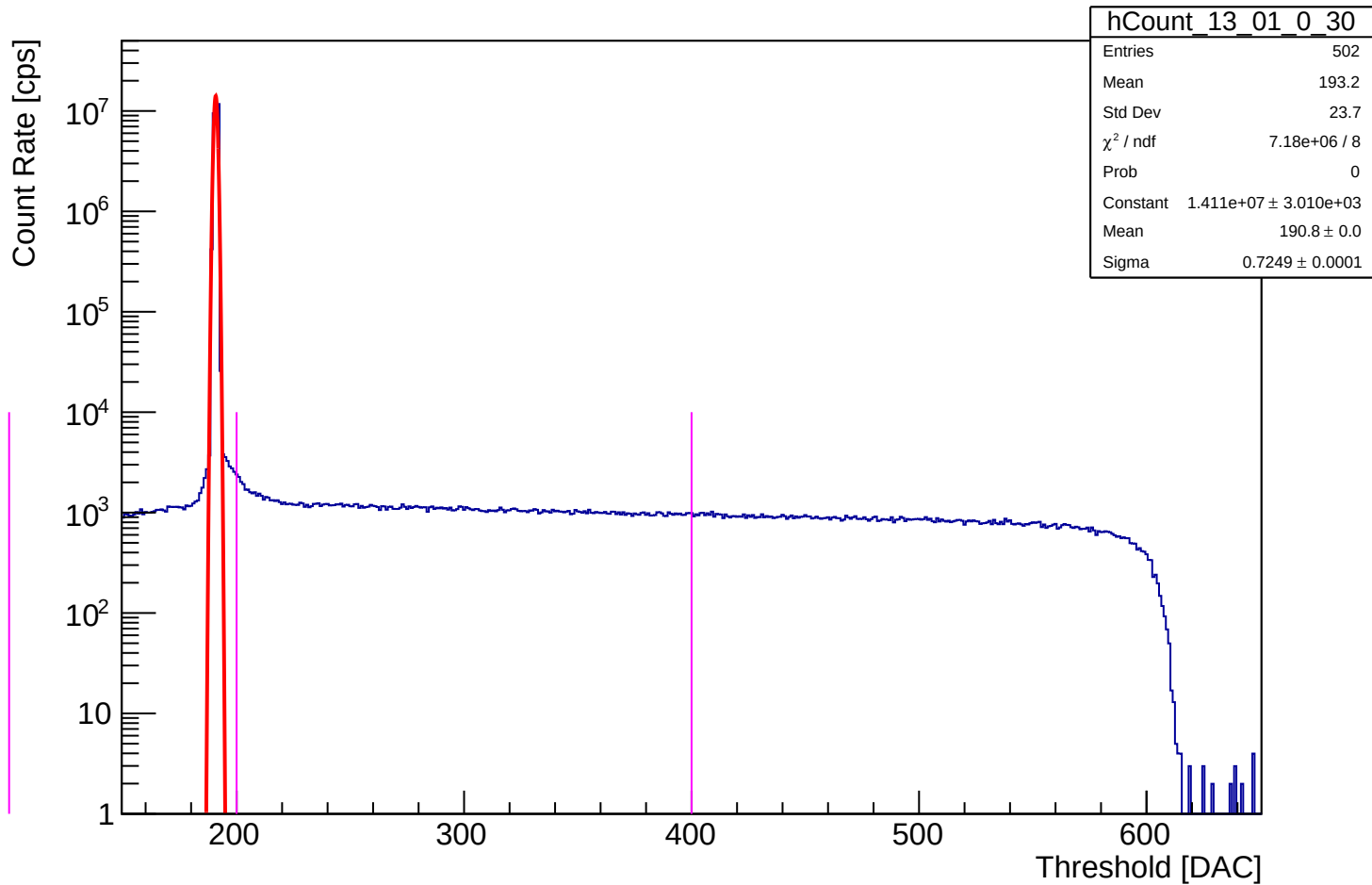
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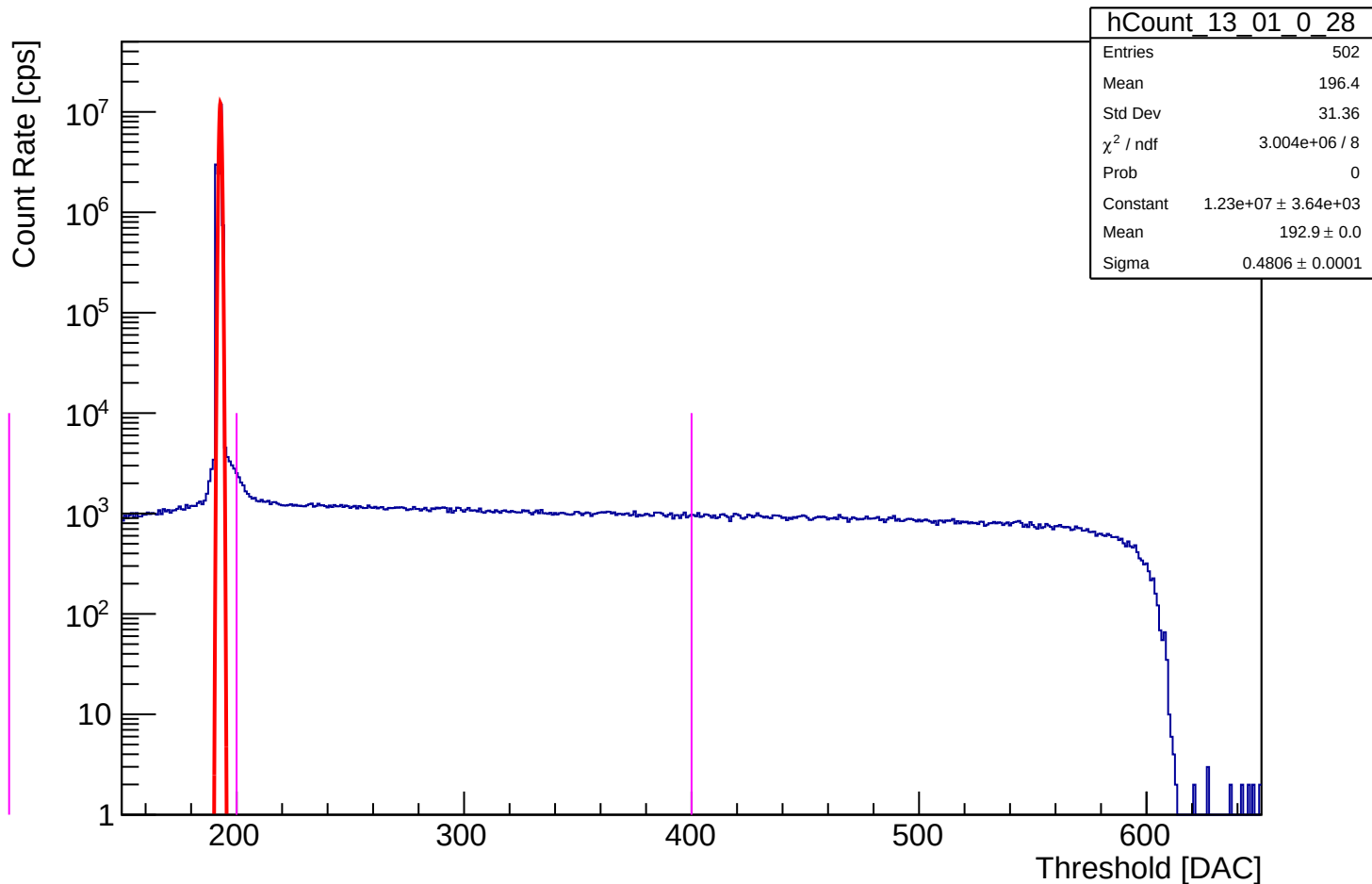
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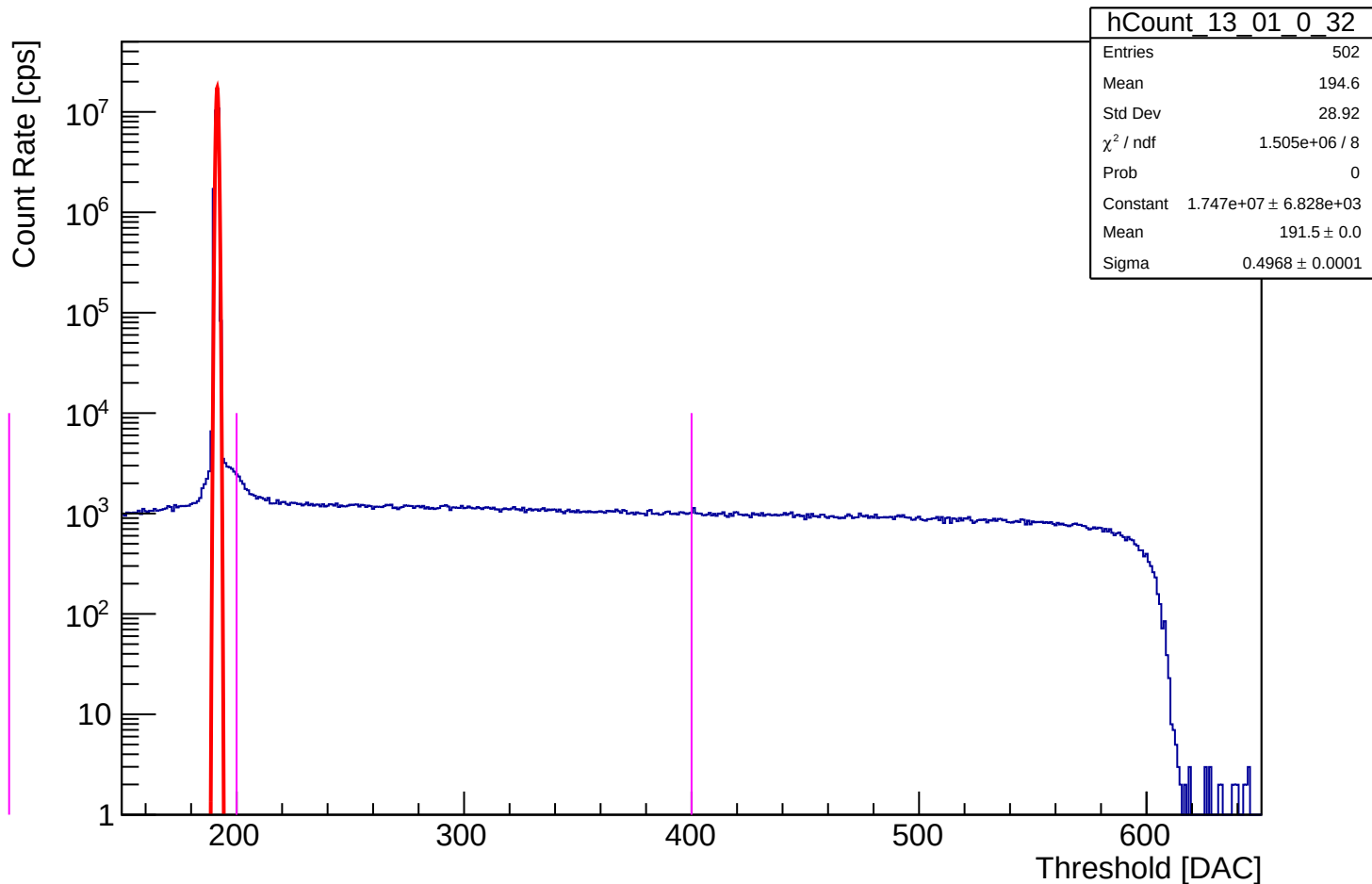
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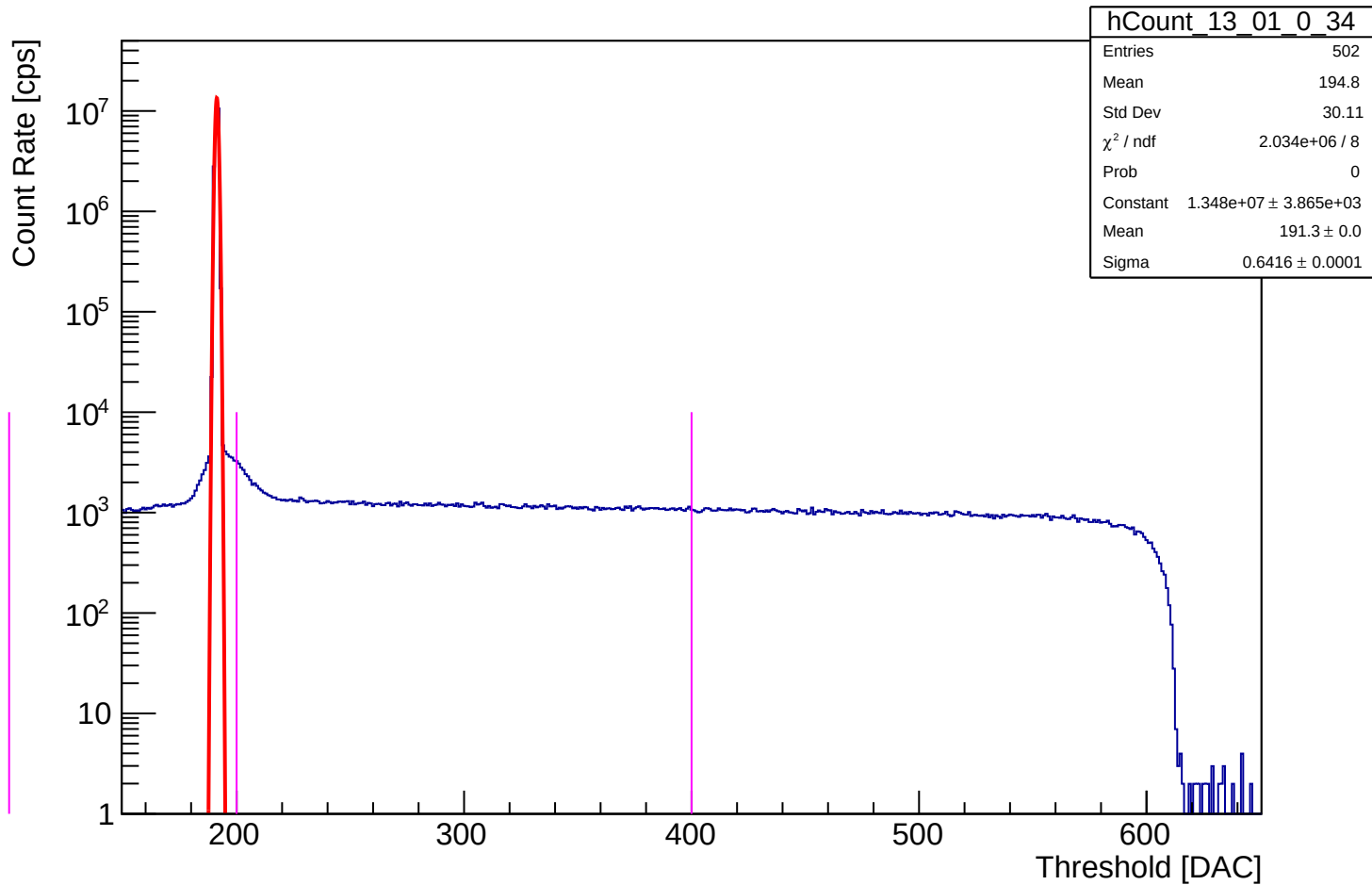
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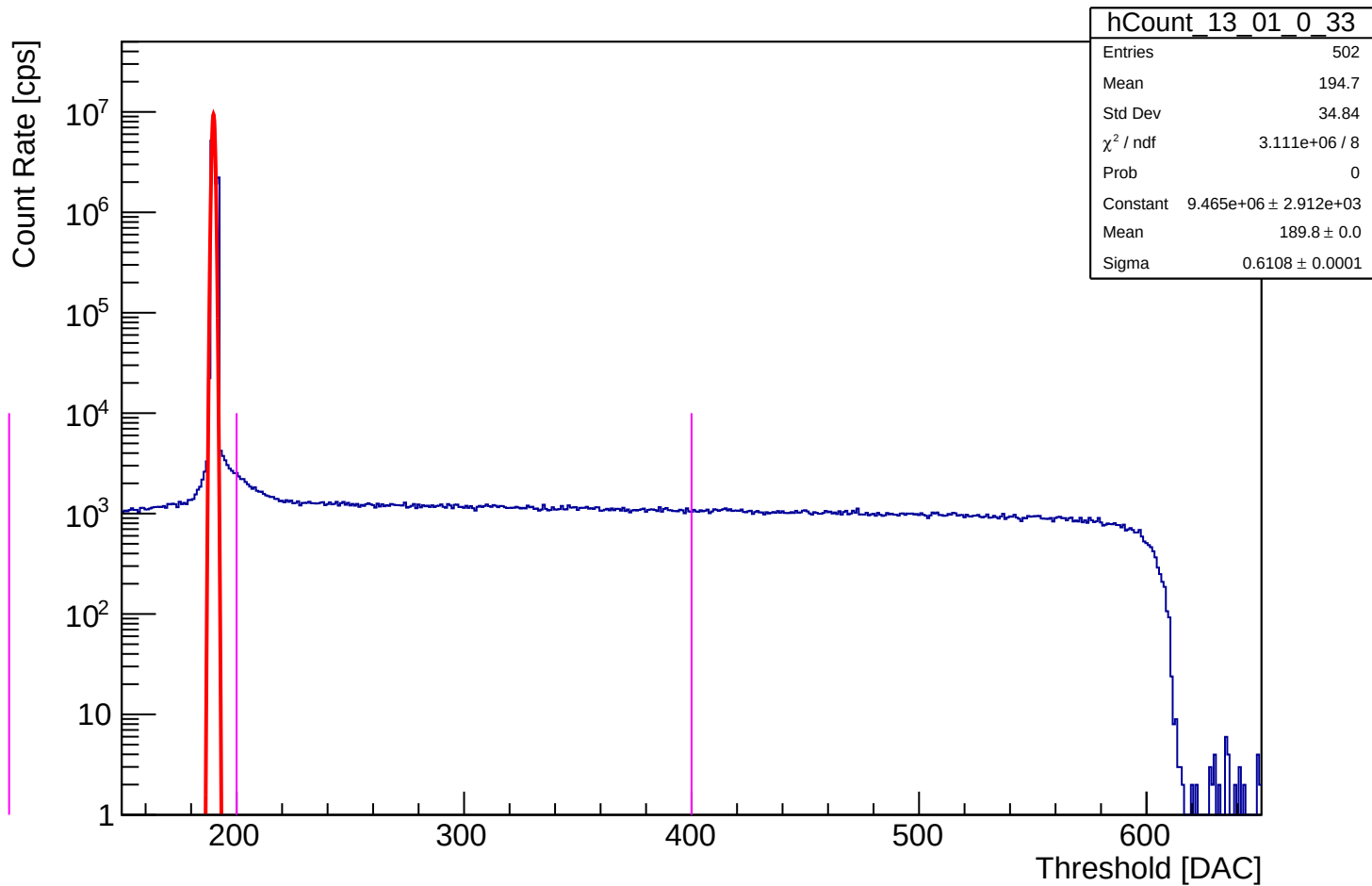
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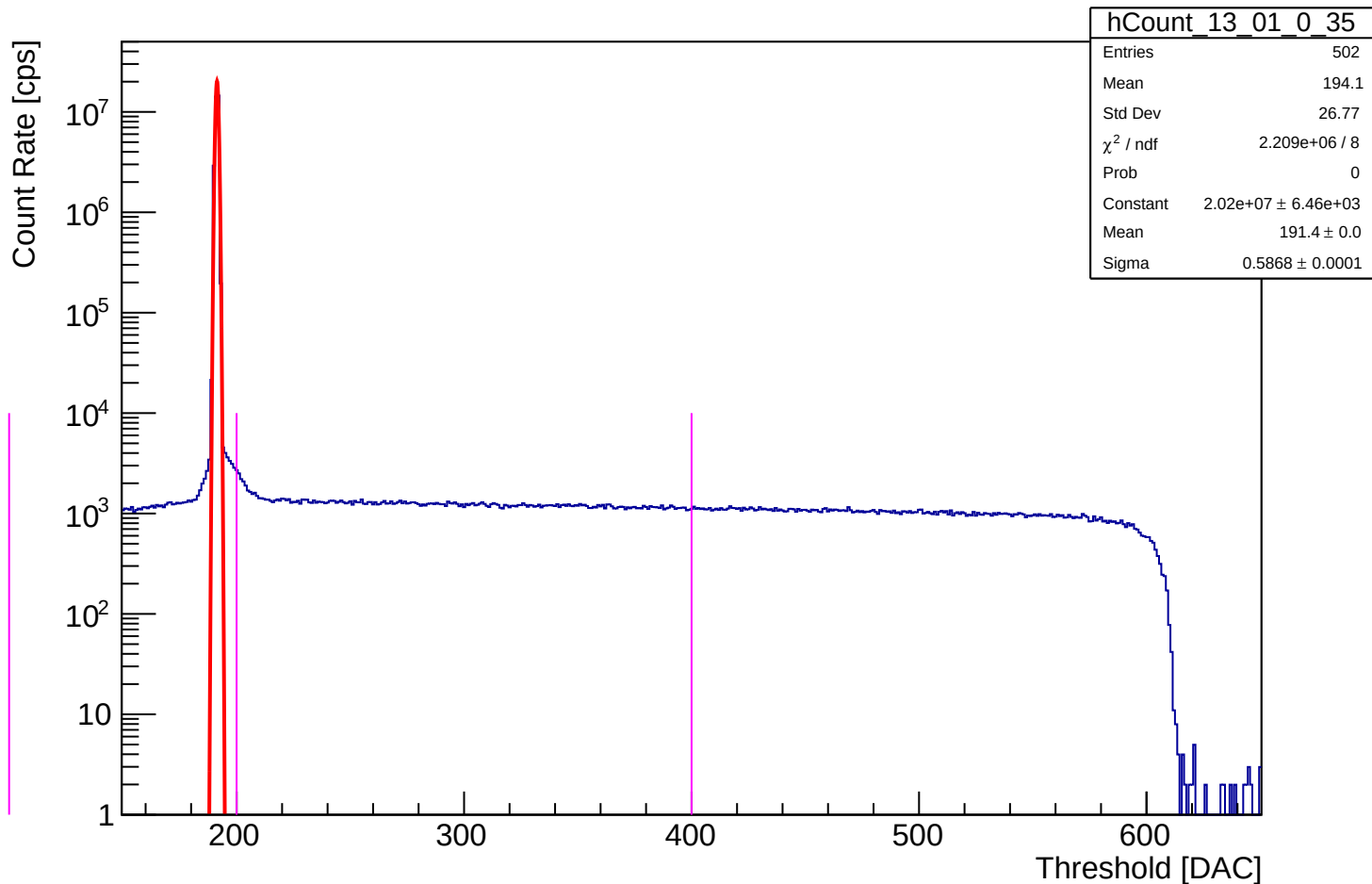
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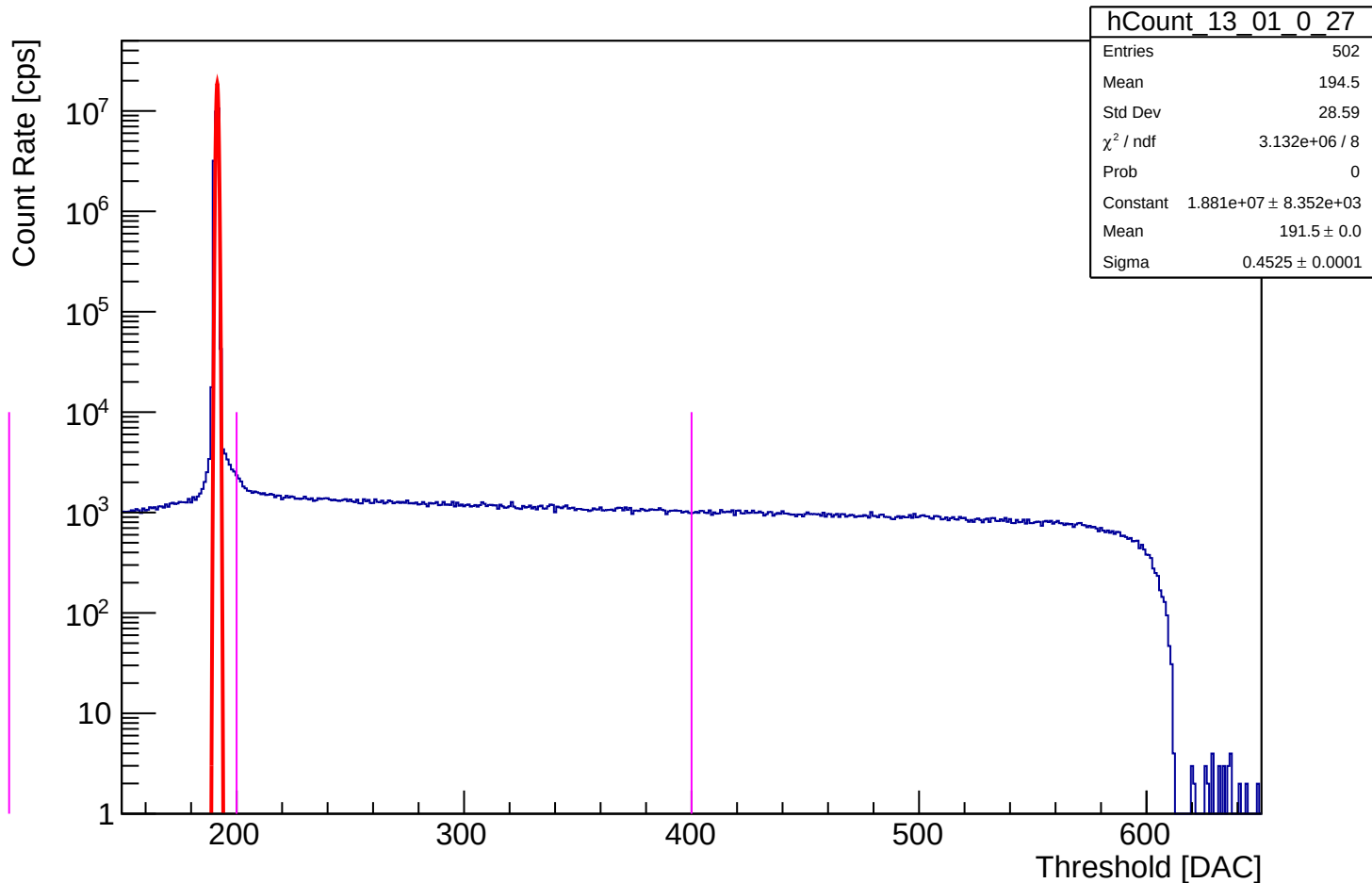
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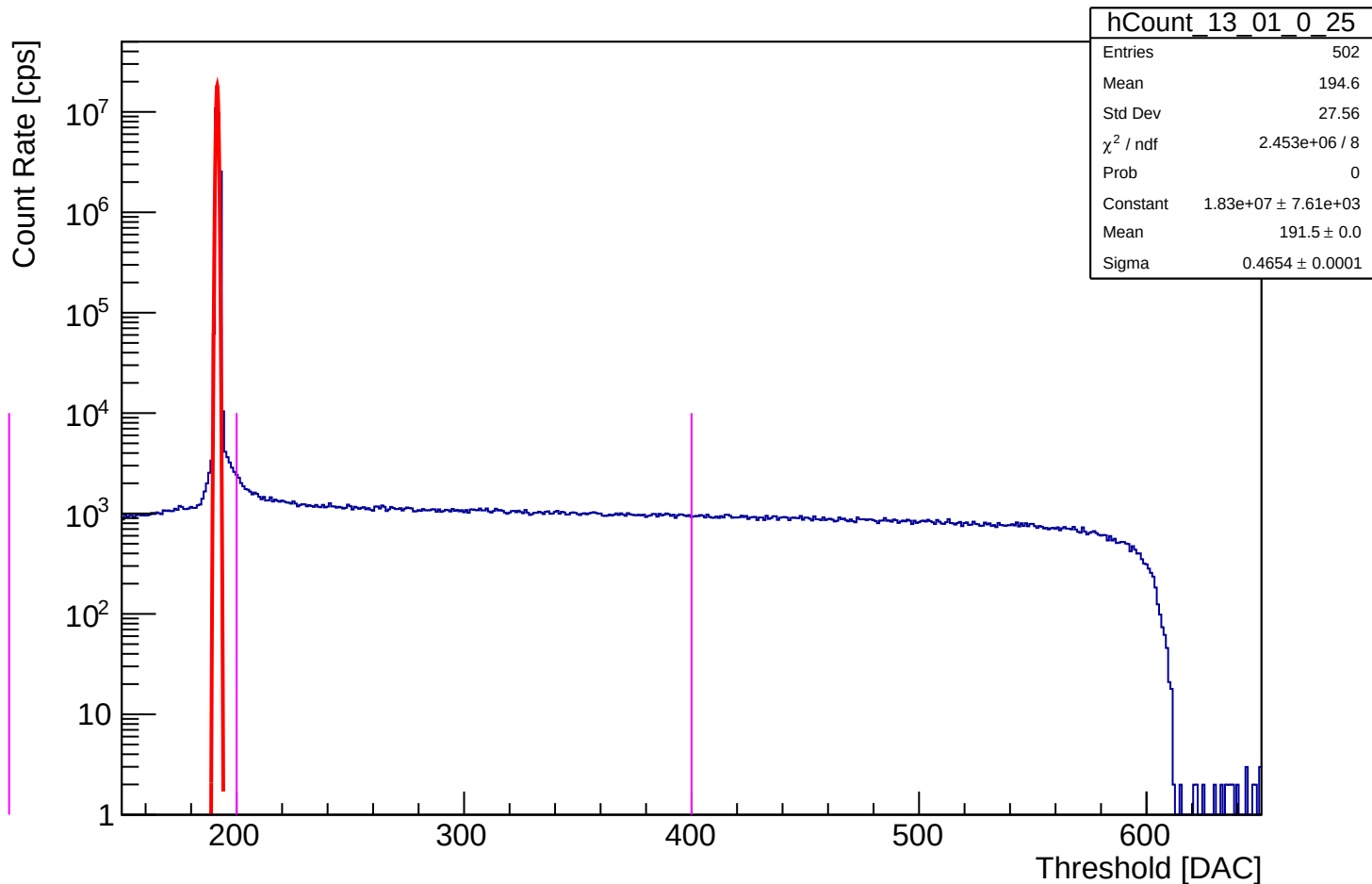
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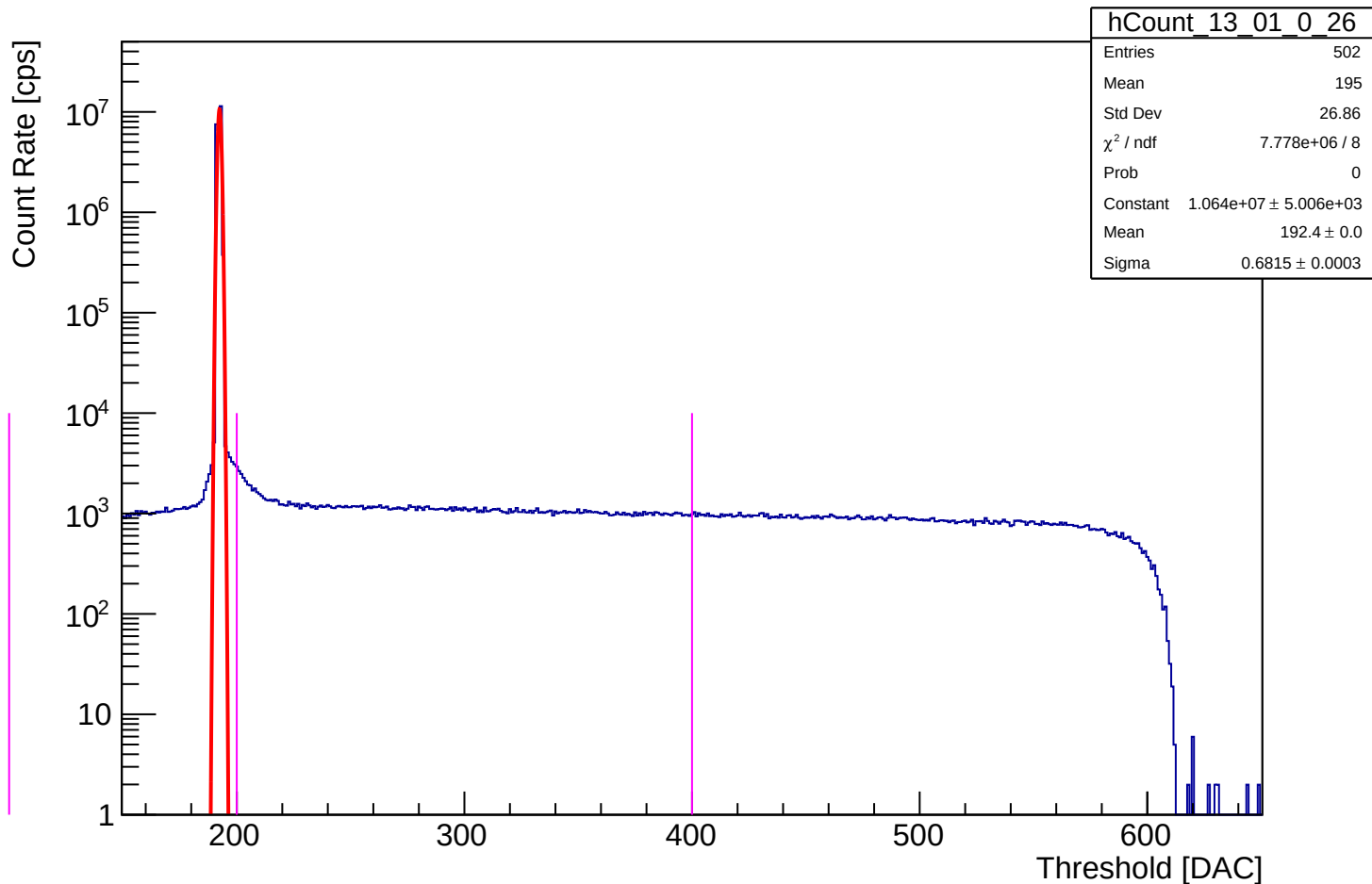
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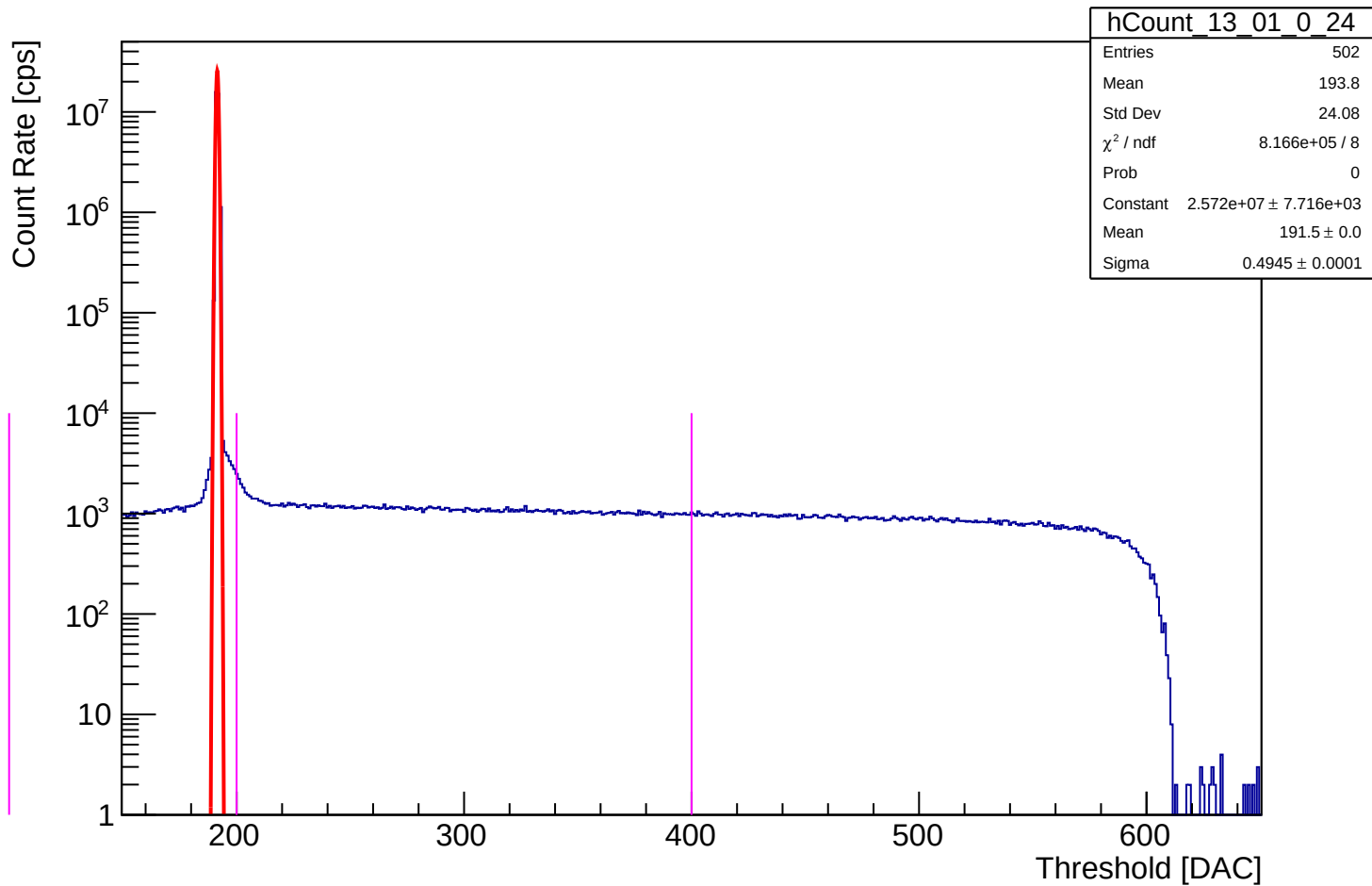
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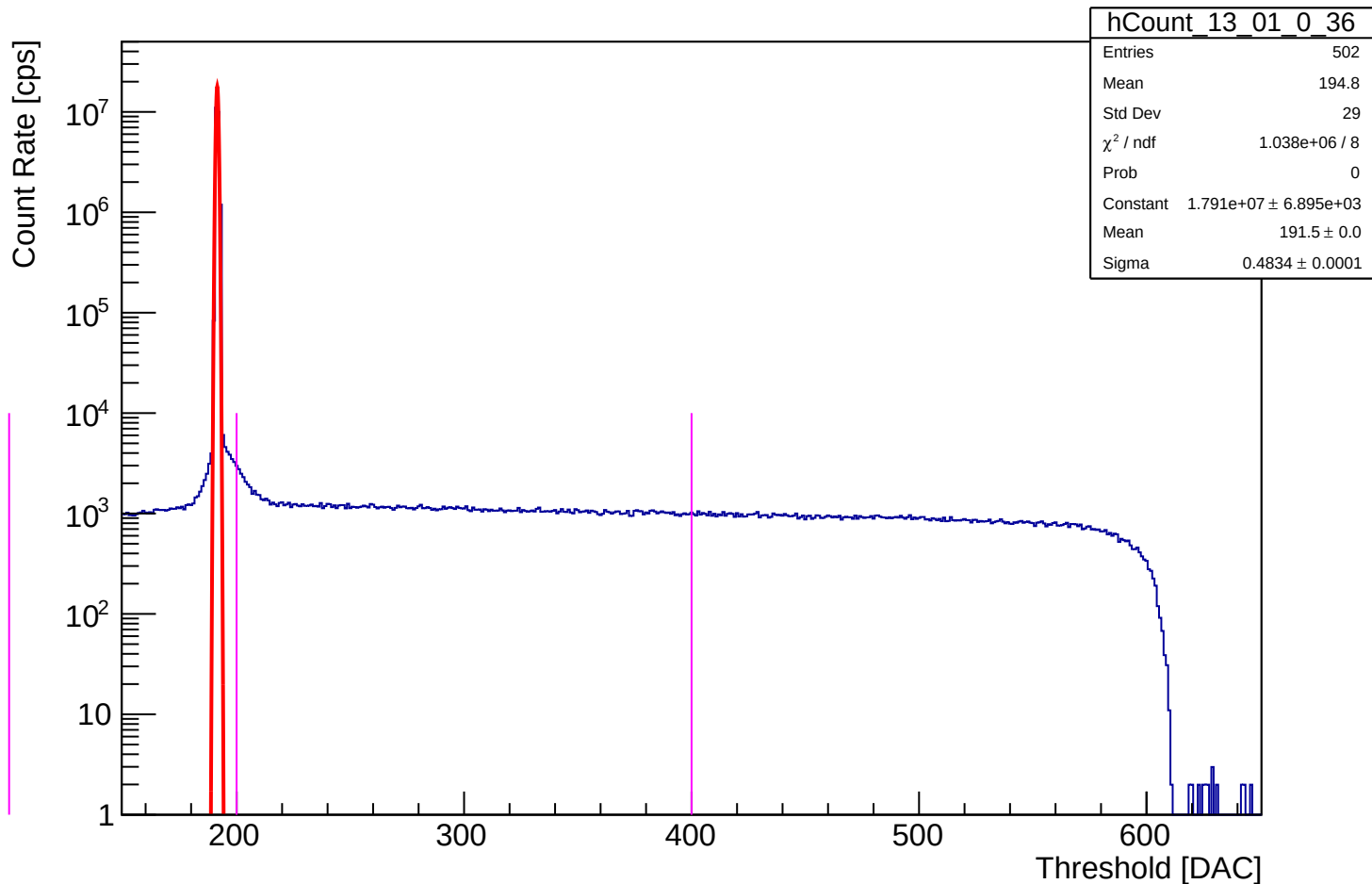
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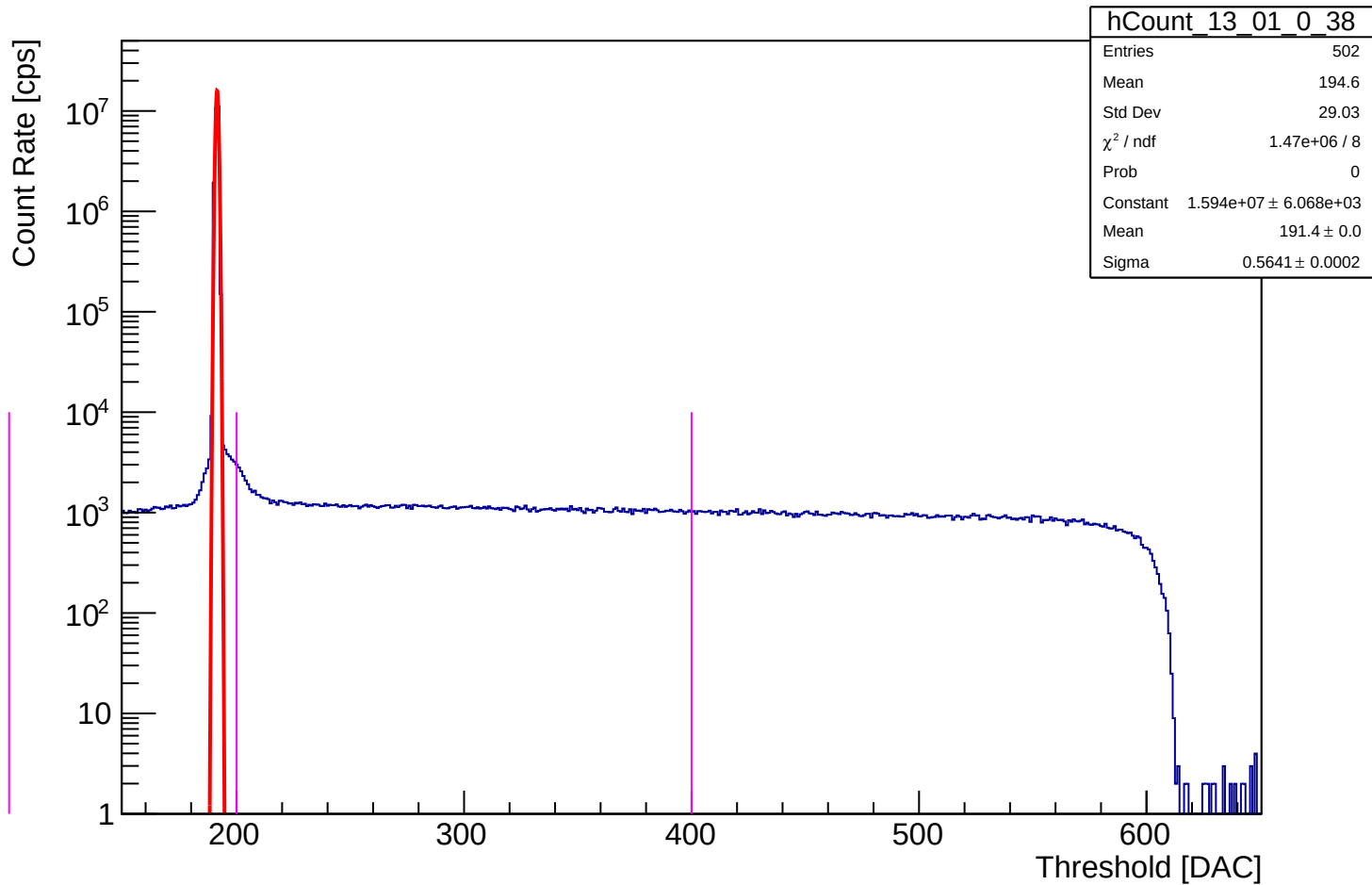
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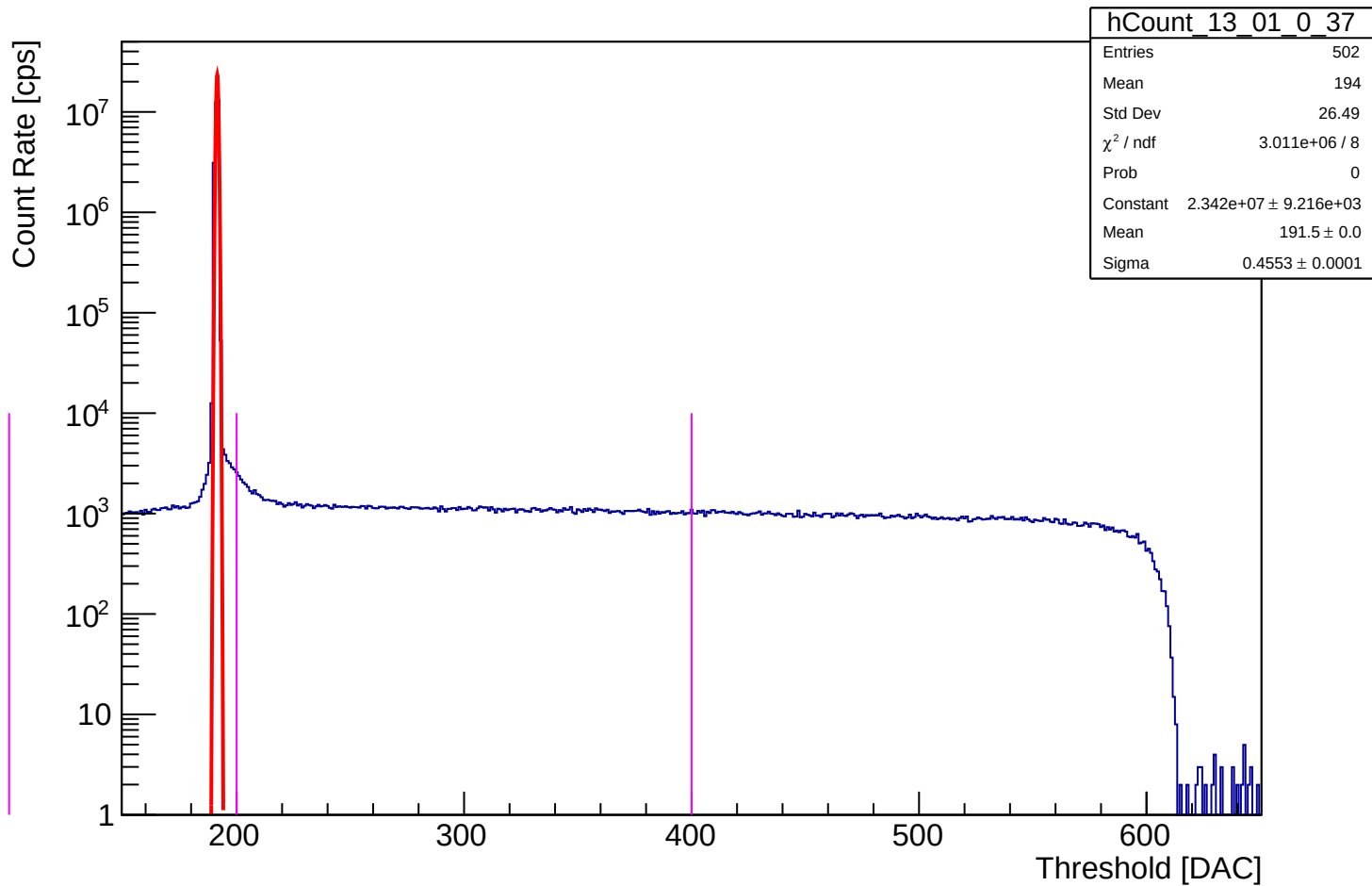
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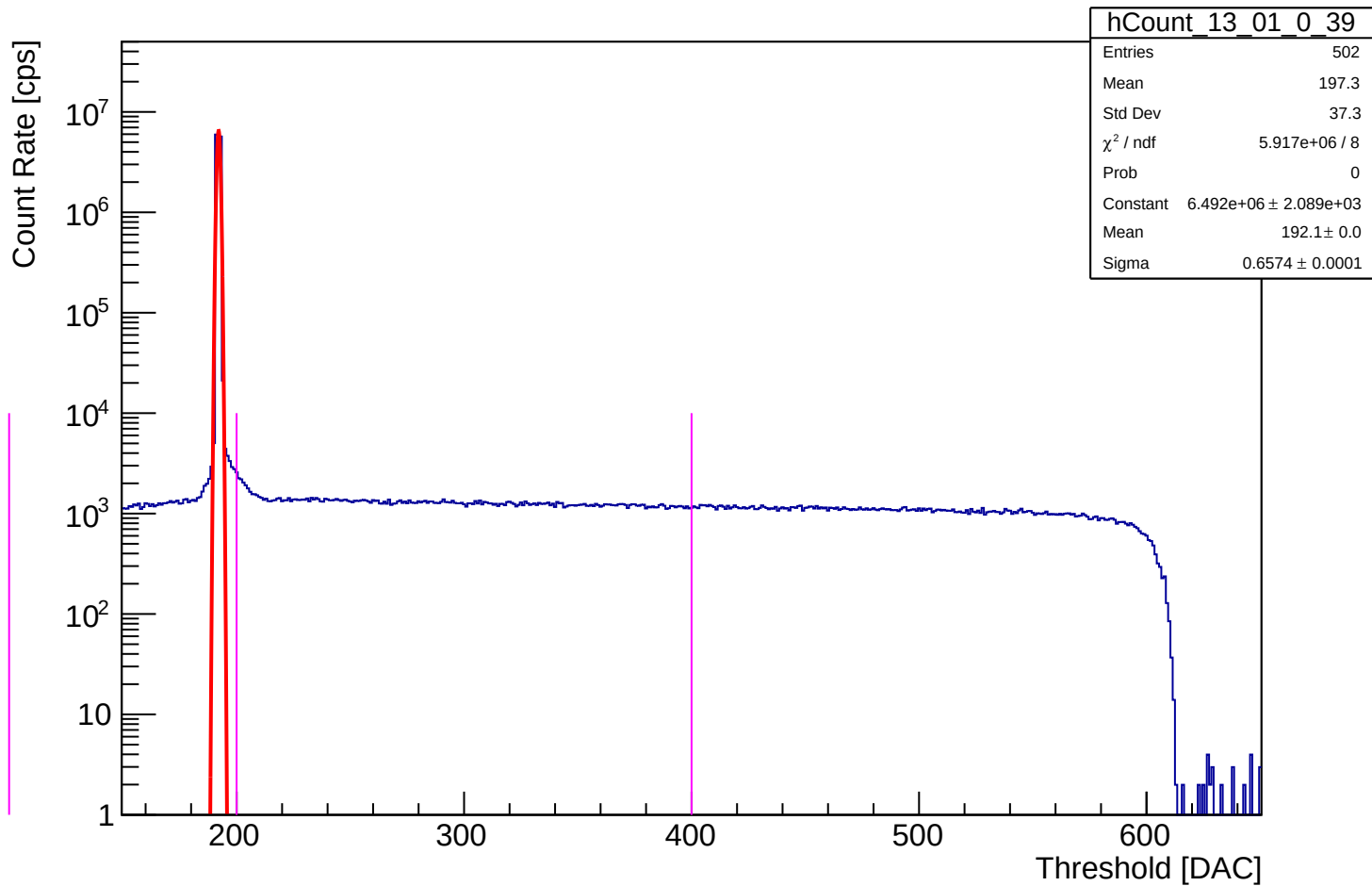
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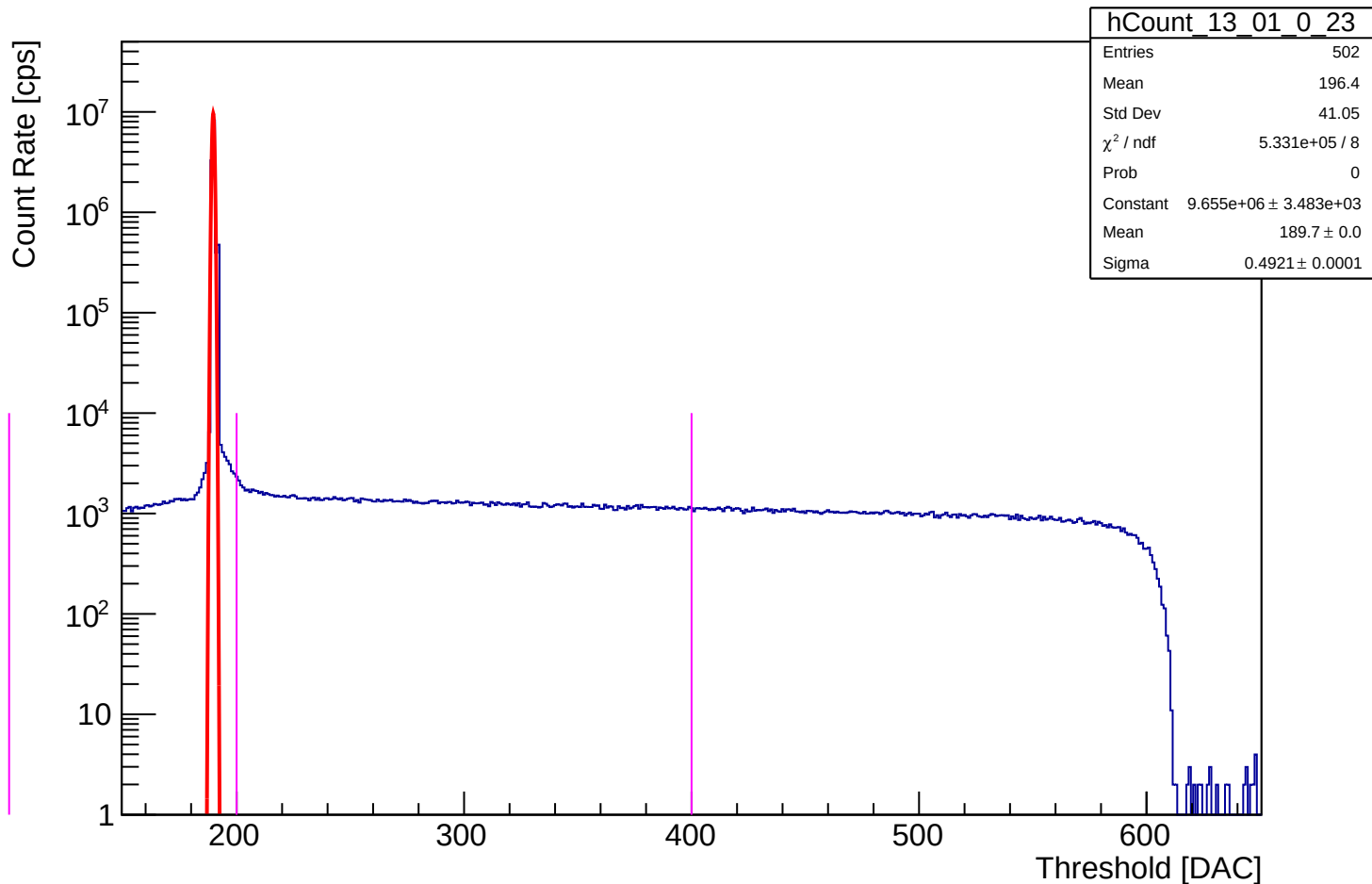
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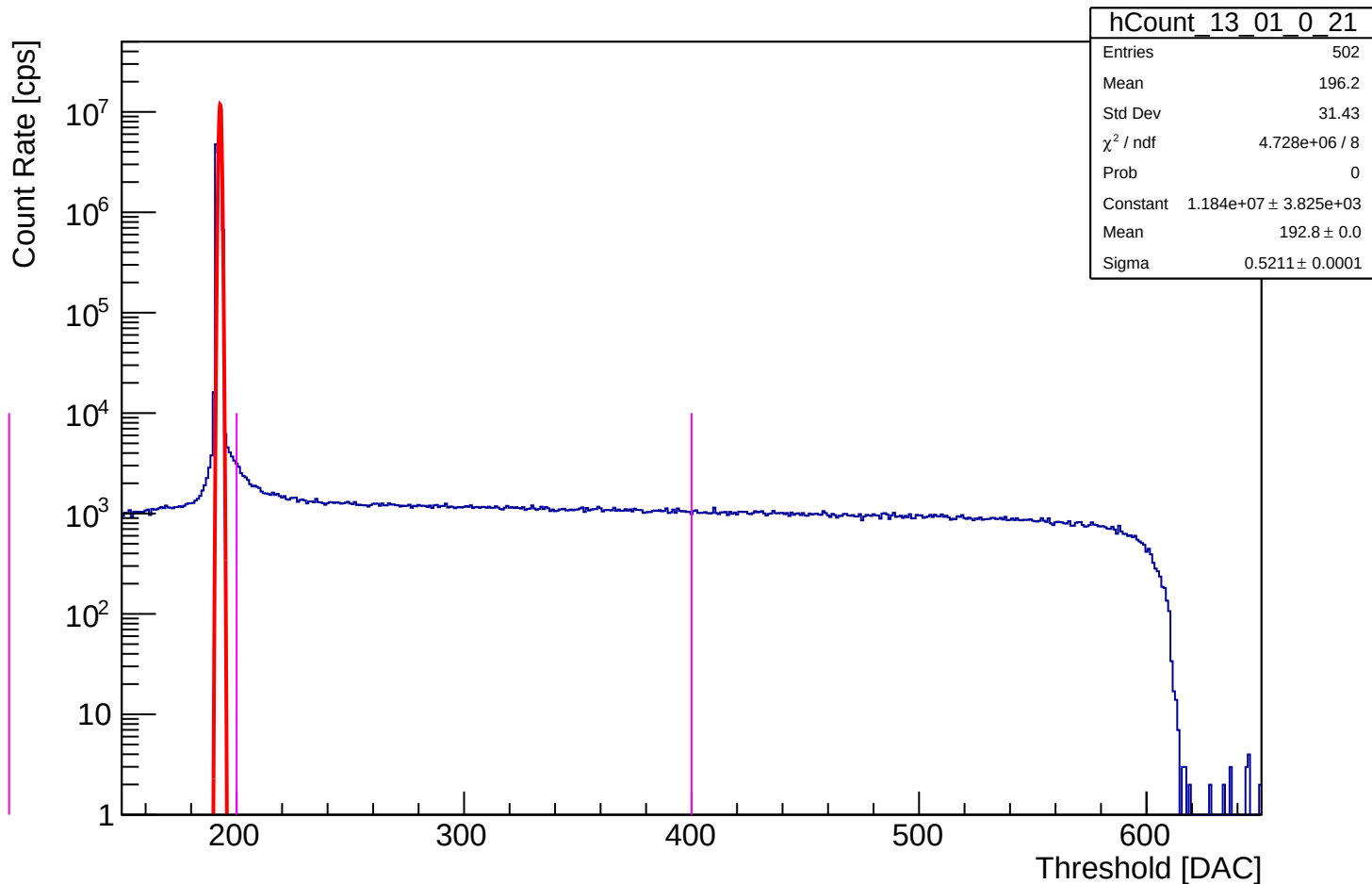
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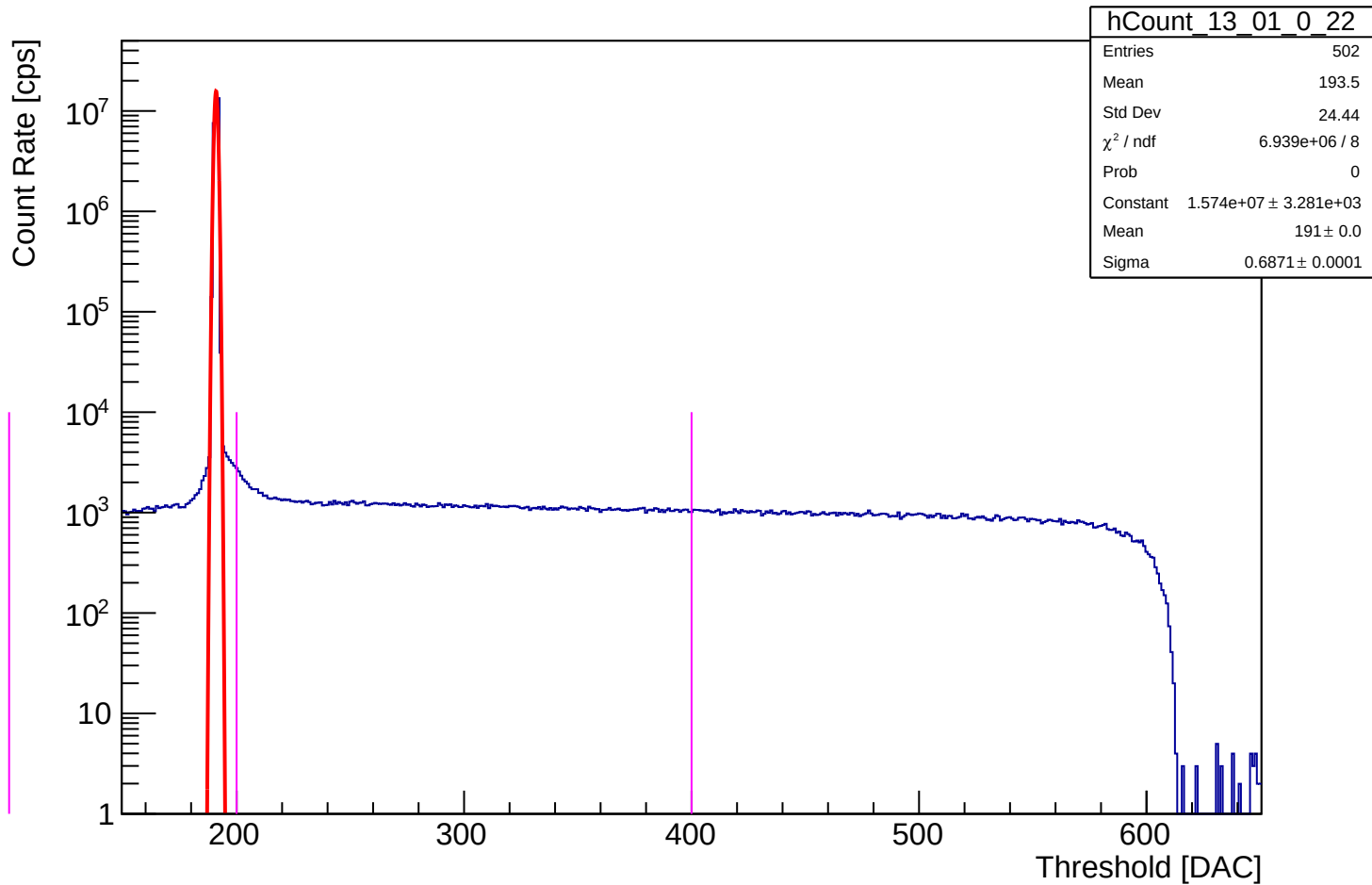
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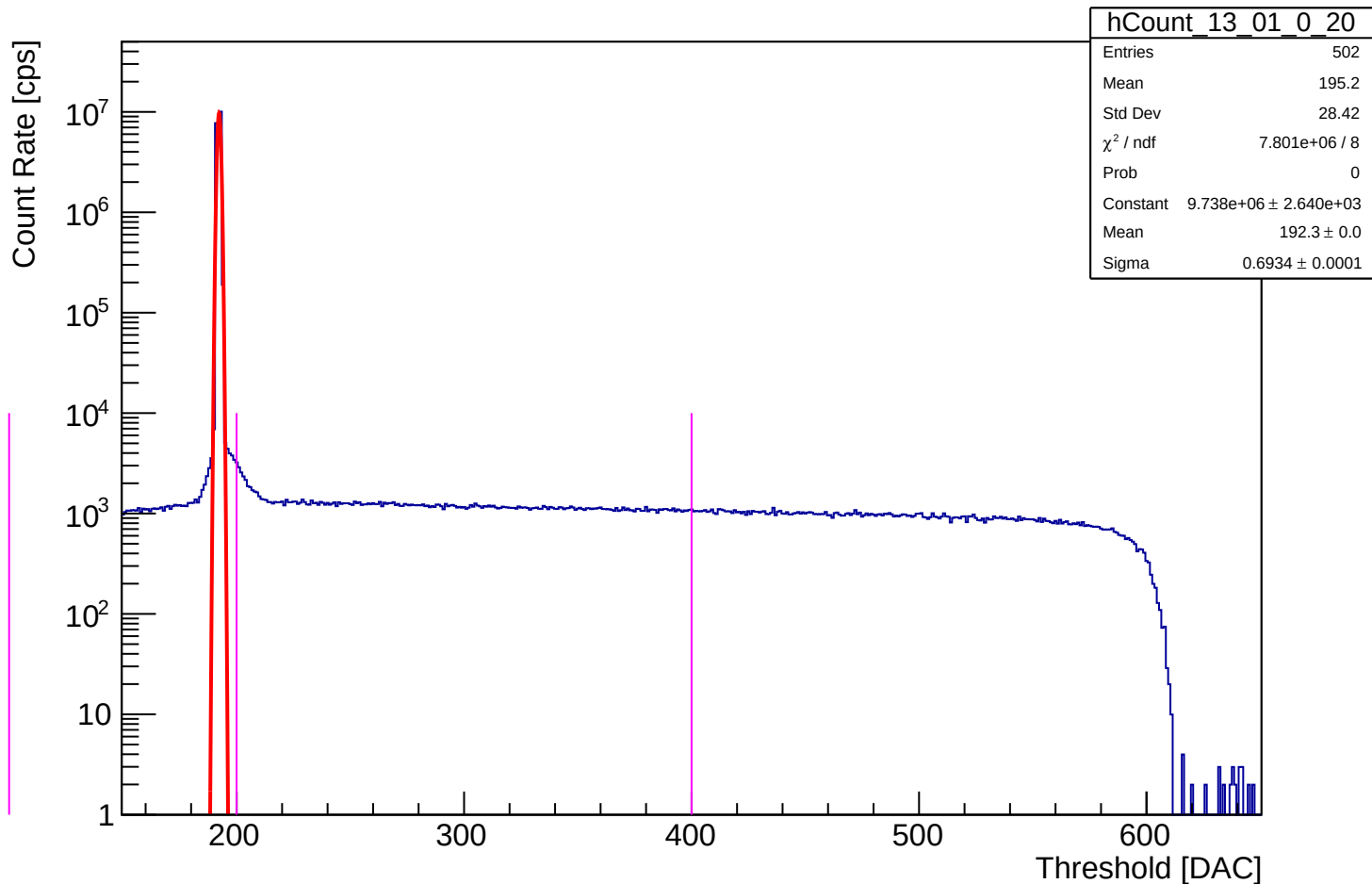
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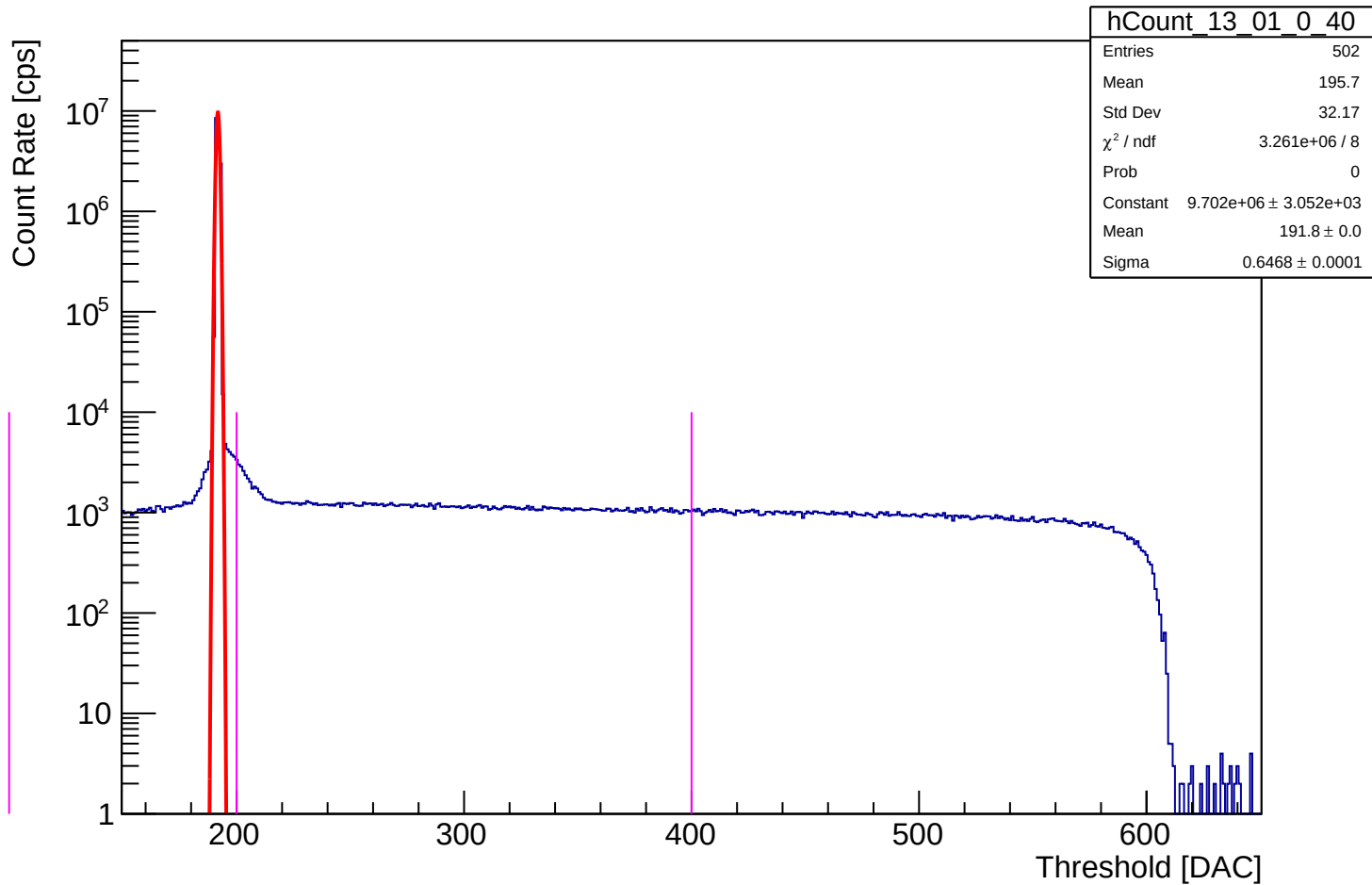
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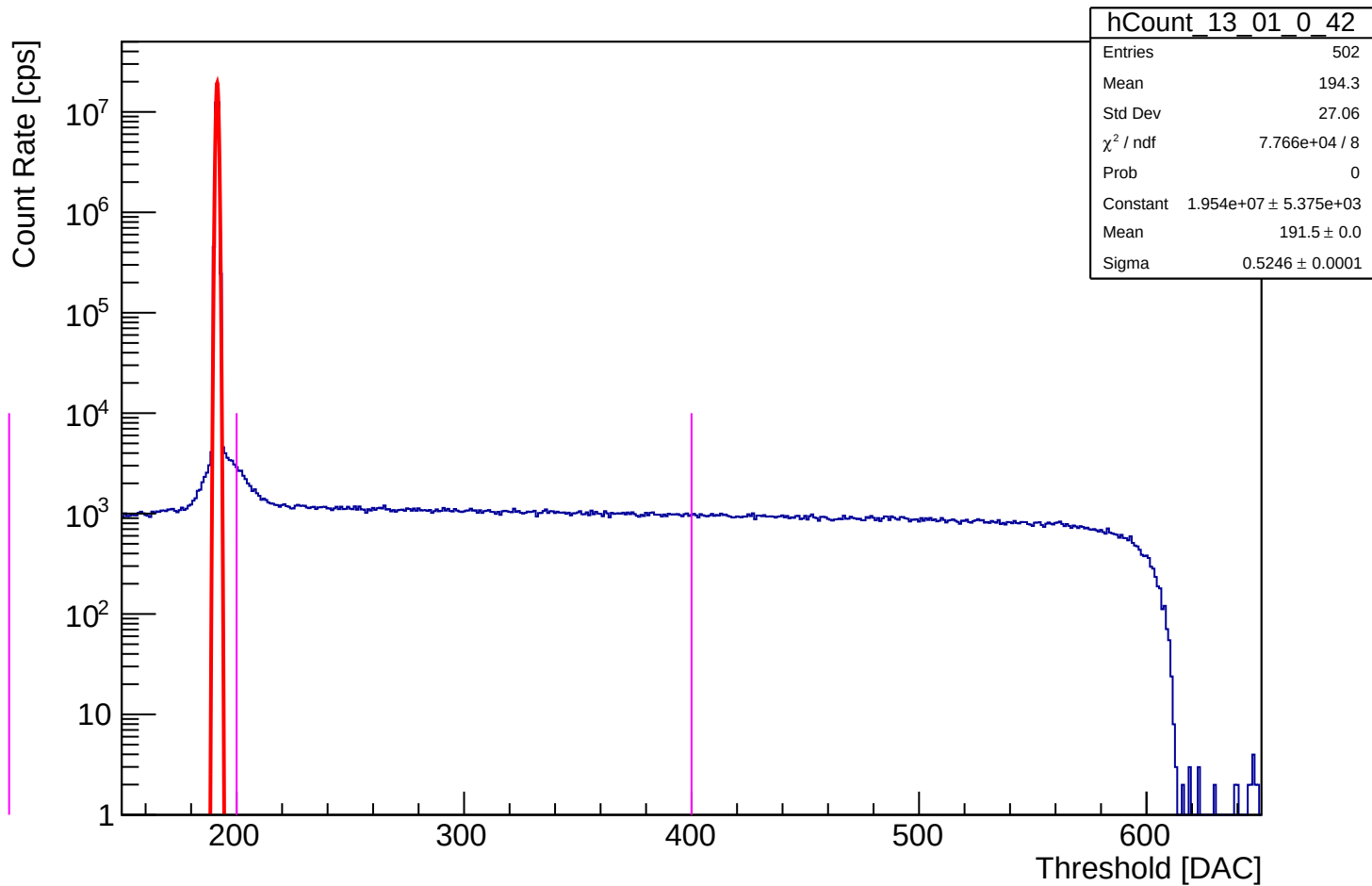
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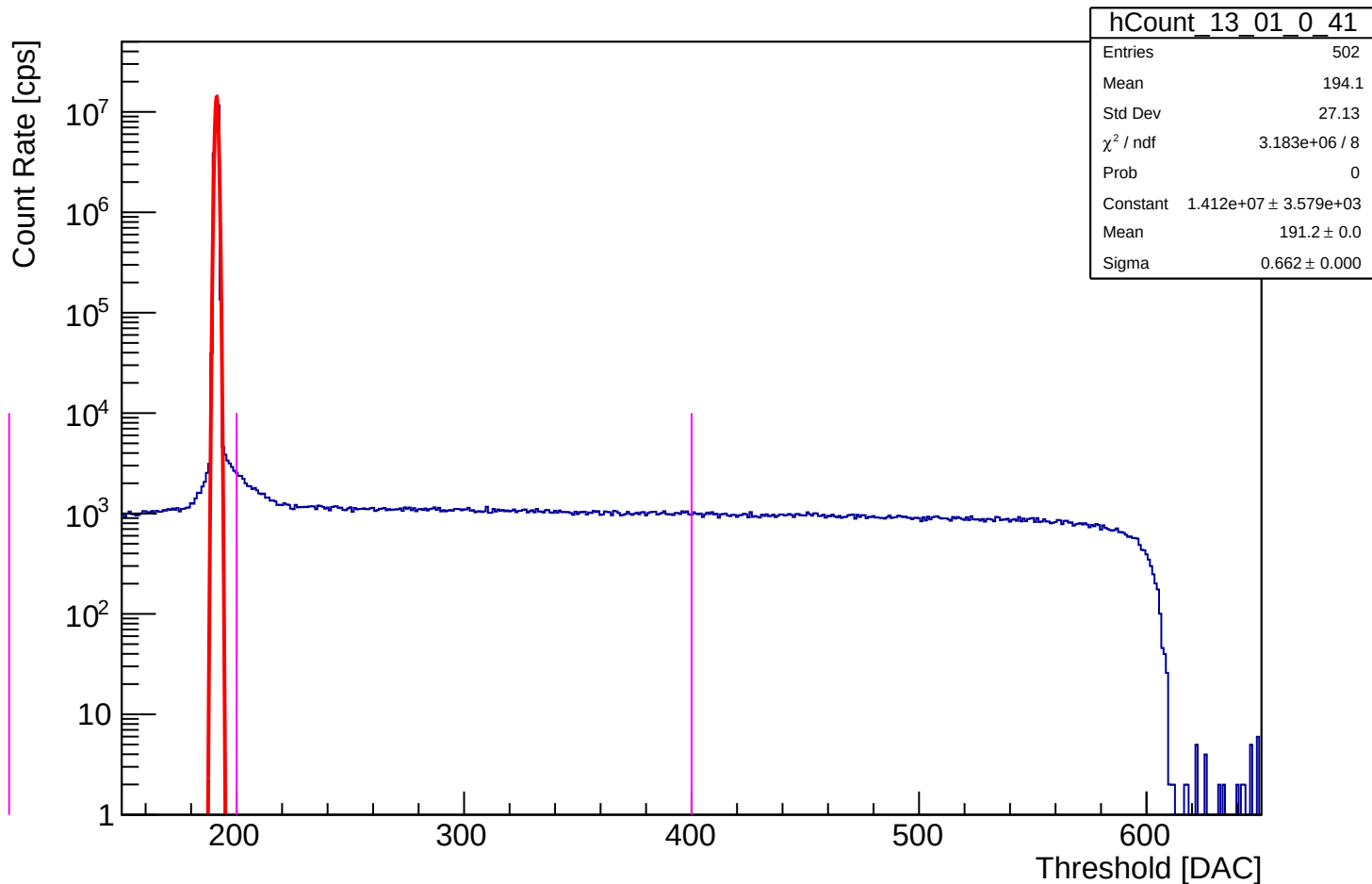
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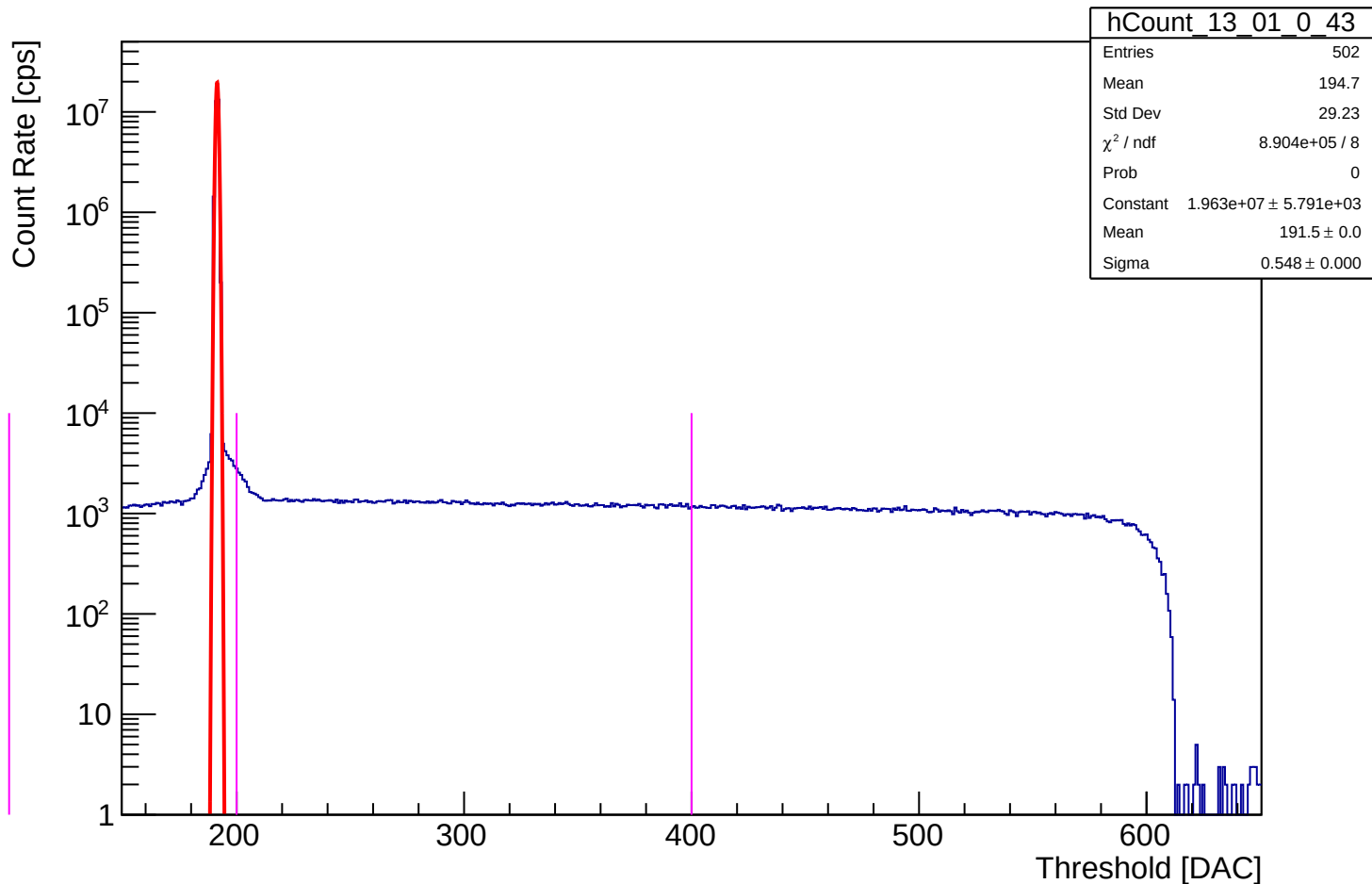
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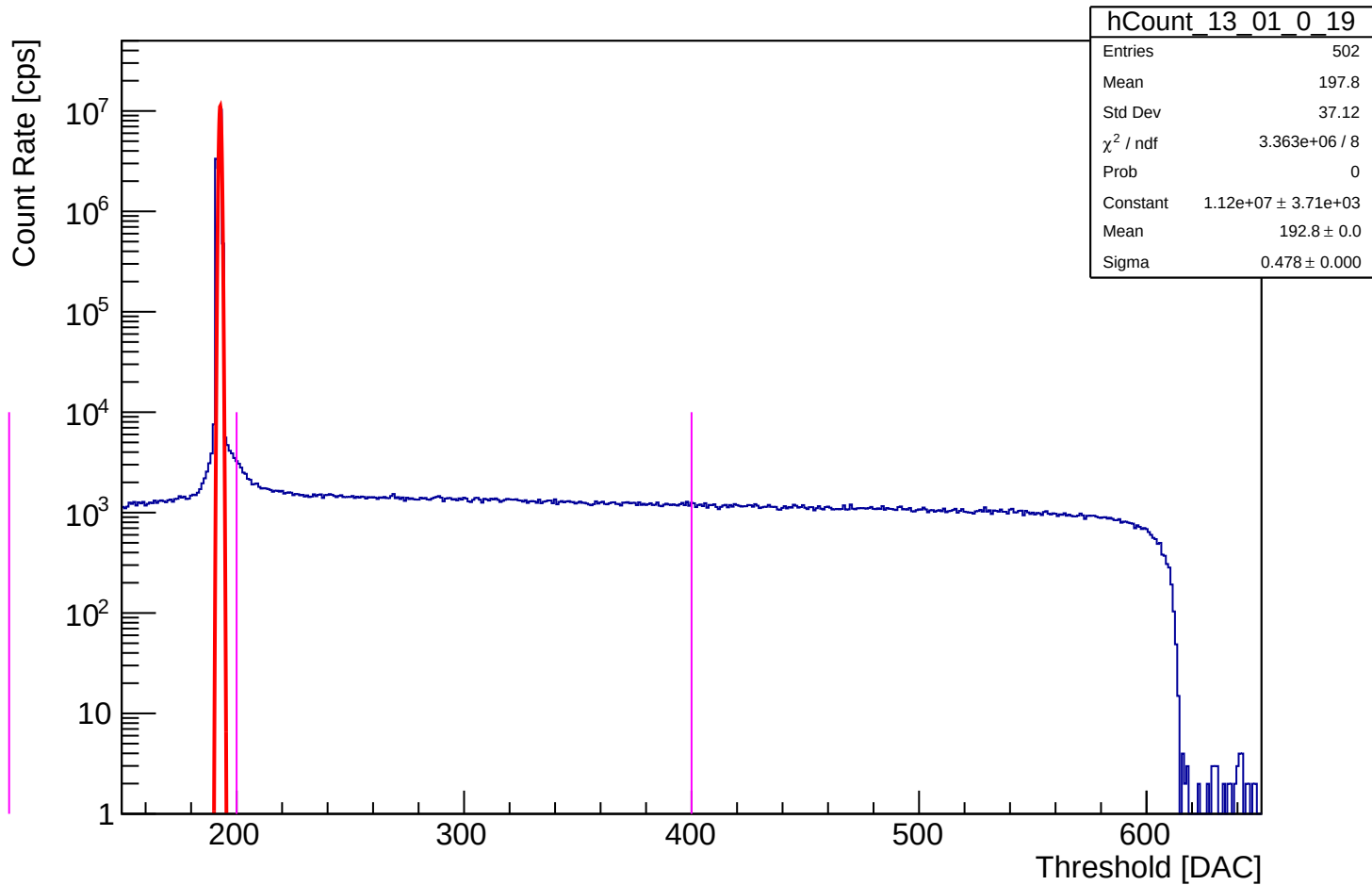
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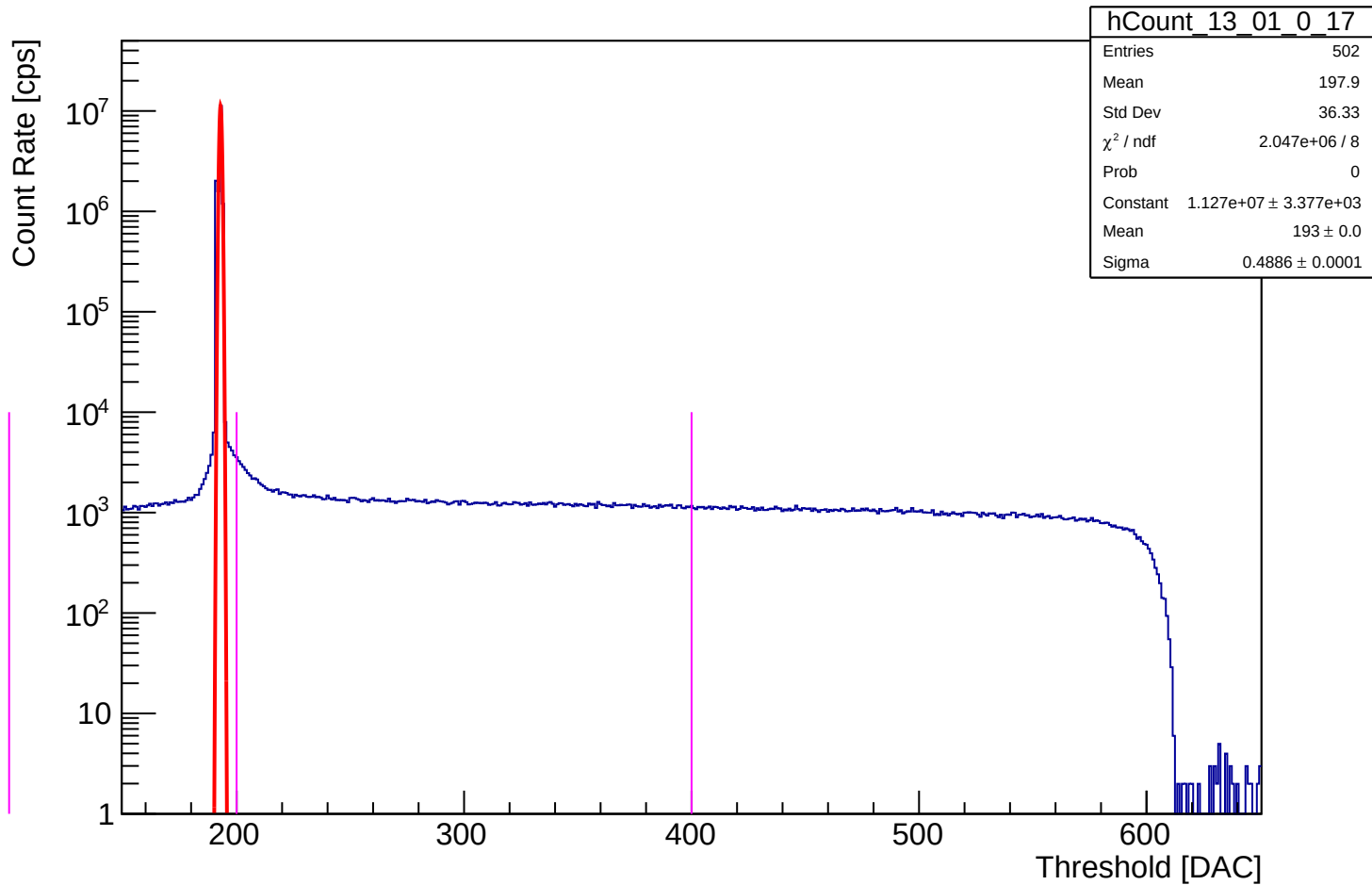
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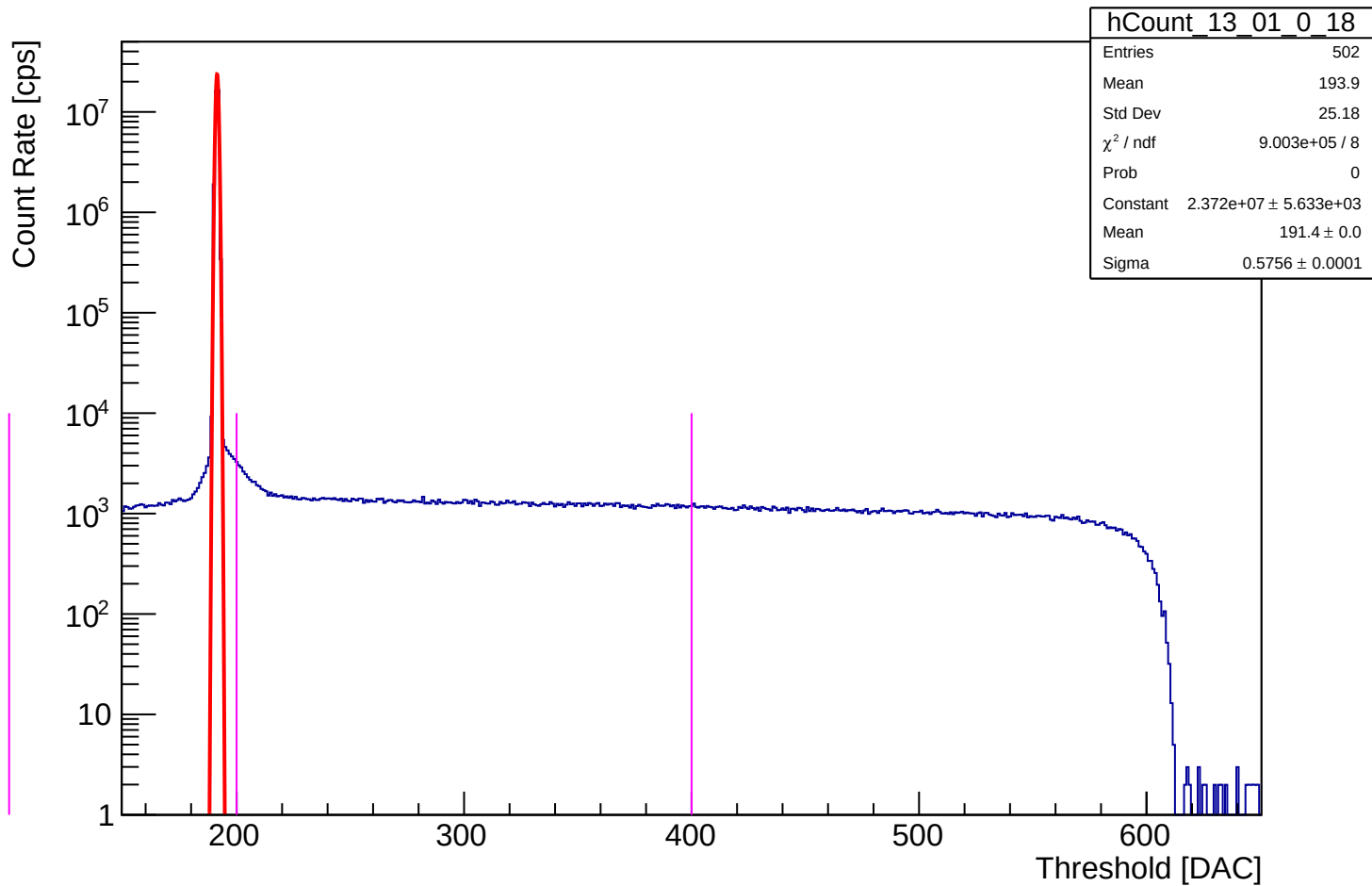
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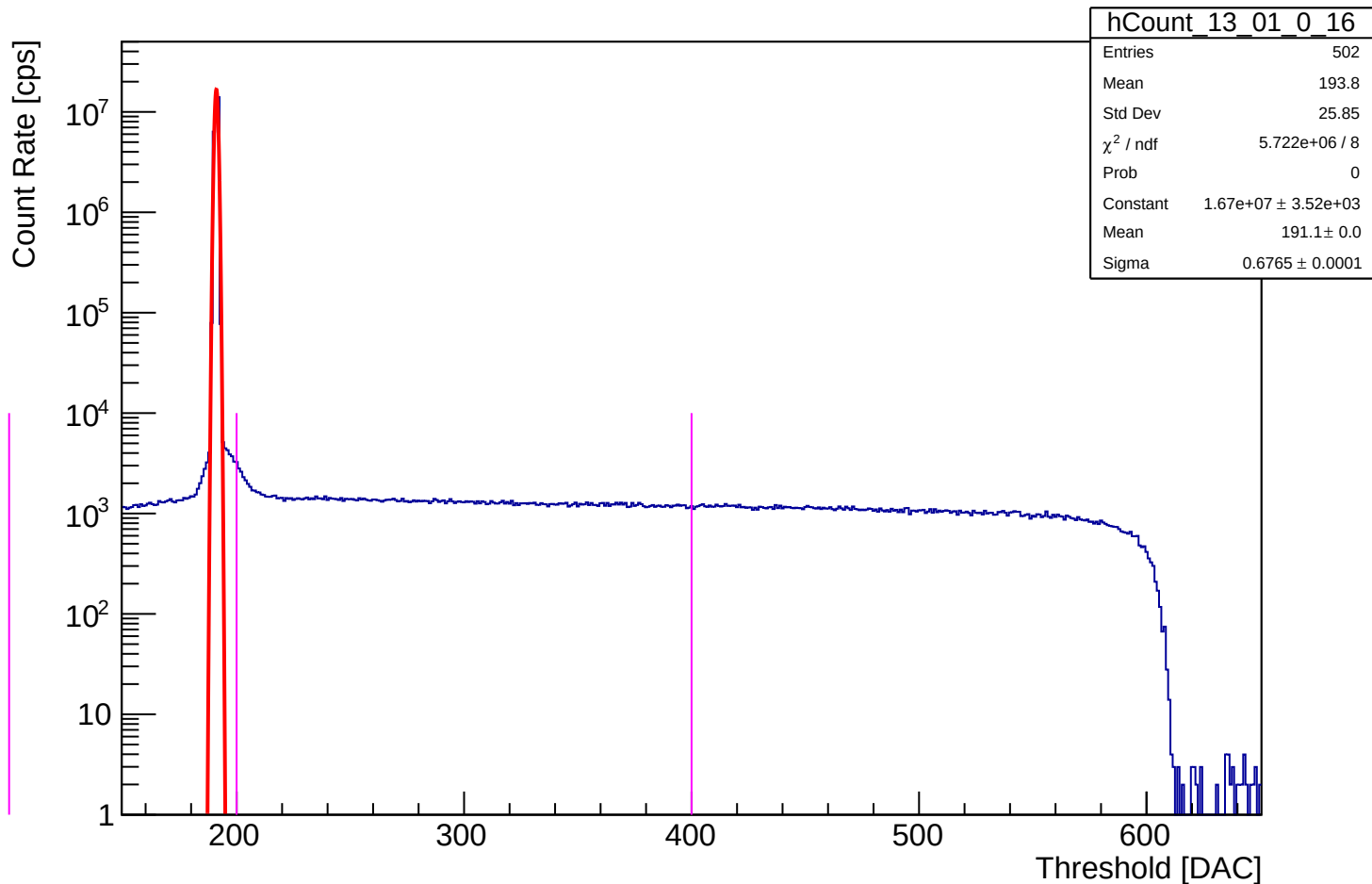
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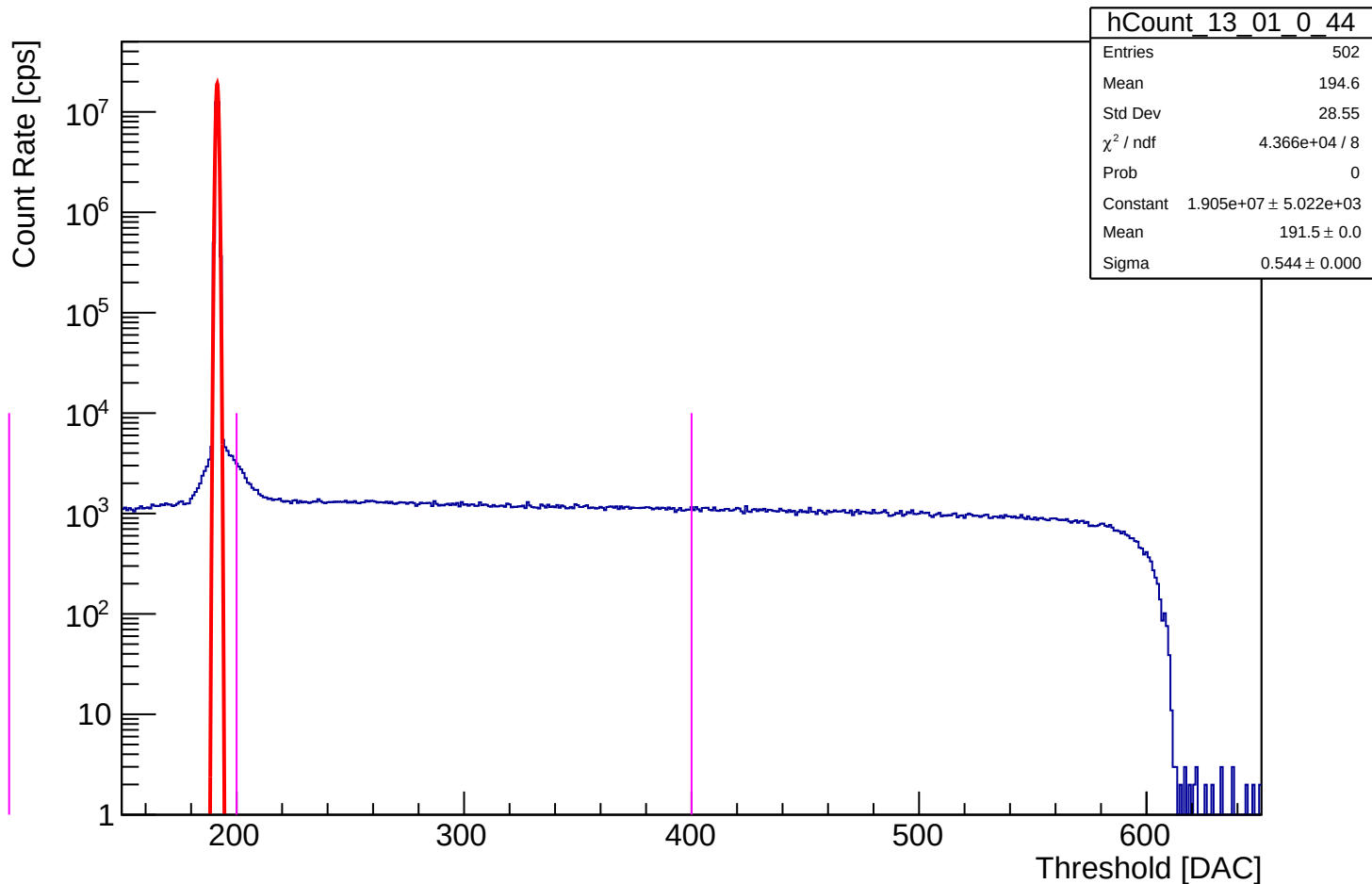
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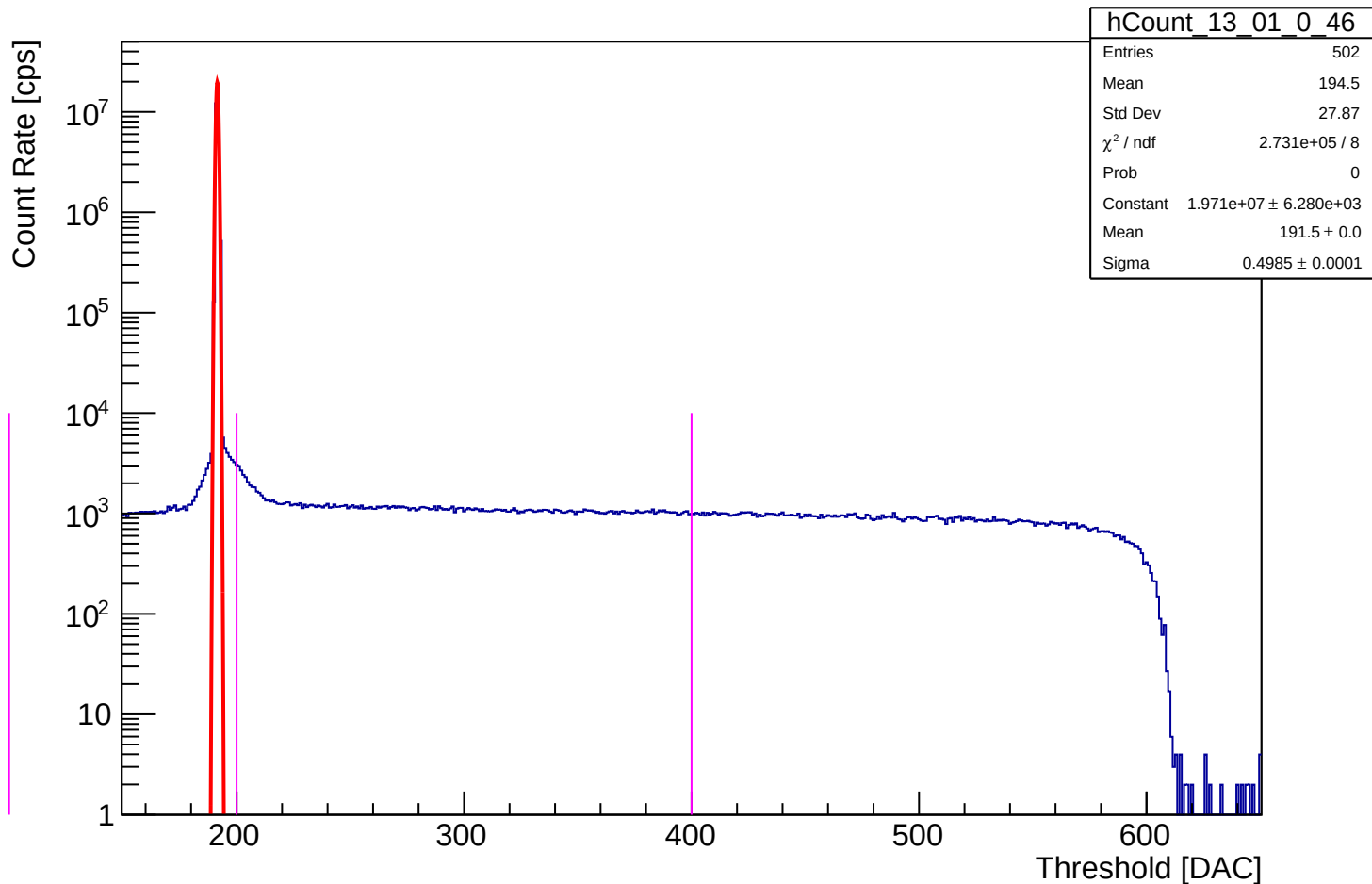
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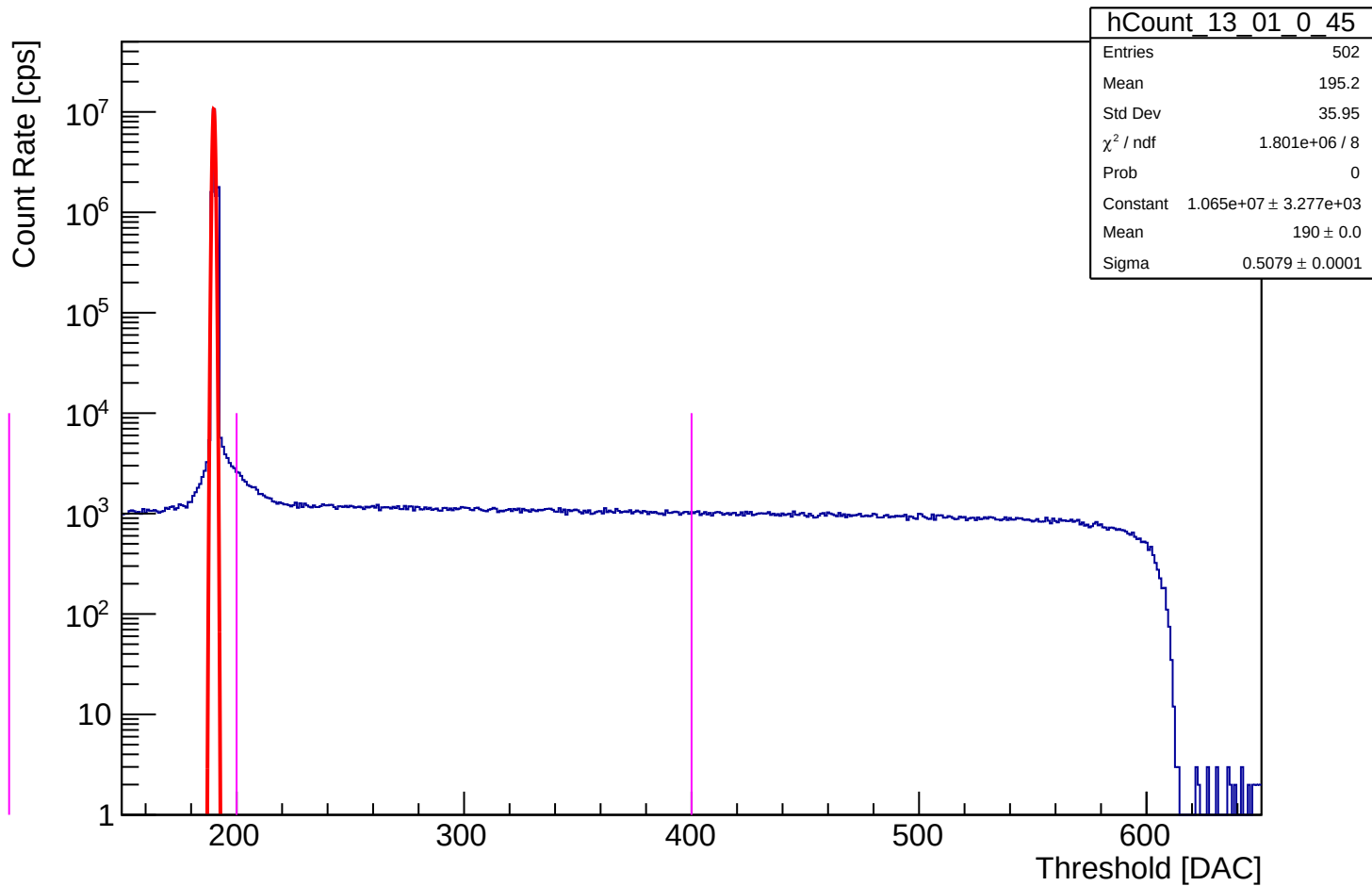
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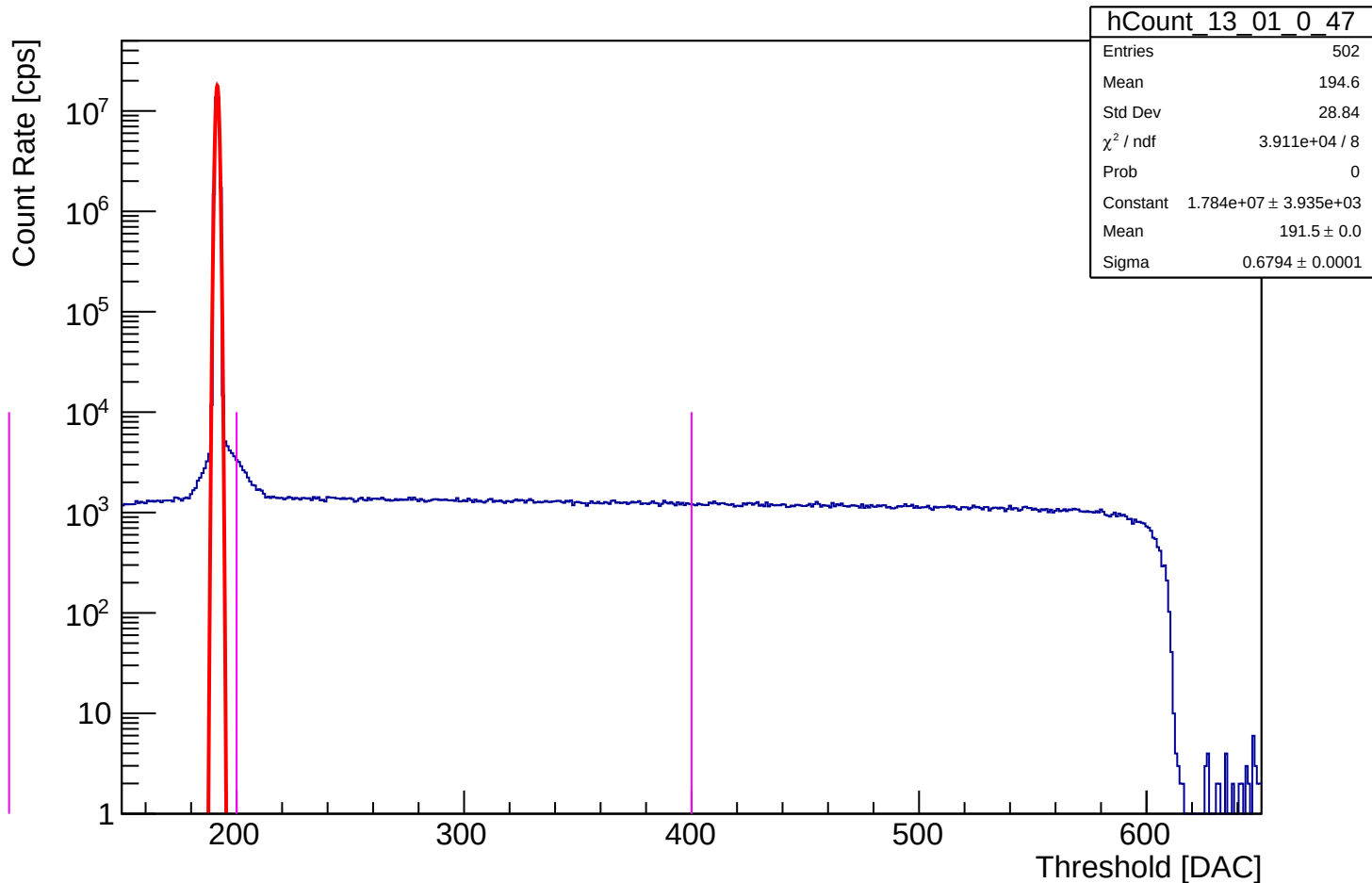
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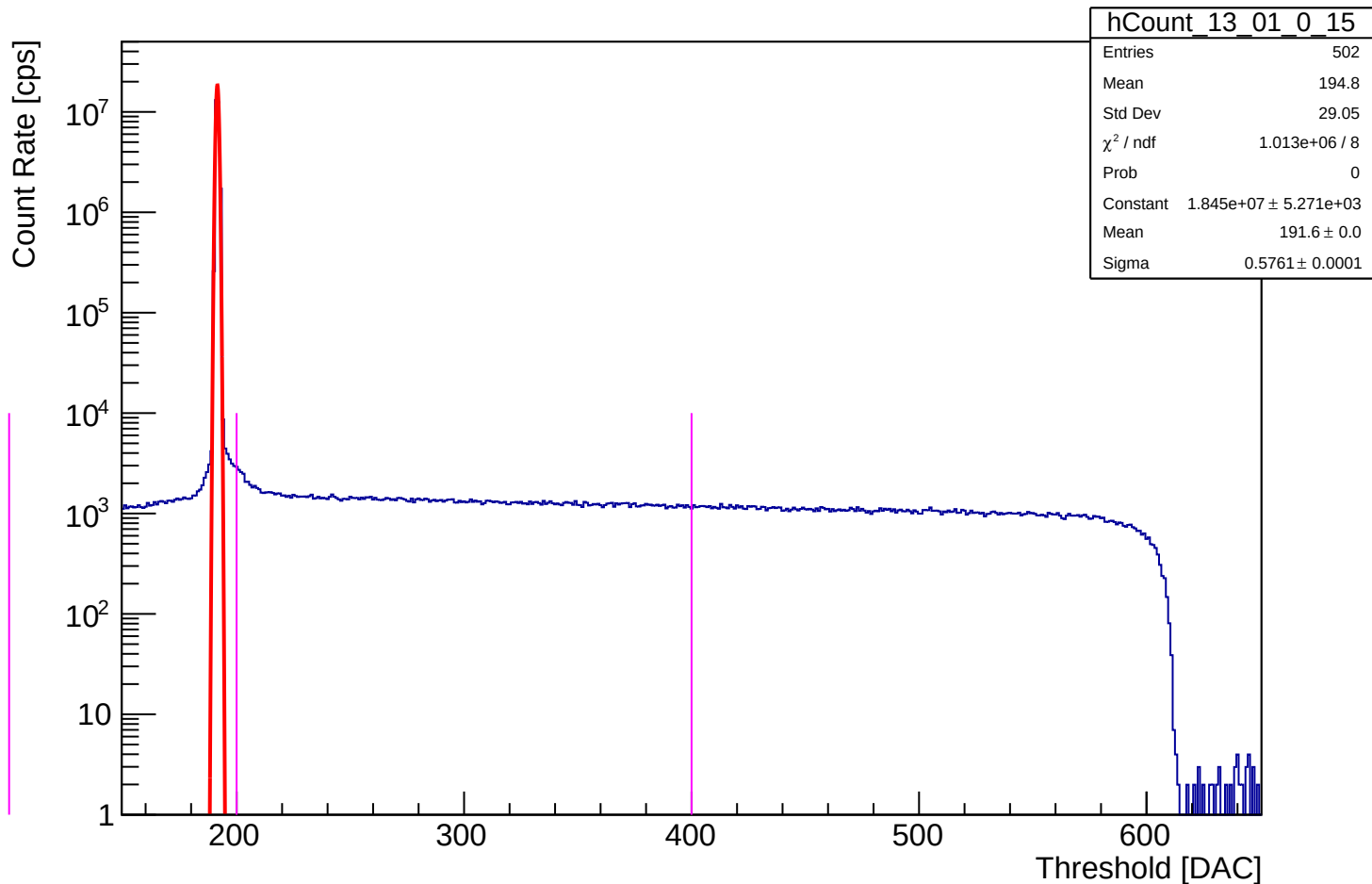
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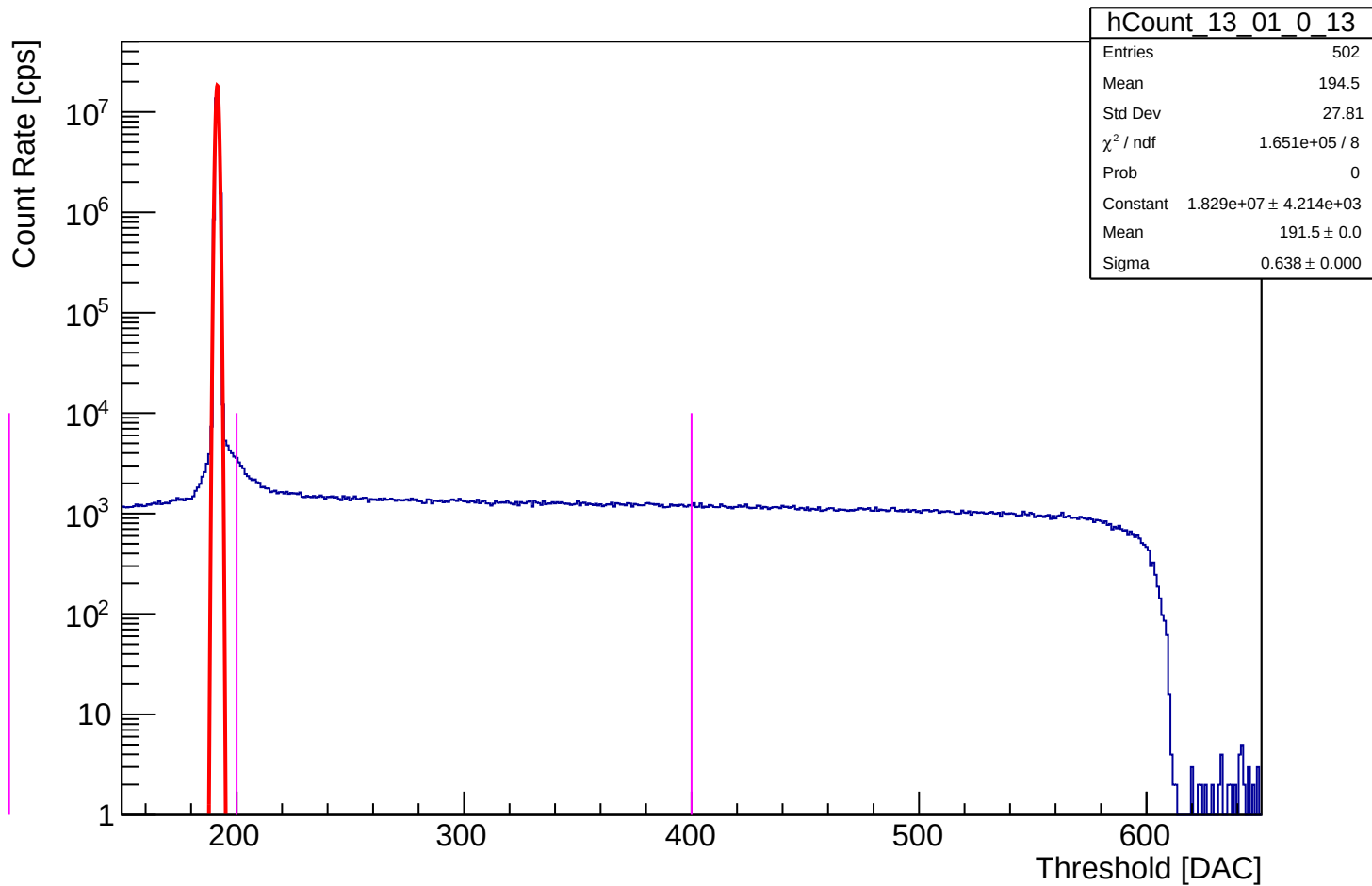
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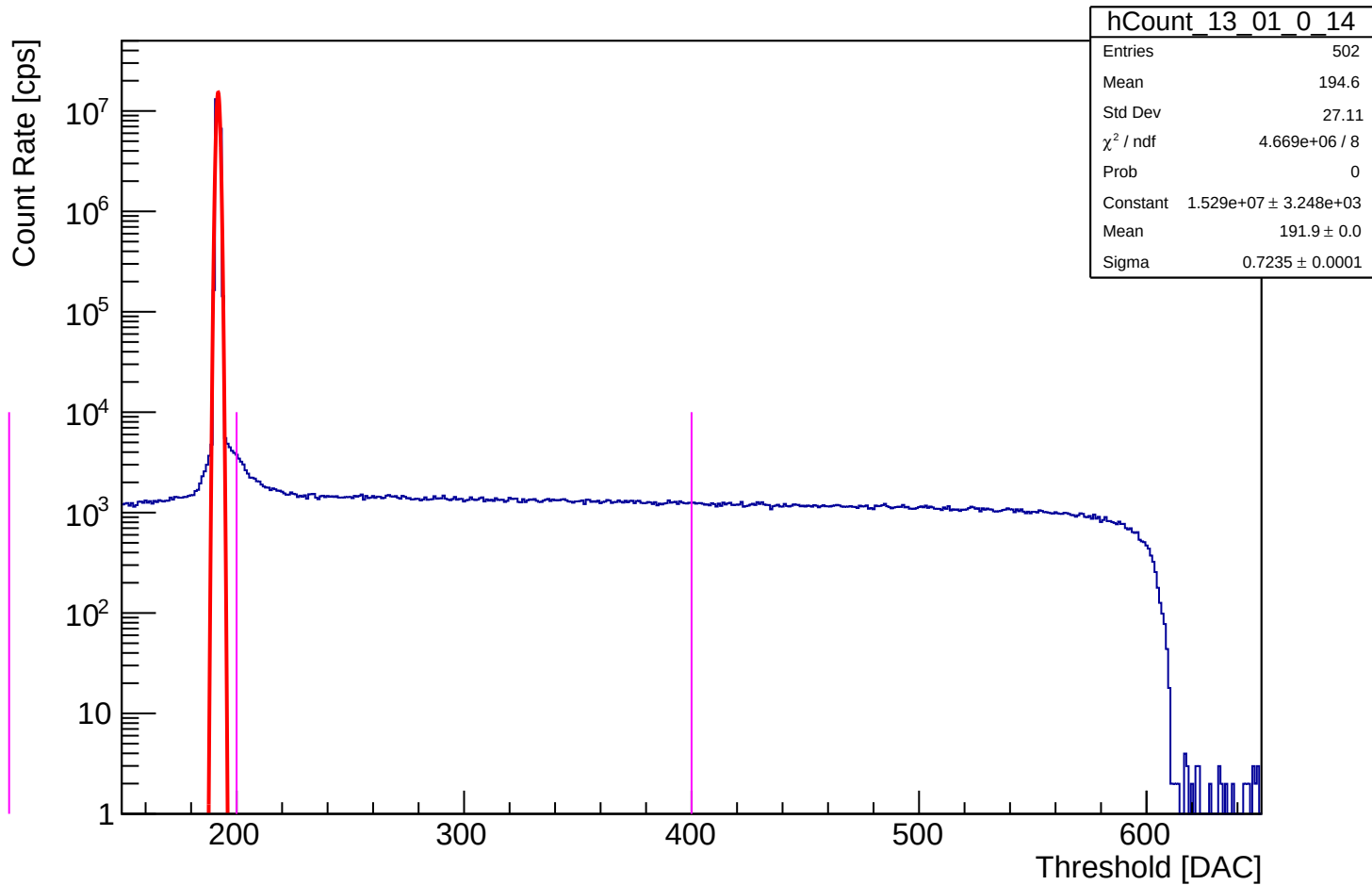
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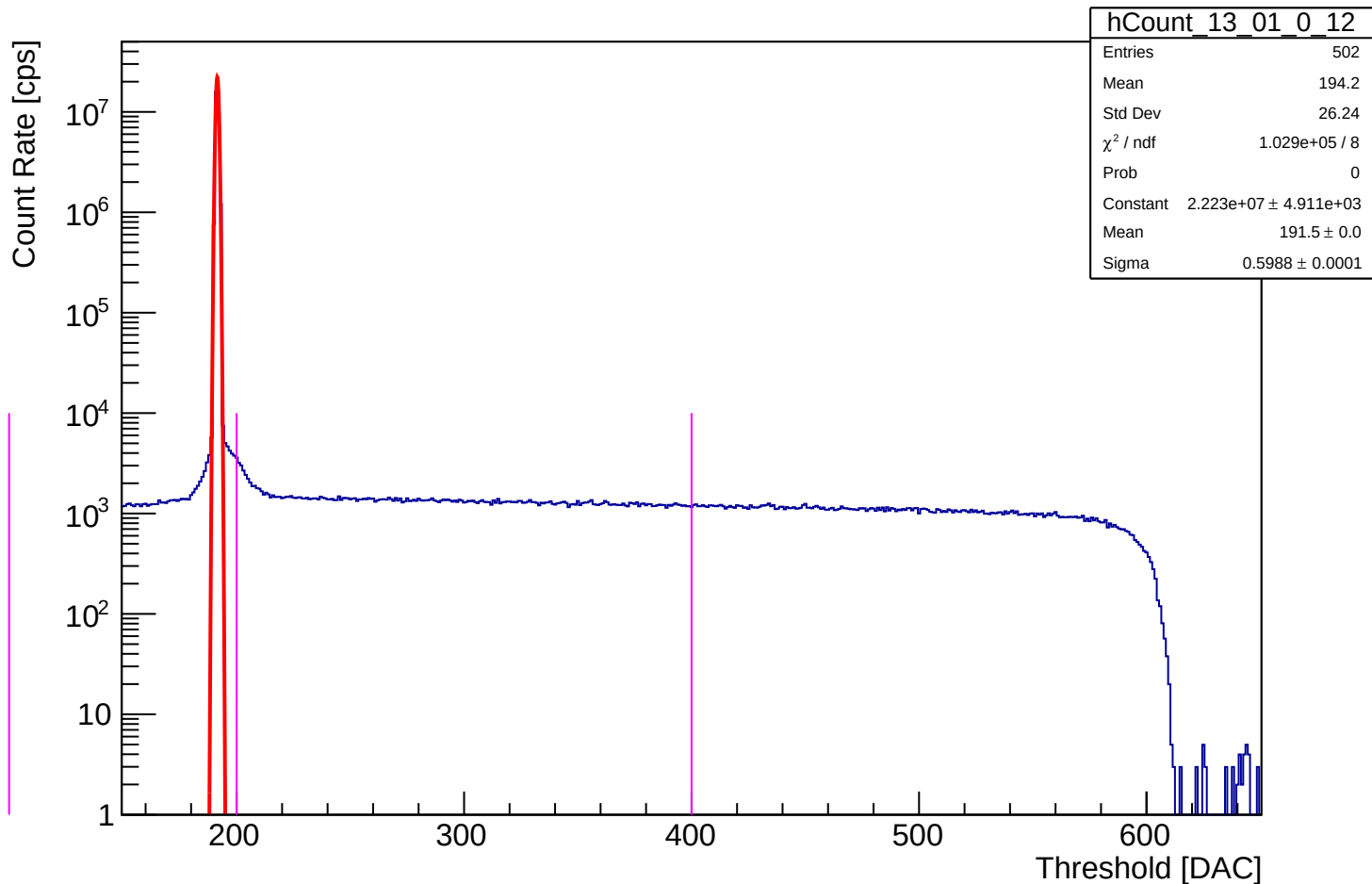
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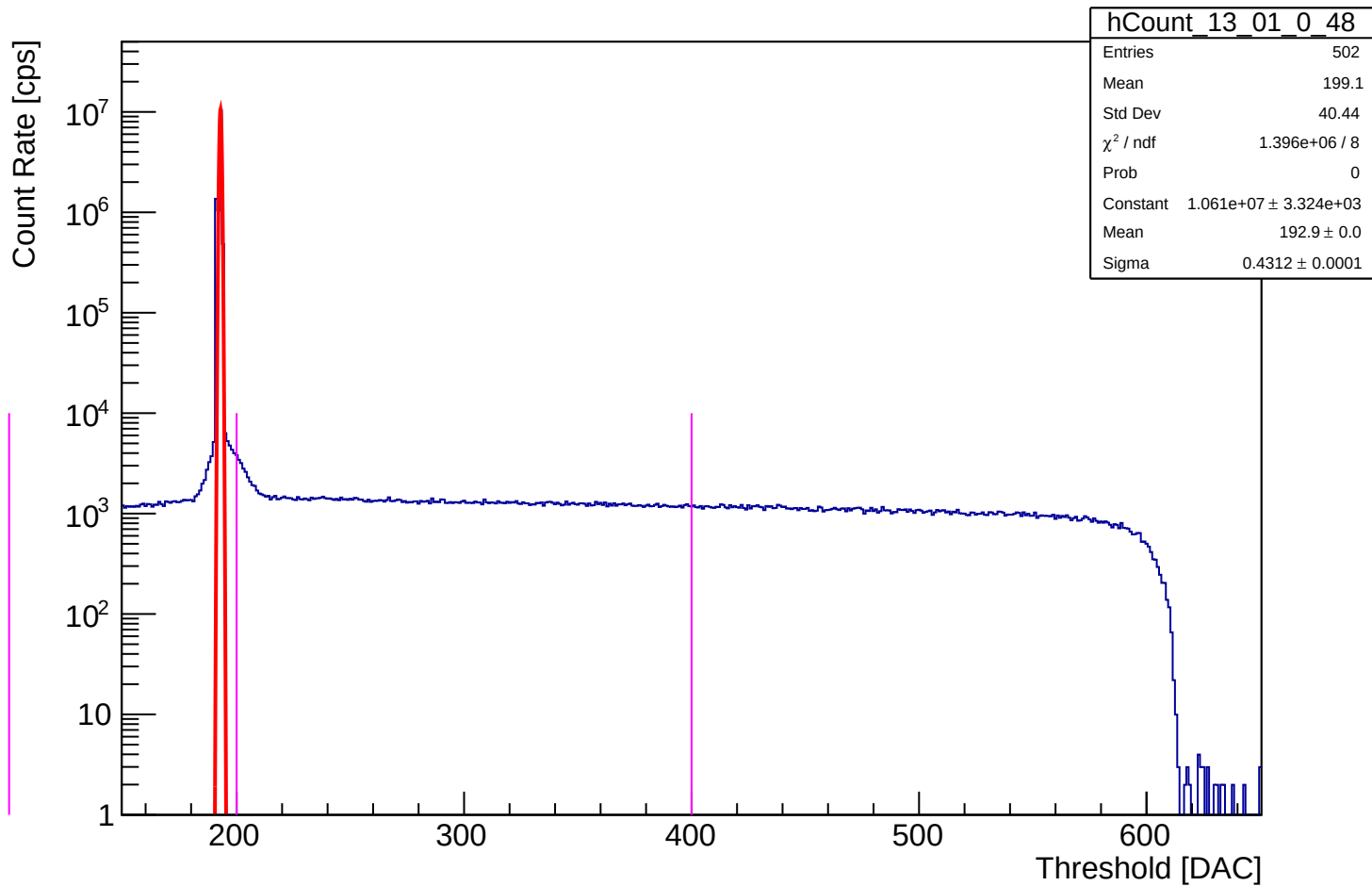
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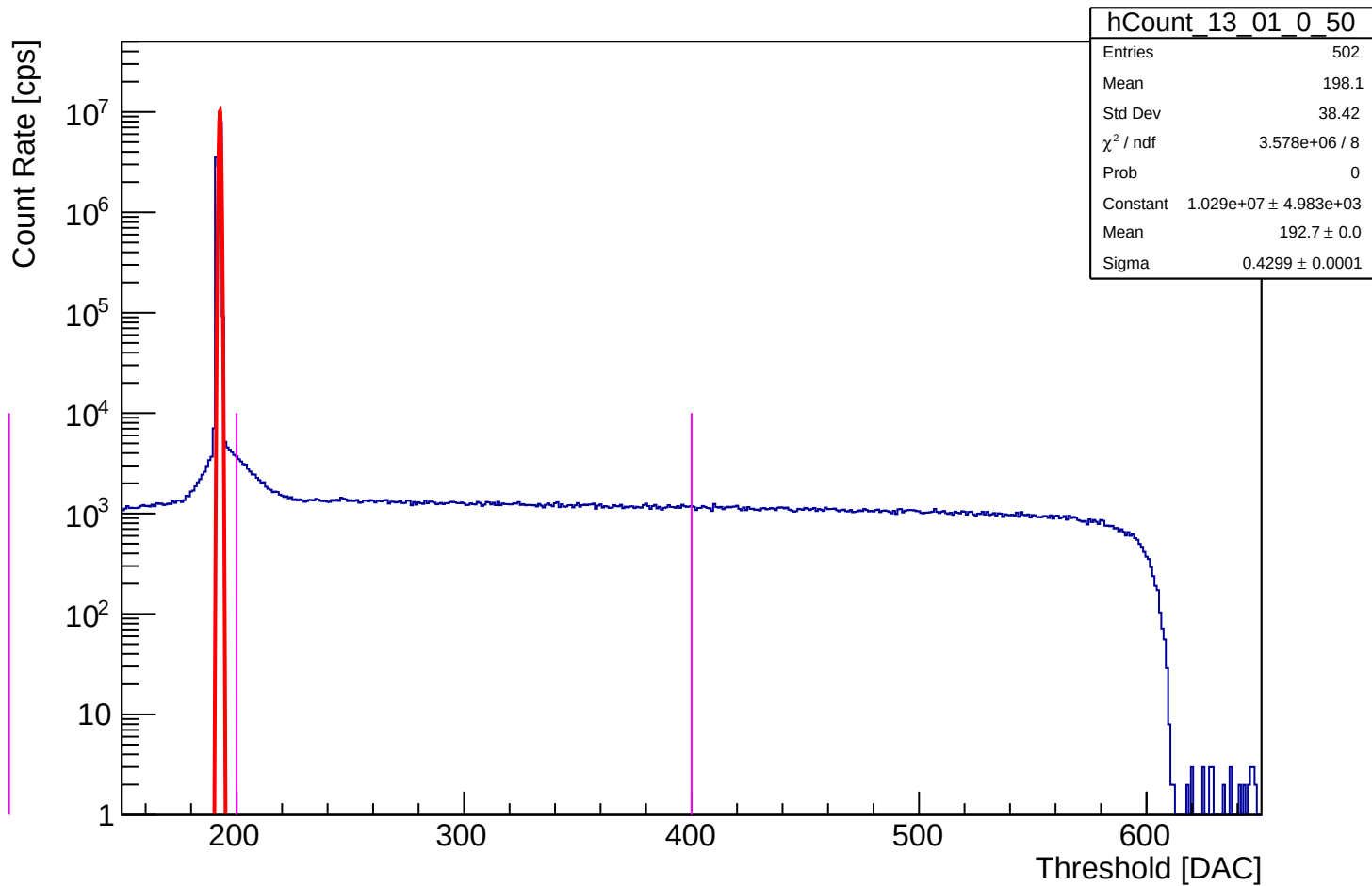
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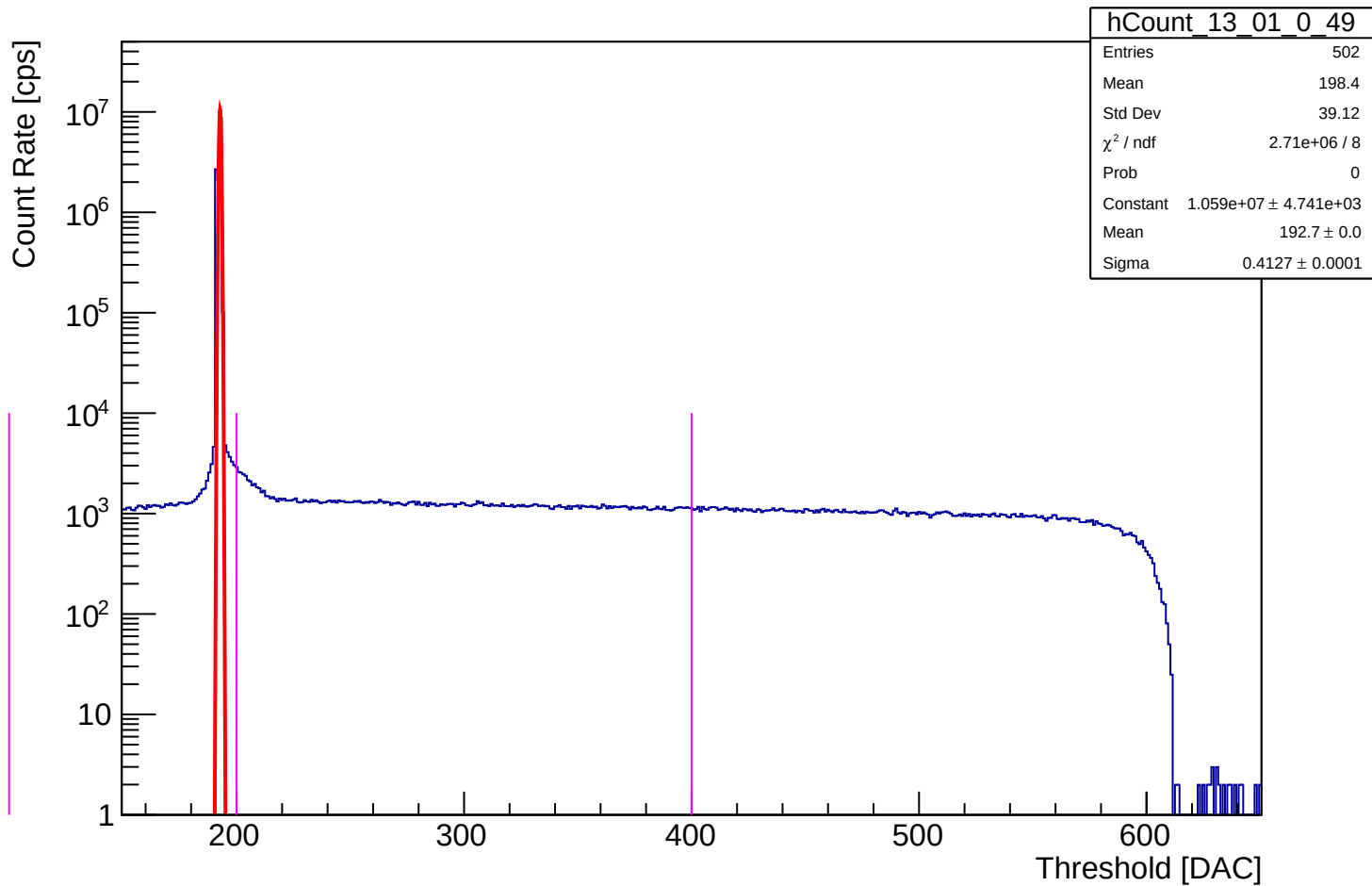
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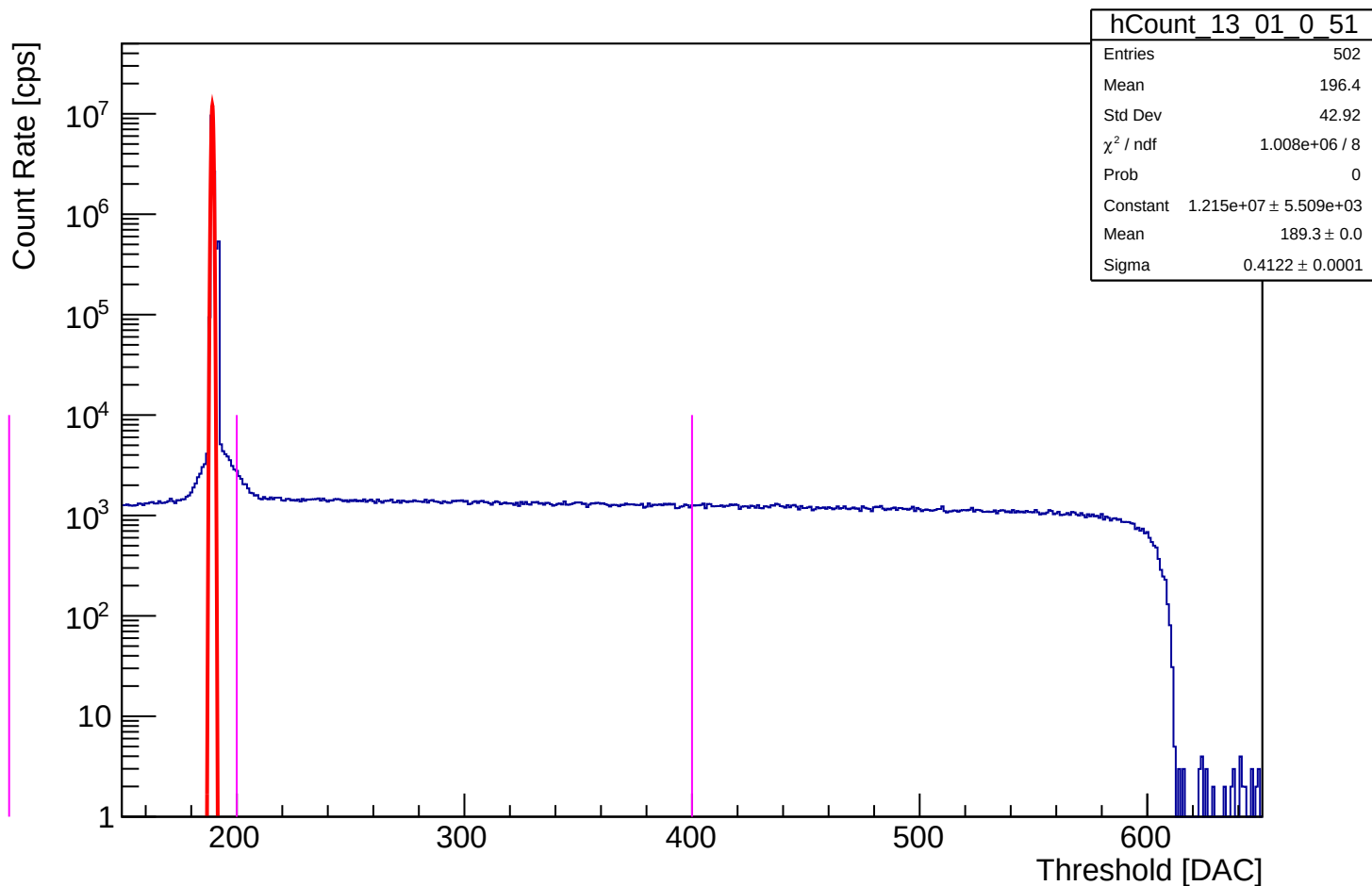
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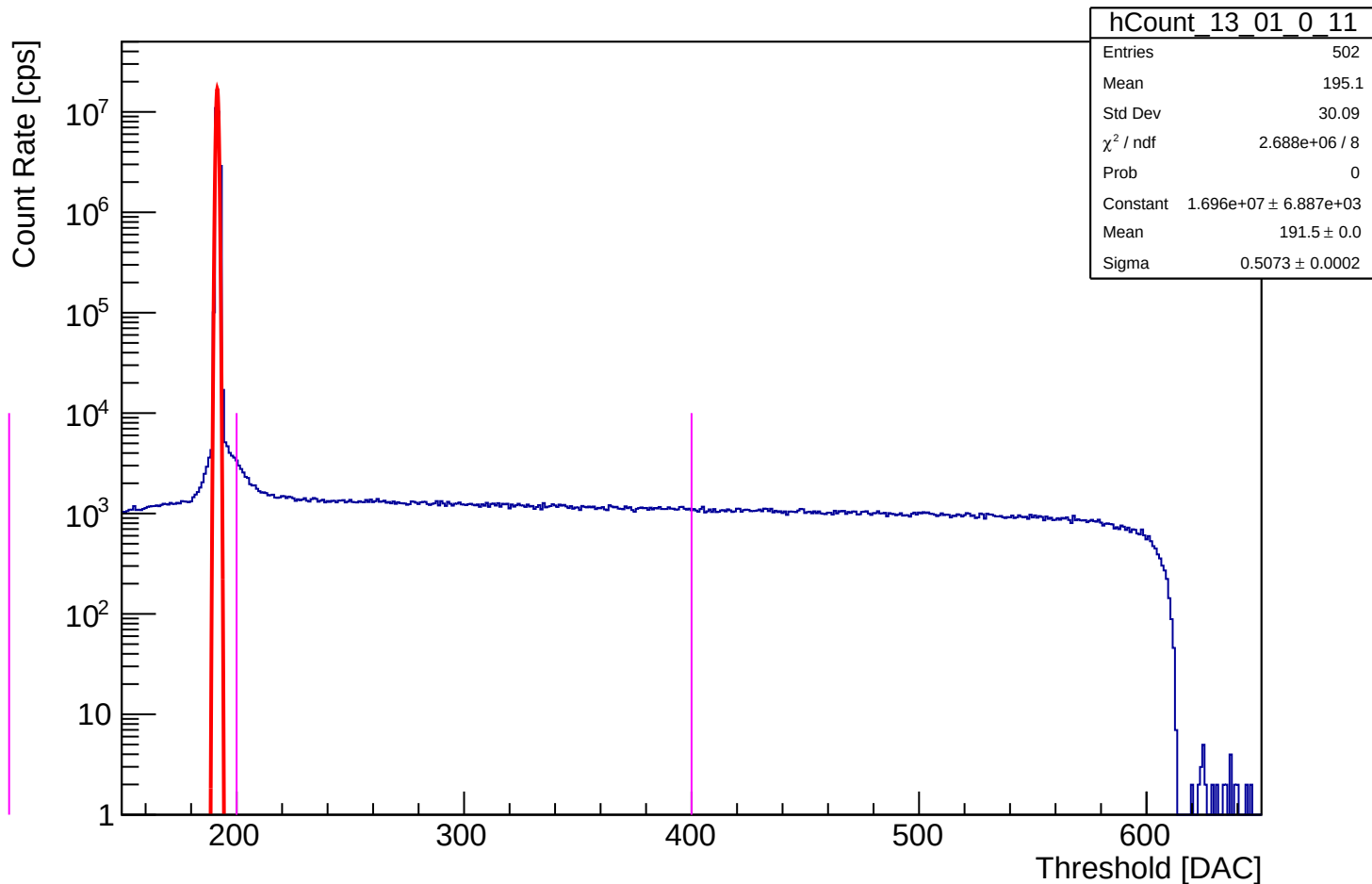
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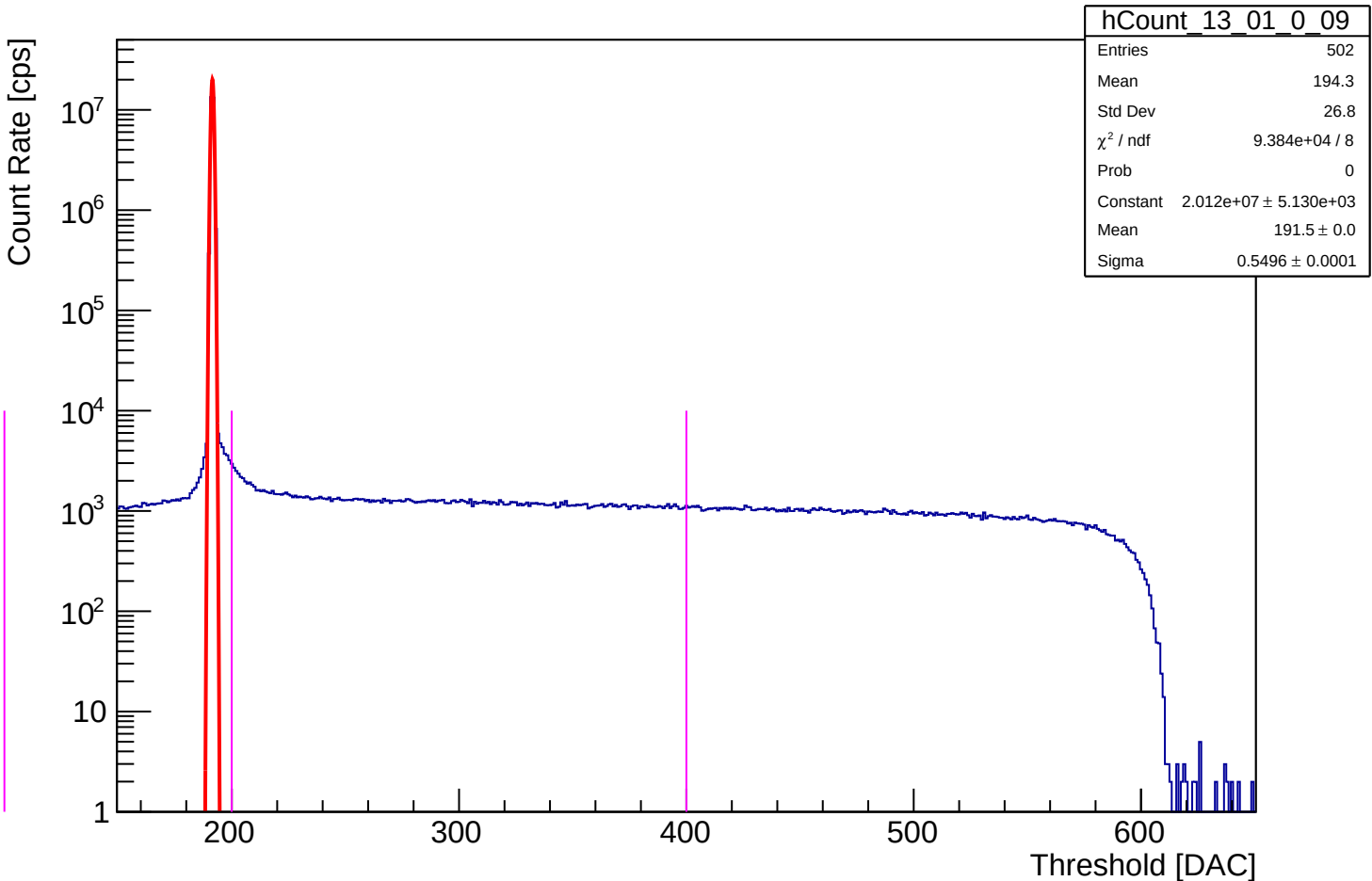


Row 1 Tile 1 Pmt 4 Pixel 40 Slot 13 Fiber 1 Asic 0 Channel 51

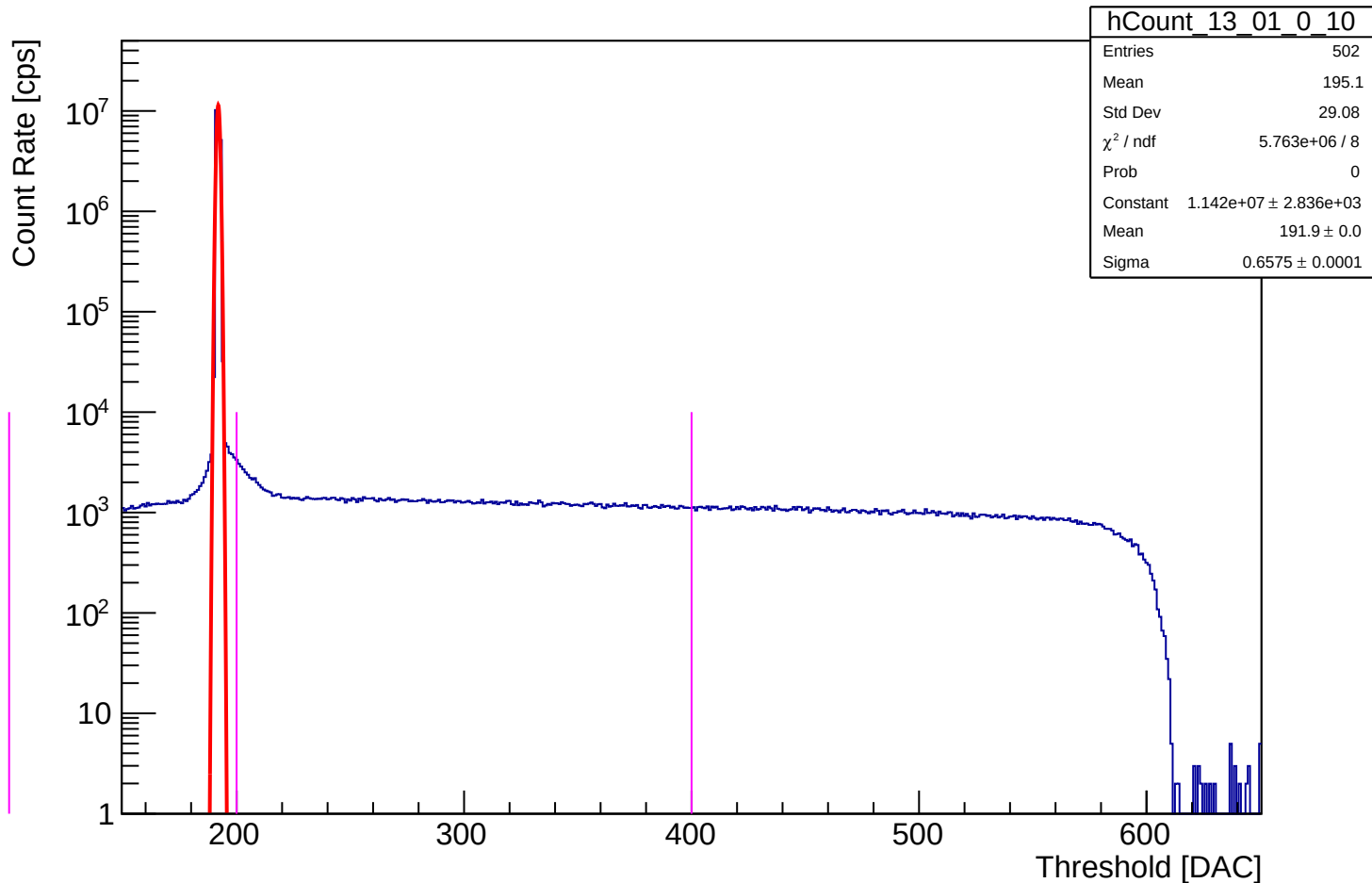


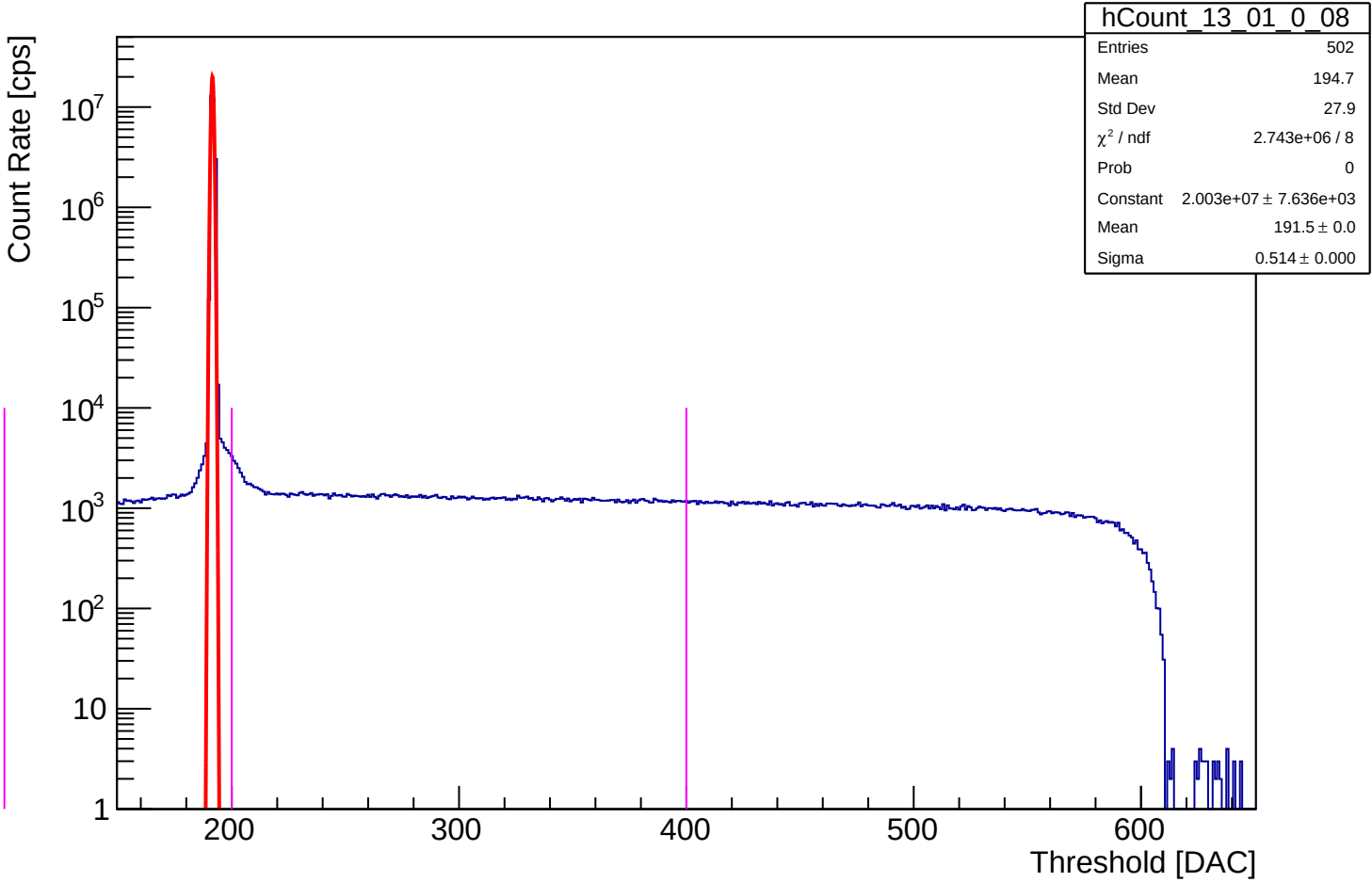
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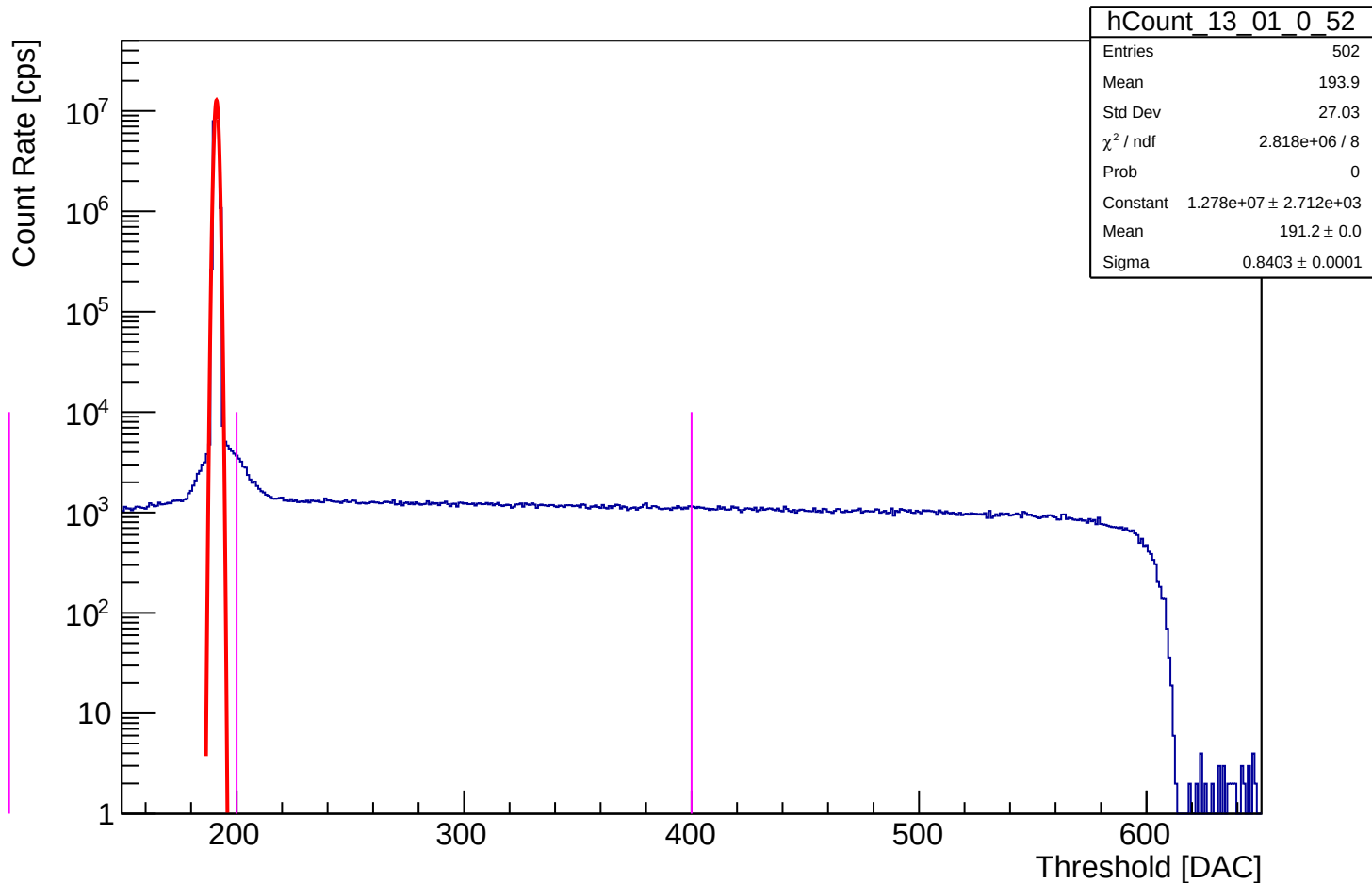


Row 1 Tile 1 Pmt 4 Pixel 43 Slot 13 Fiber 1 Asic 0 Channel 10

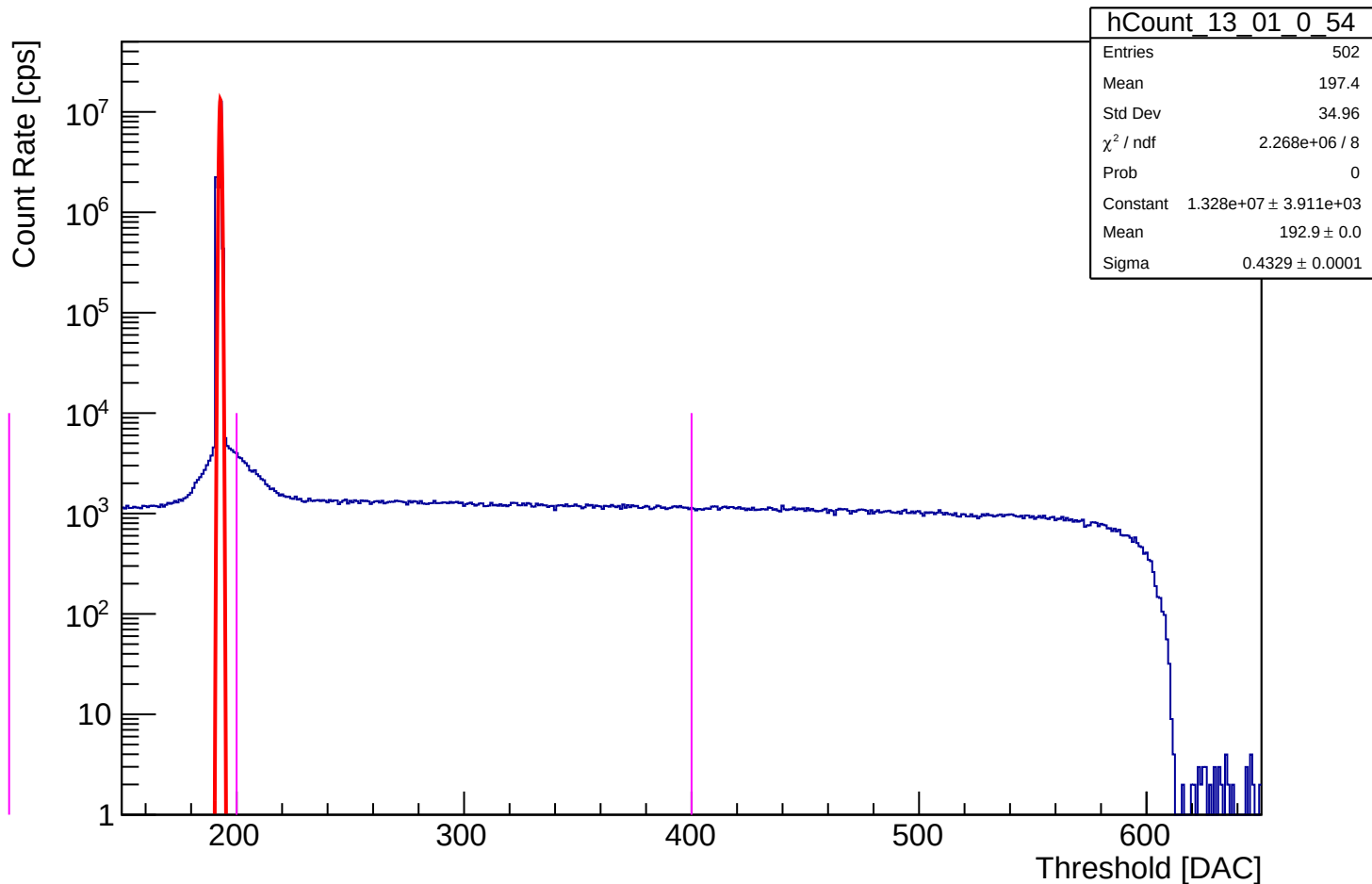




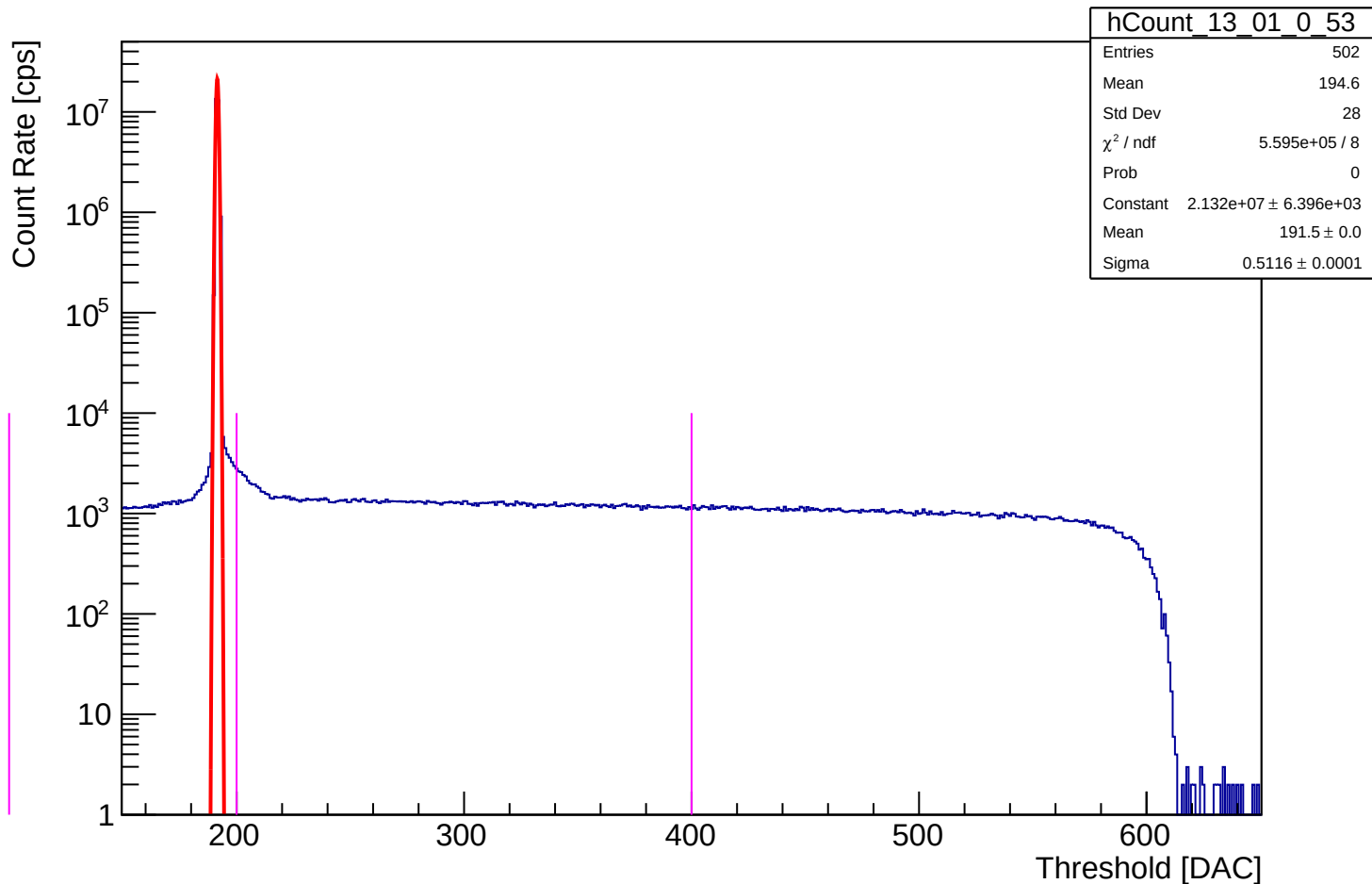
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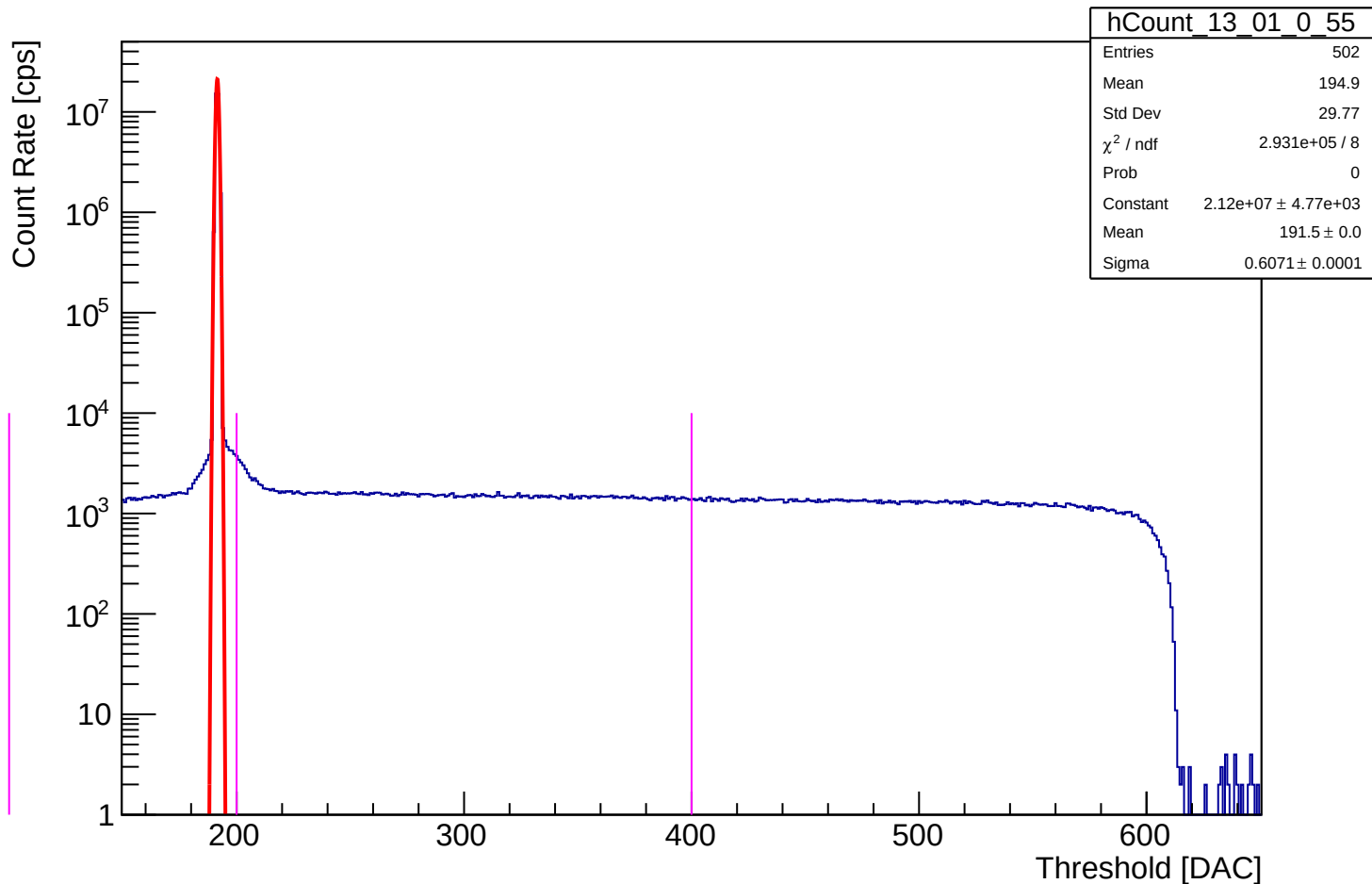
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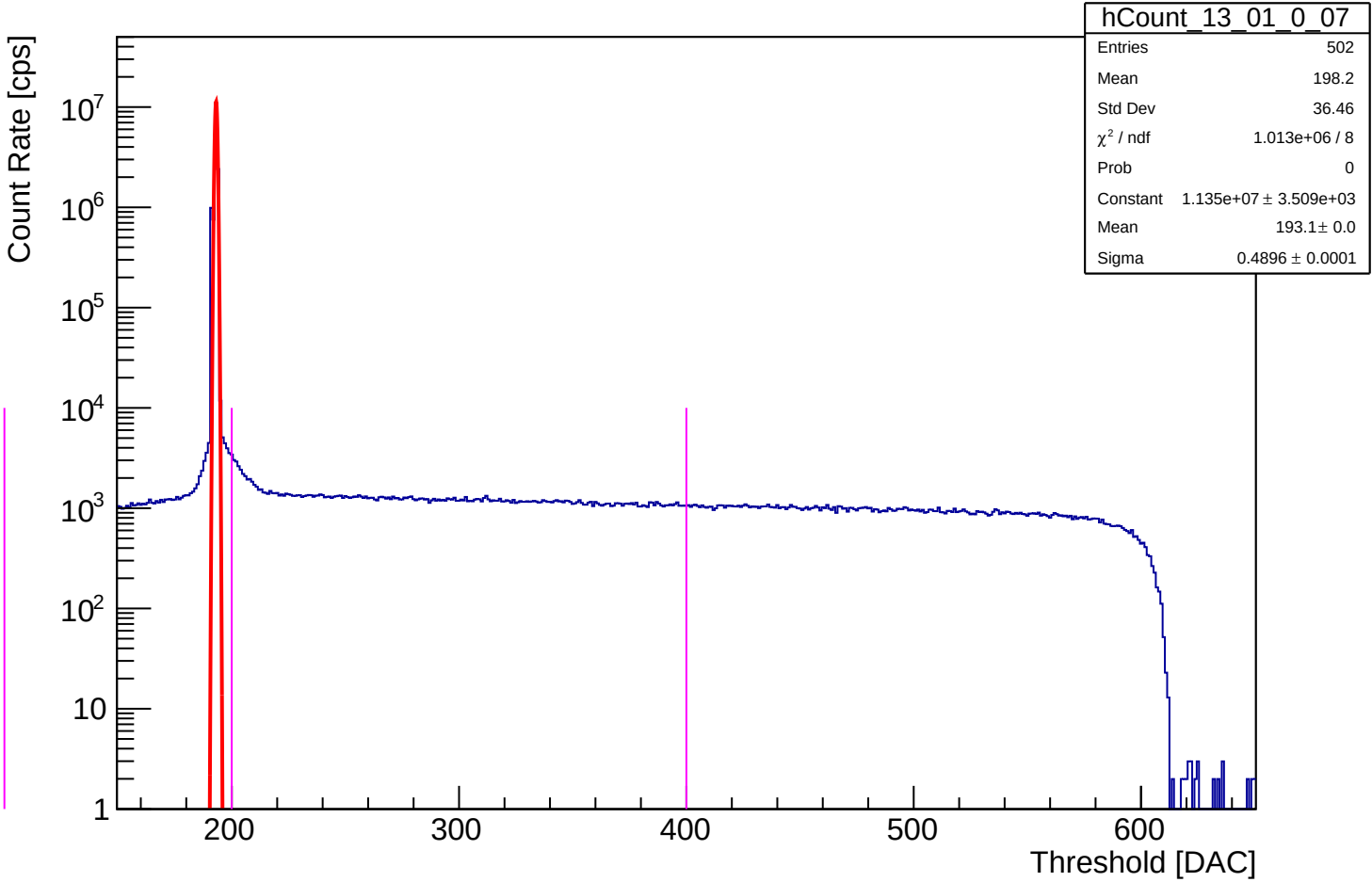


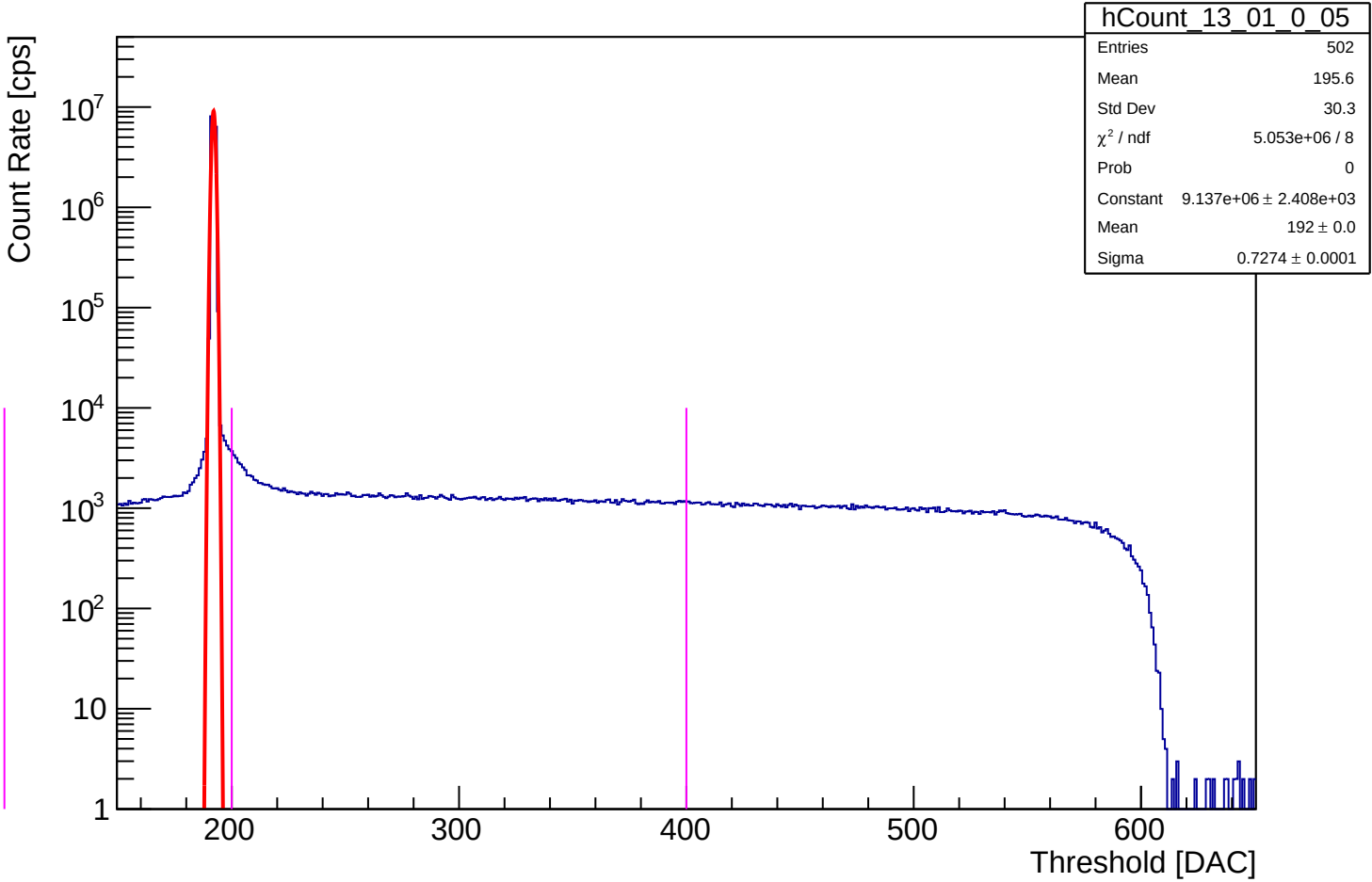
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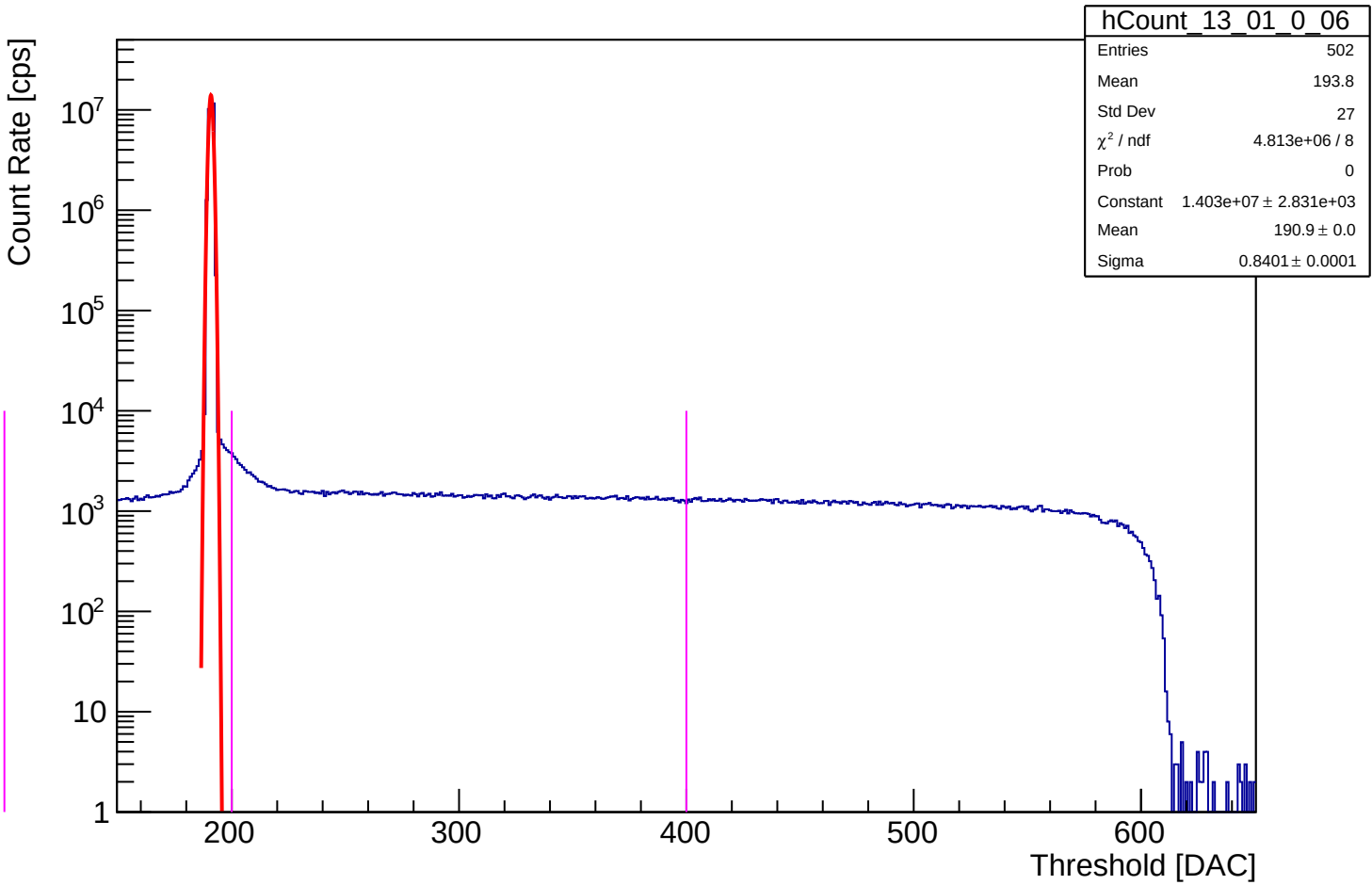


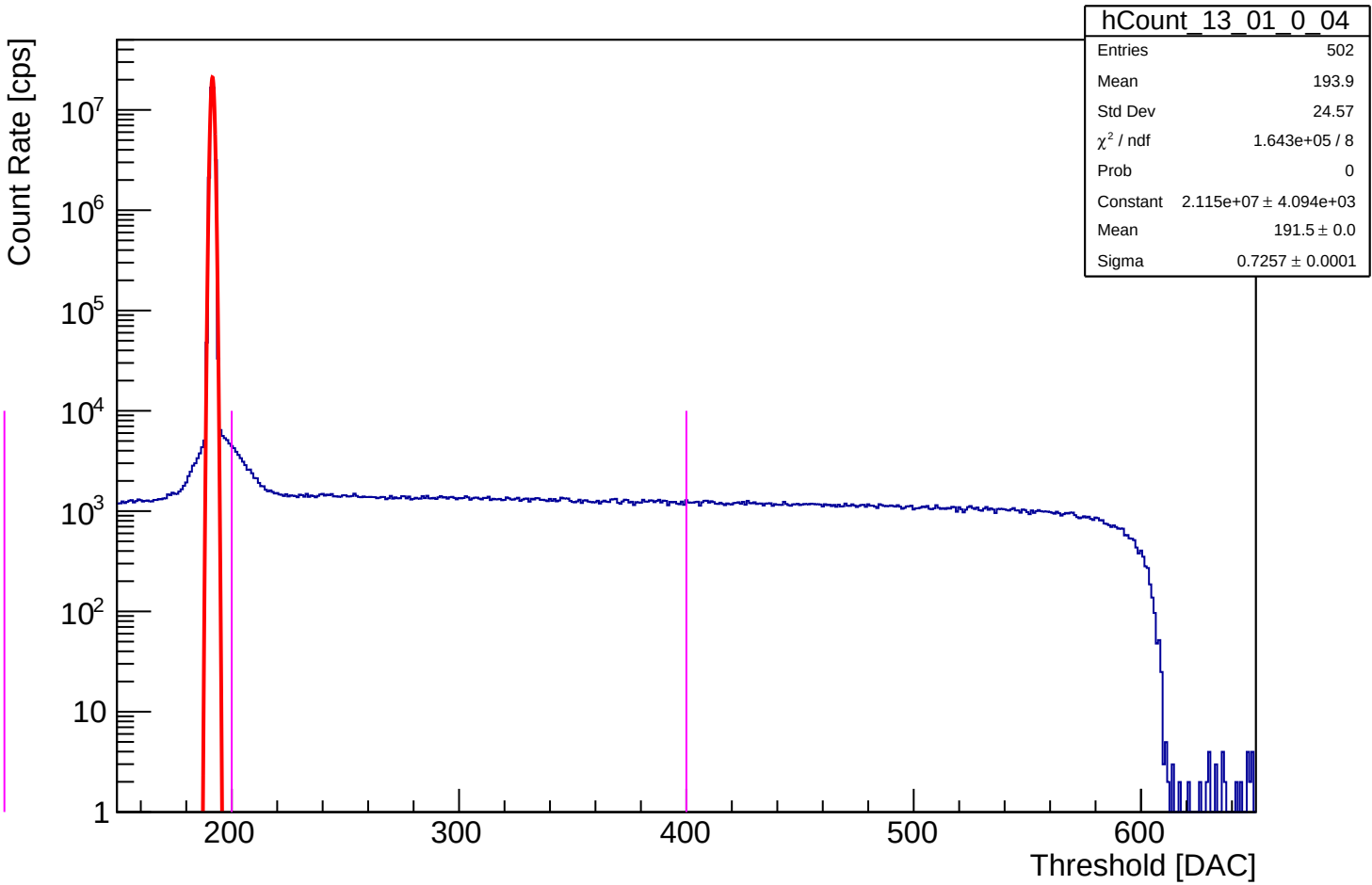
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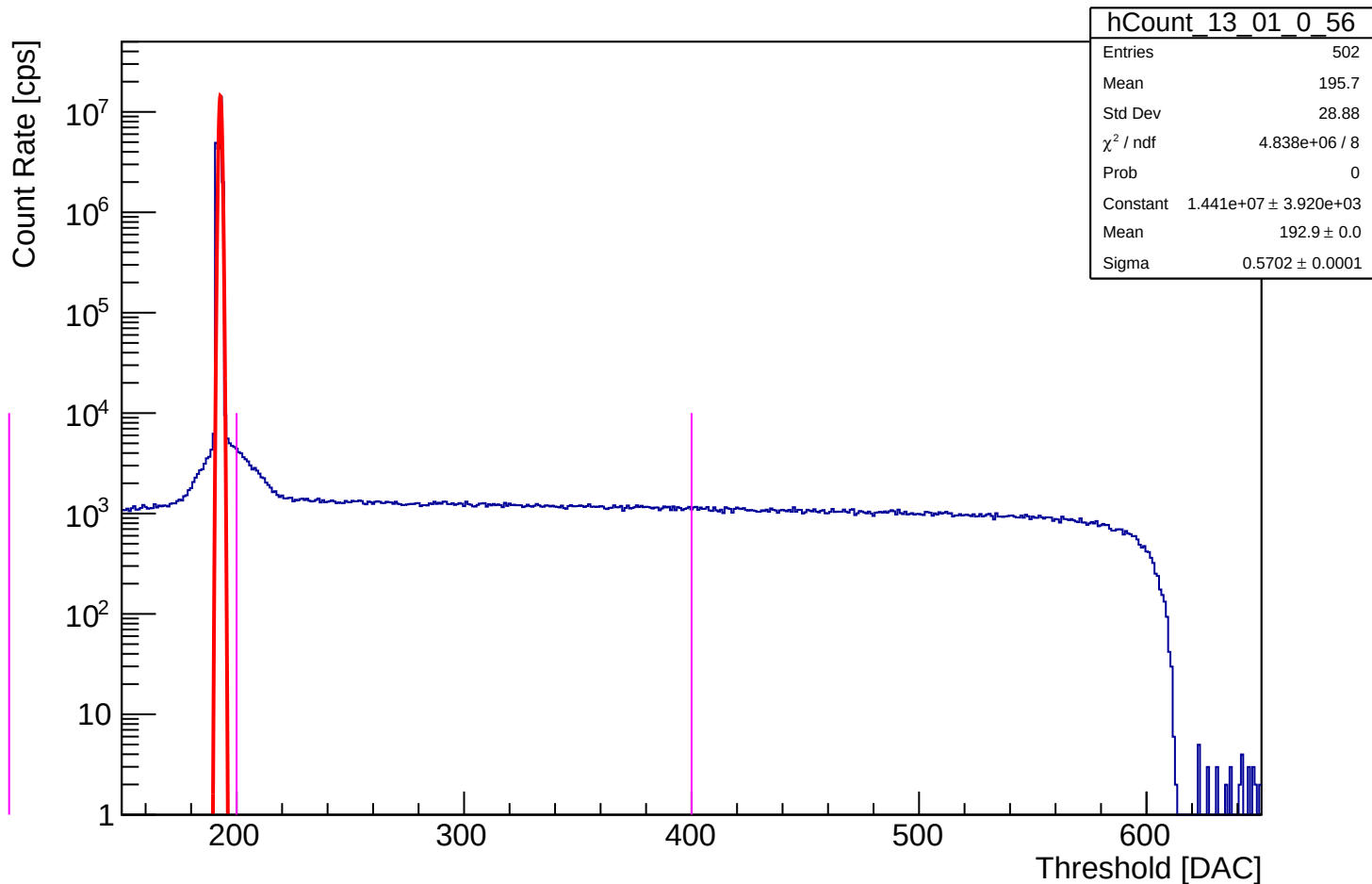




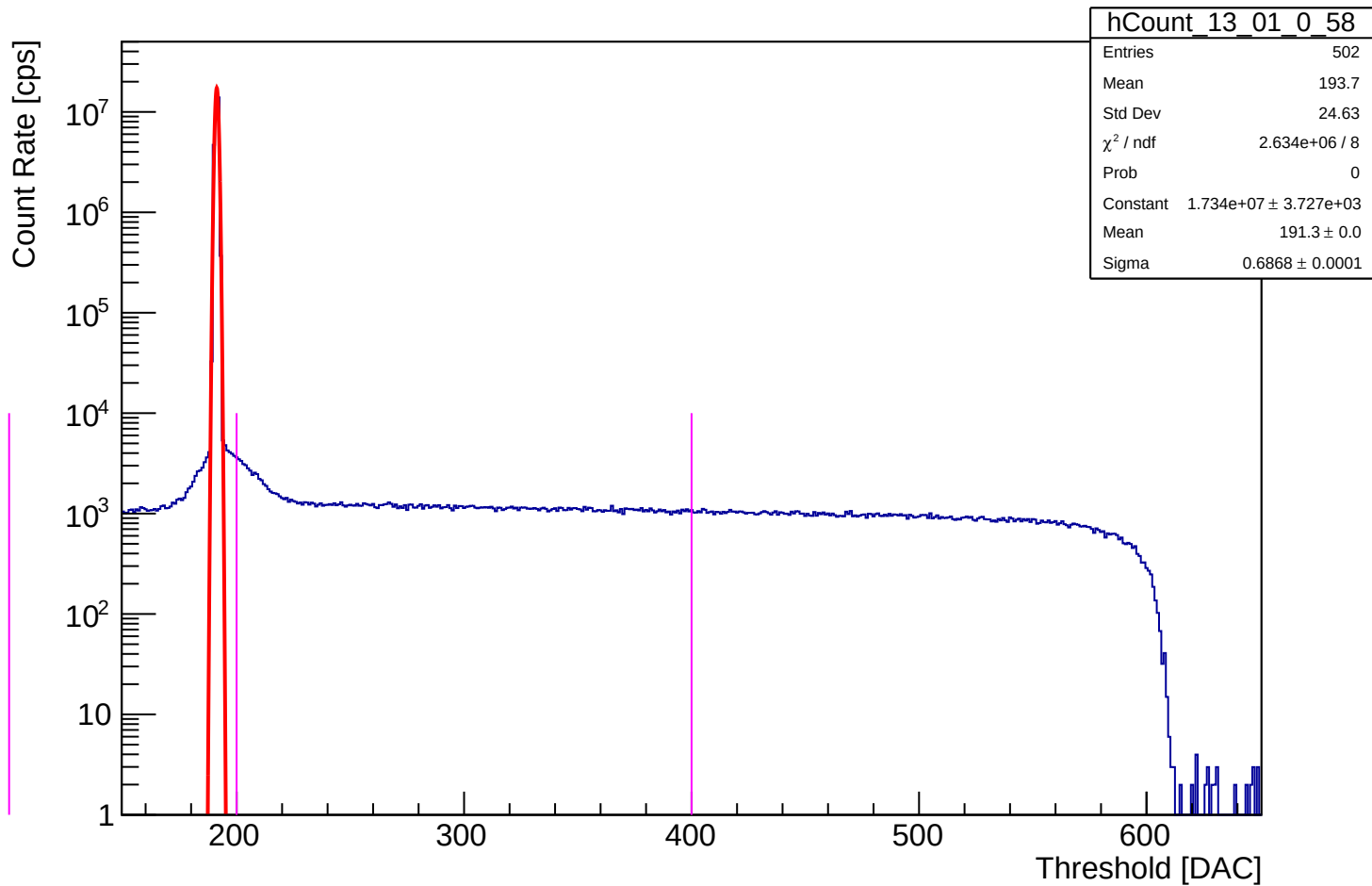




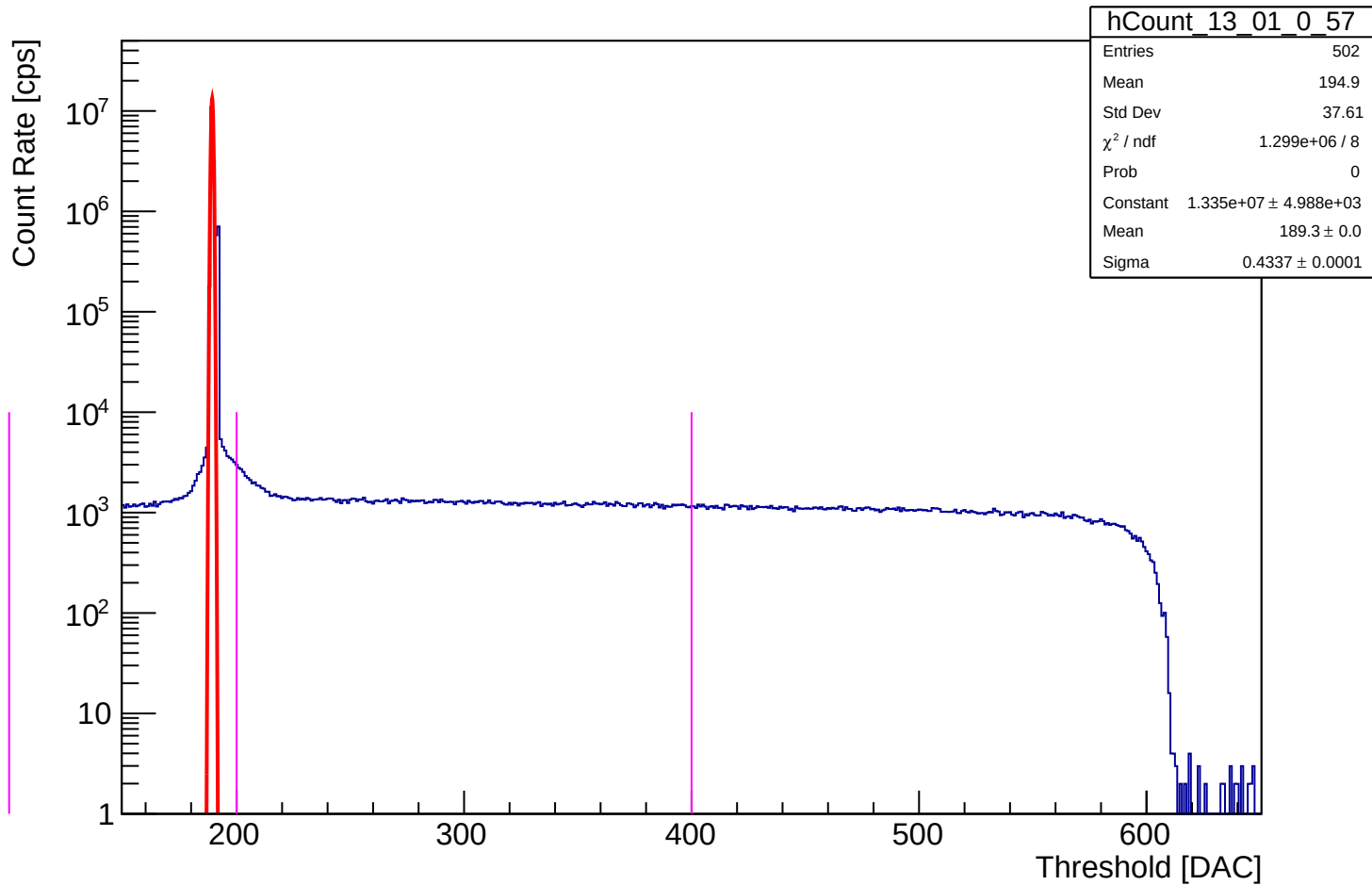
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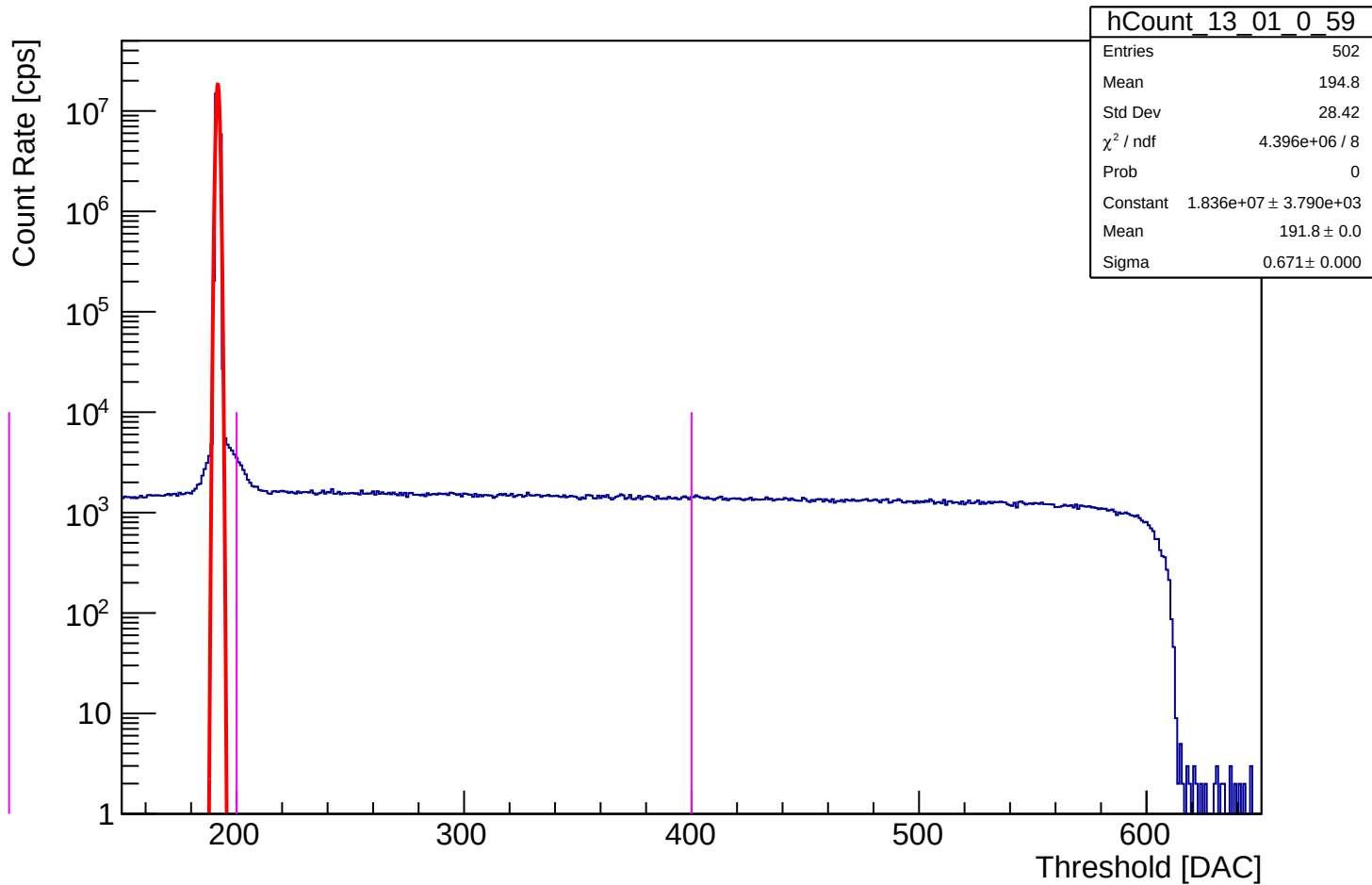
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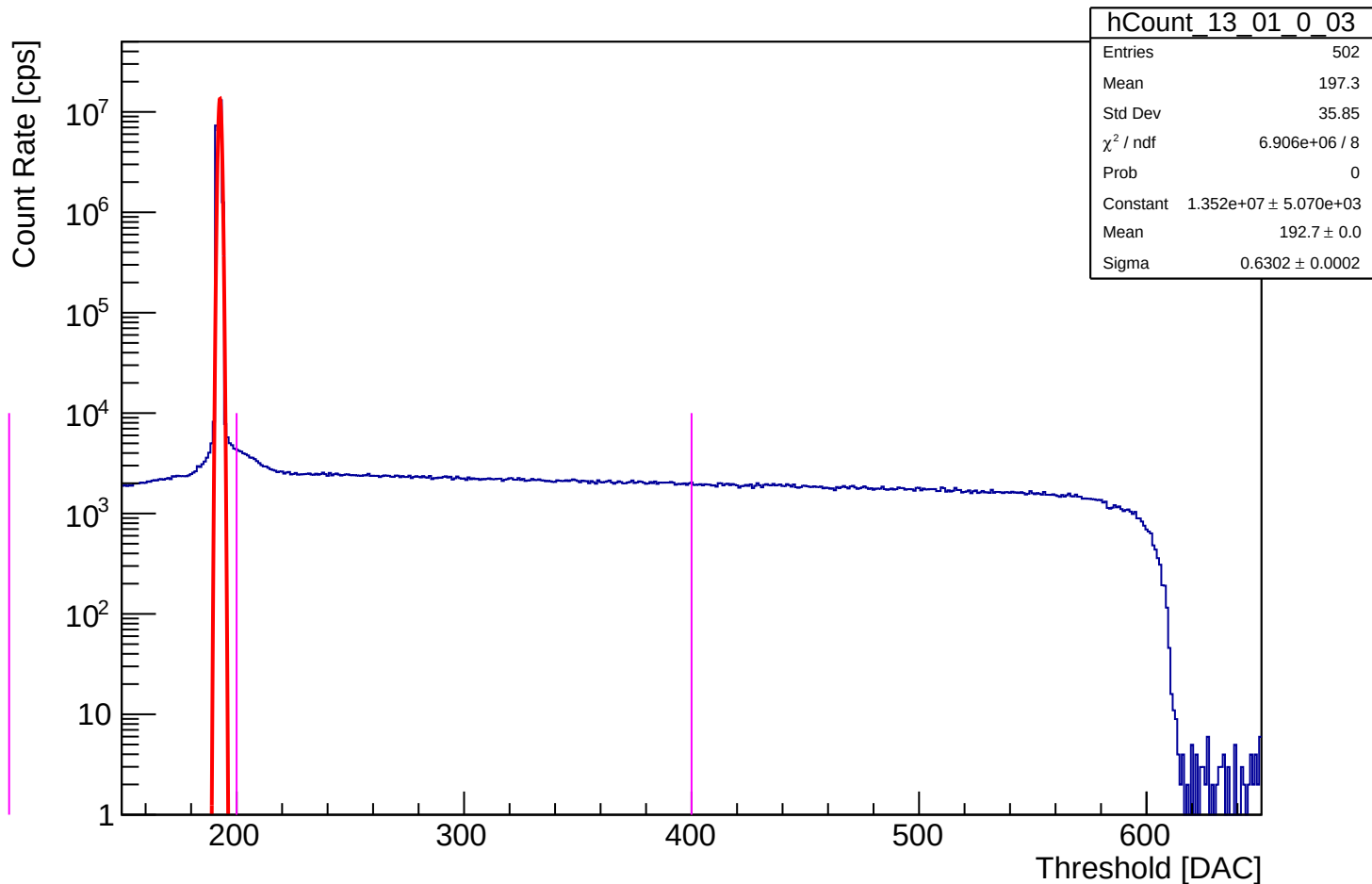
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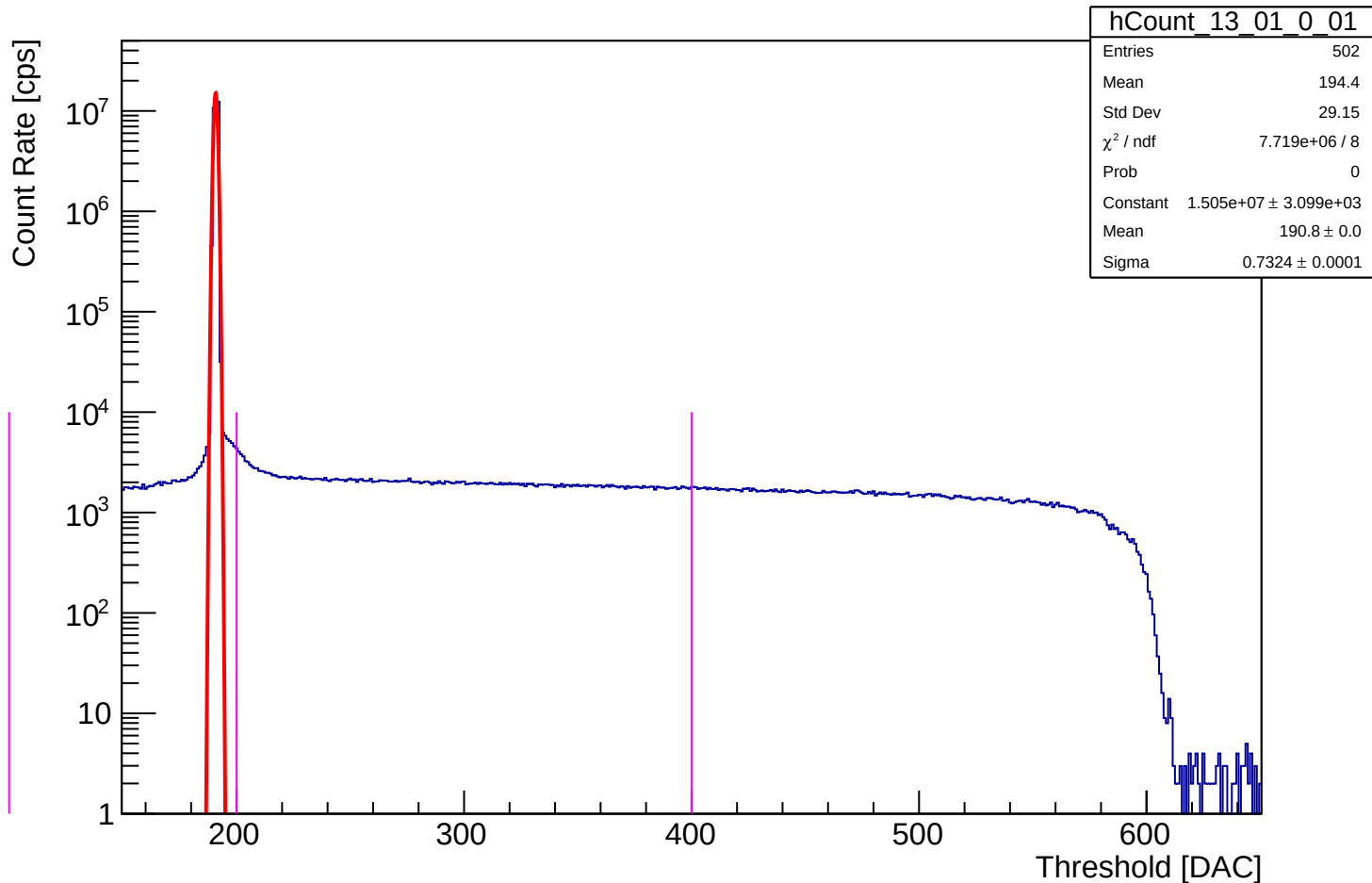
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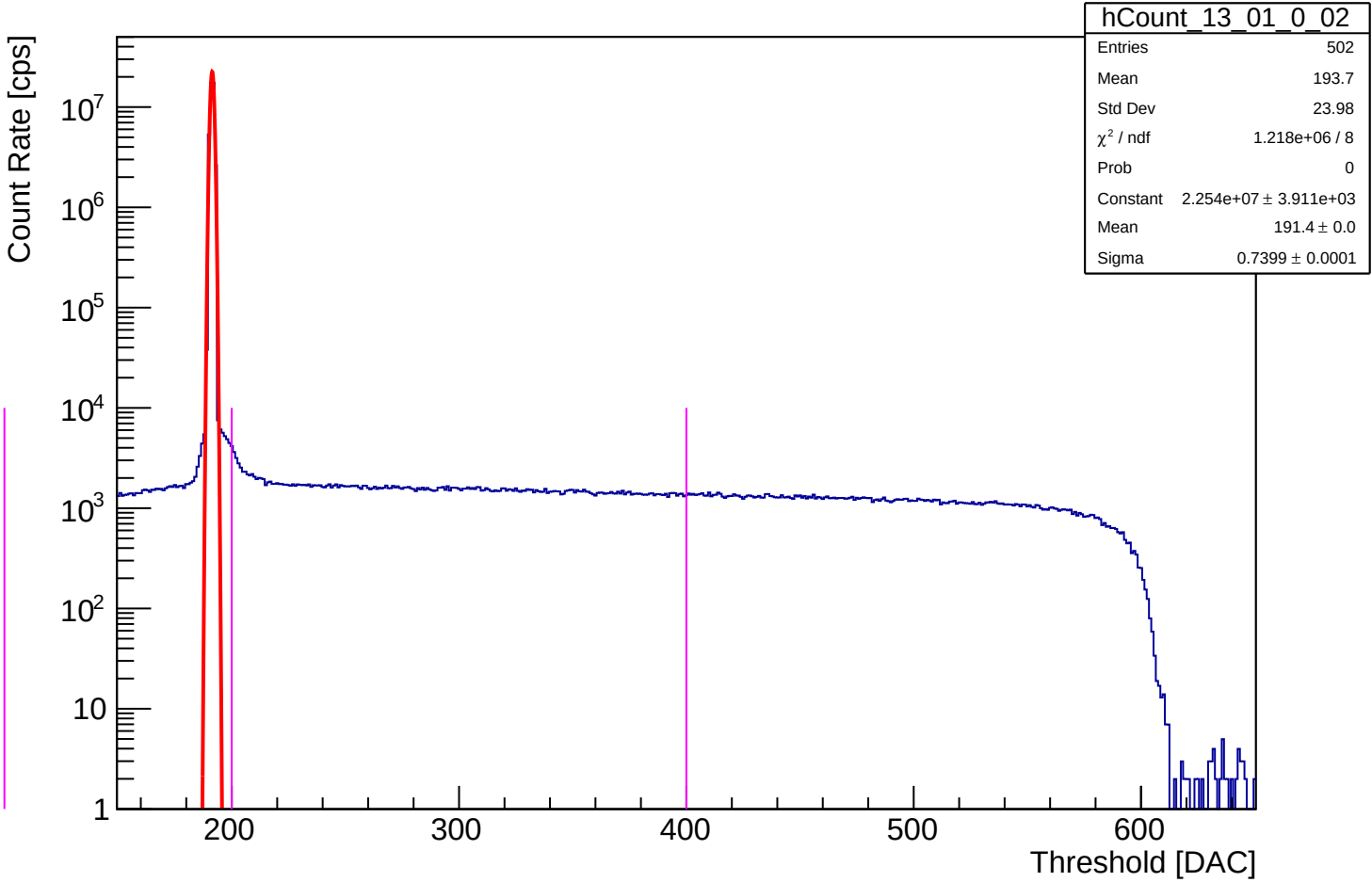


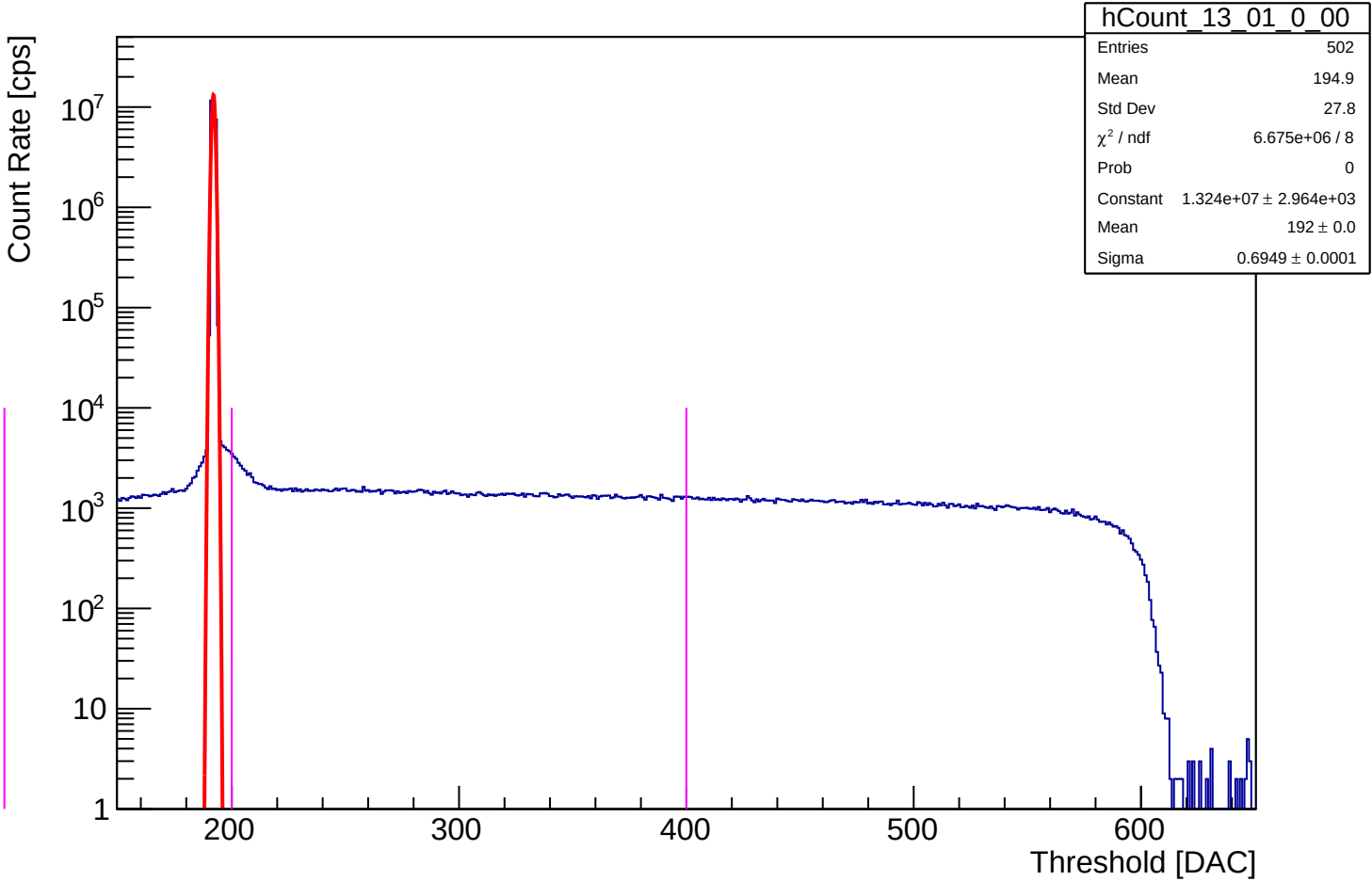
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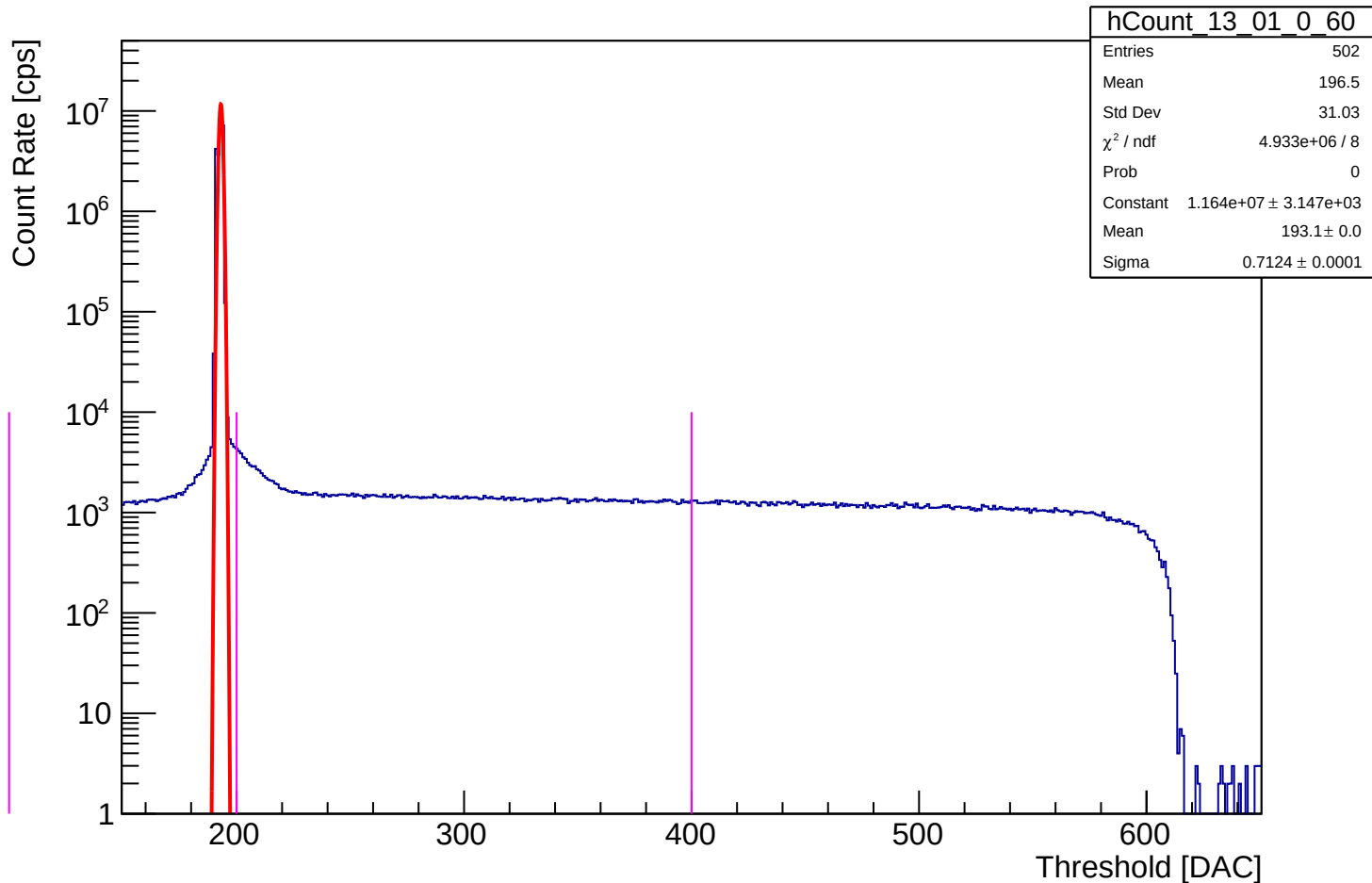
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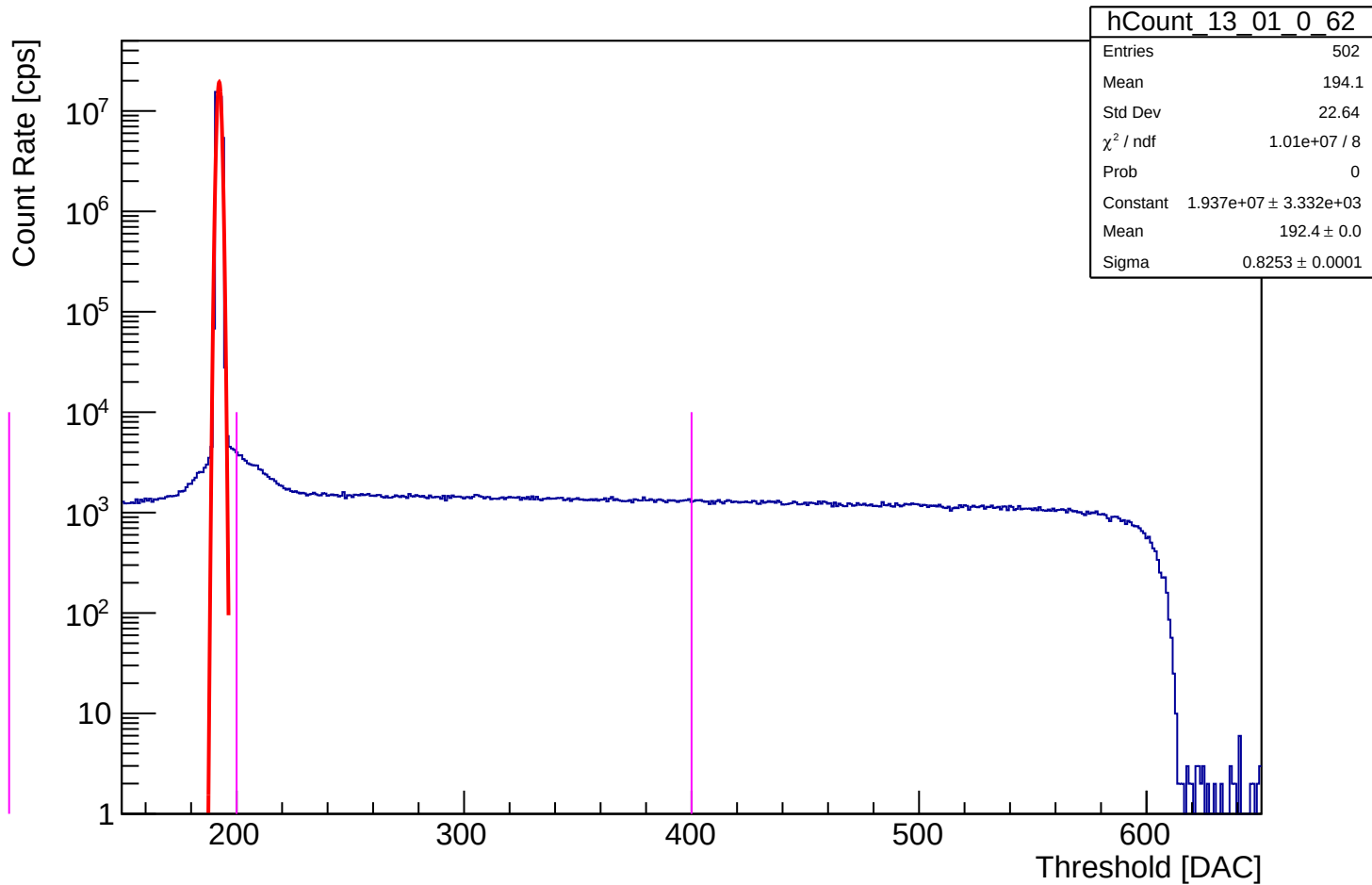




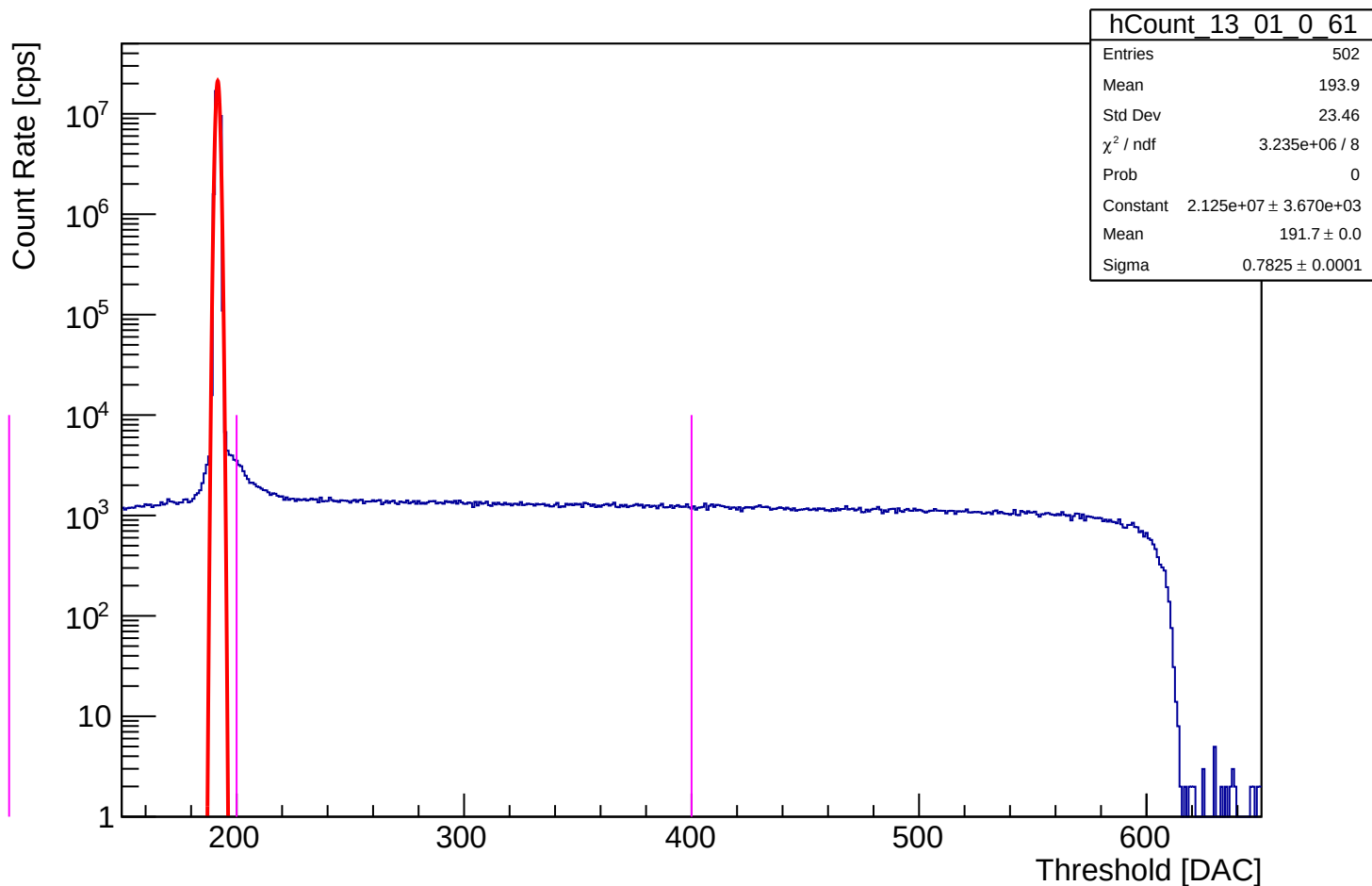
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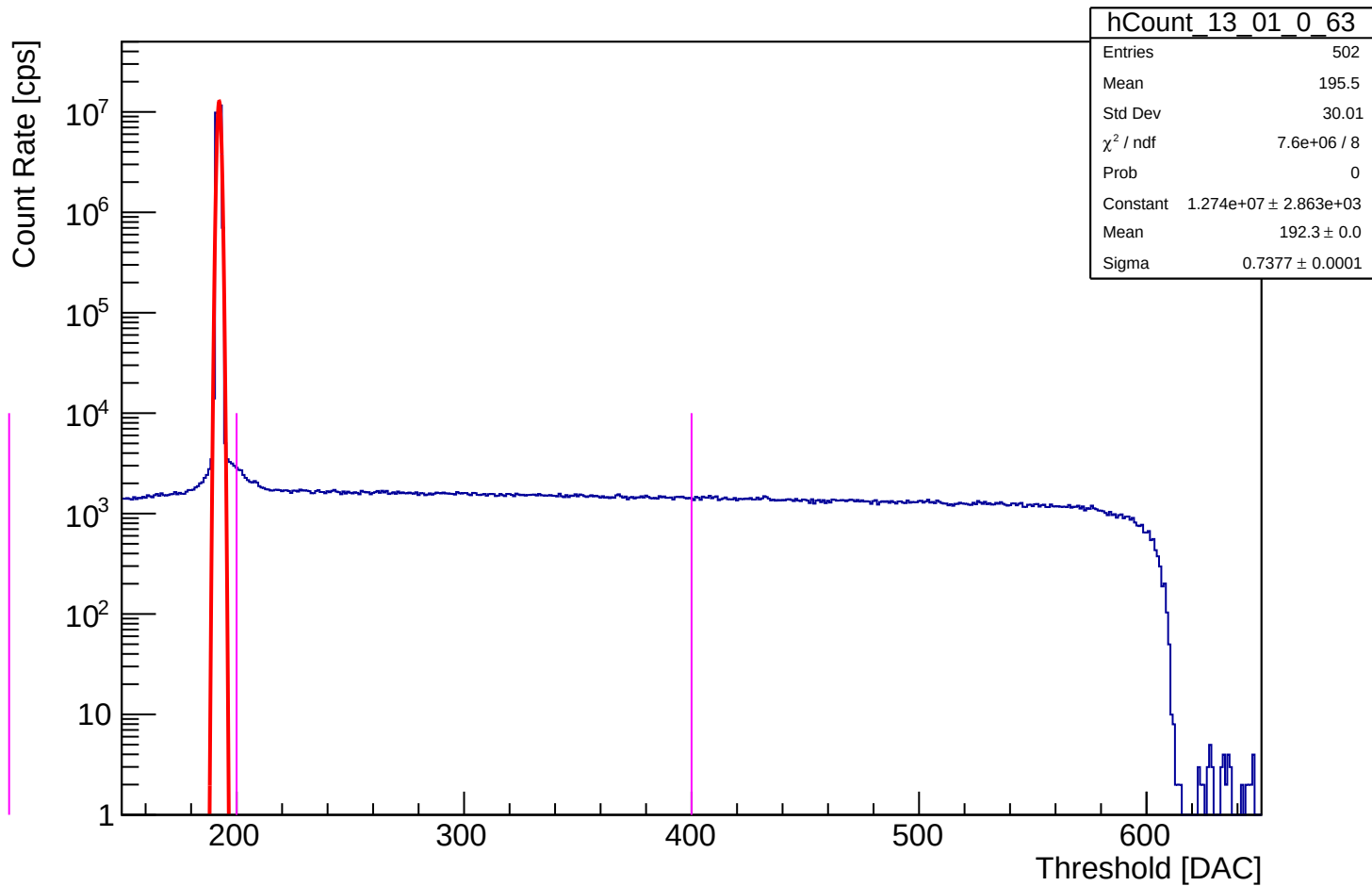
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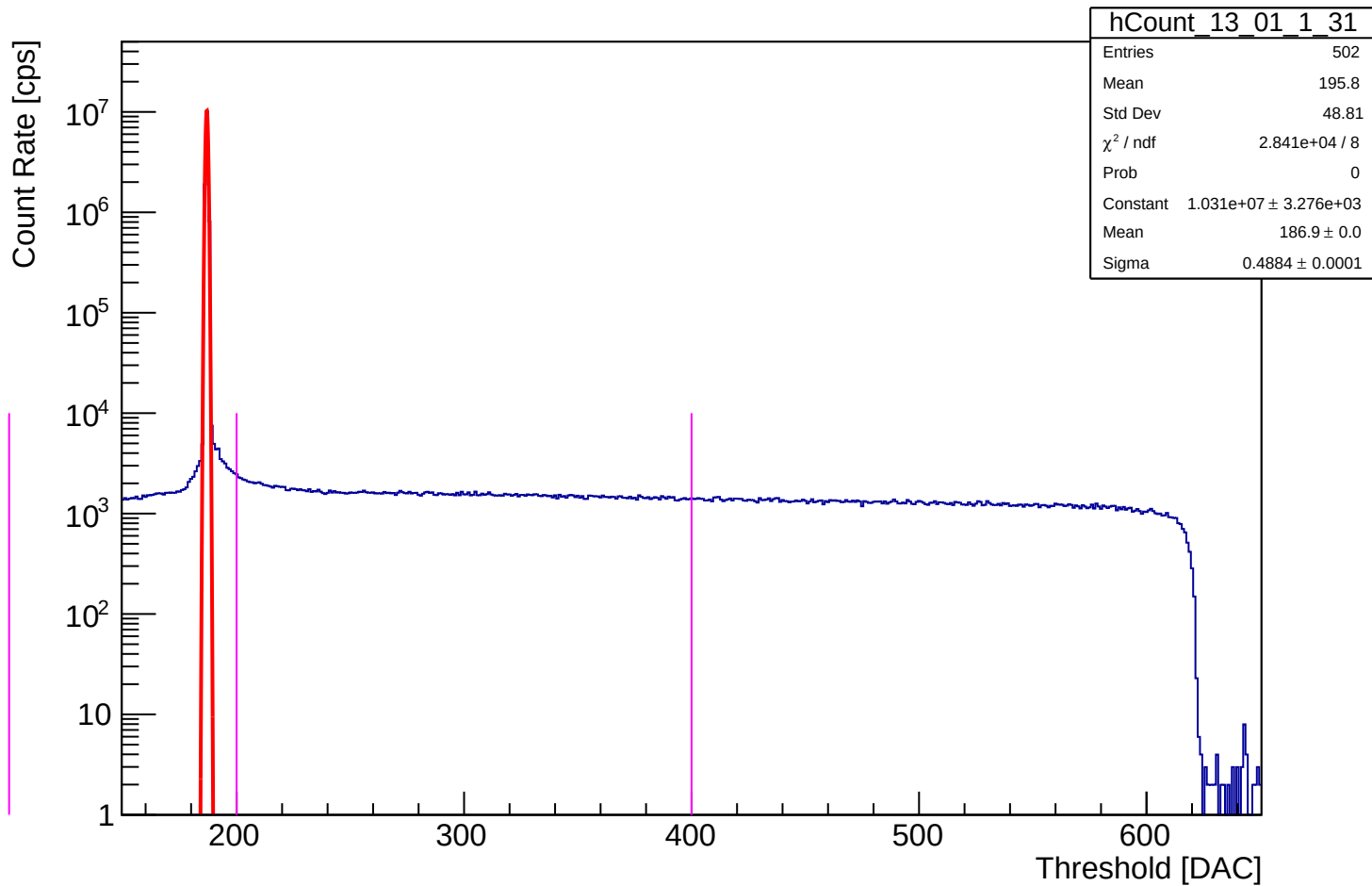
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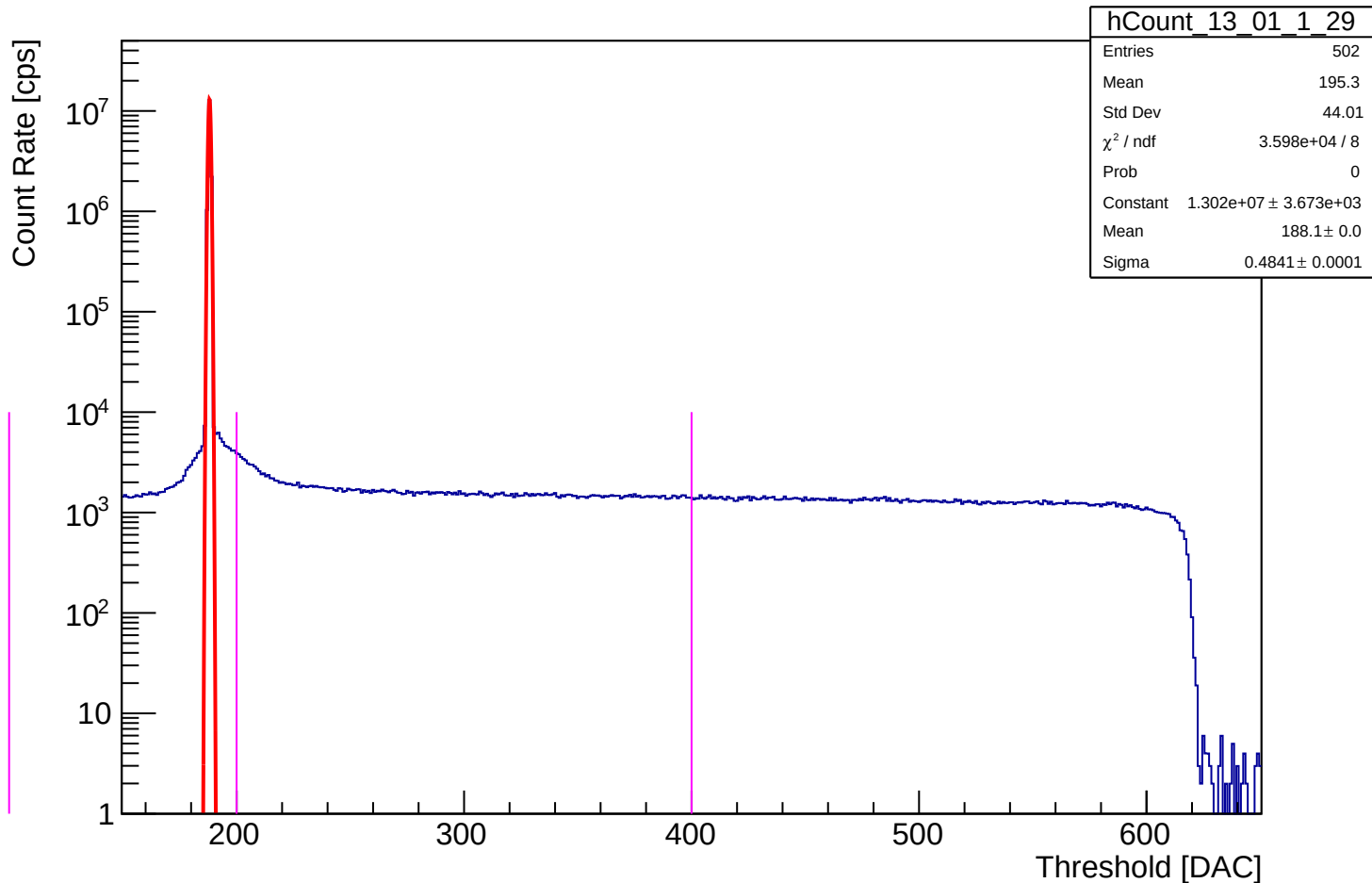
Row 1 Tile 1 Pmt 4 Pixel 64 Slot 13 Fiber 1 Asic 0 Channel 63



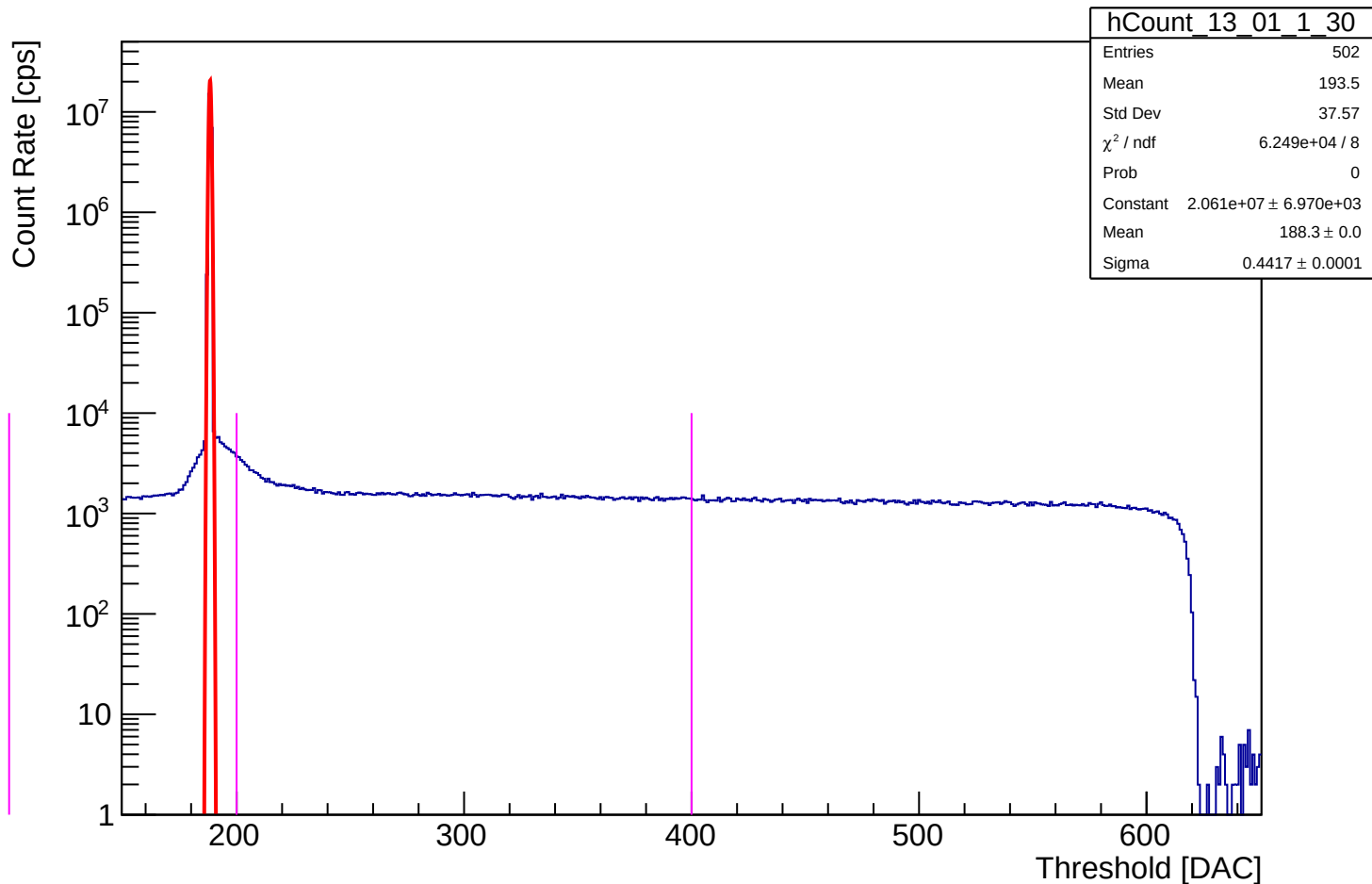
Row 1 Tile 1 Pmt 5 Pixel 1 Slot 13 Fiber 1 Asic 1 Channel 31



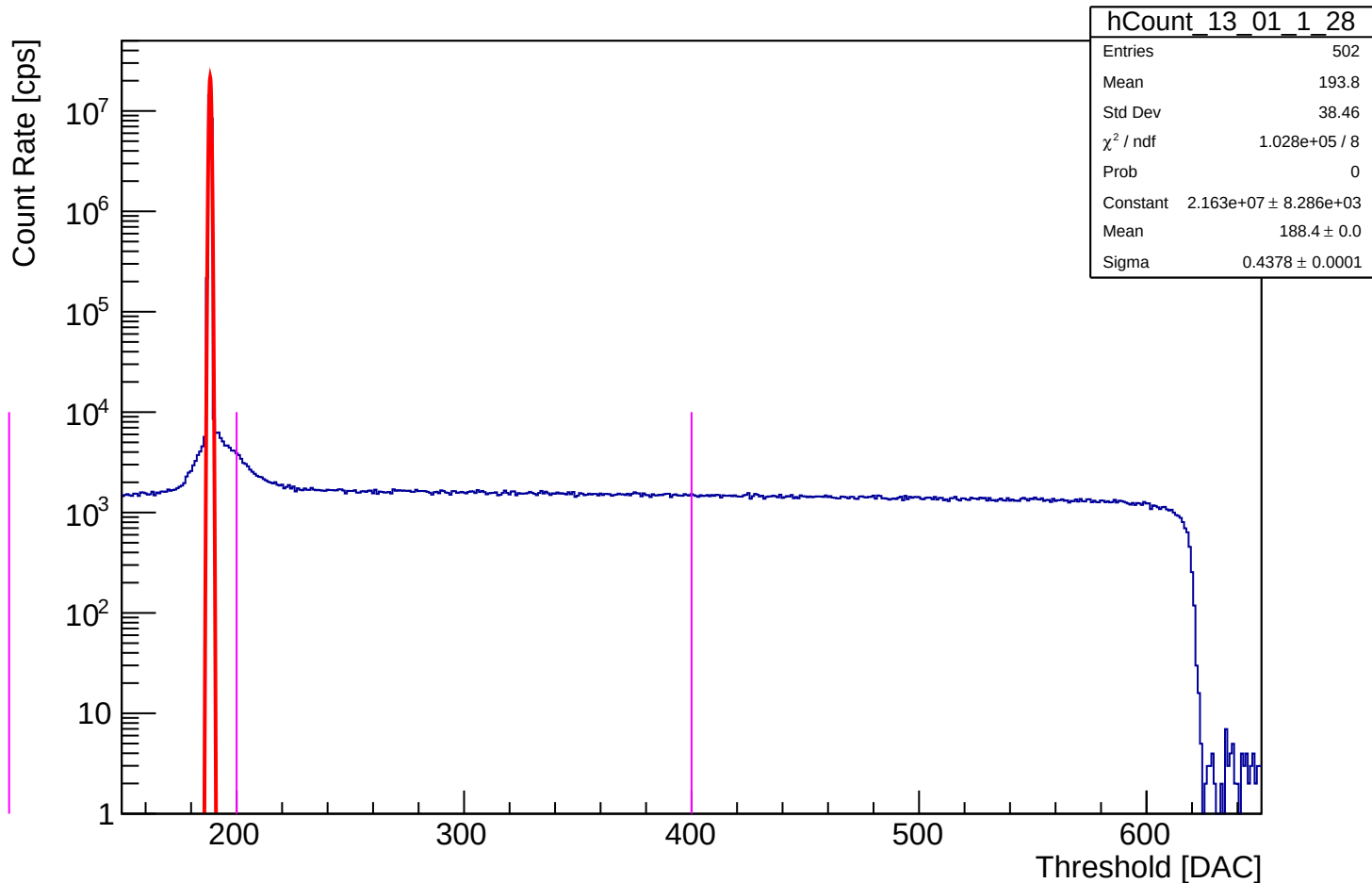
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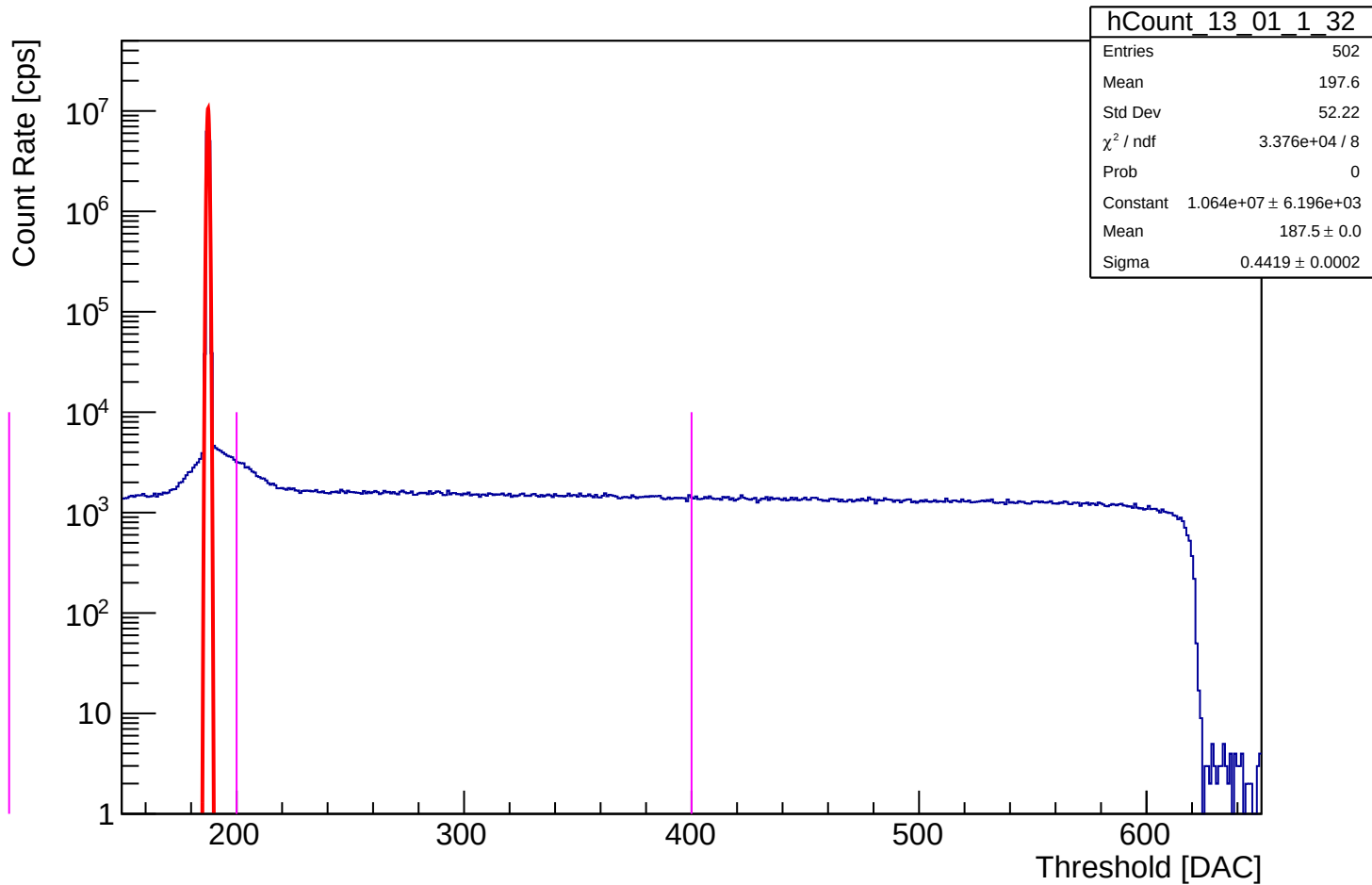
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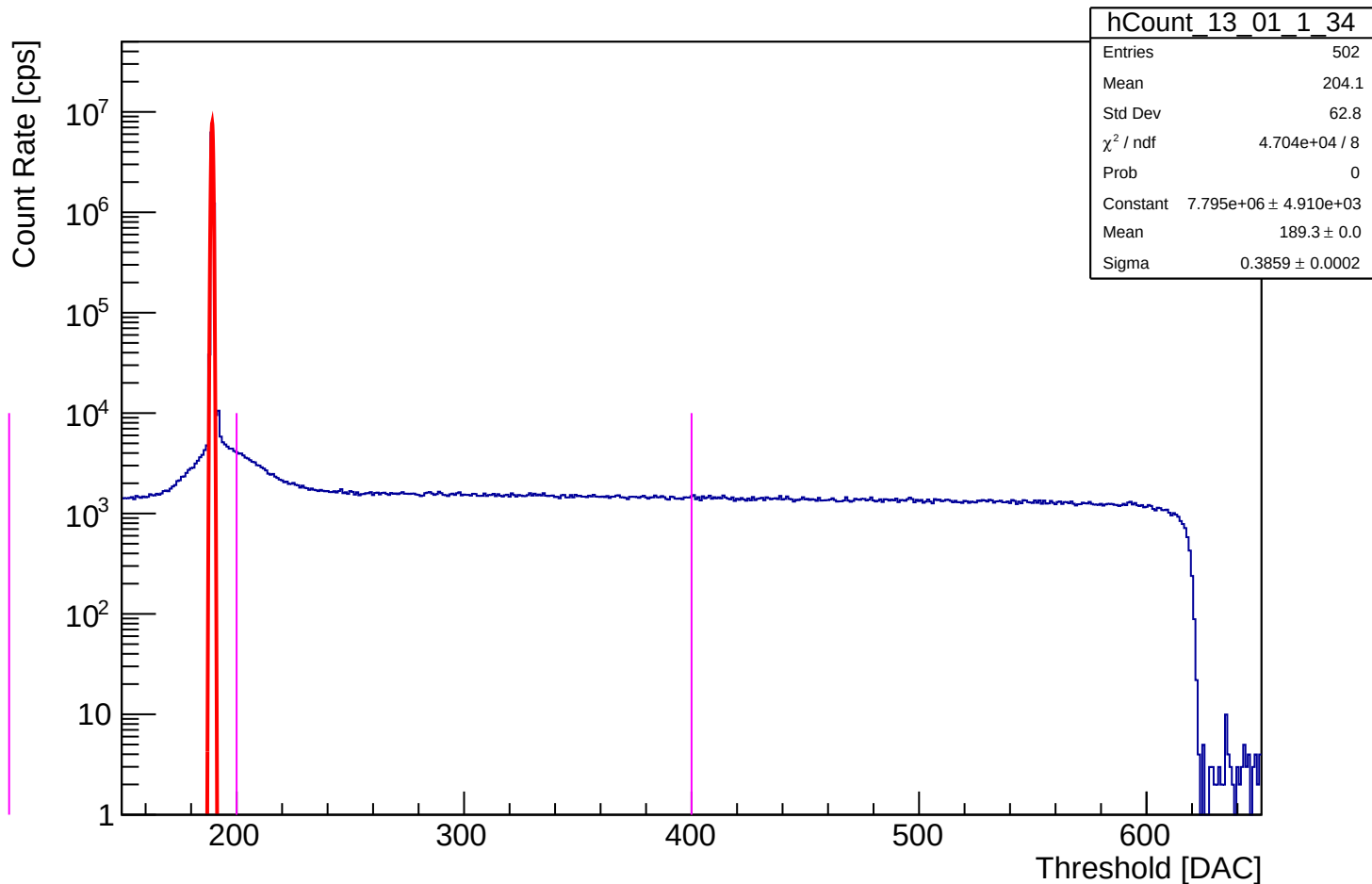
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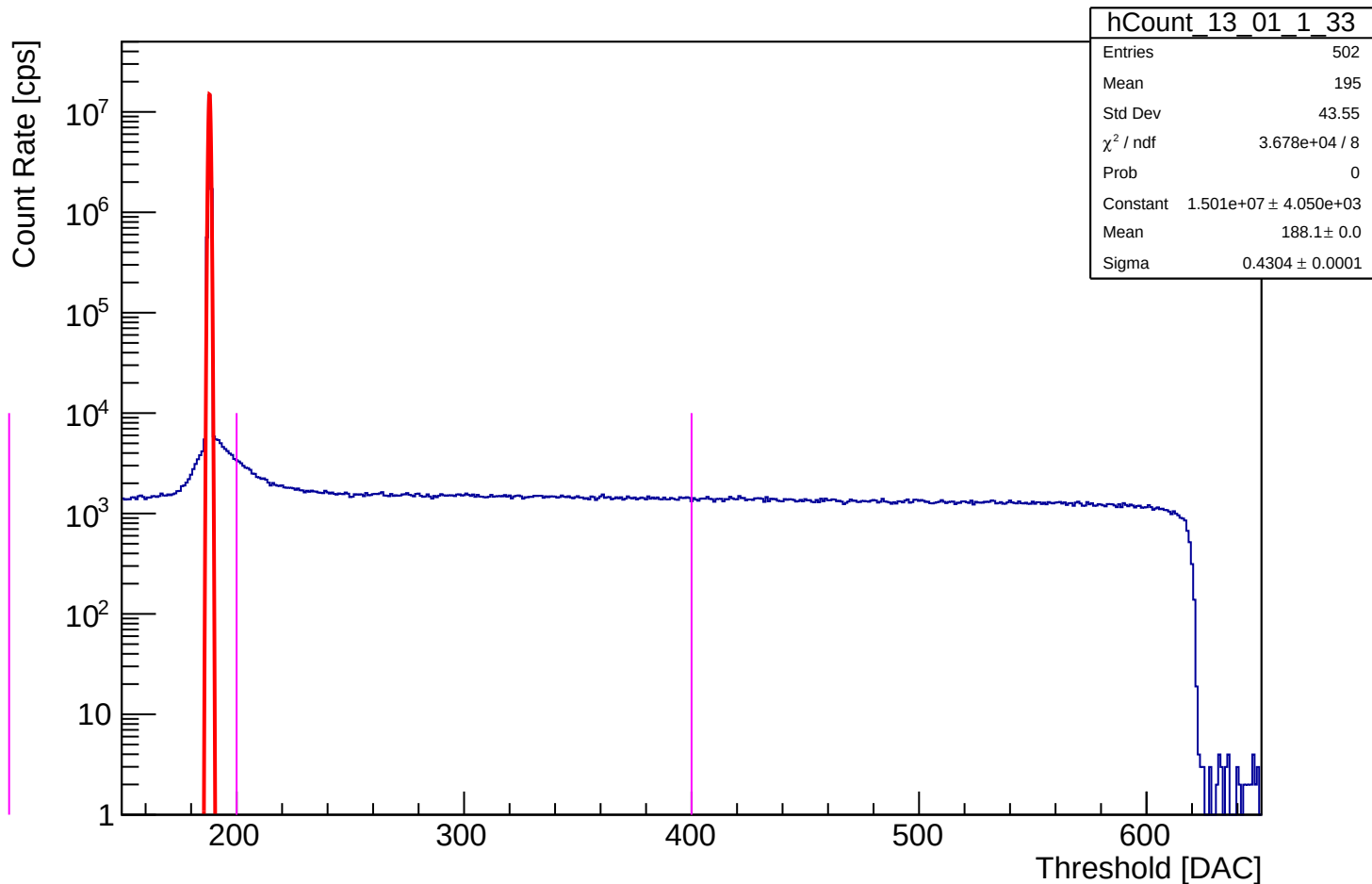
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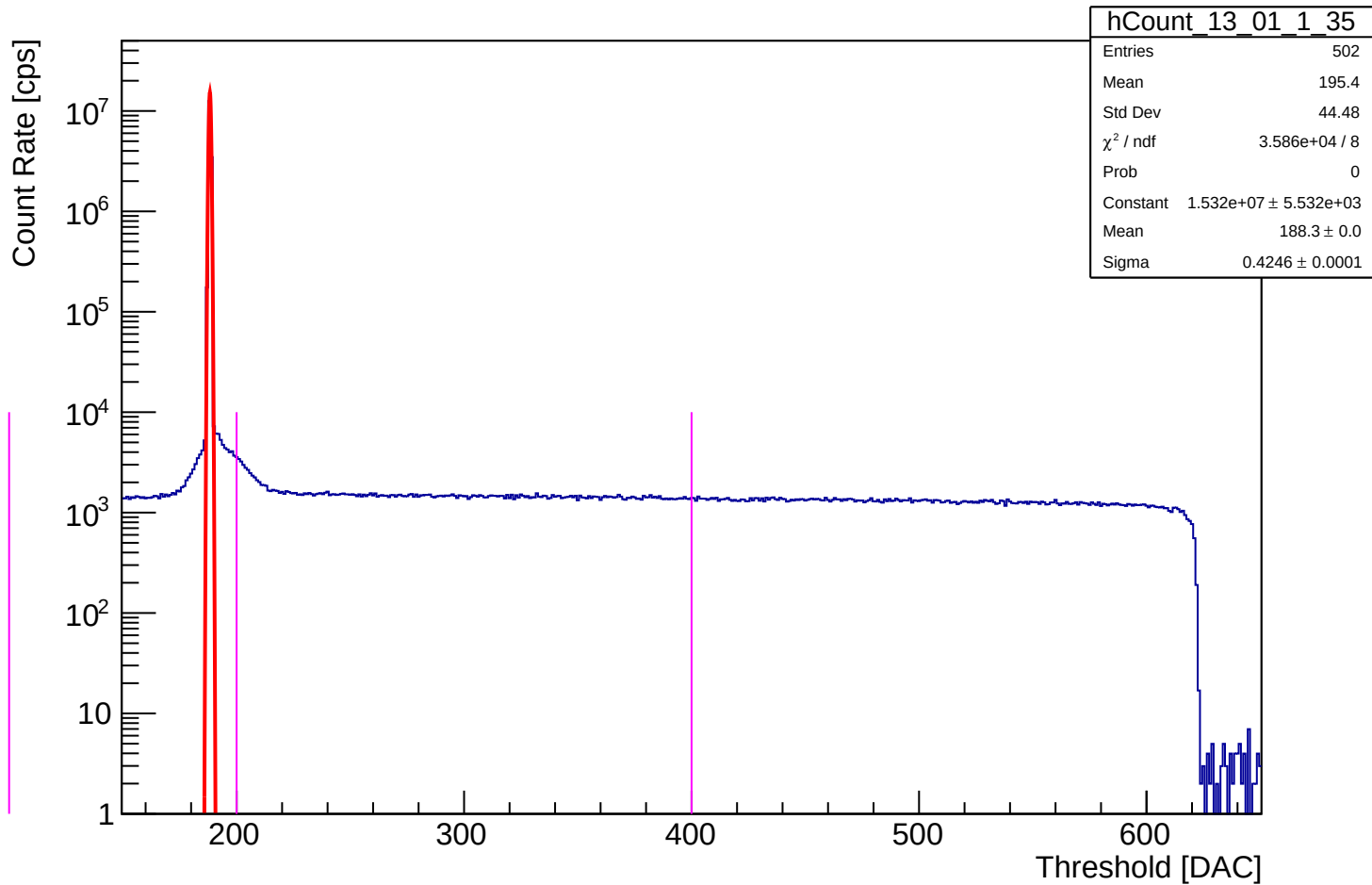
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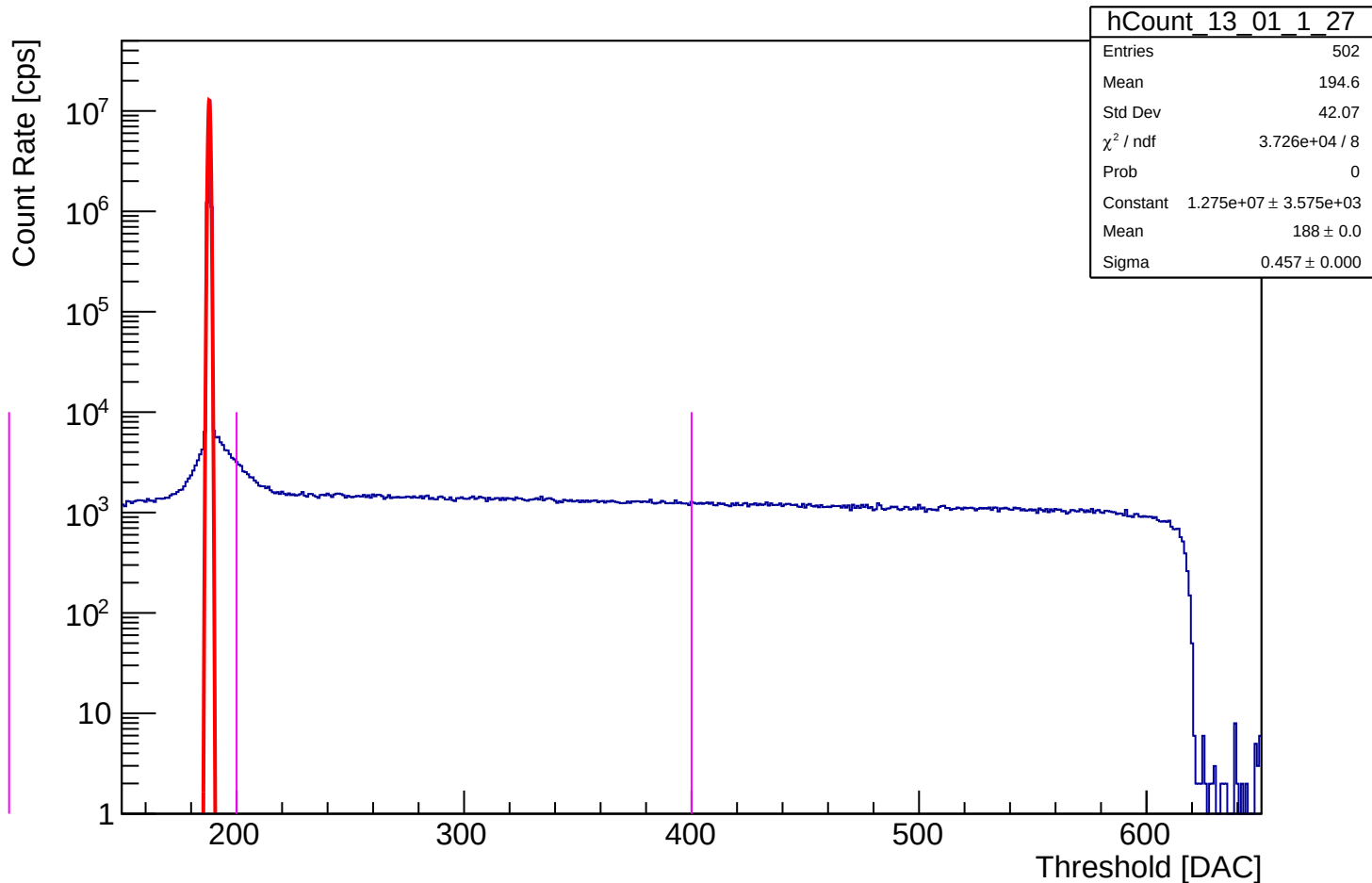
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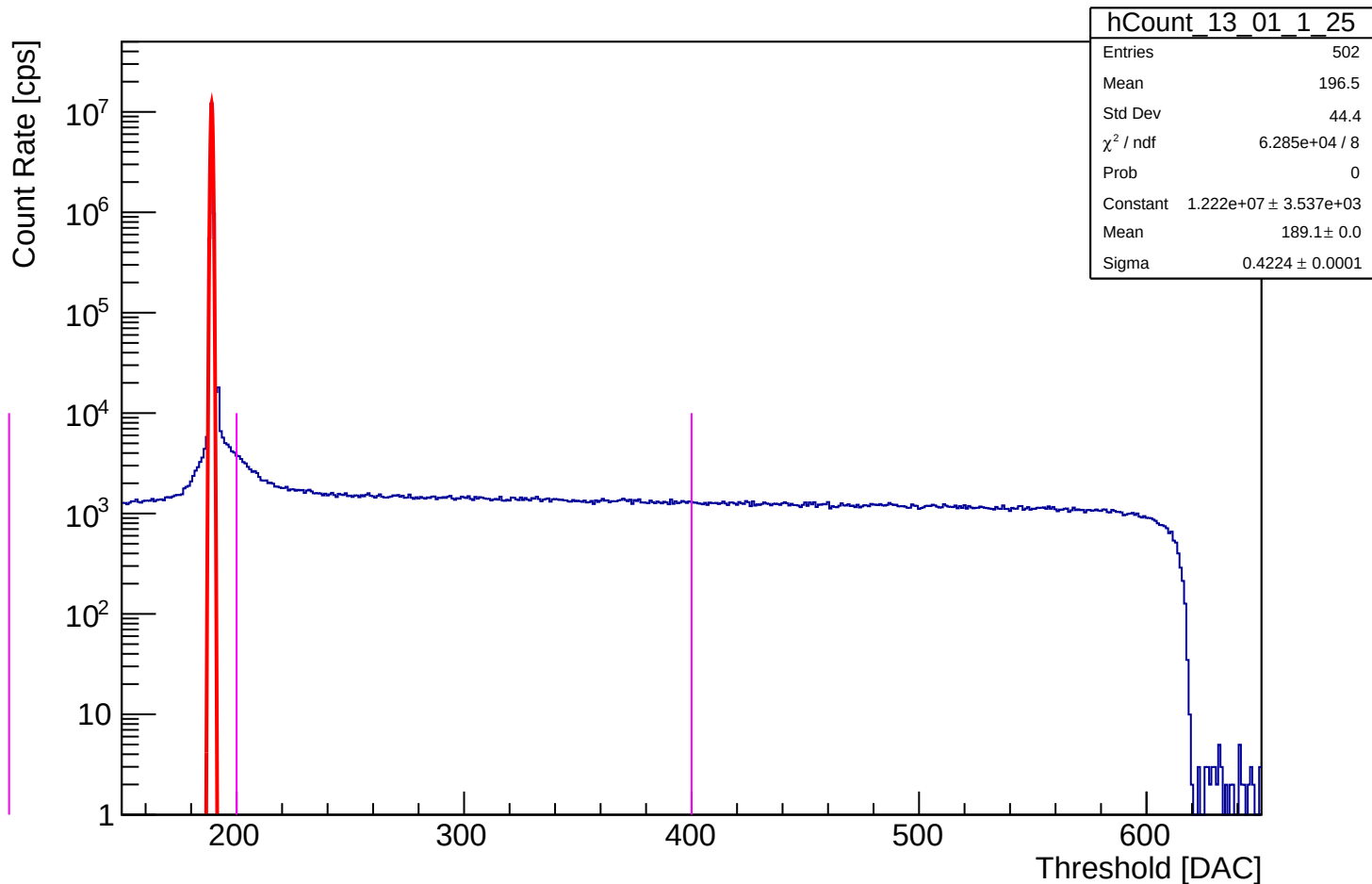
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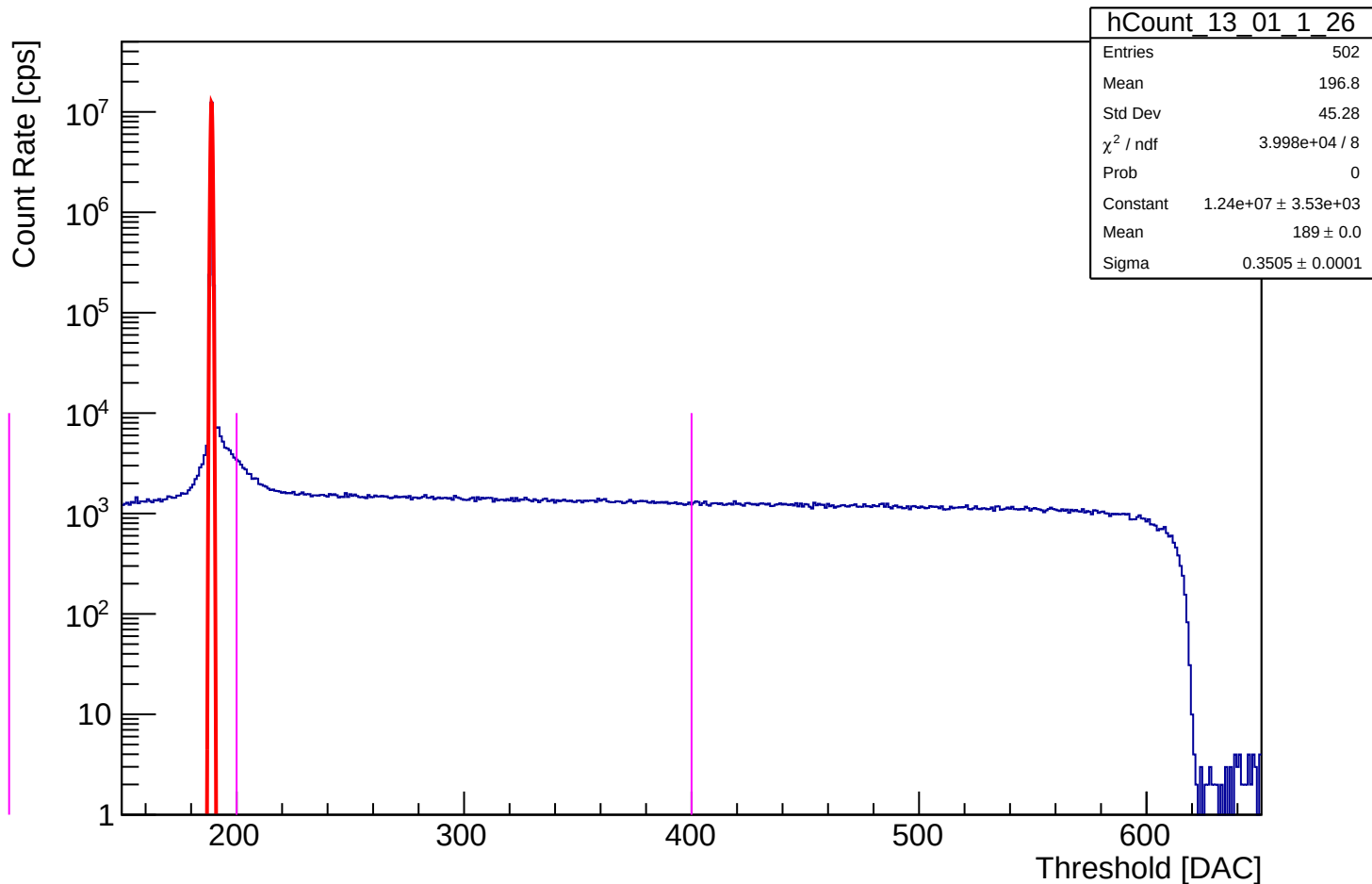
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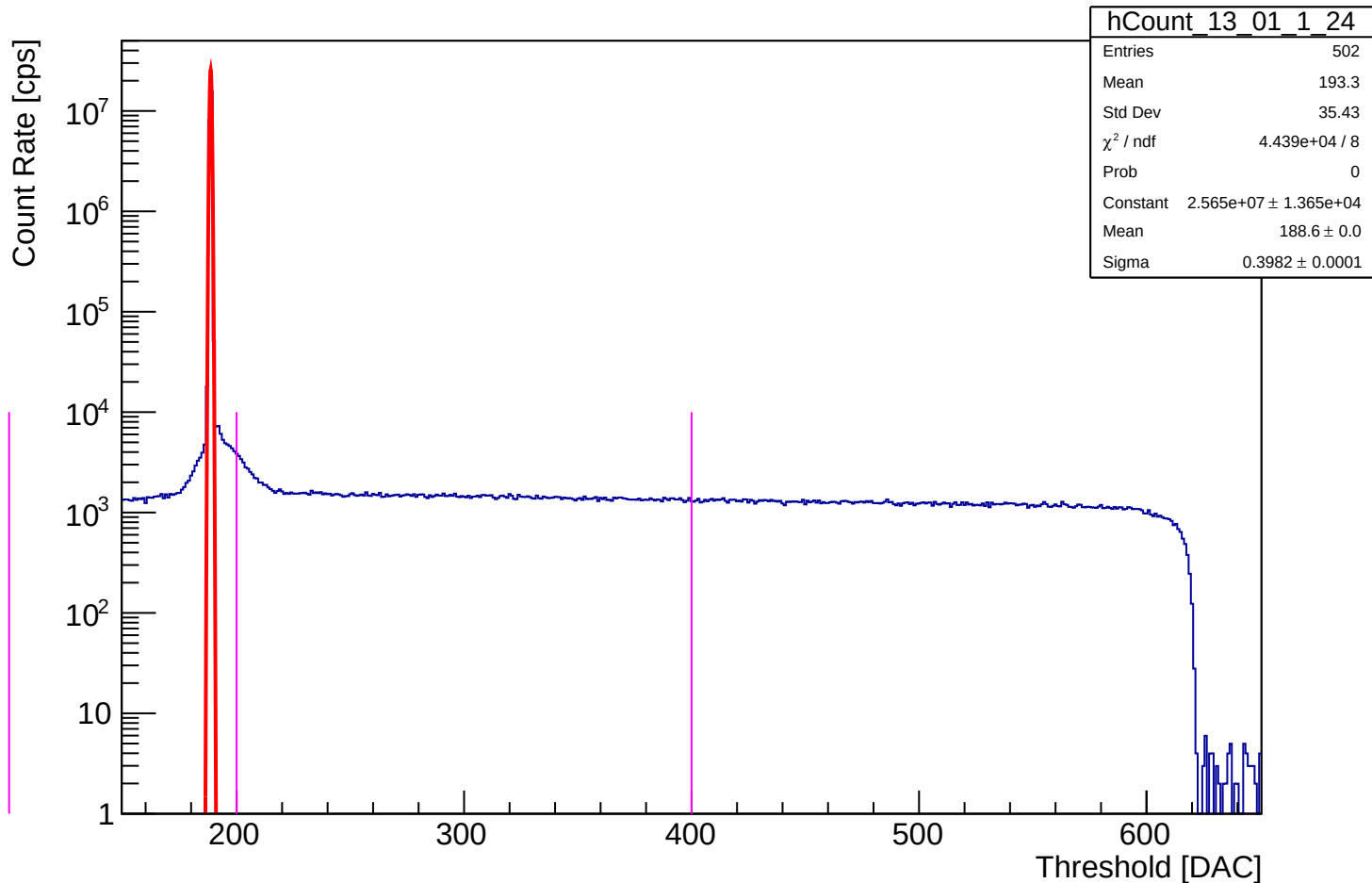
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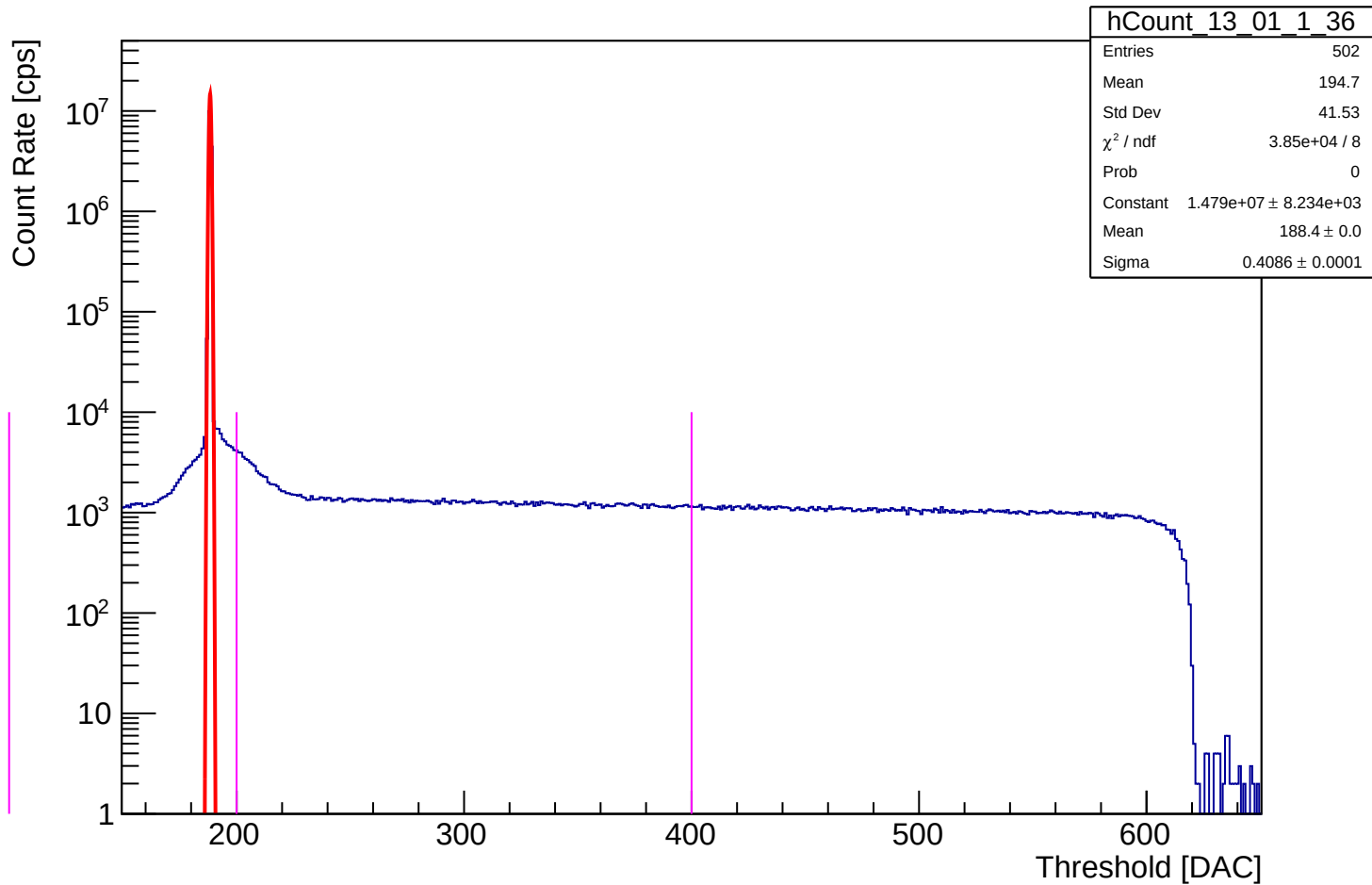
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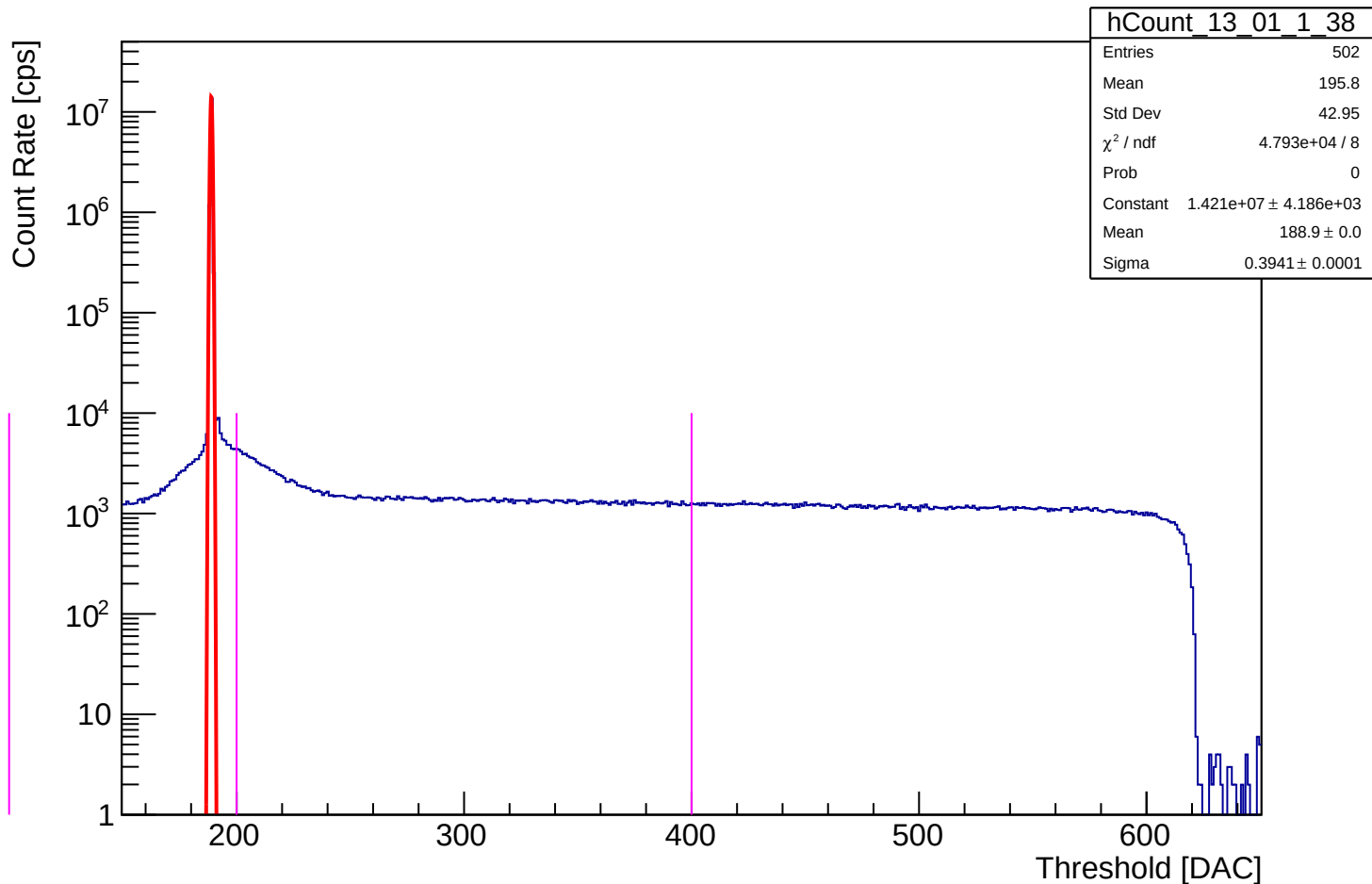
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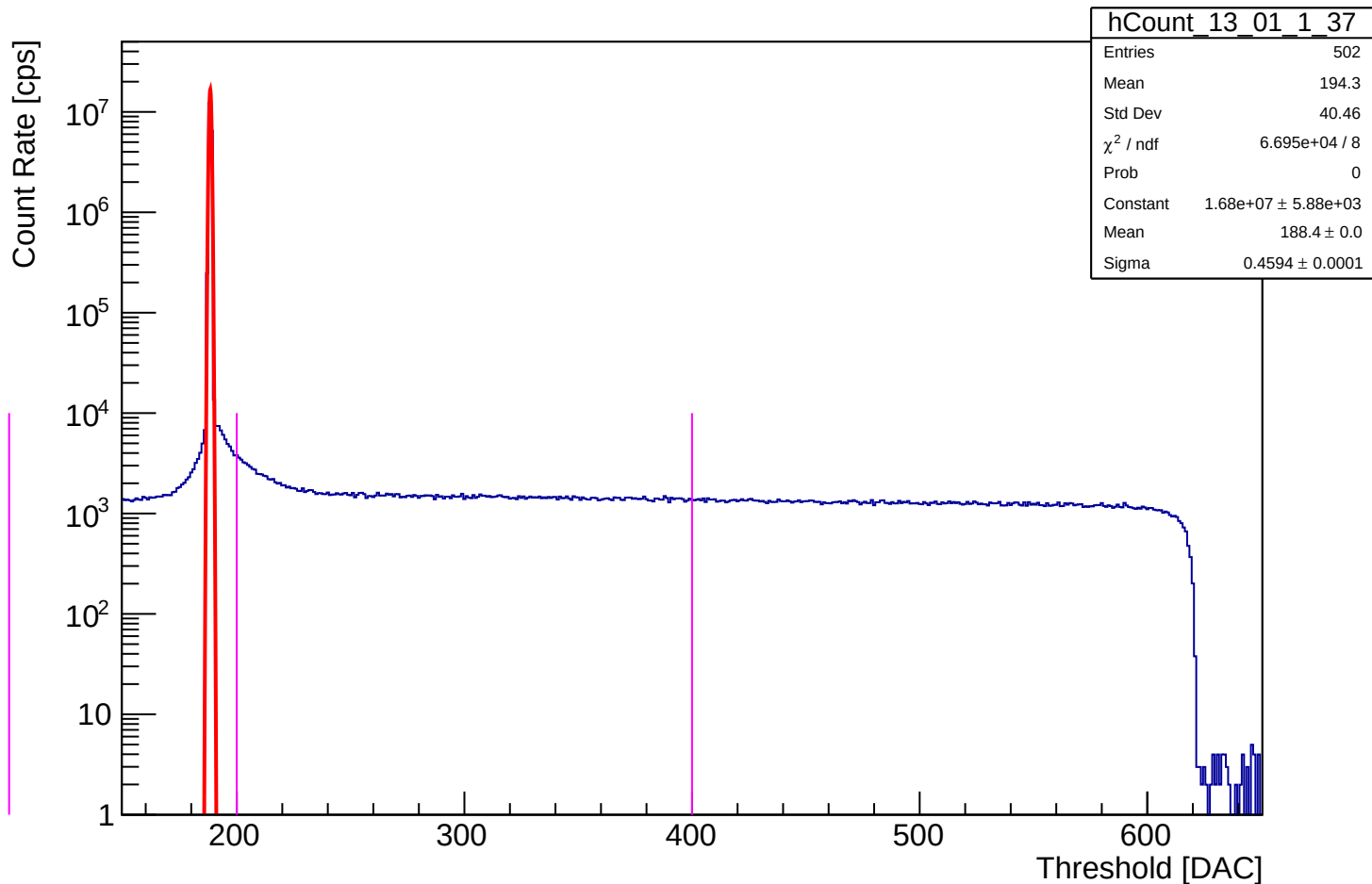
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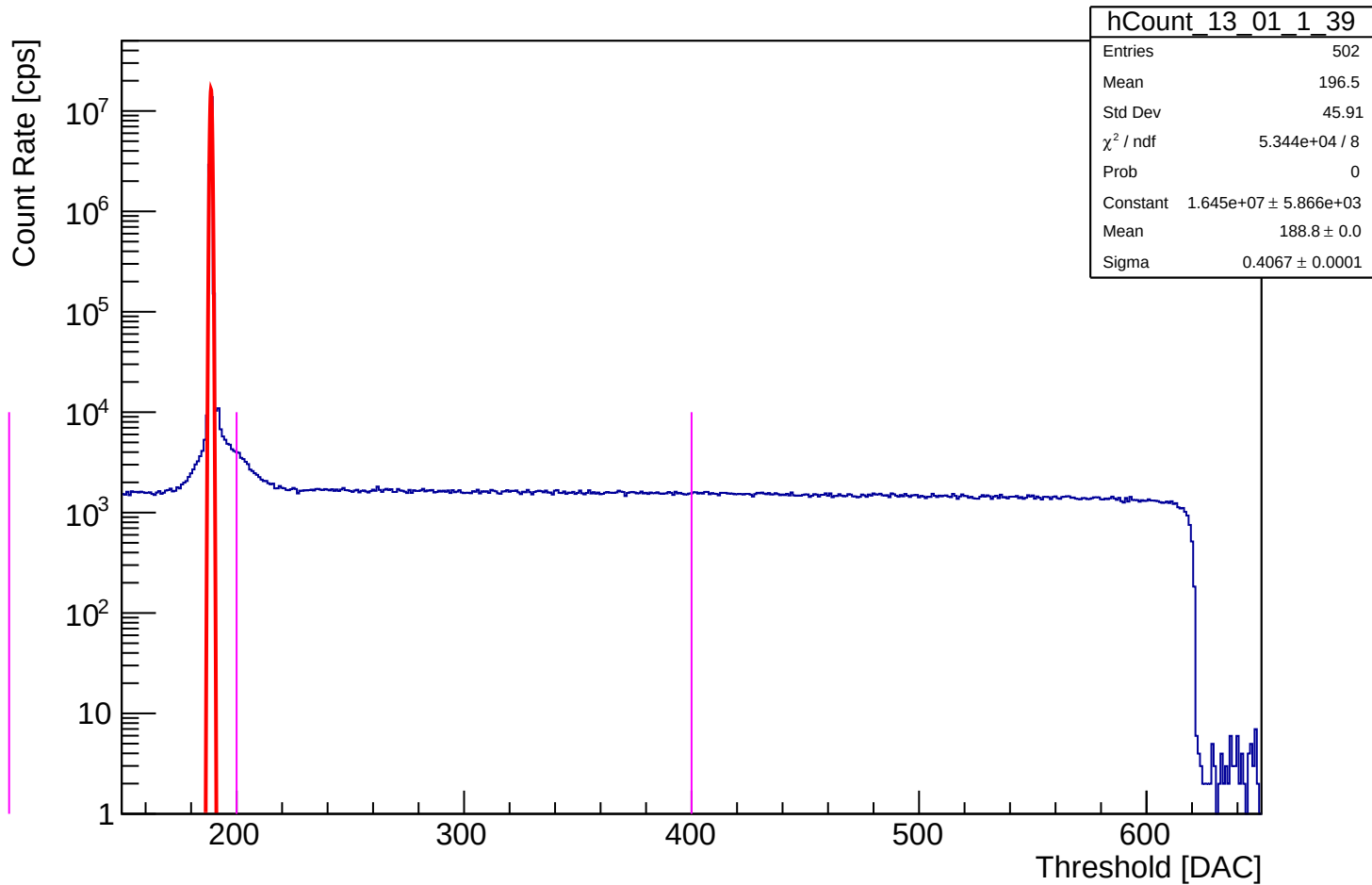
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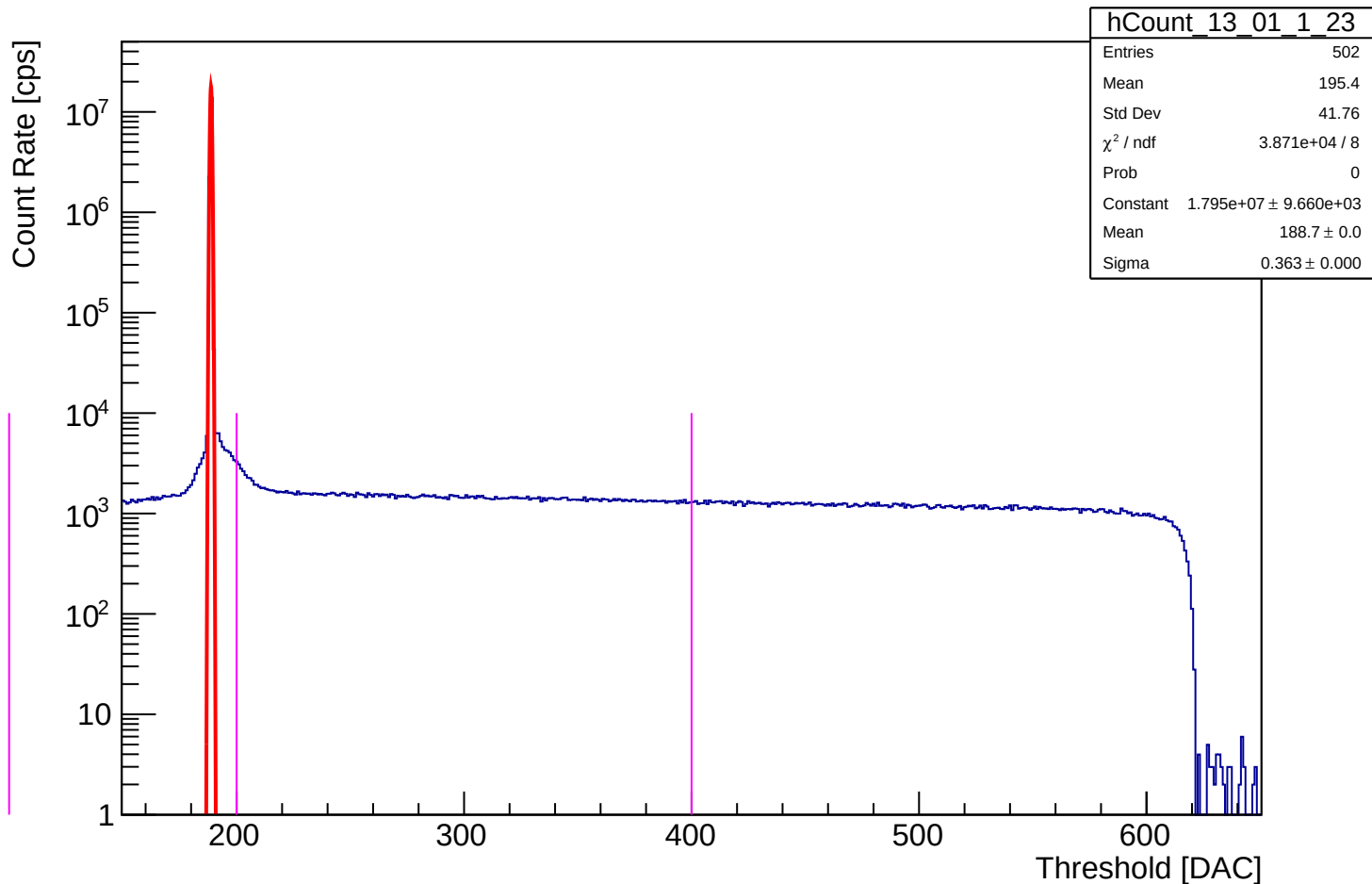
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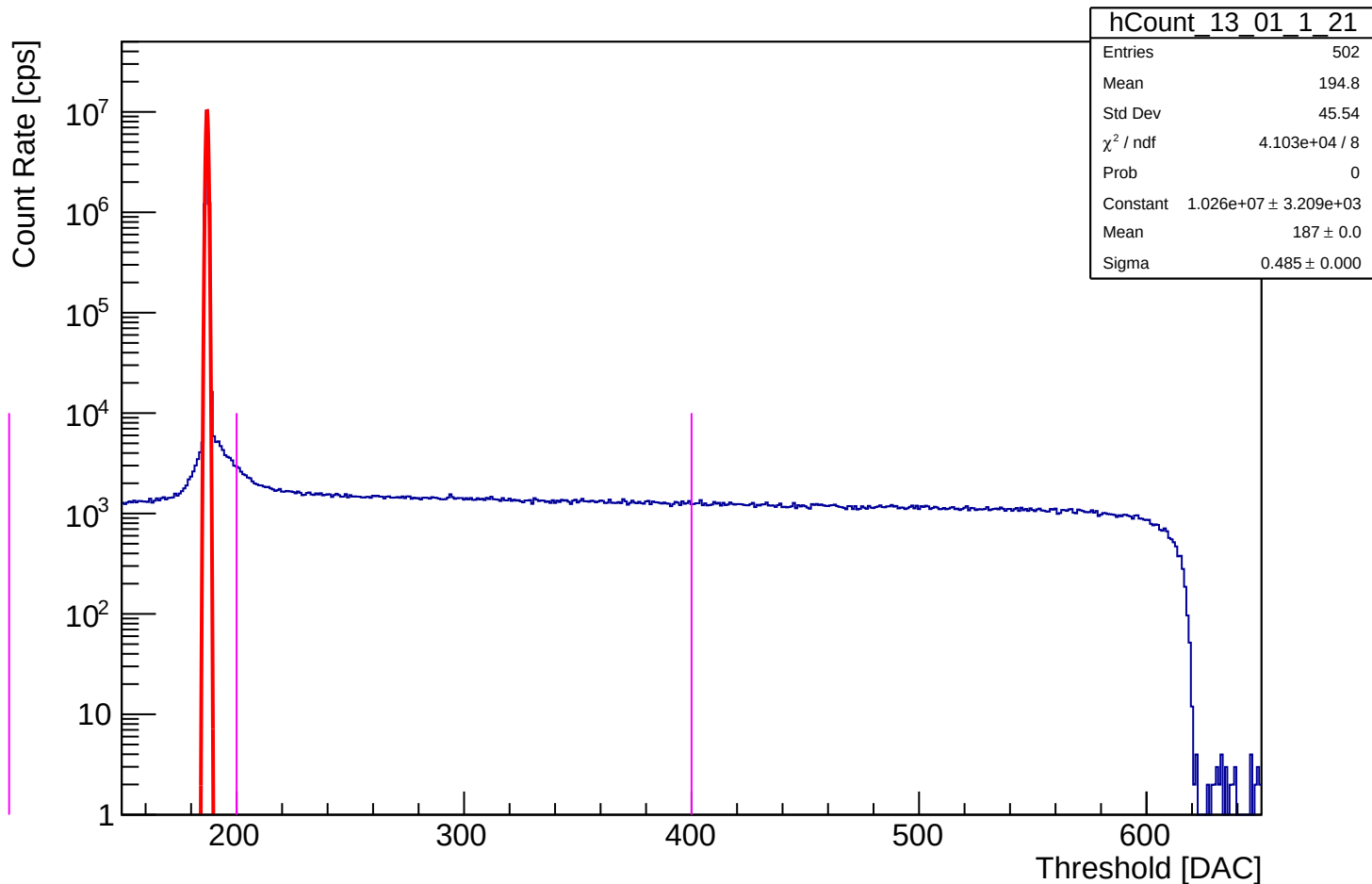
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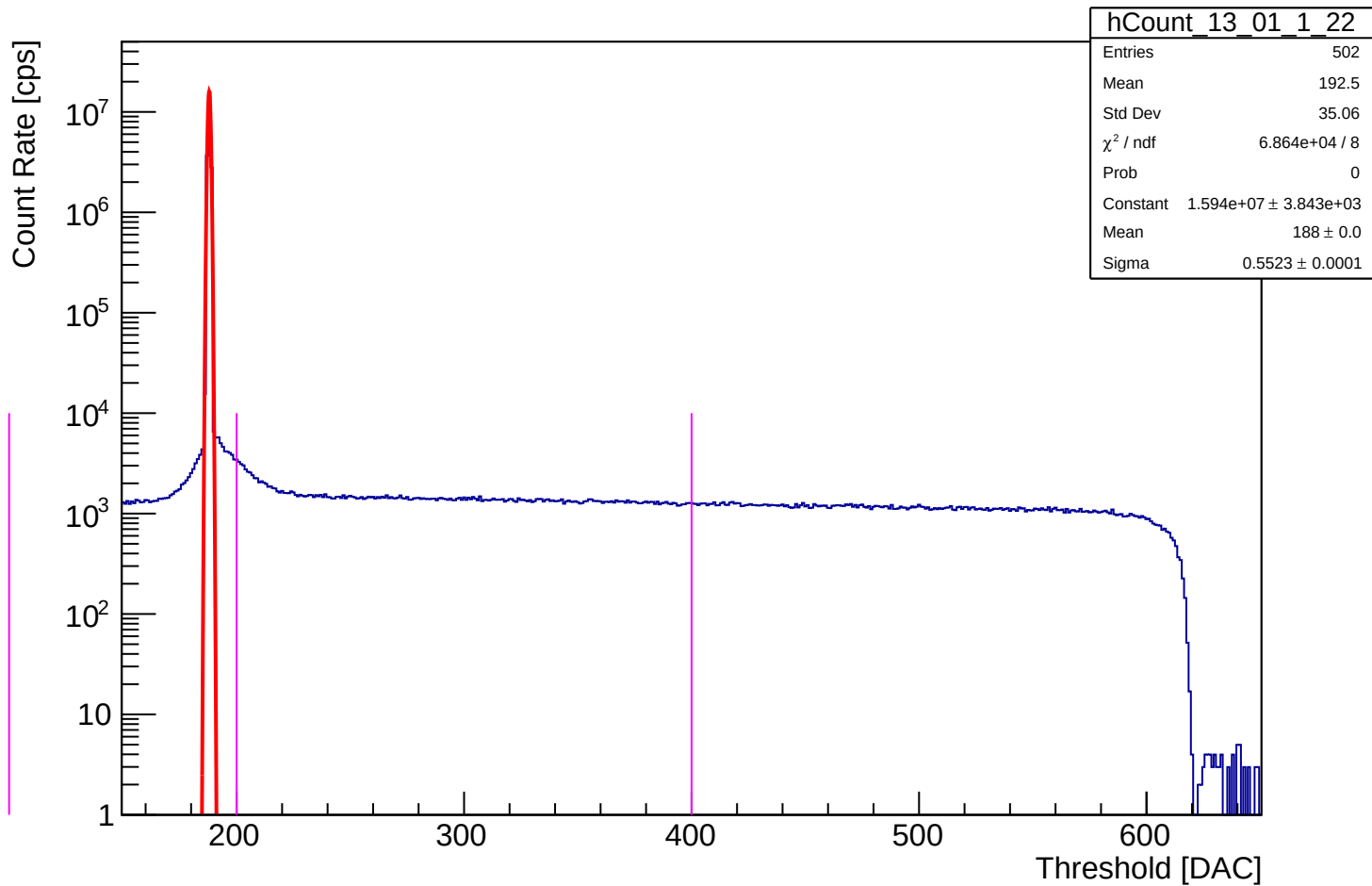
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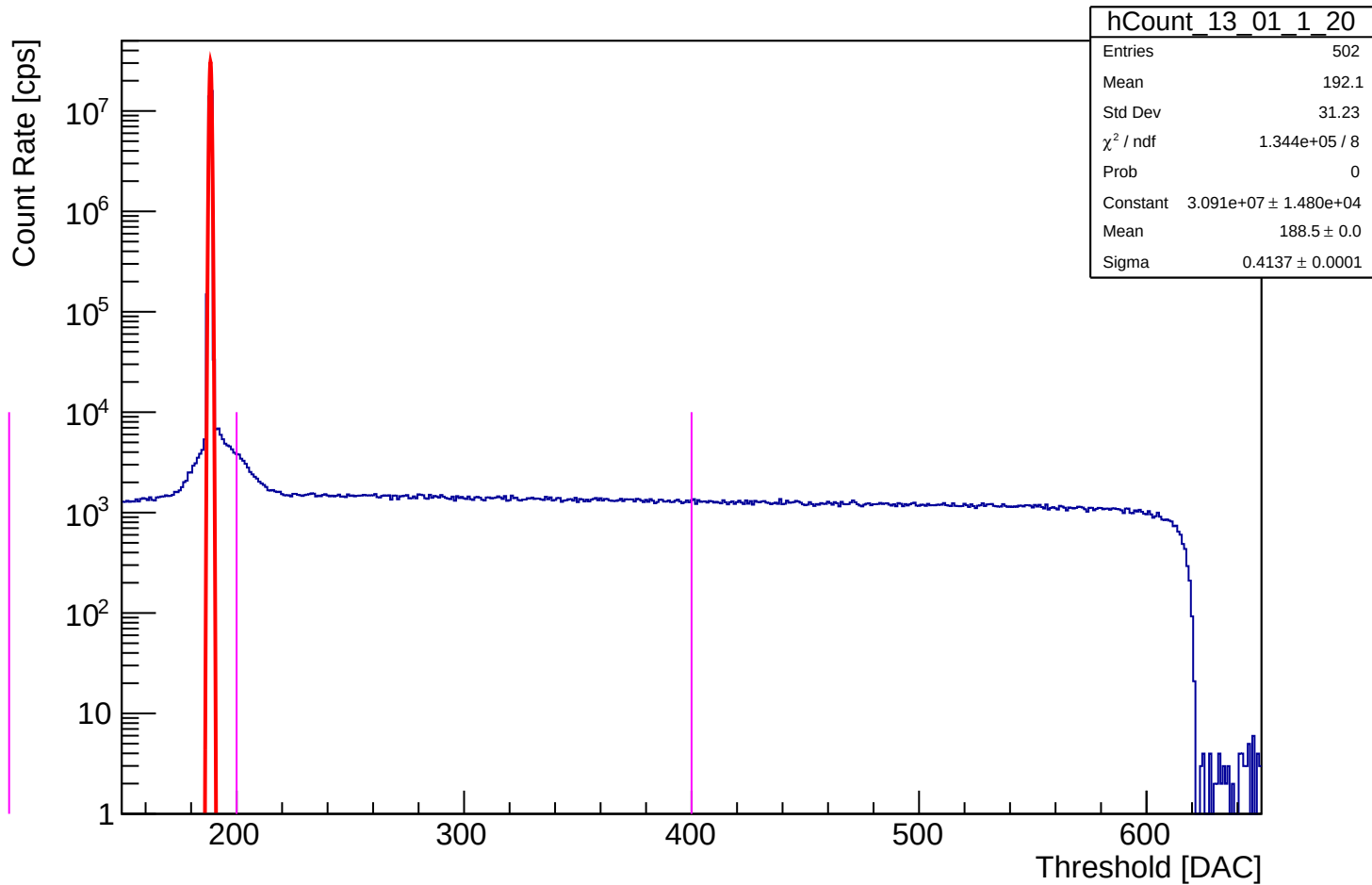
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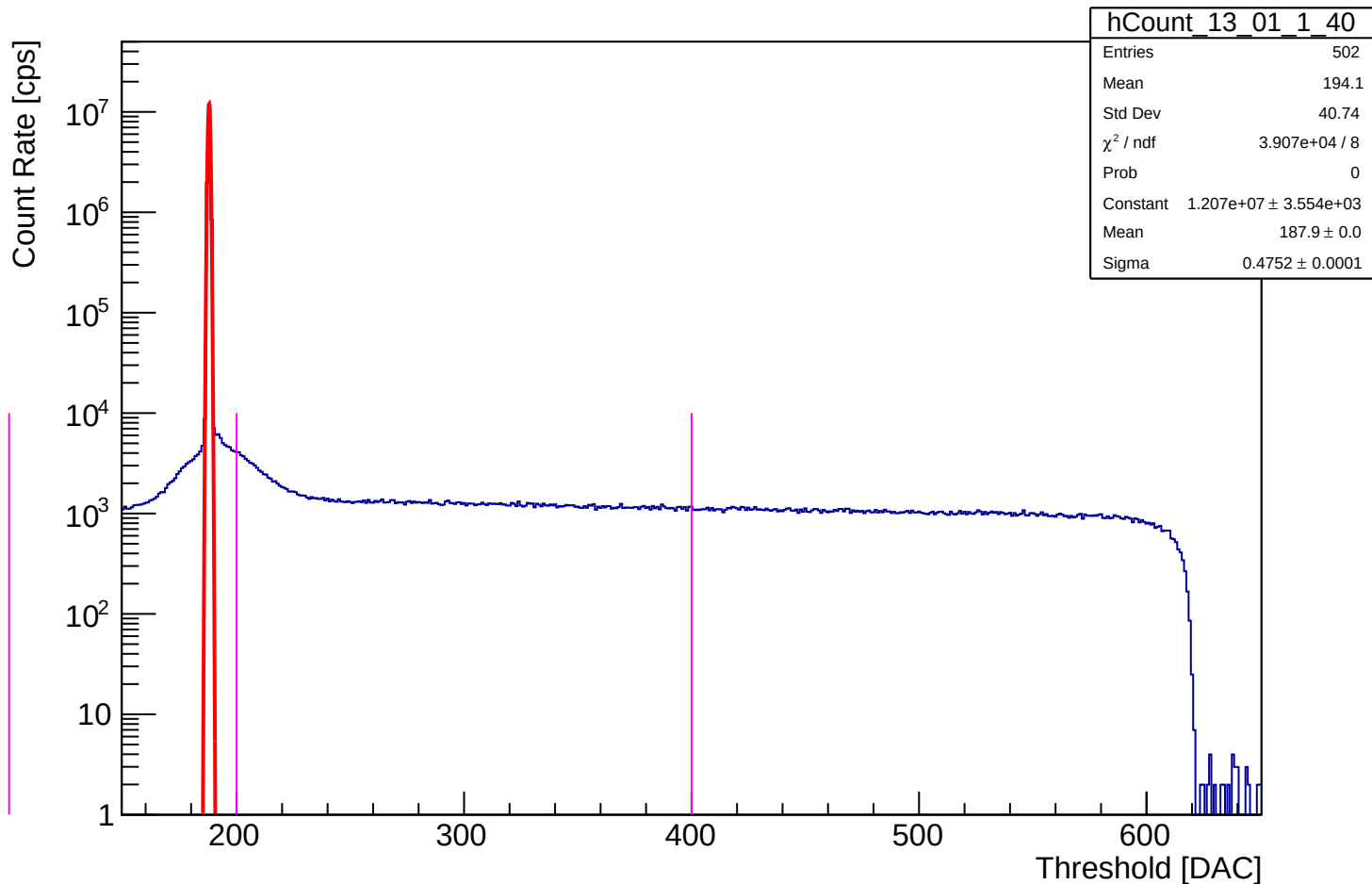
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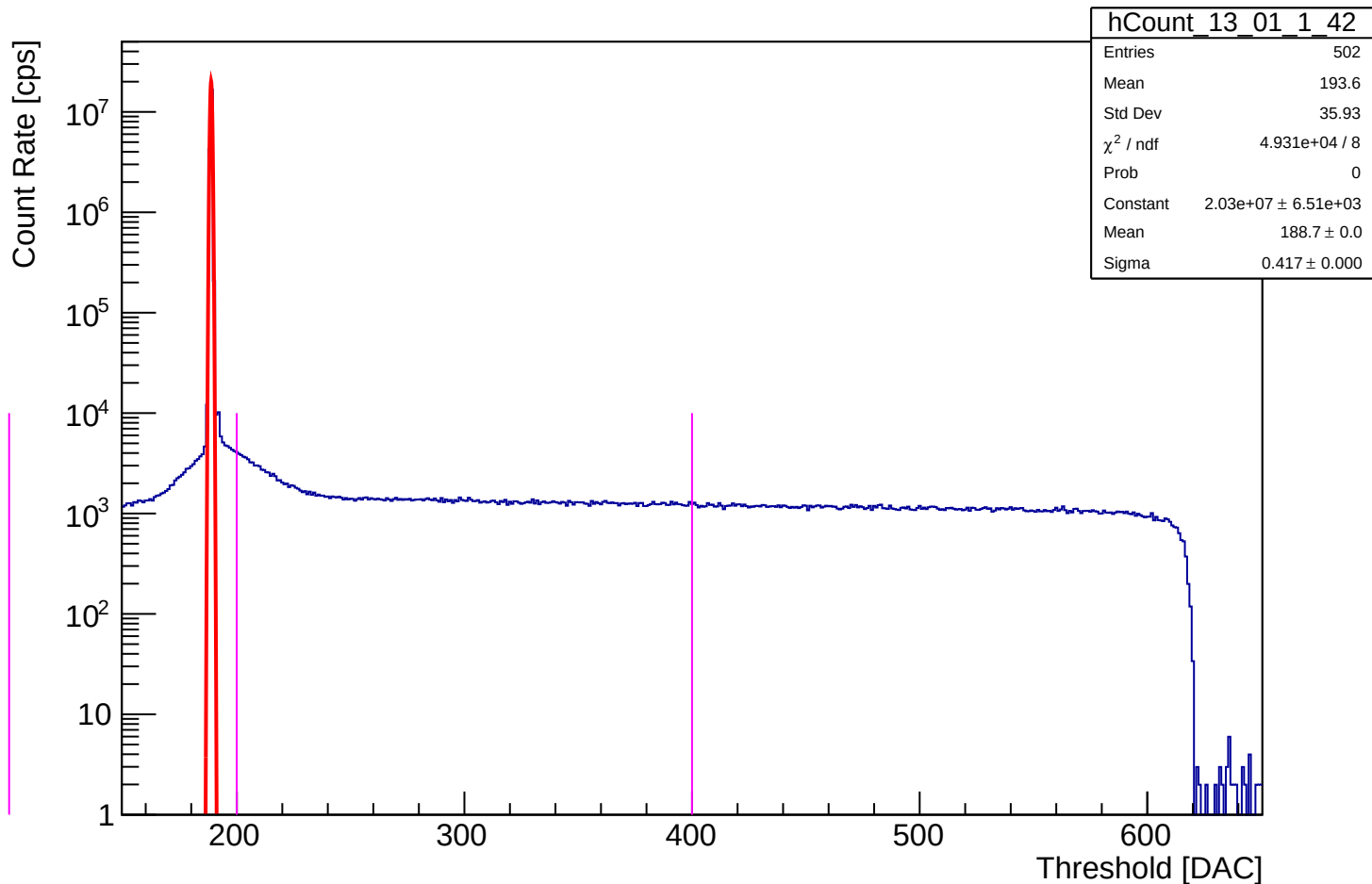


Row 1 Tile 1 Pmt 5 Pixel 20 Slot 13 Fiber 1 Asic 1 Channel 20

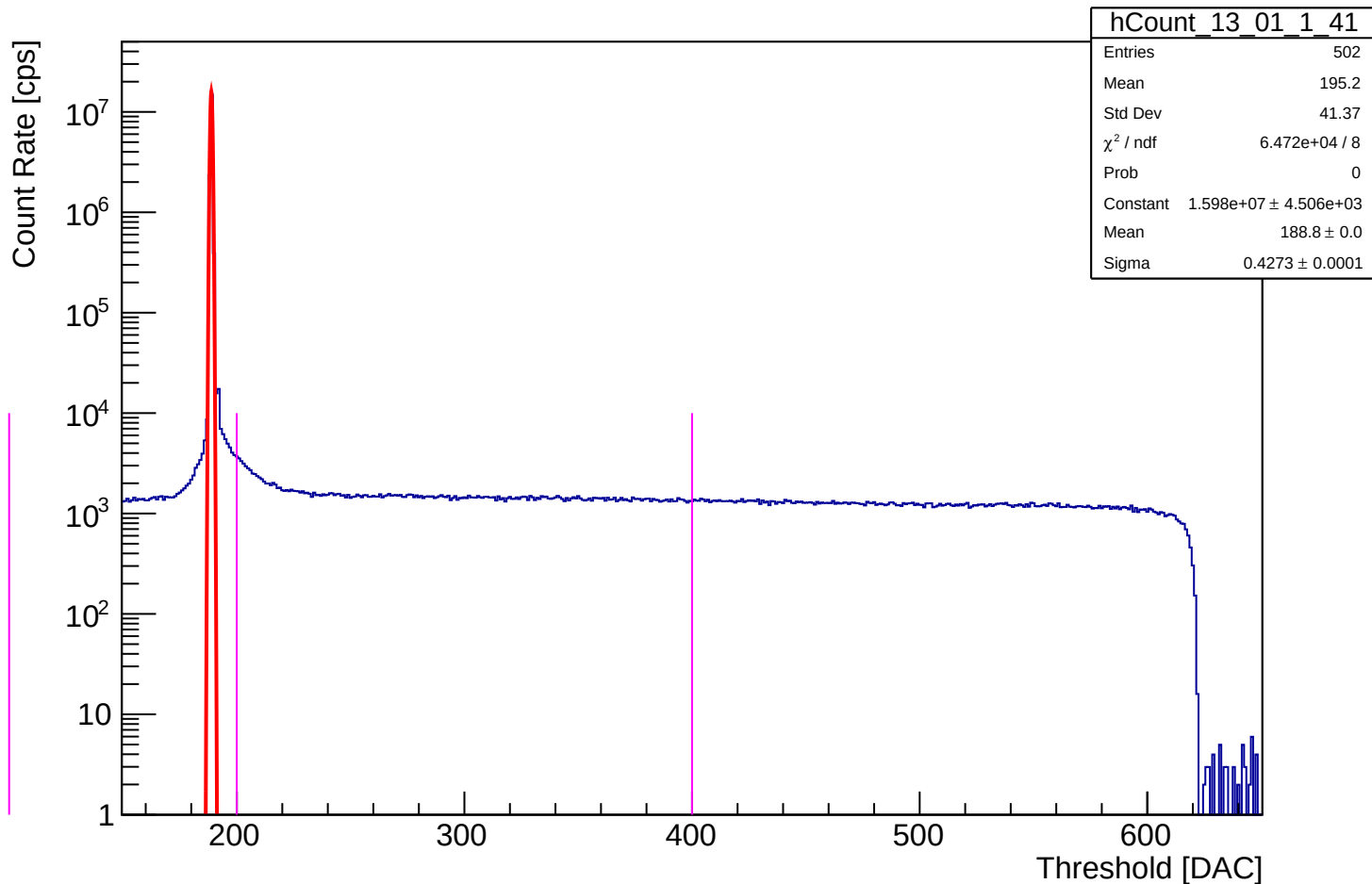


Row 1 Tile 1 Pmt 5 Pixel 21 Slot 13 Fiber 1 Asic 1 Channel 40

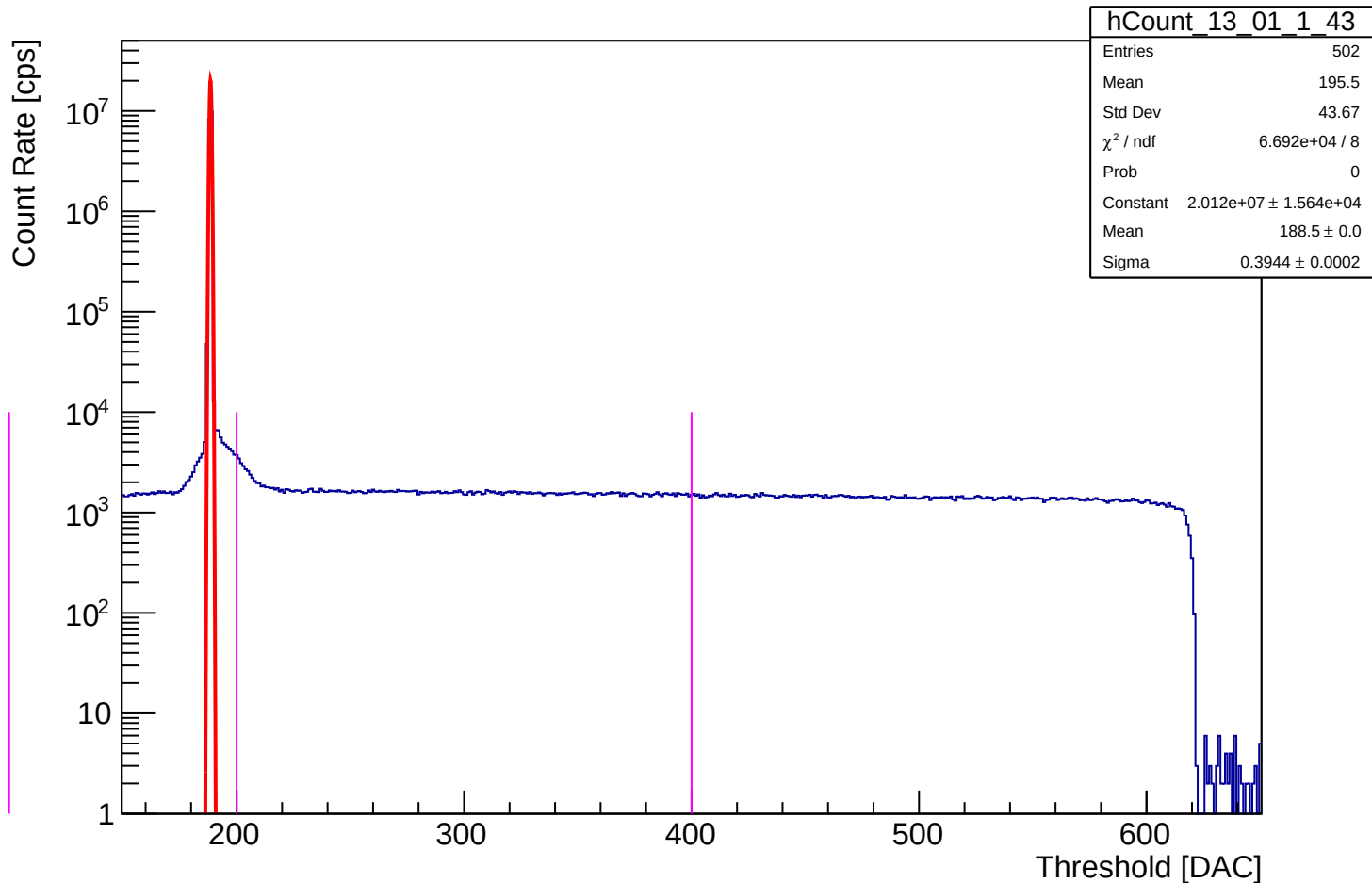




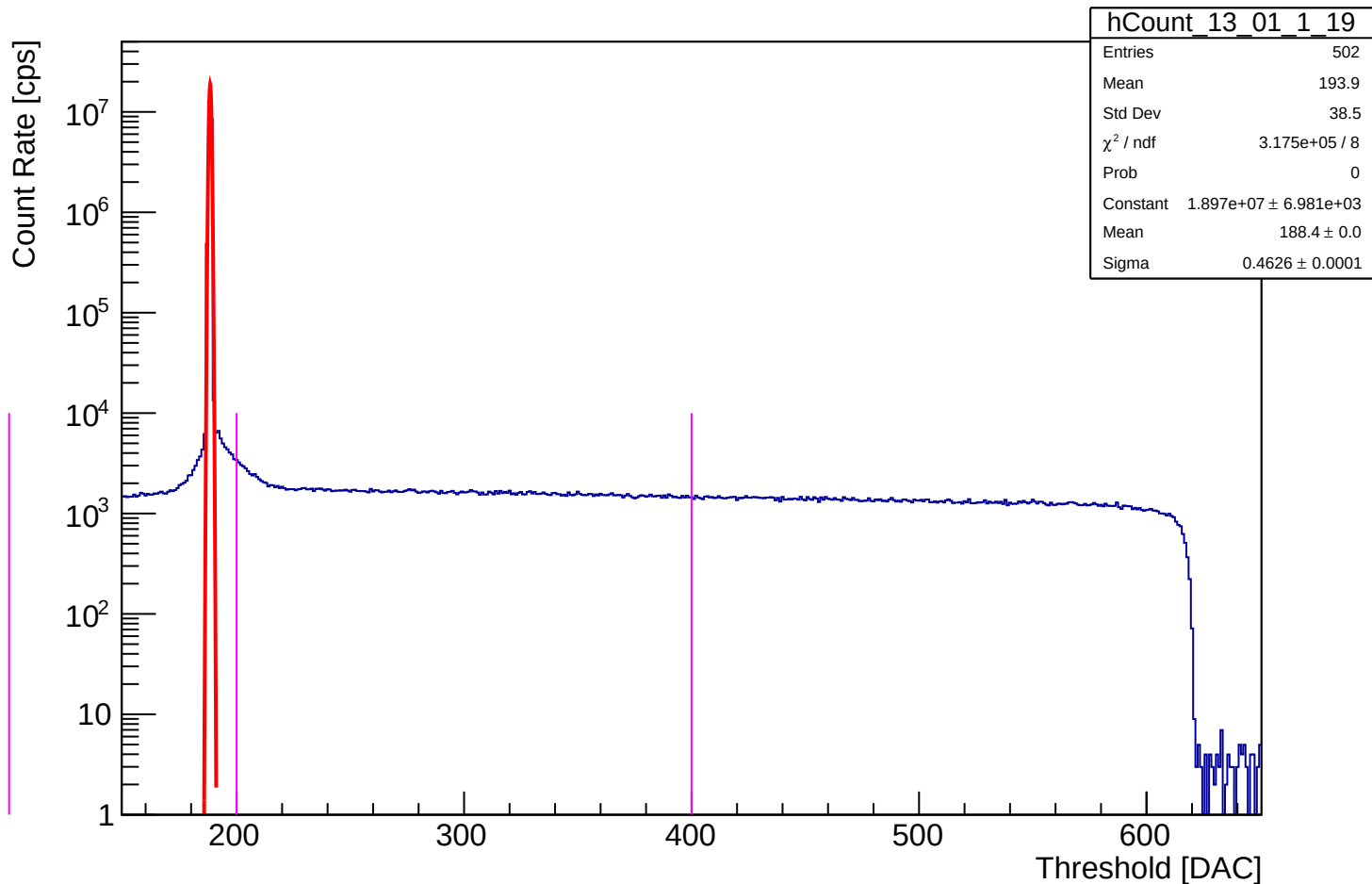
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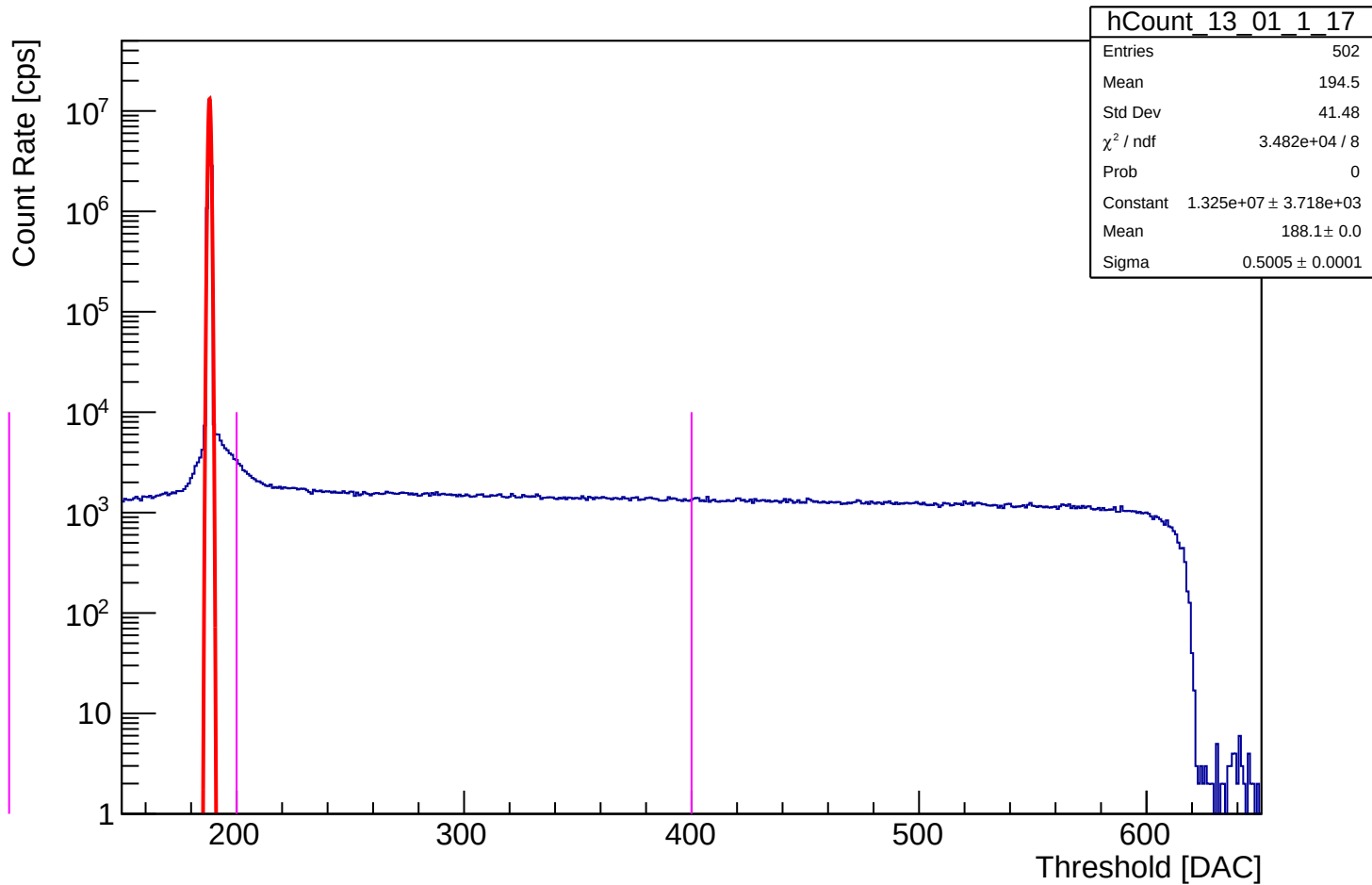
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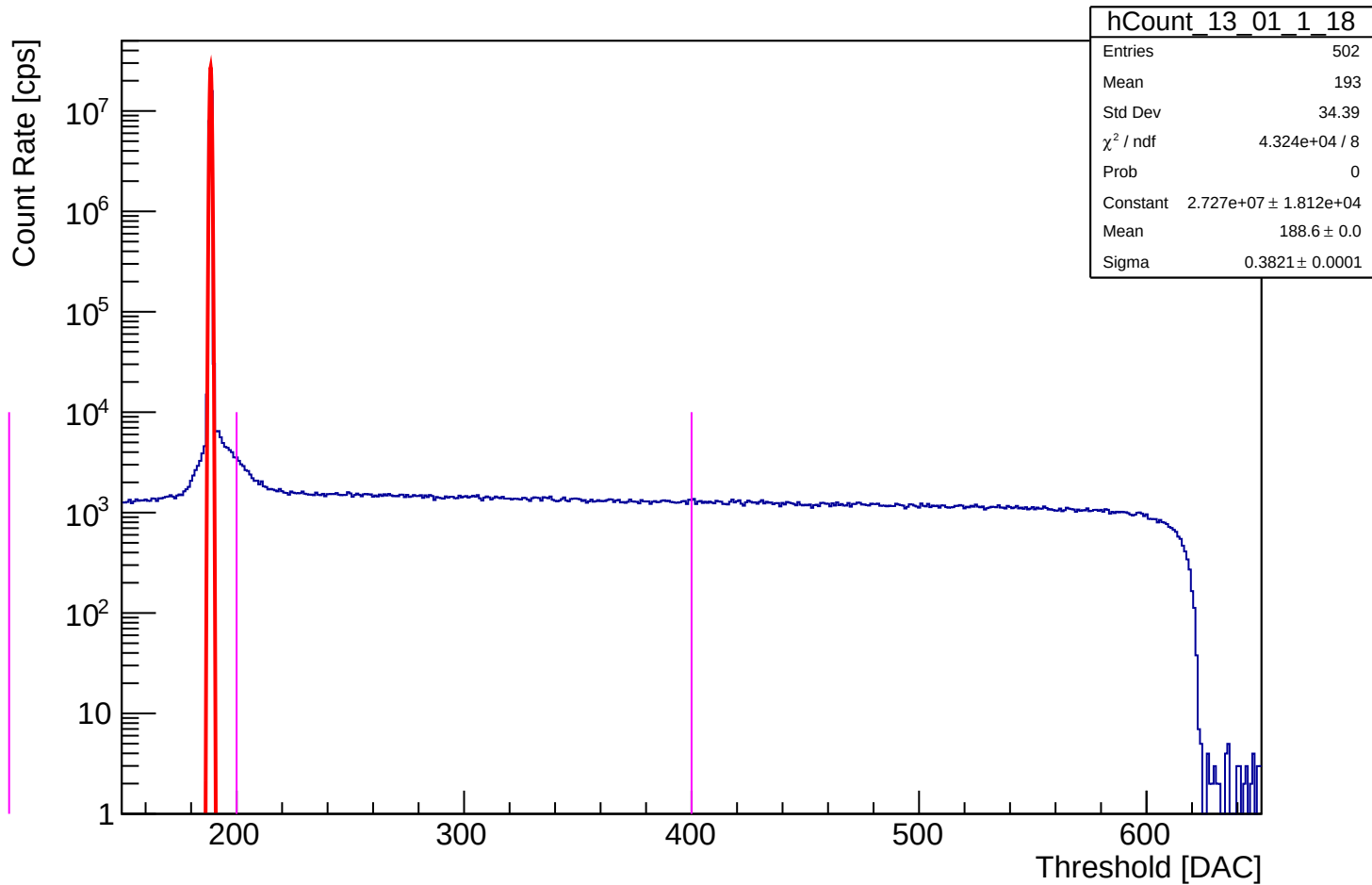
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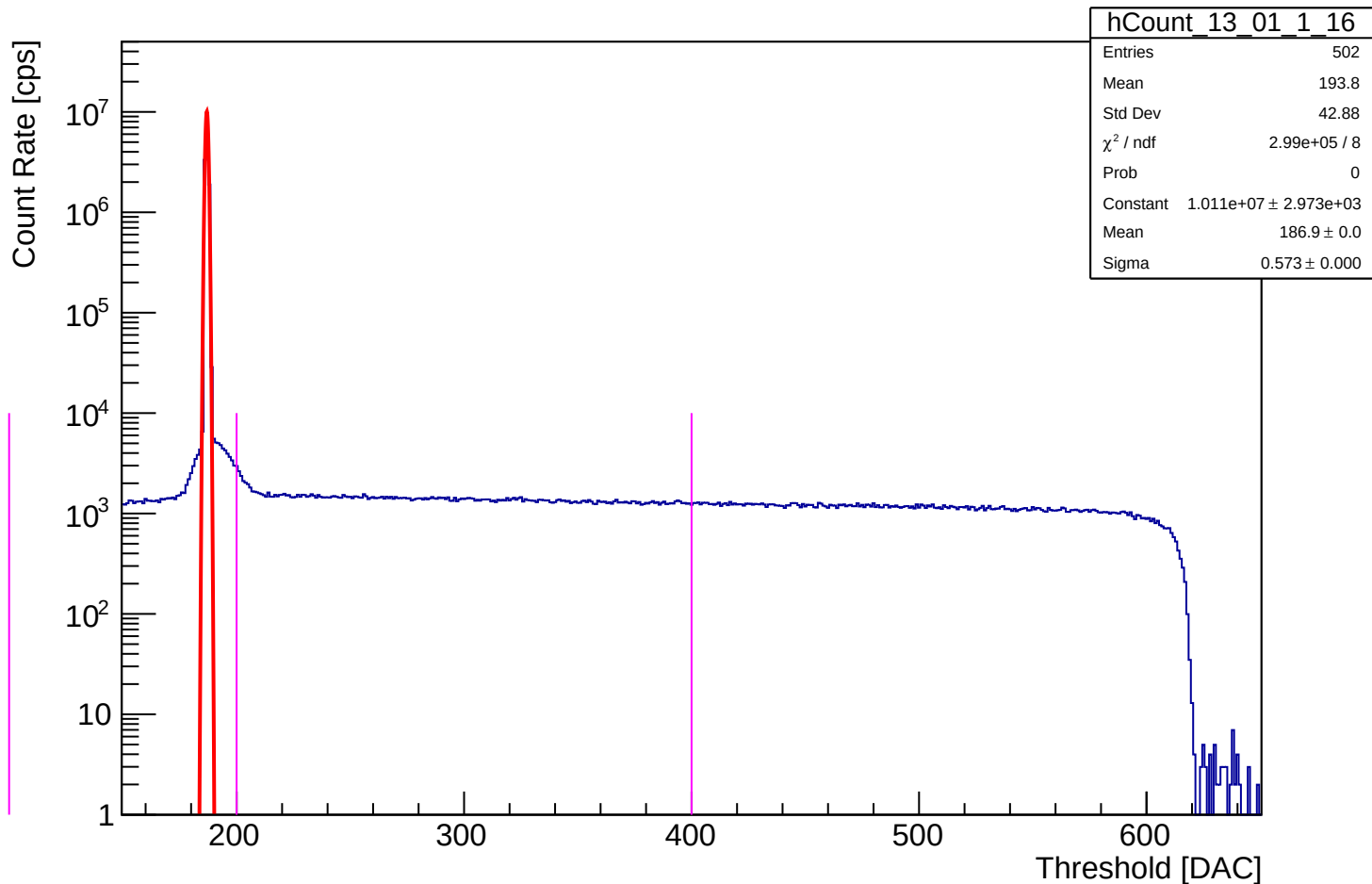
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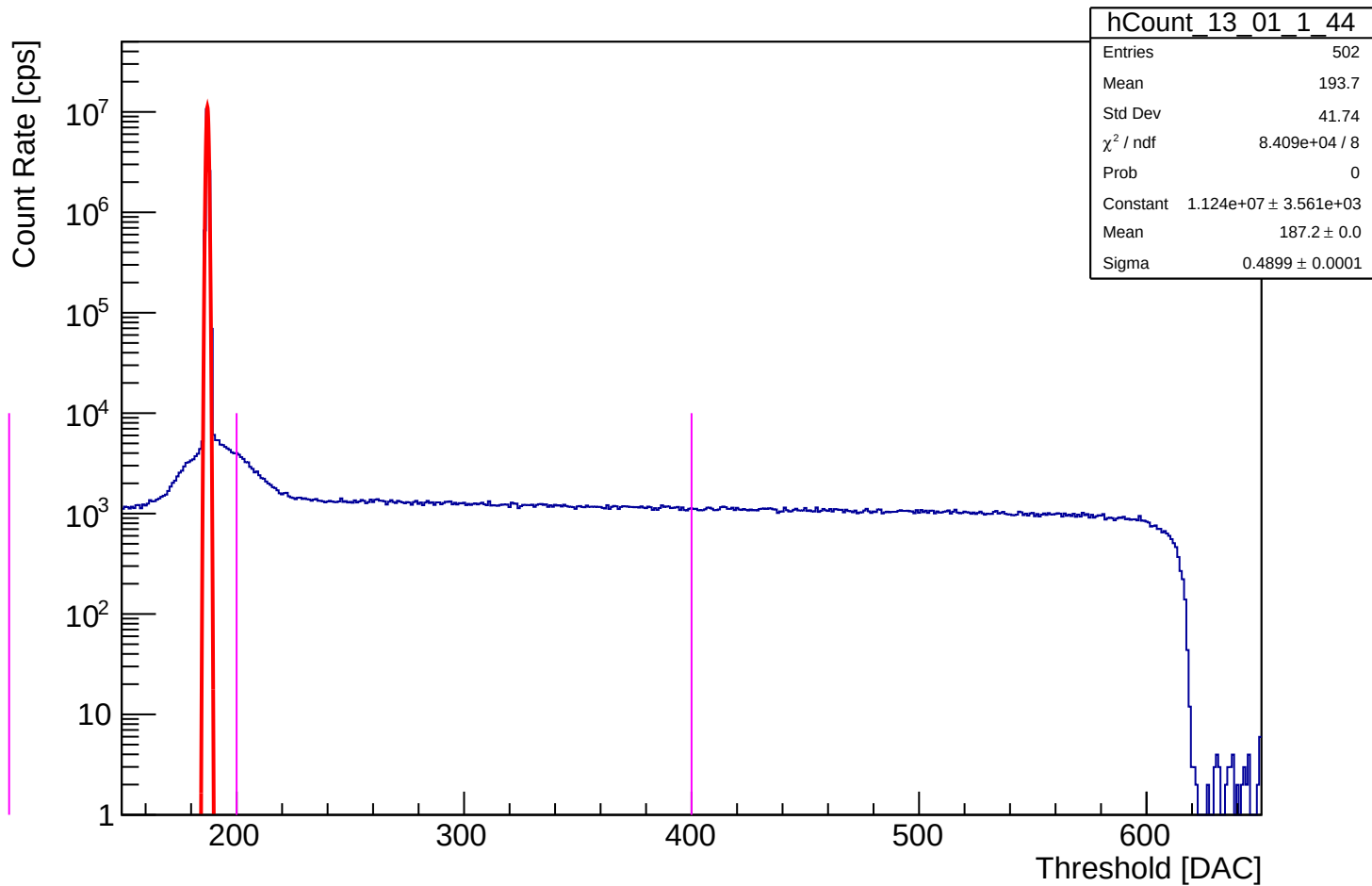
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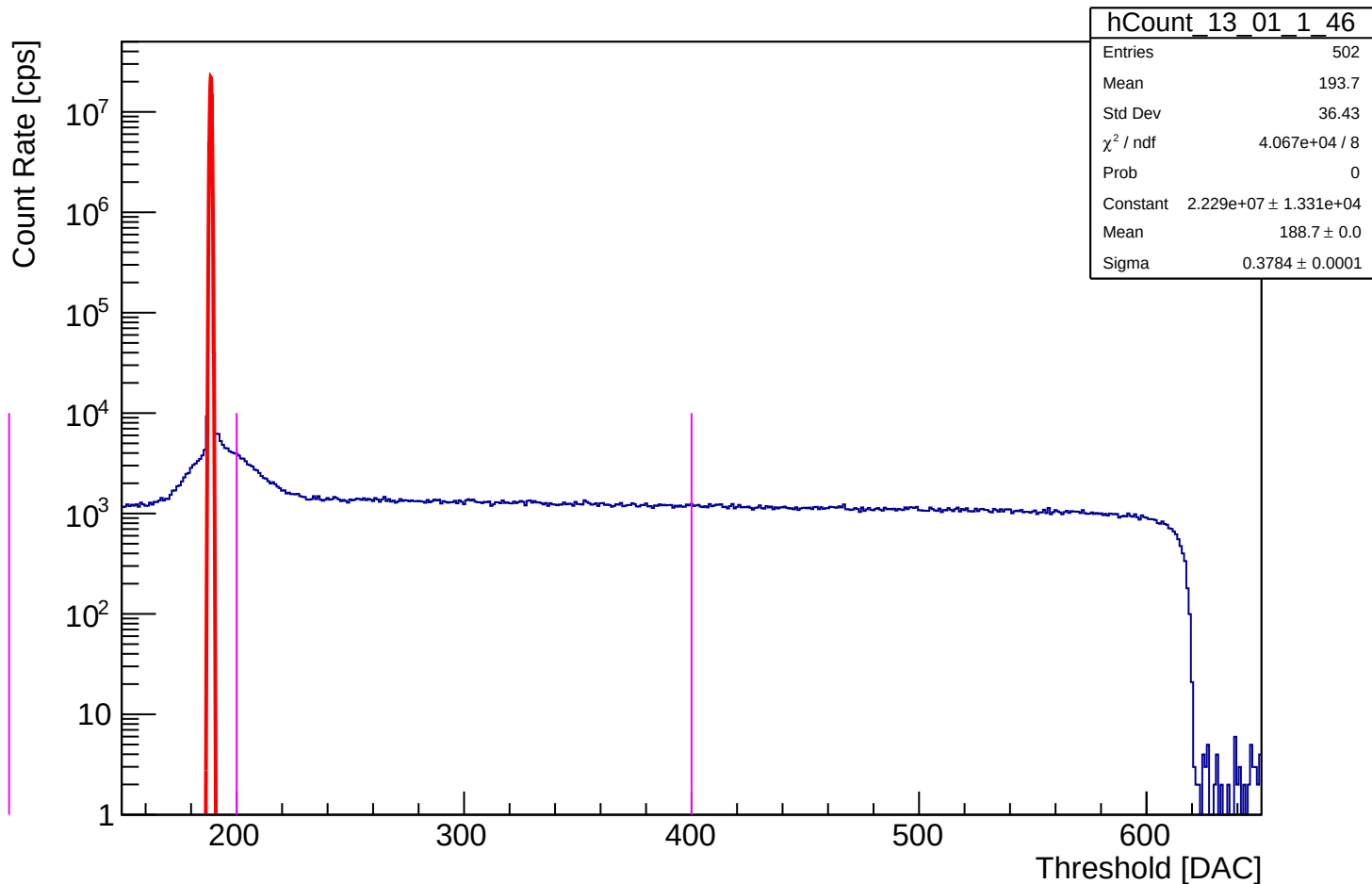
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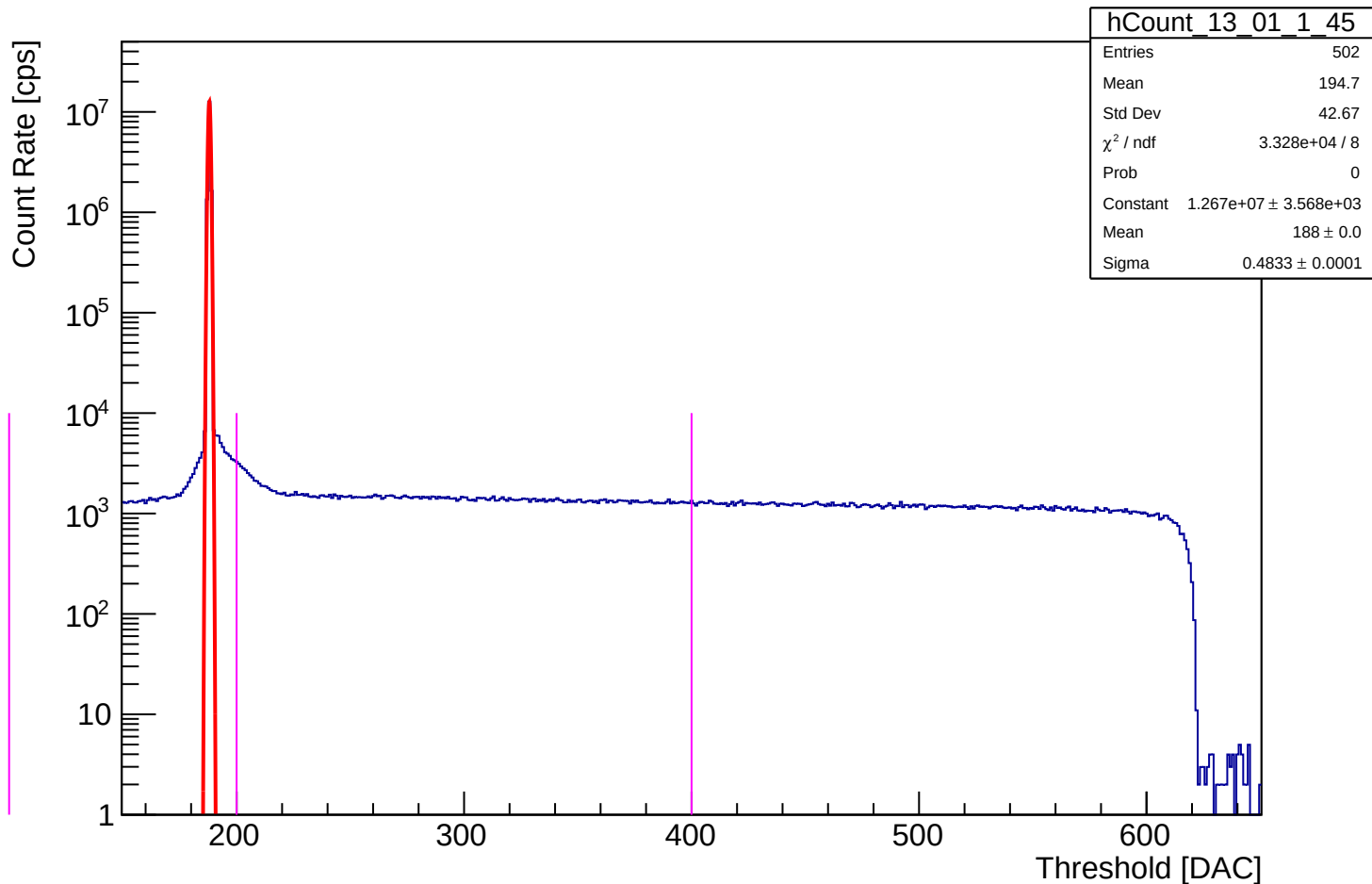
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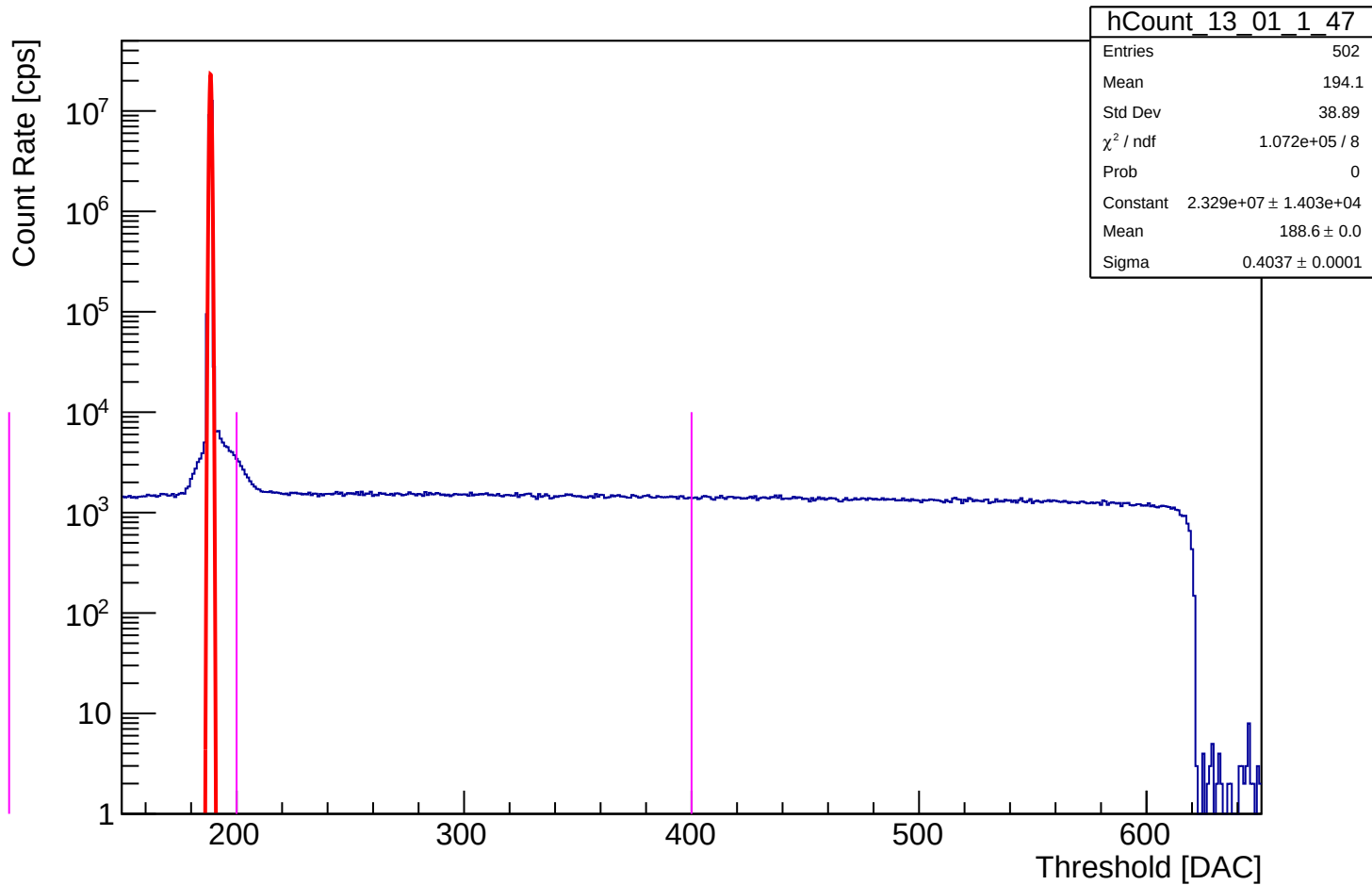
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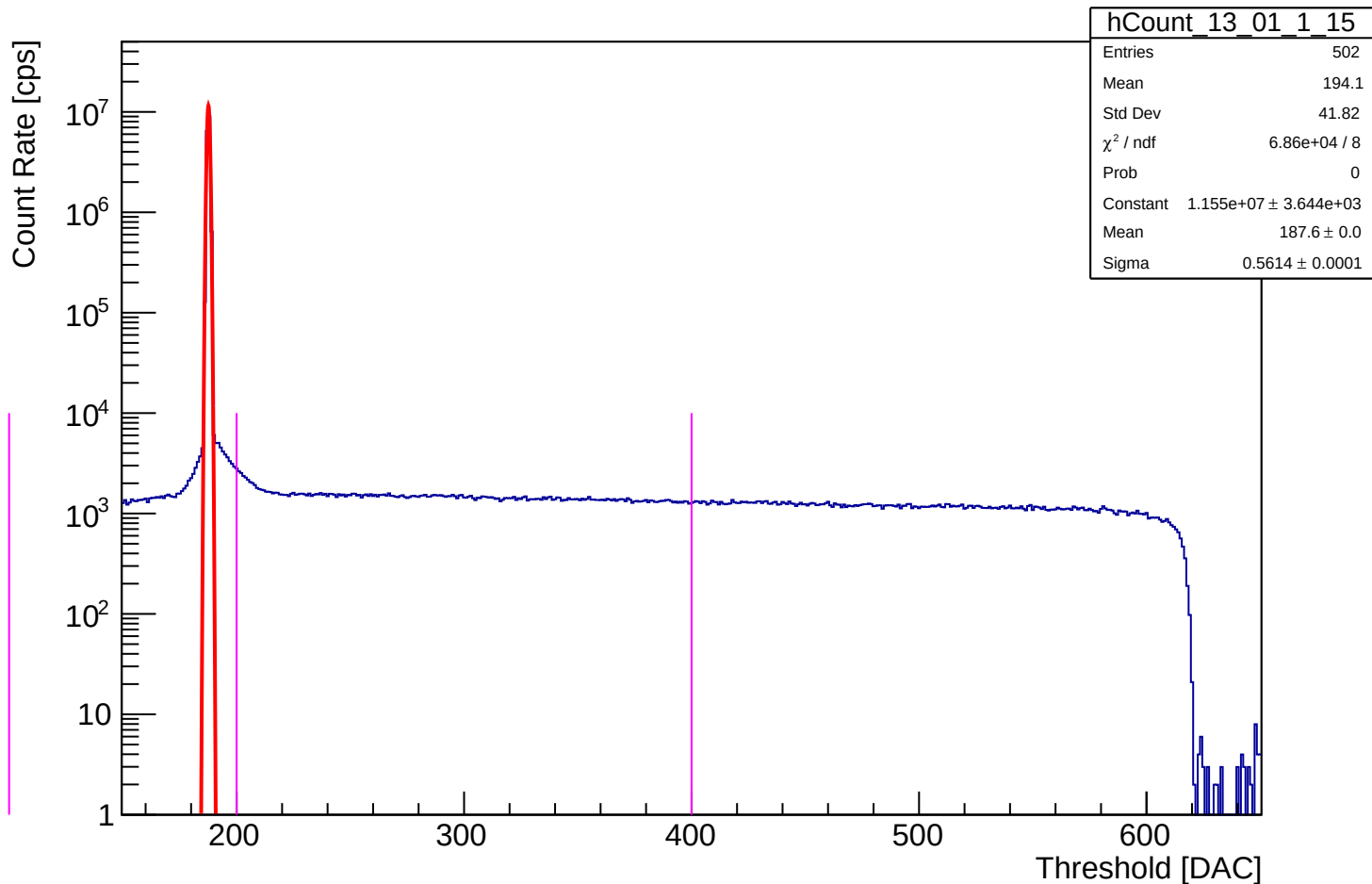
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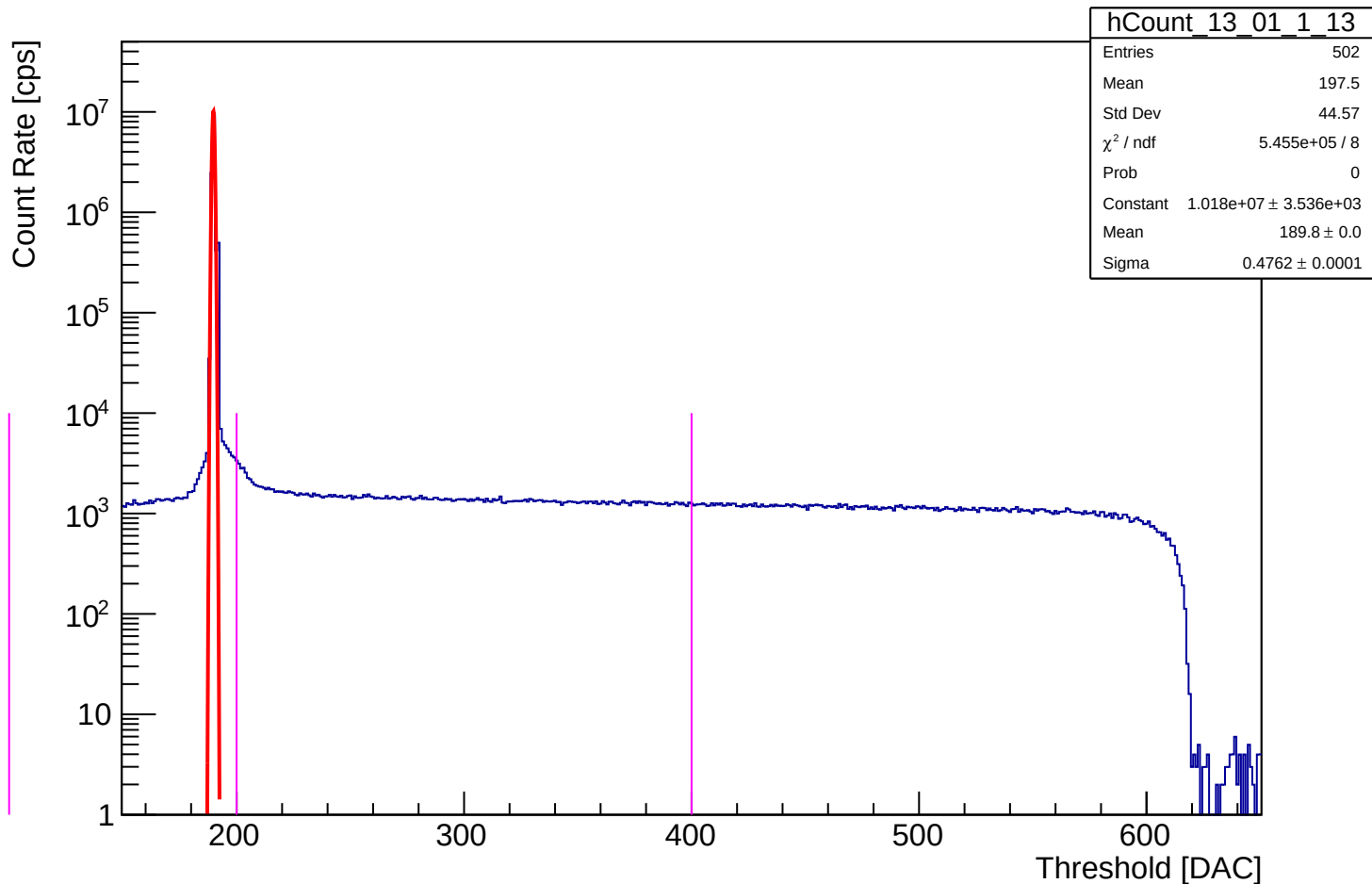
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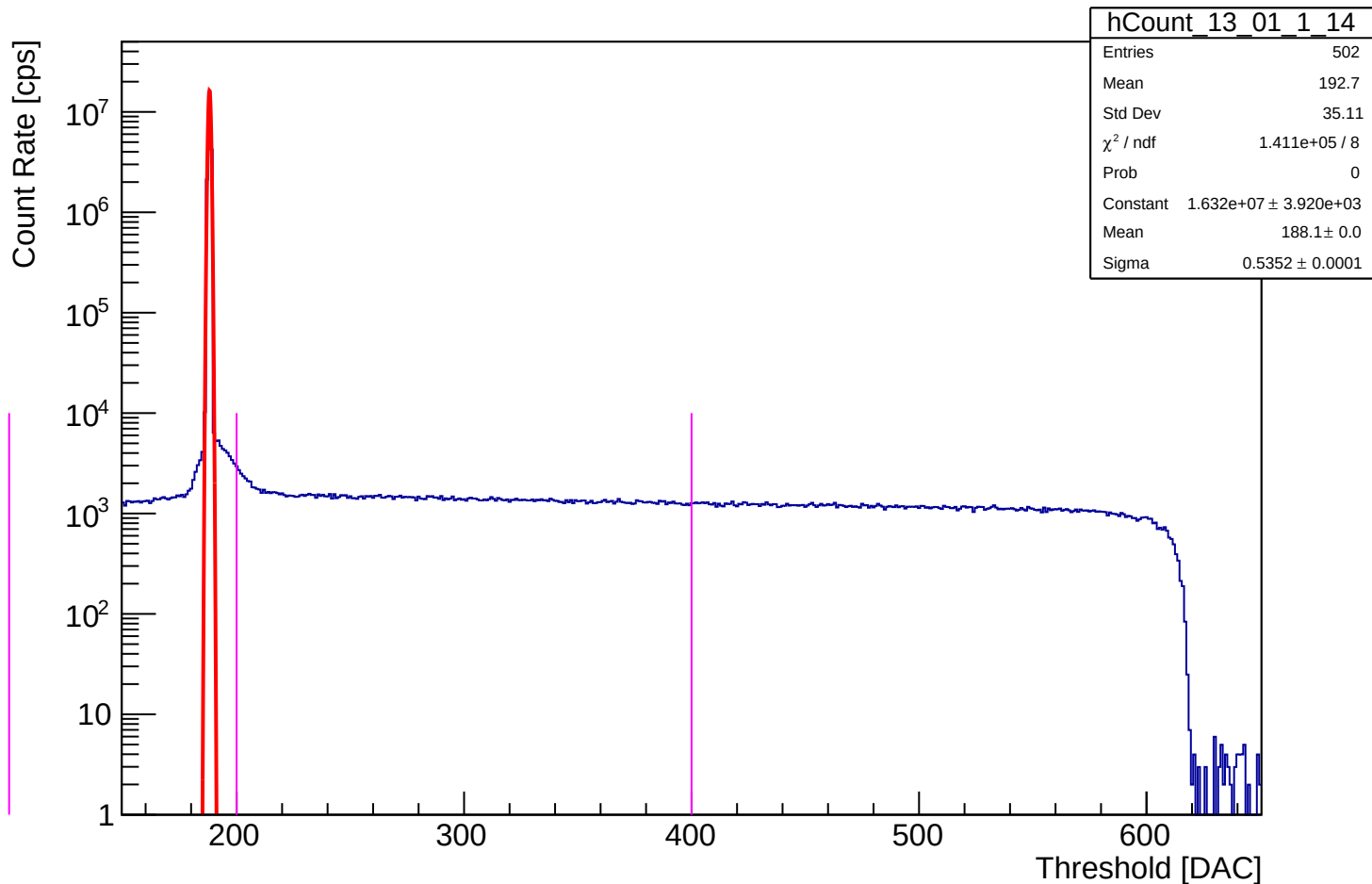
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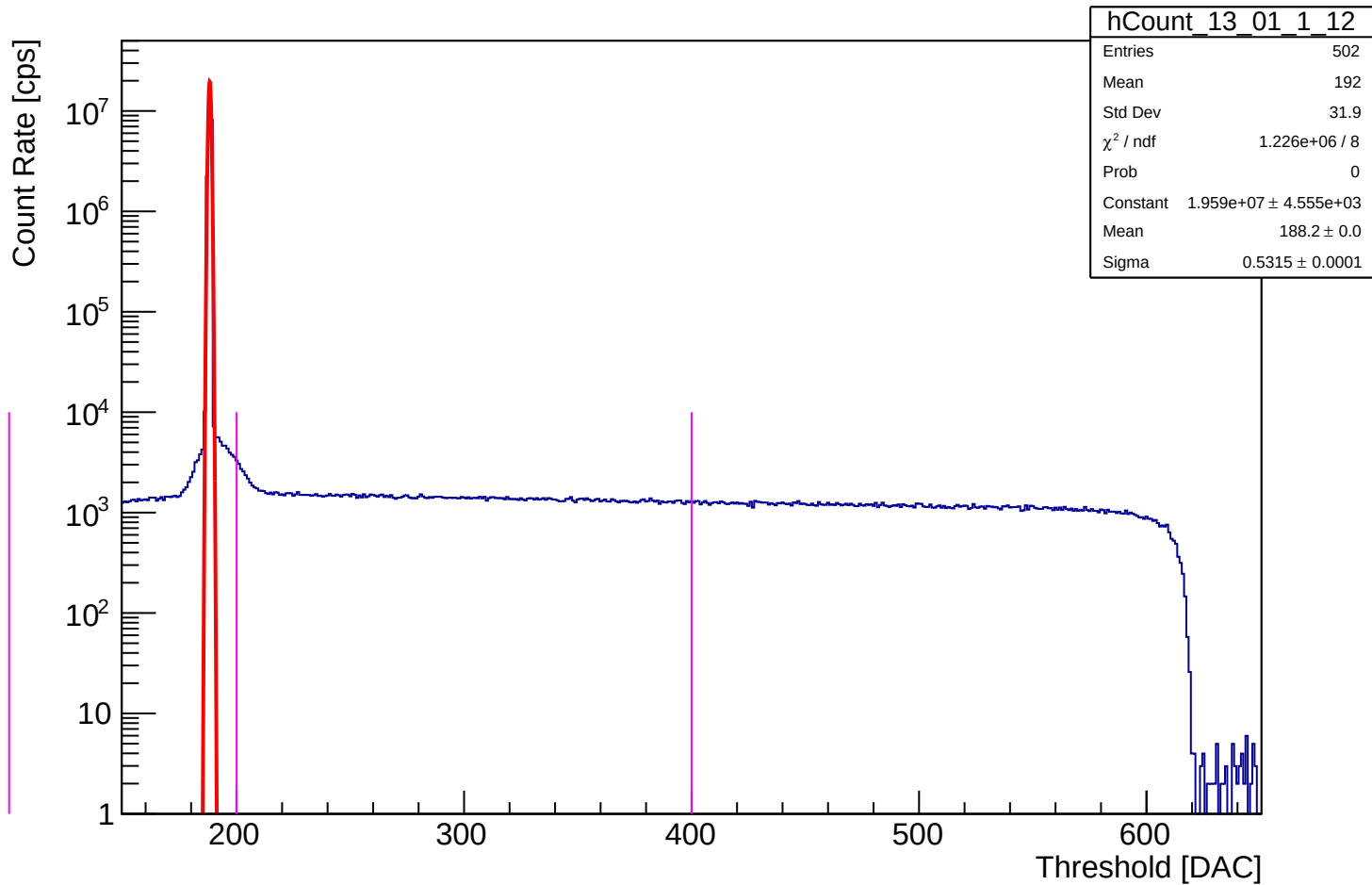
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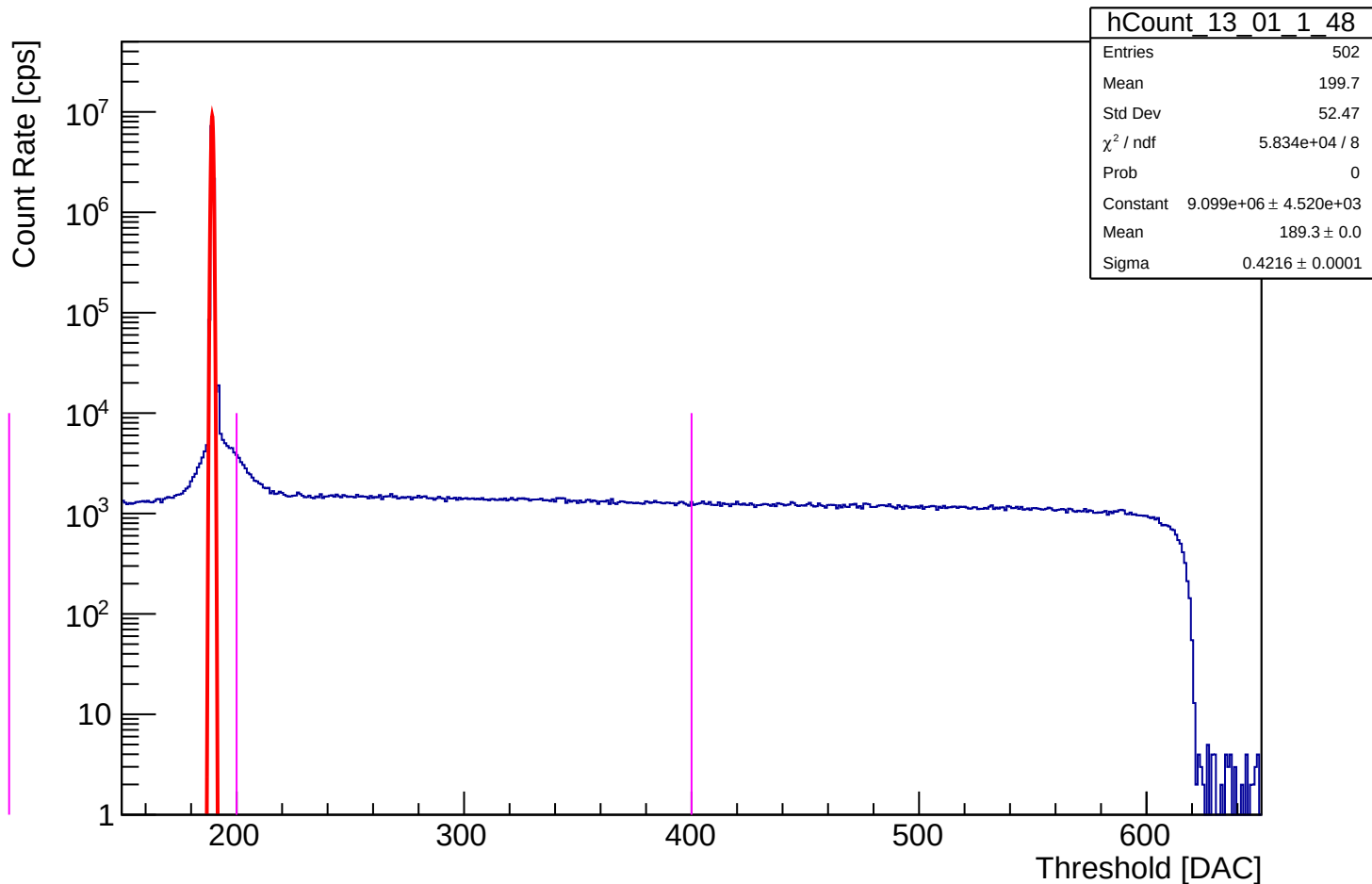
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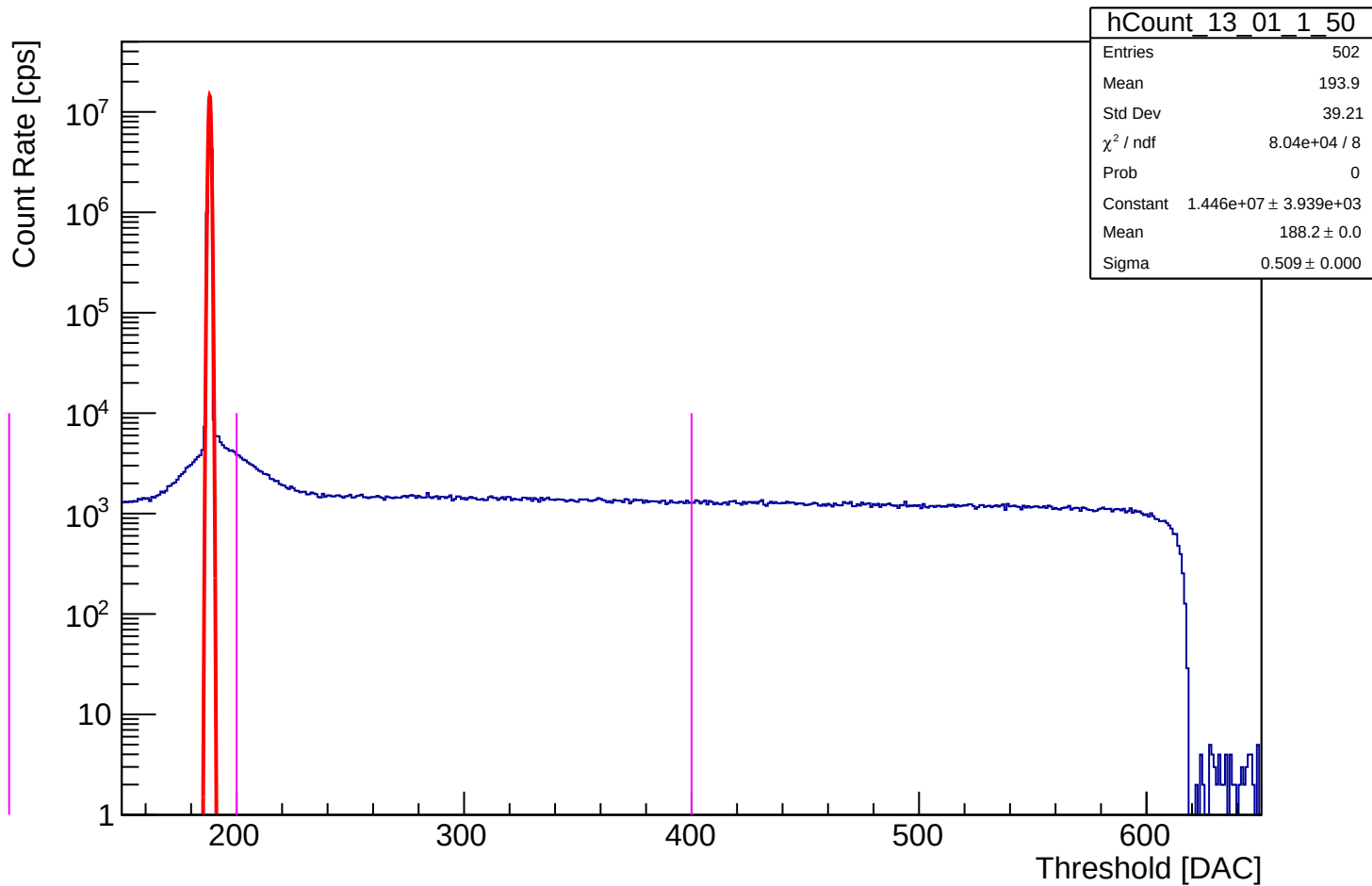
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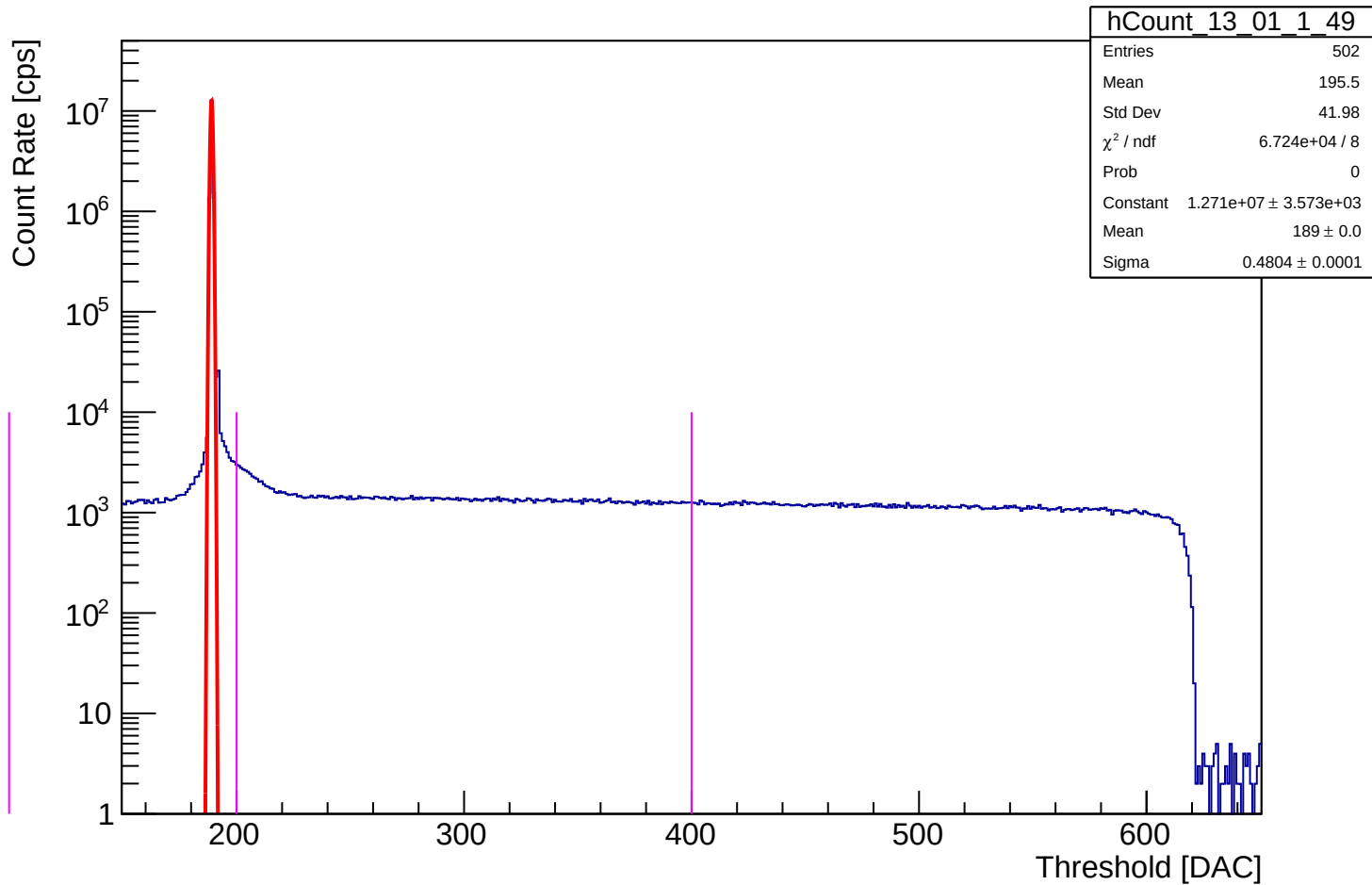
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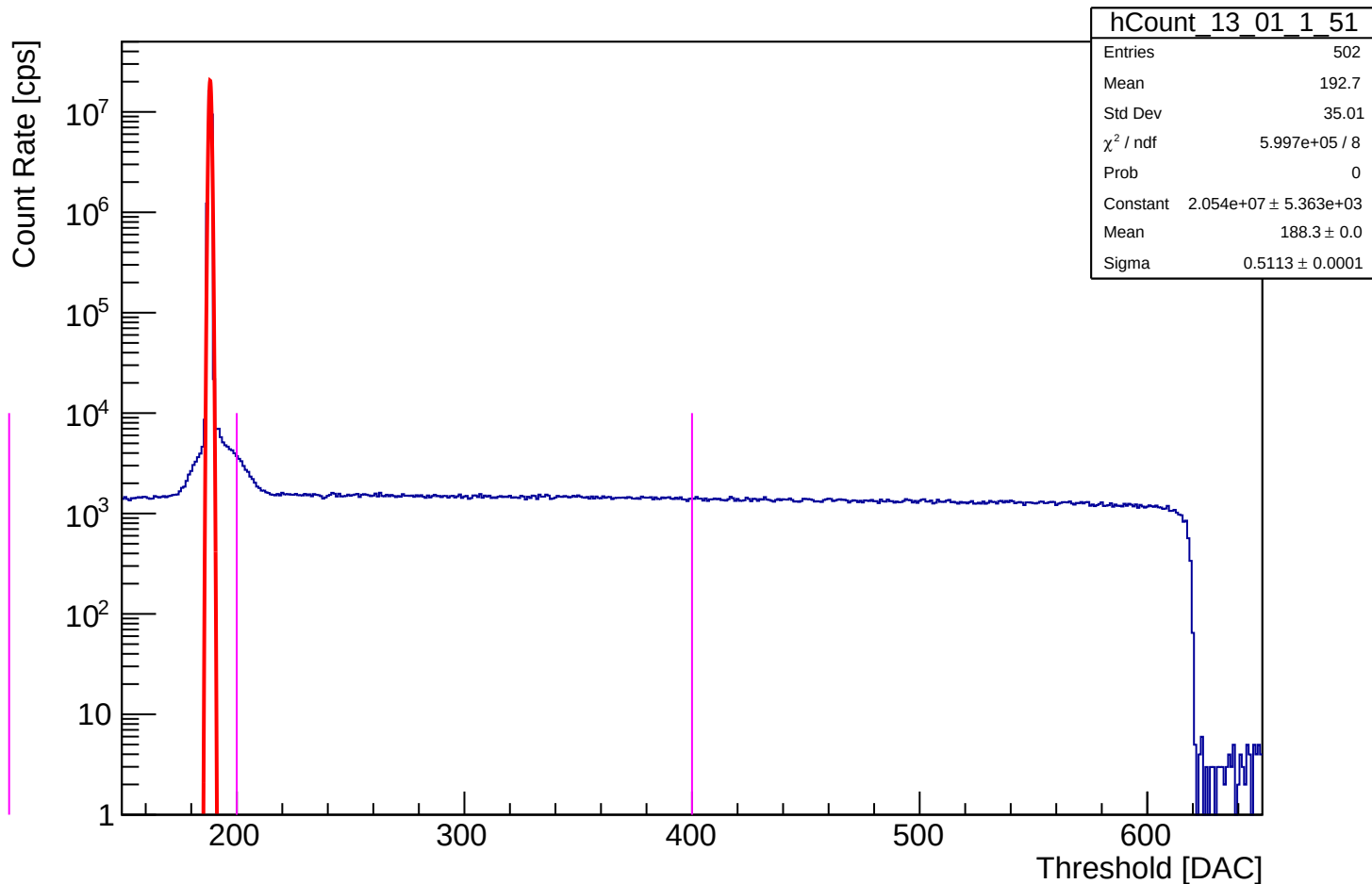


Row 1 Tile 1 Pmt 5 Pixel 38 Slot 13 Fiber 1 Asic 1 Channel 50

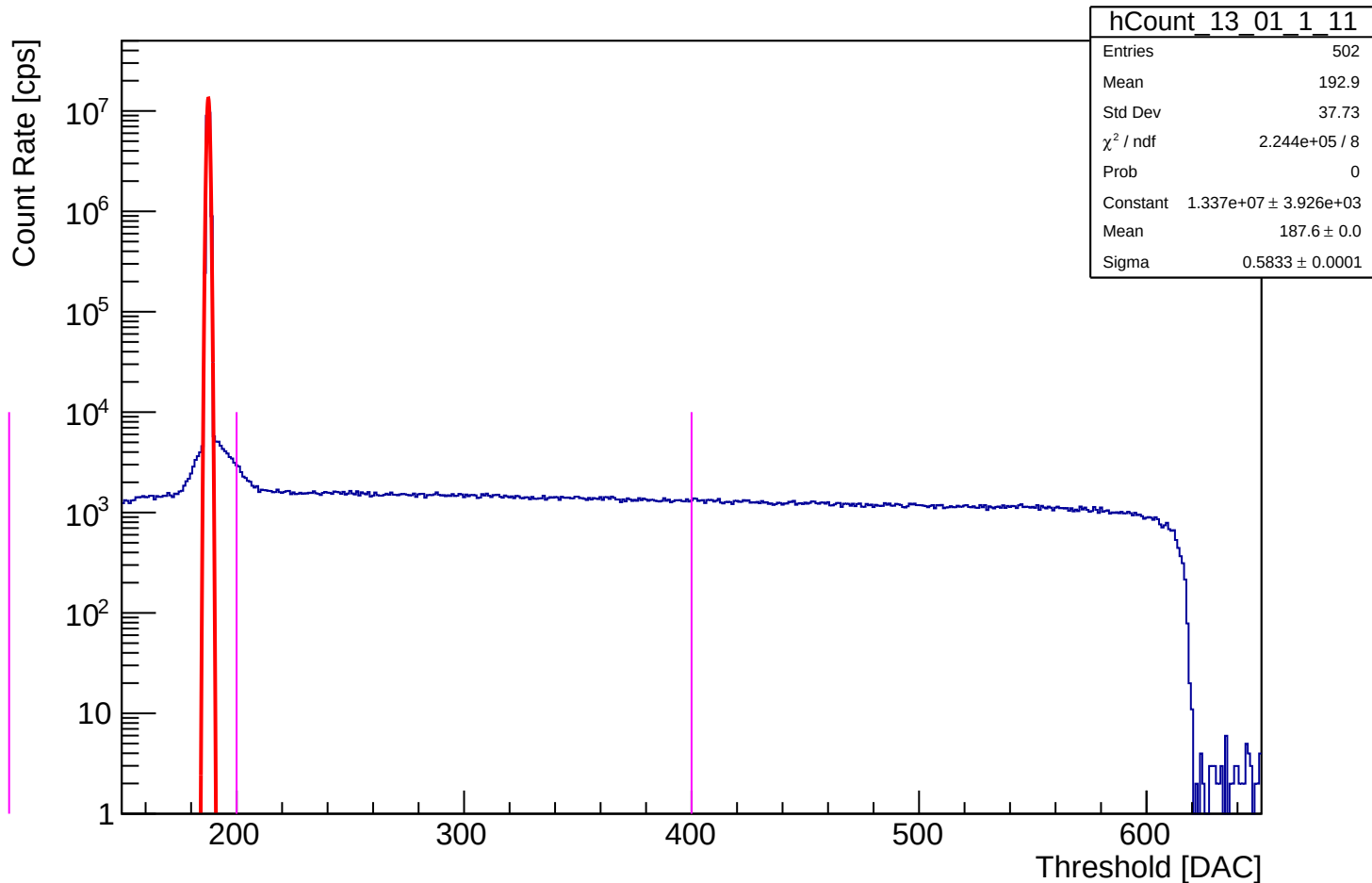


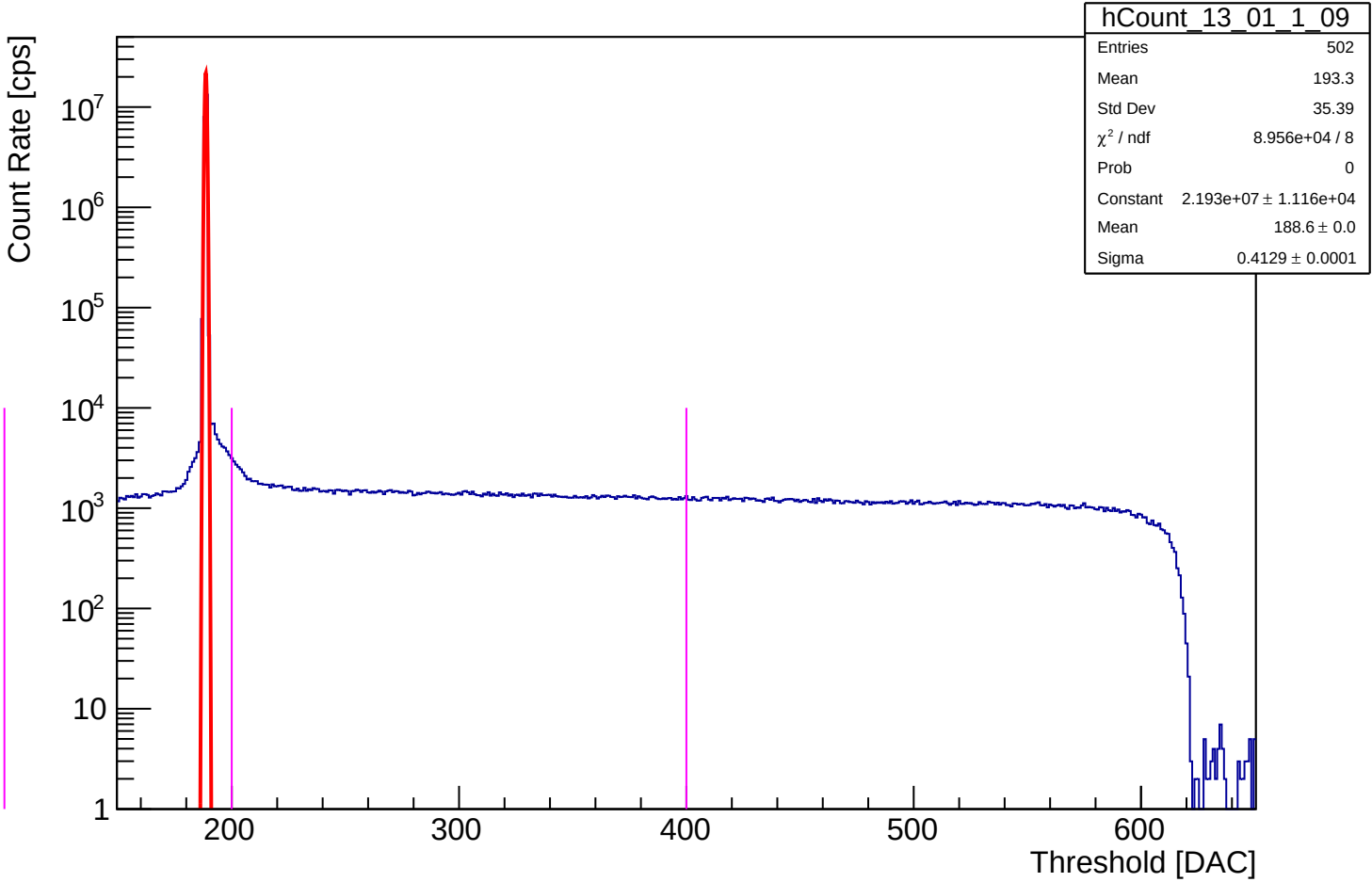
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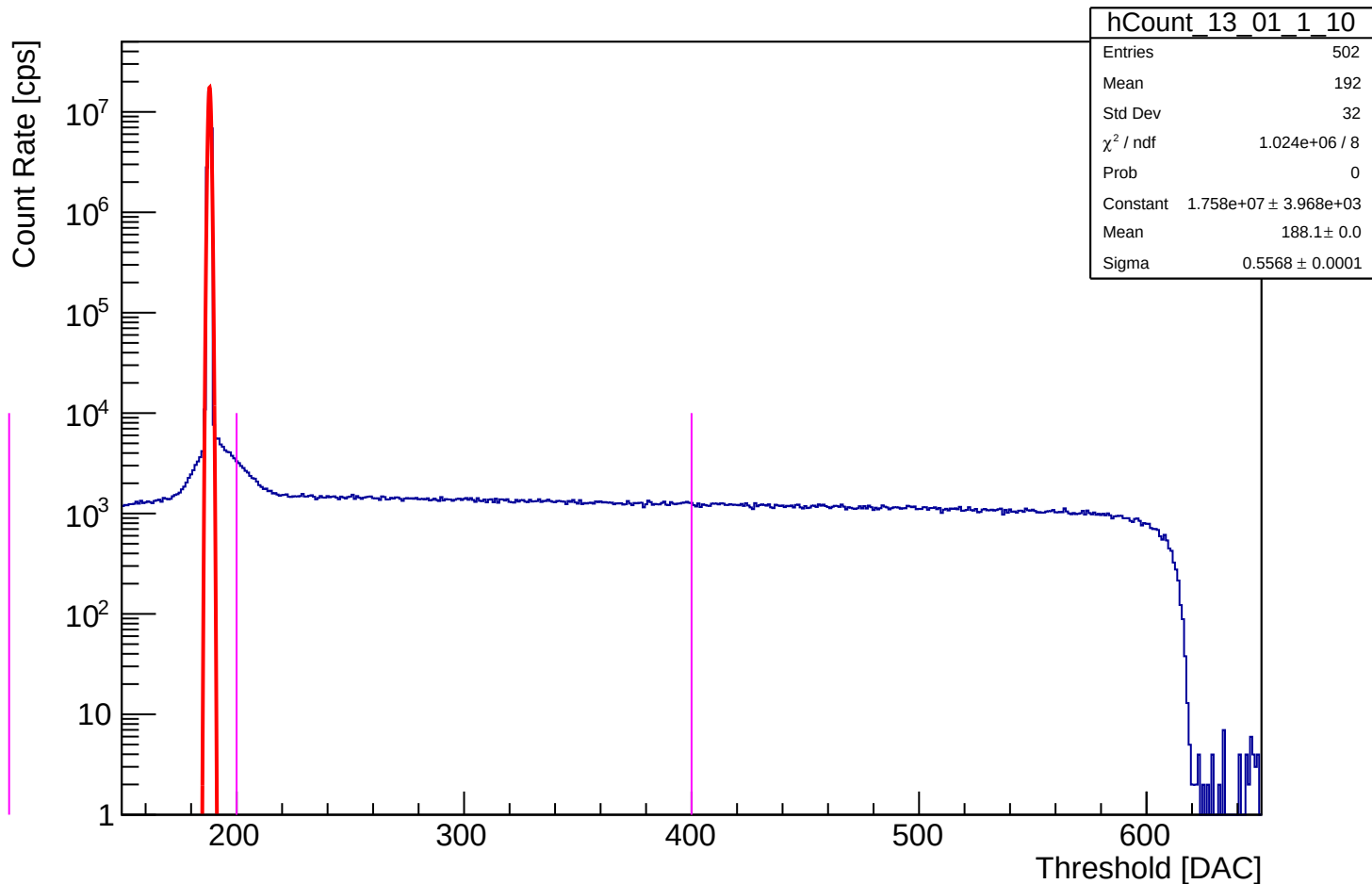


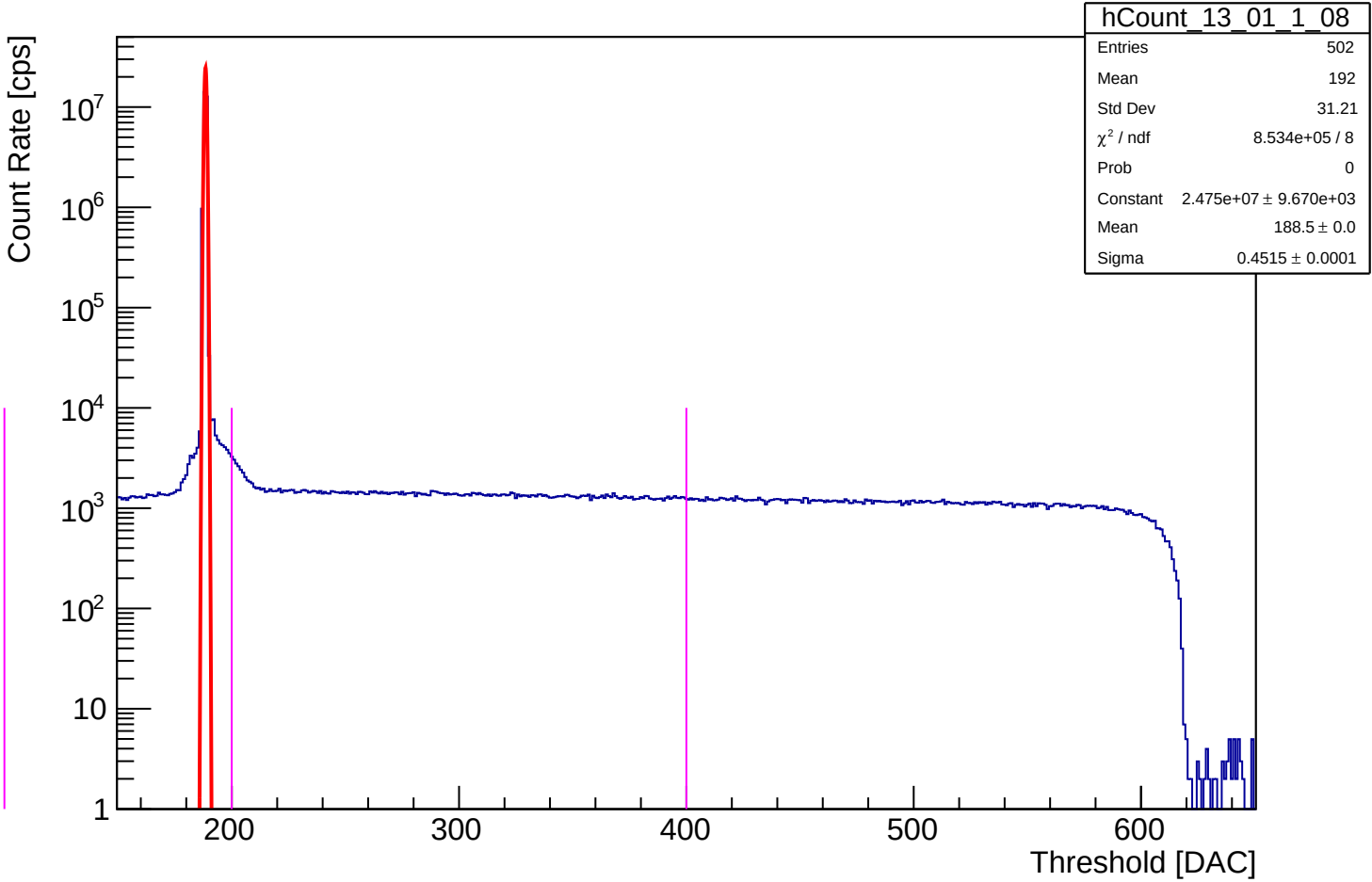
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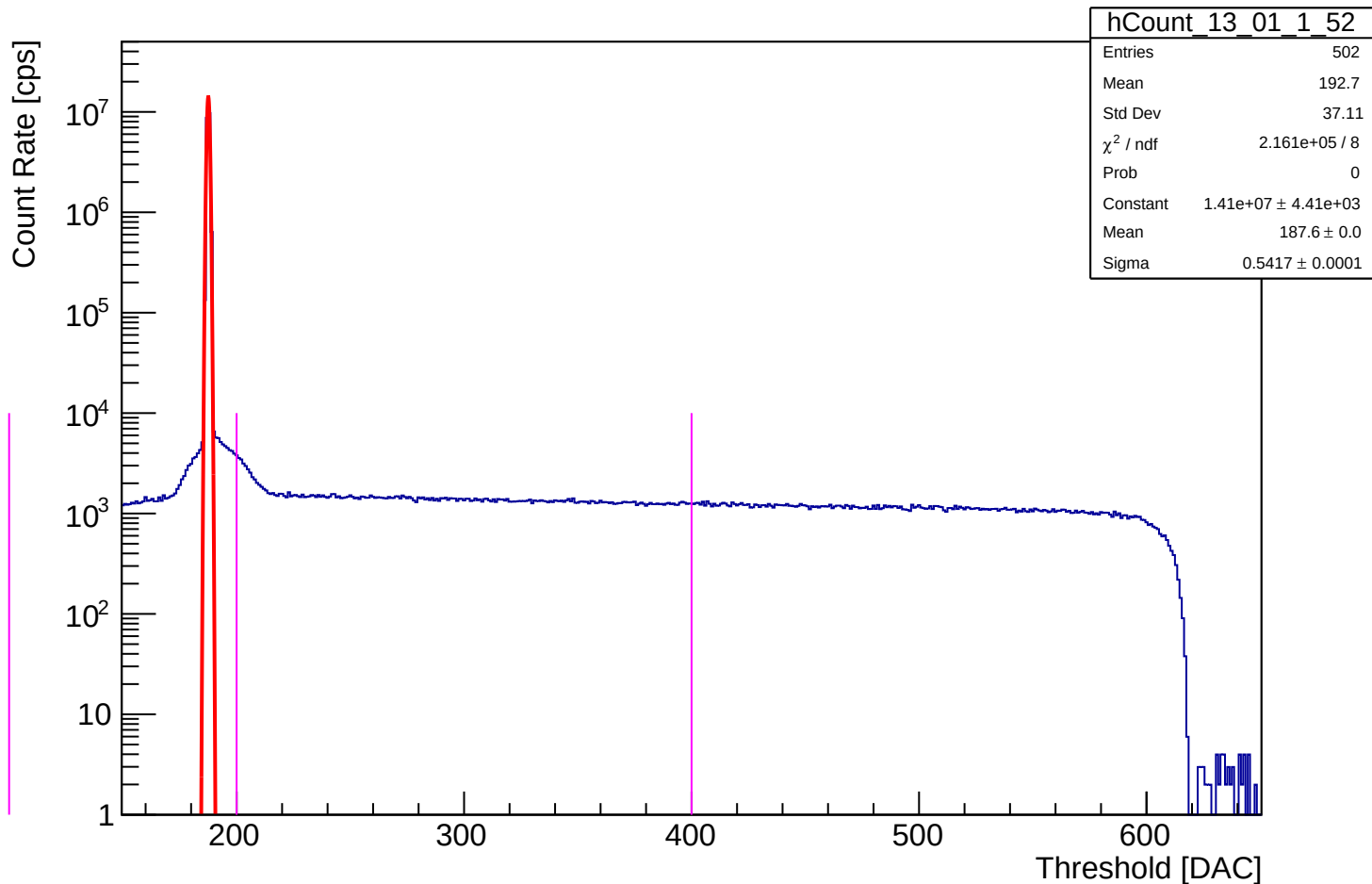


Row 1 Tile 1 Pmt 5 Pixel 43 Slot 13 Fiber 1 Asic 1 Channel 10

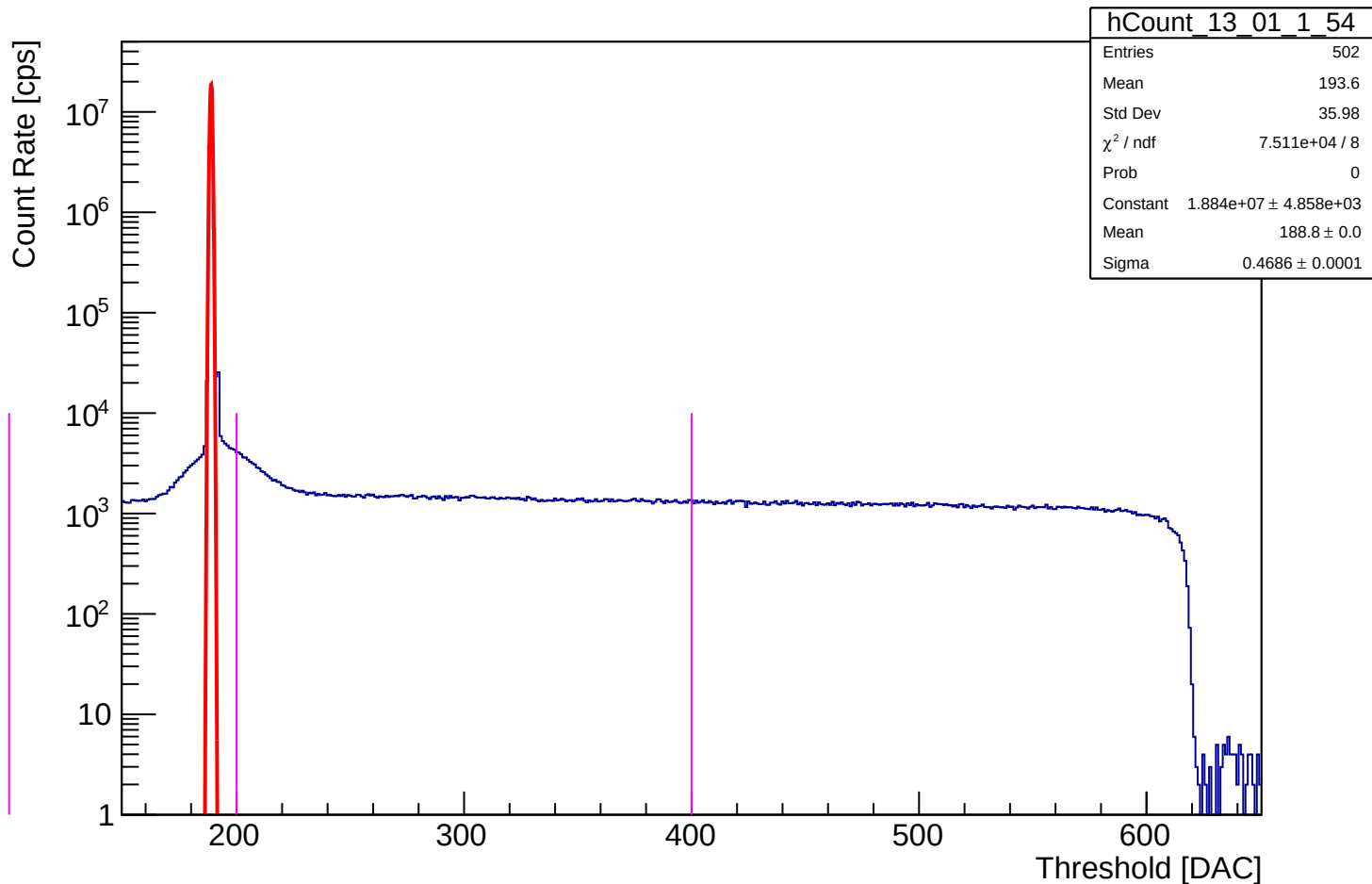




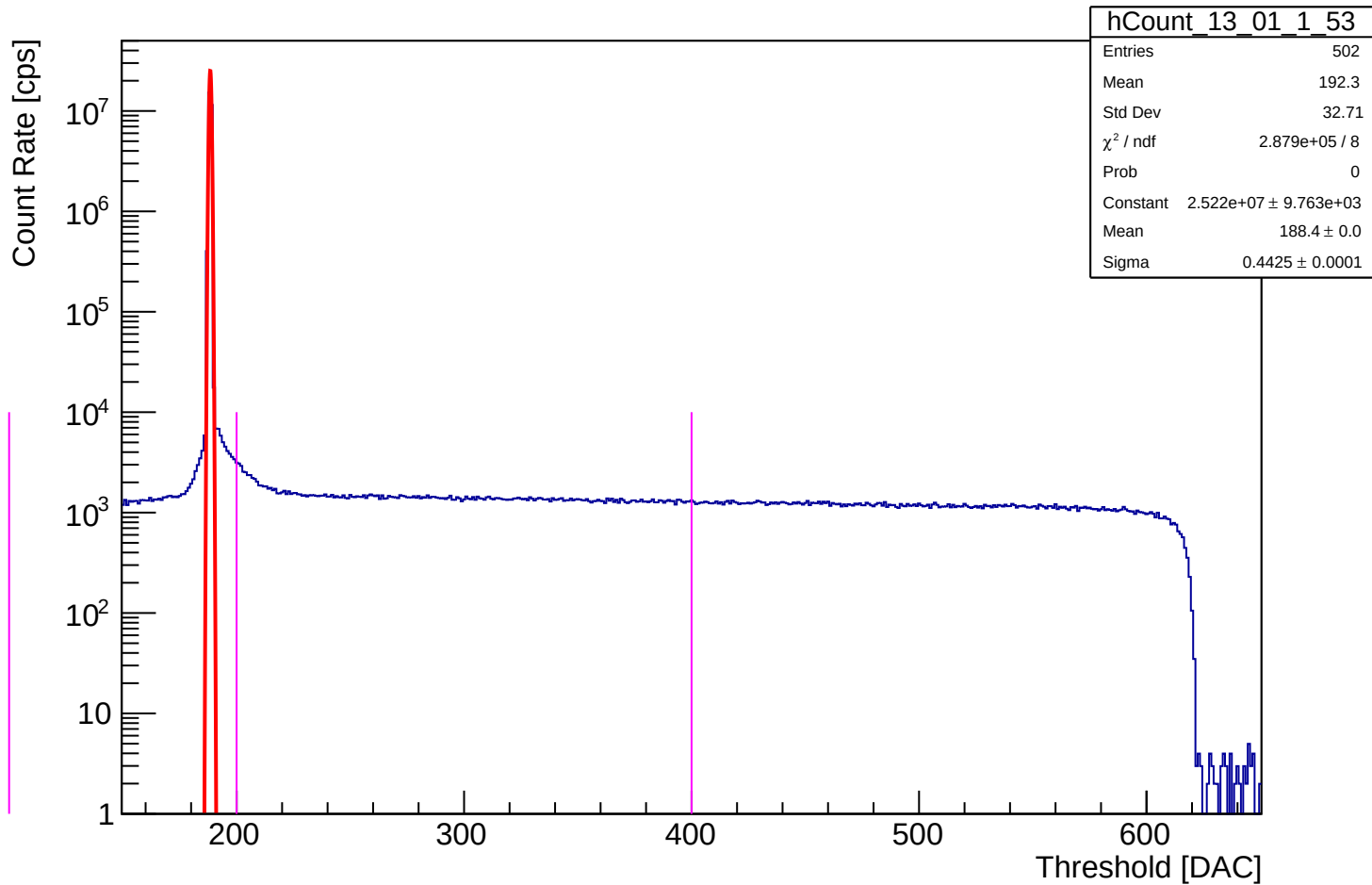
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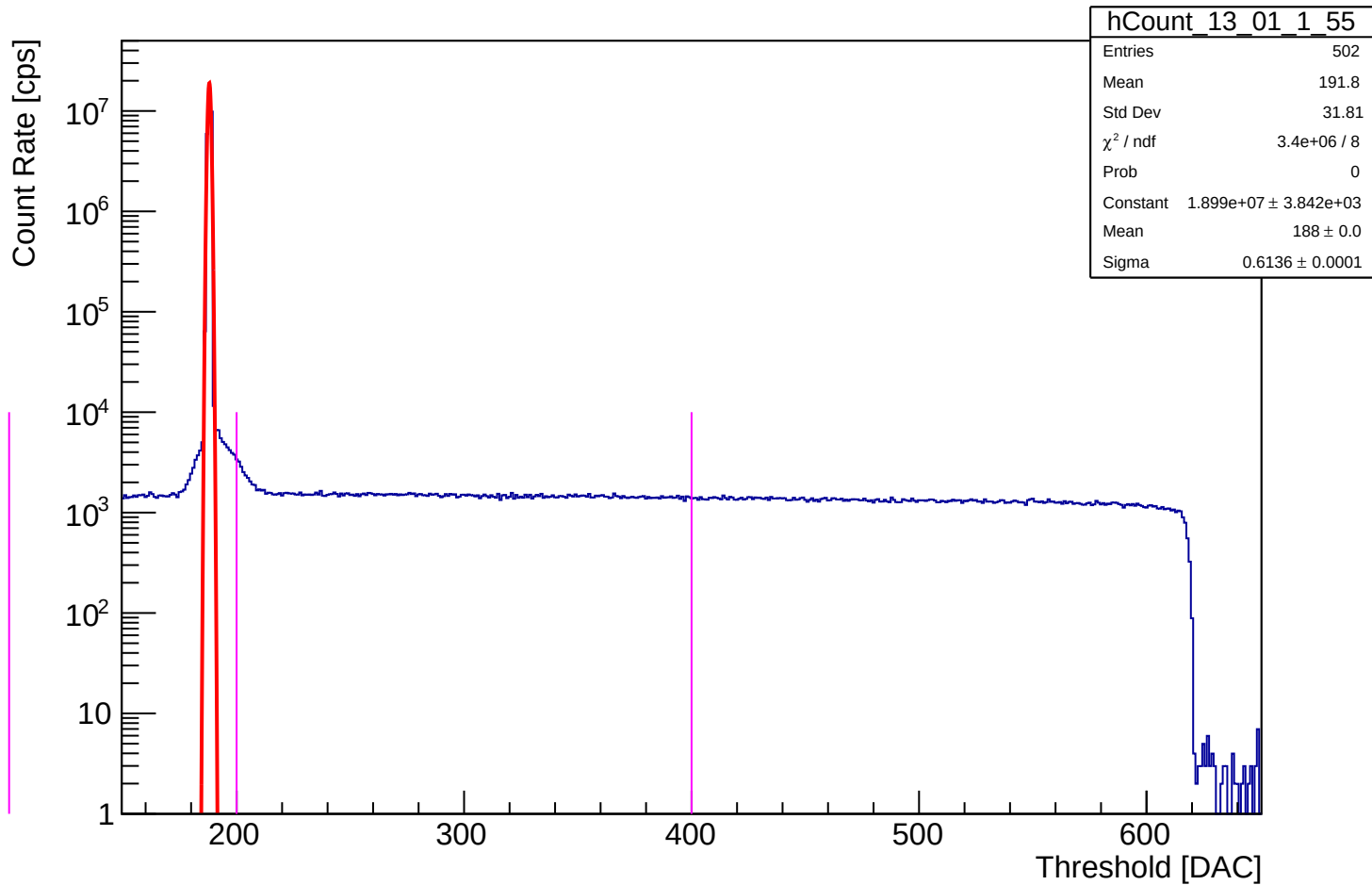
Row 1 Tile 1 Pmt 5 Pixel 46 Slot 13 Fiber 1 Asic 1 Channel 54

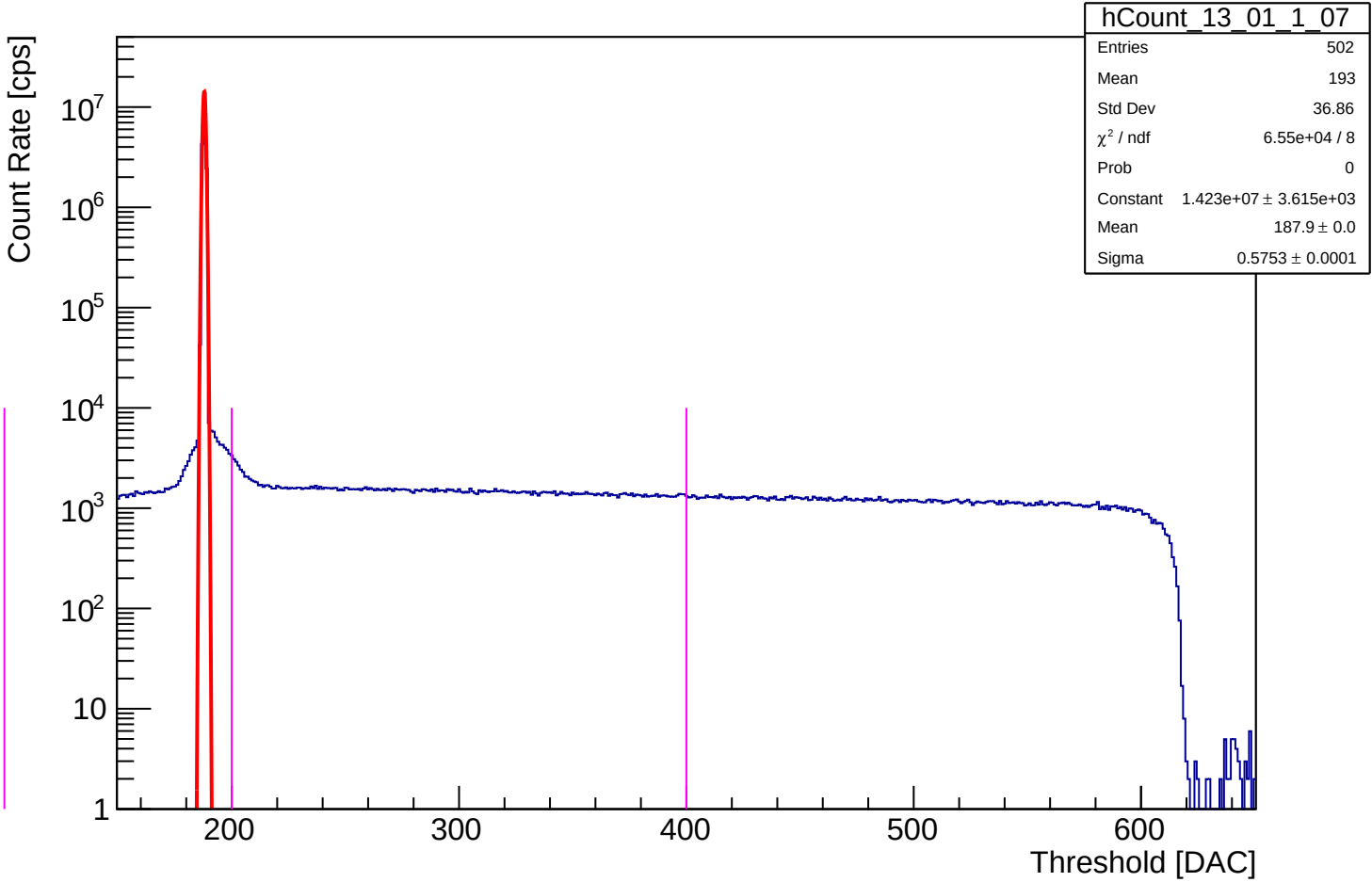


Row 1 Tile 1 Pmt 5 Pixel 47 Slot 13 Fiber 1 Asic 1 Channel 53

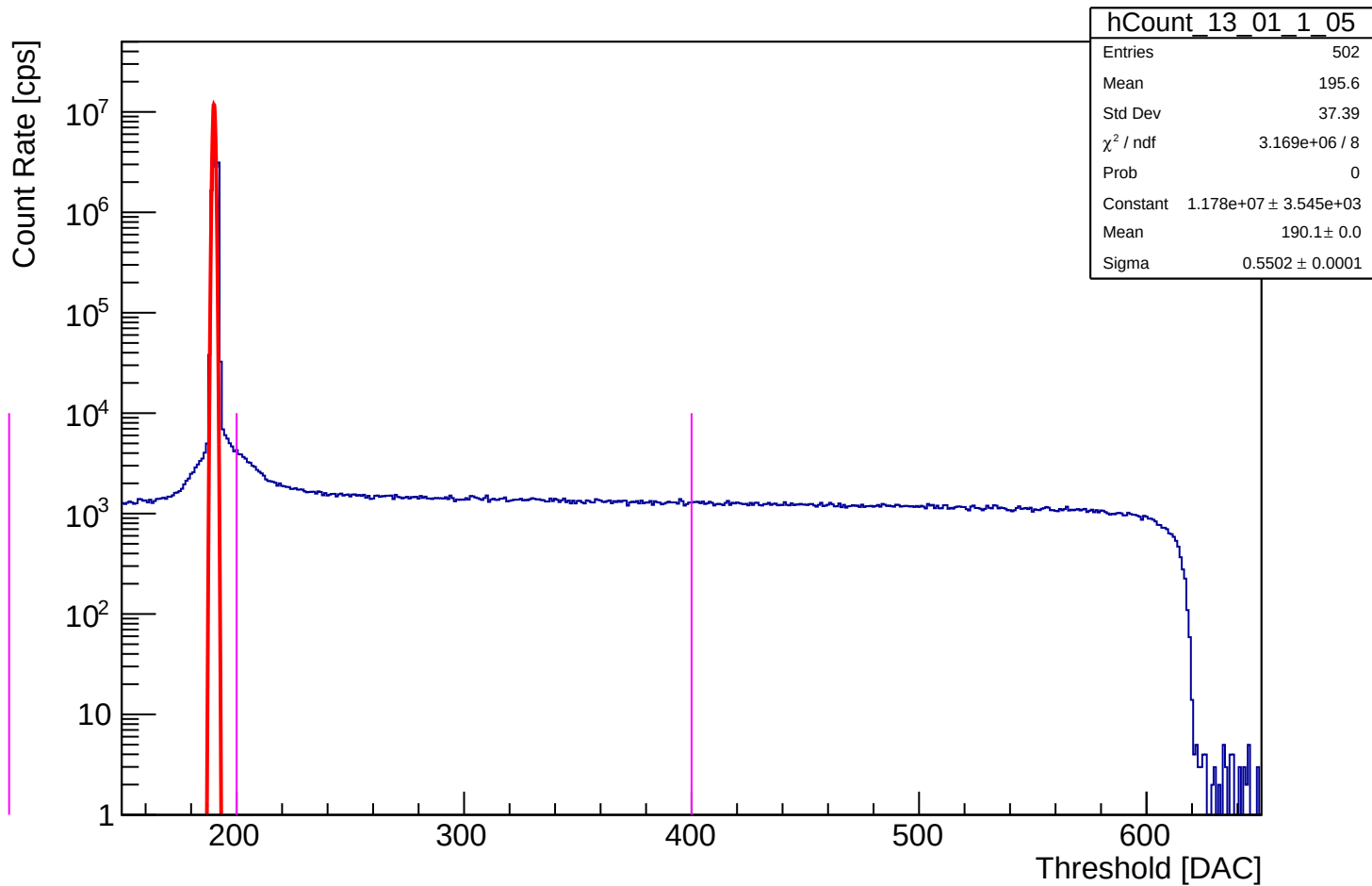


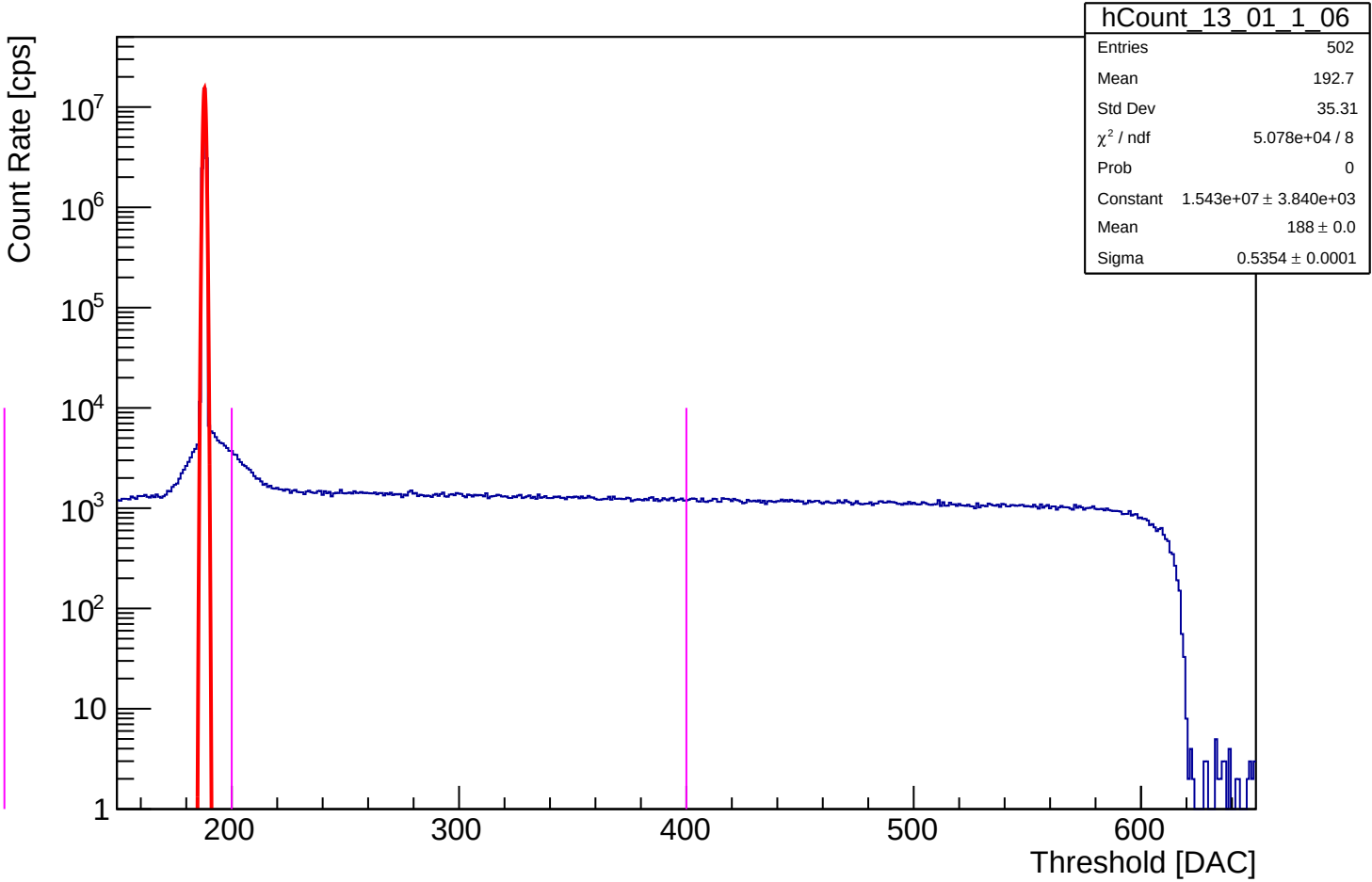
Row 1 Tile 1 Pmt 5 Pixel 48 Slot 13 Fiber 1 Asic 1 Channel 55

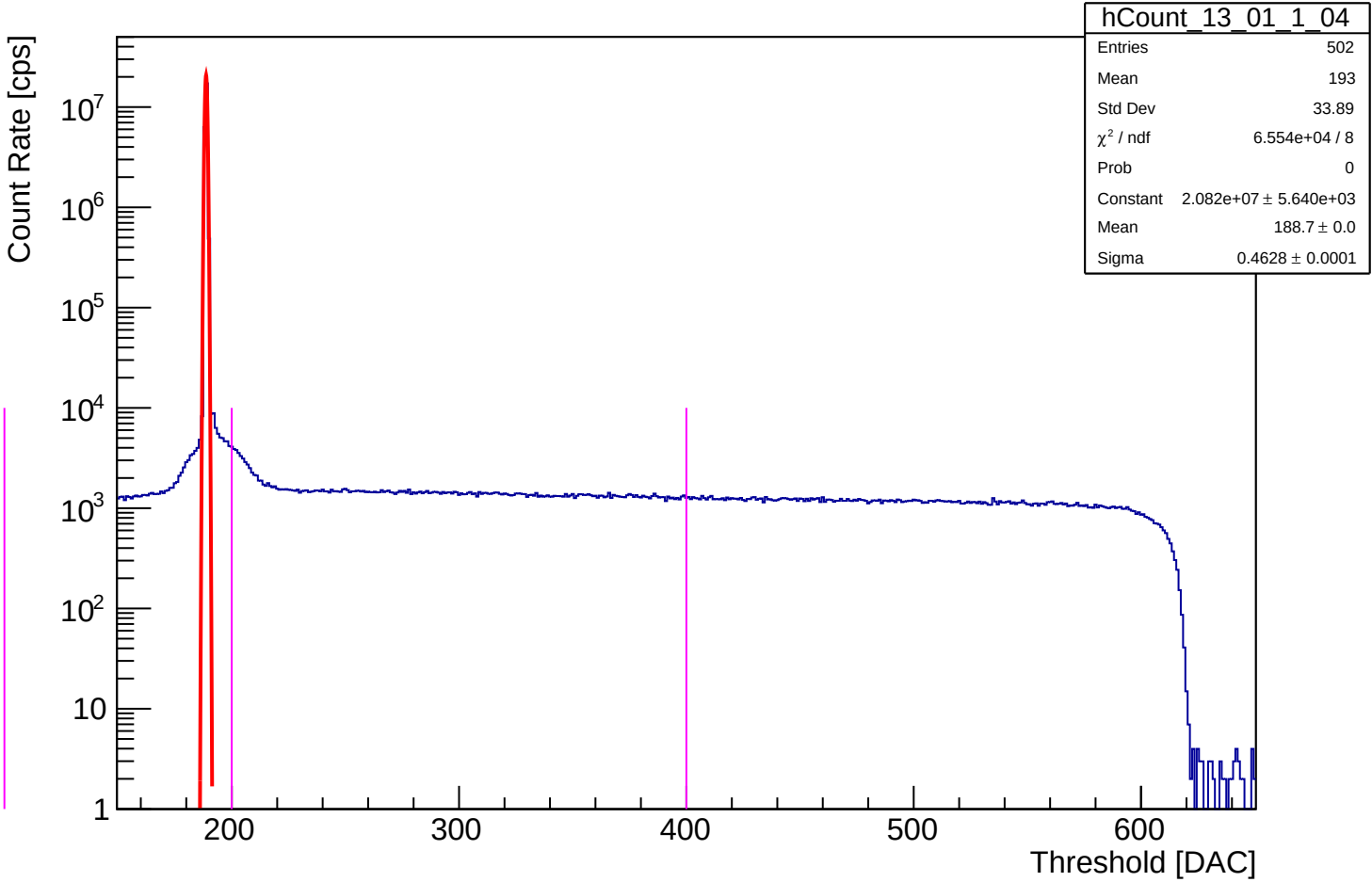




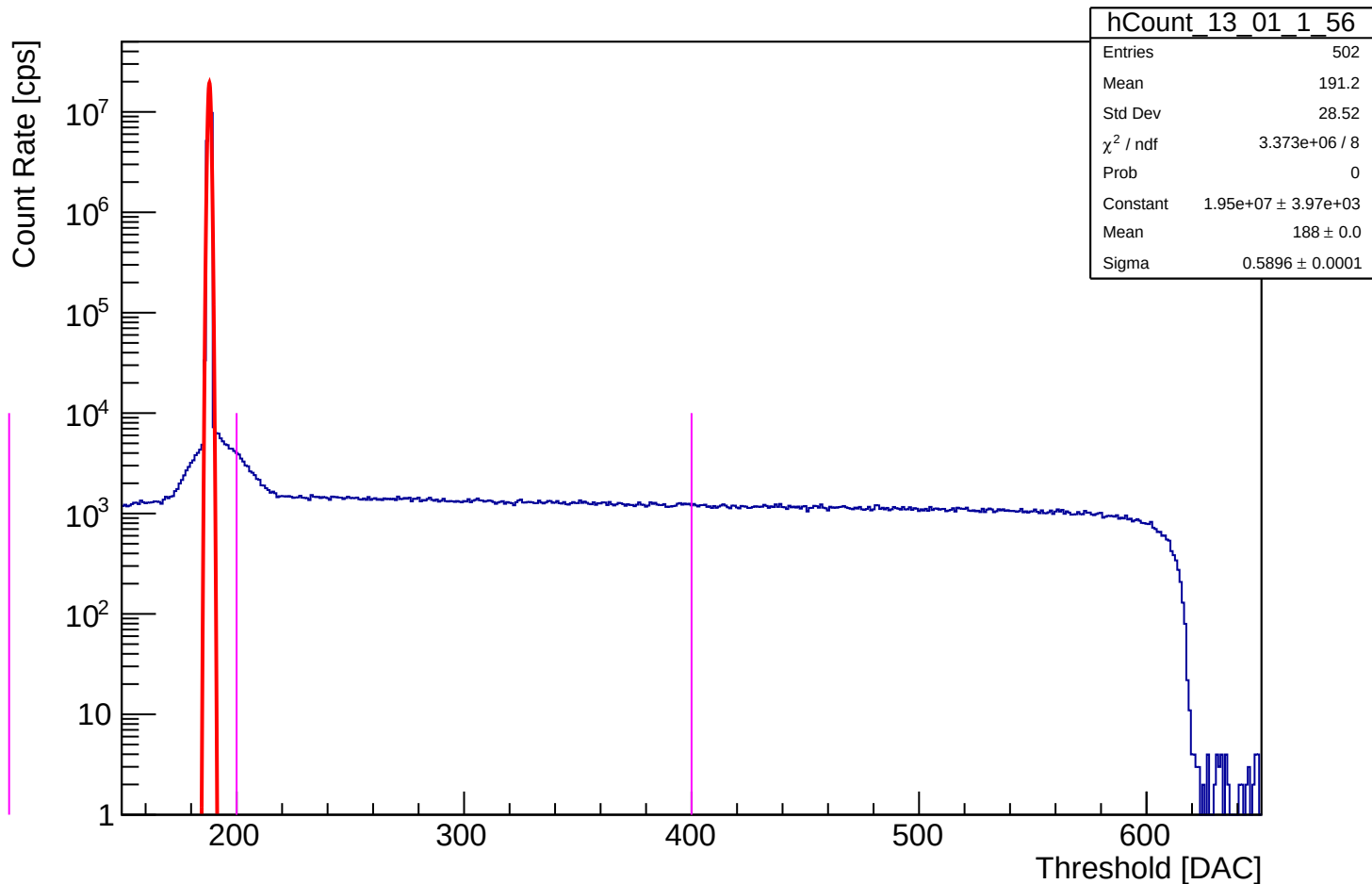
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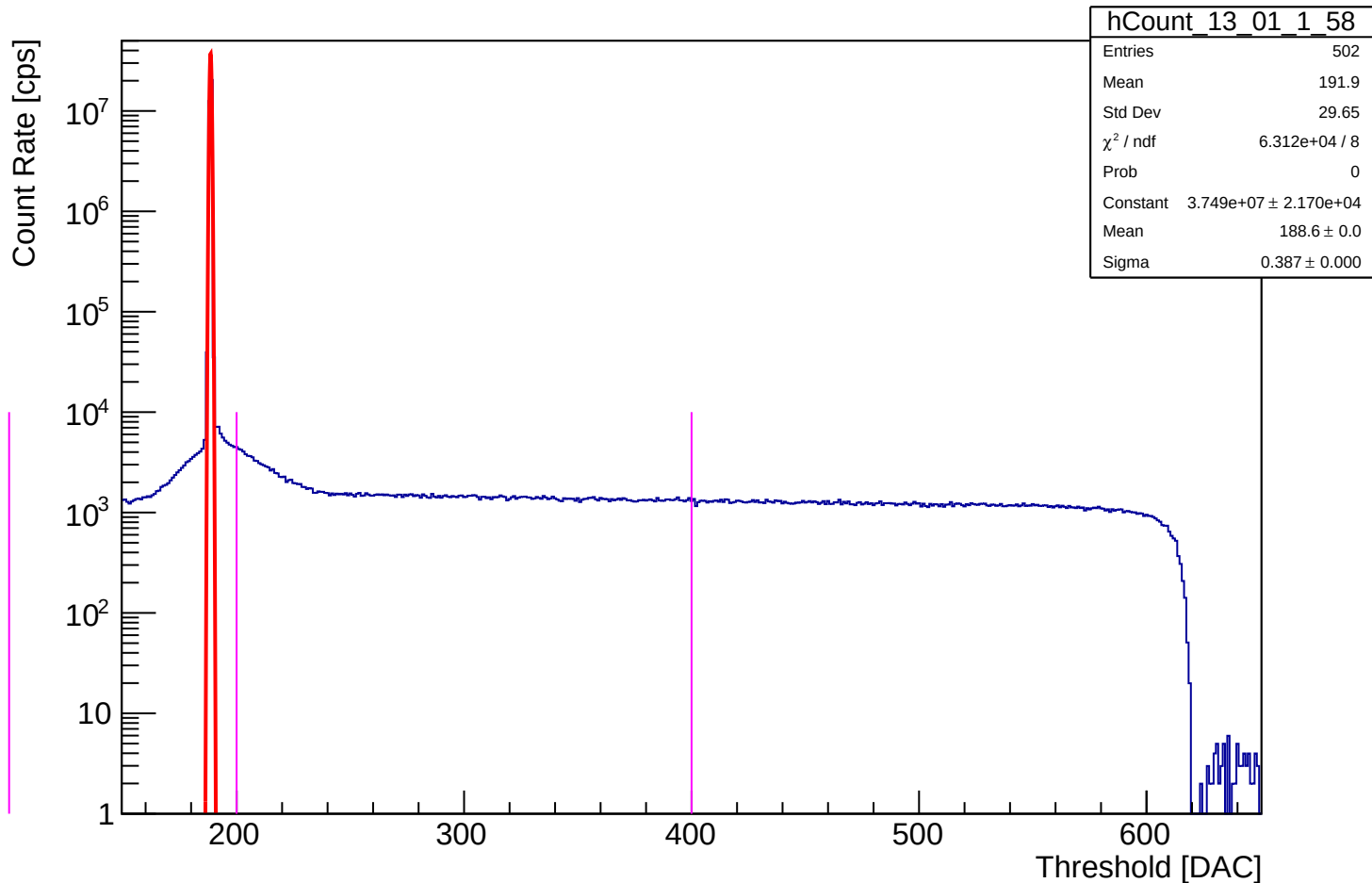




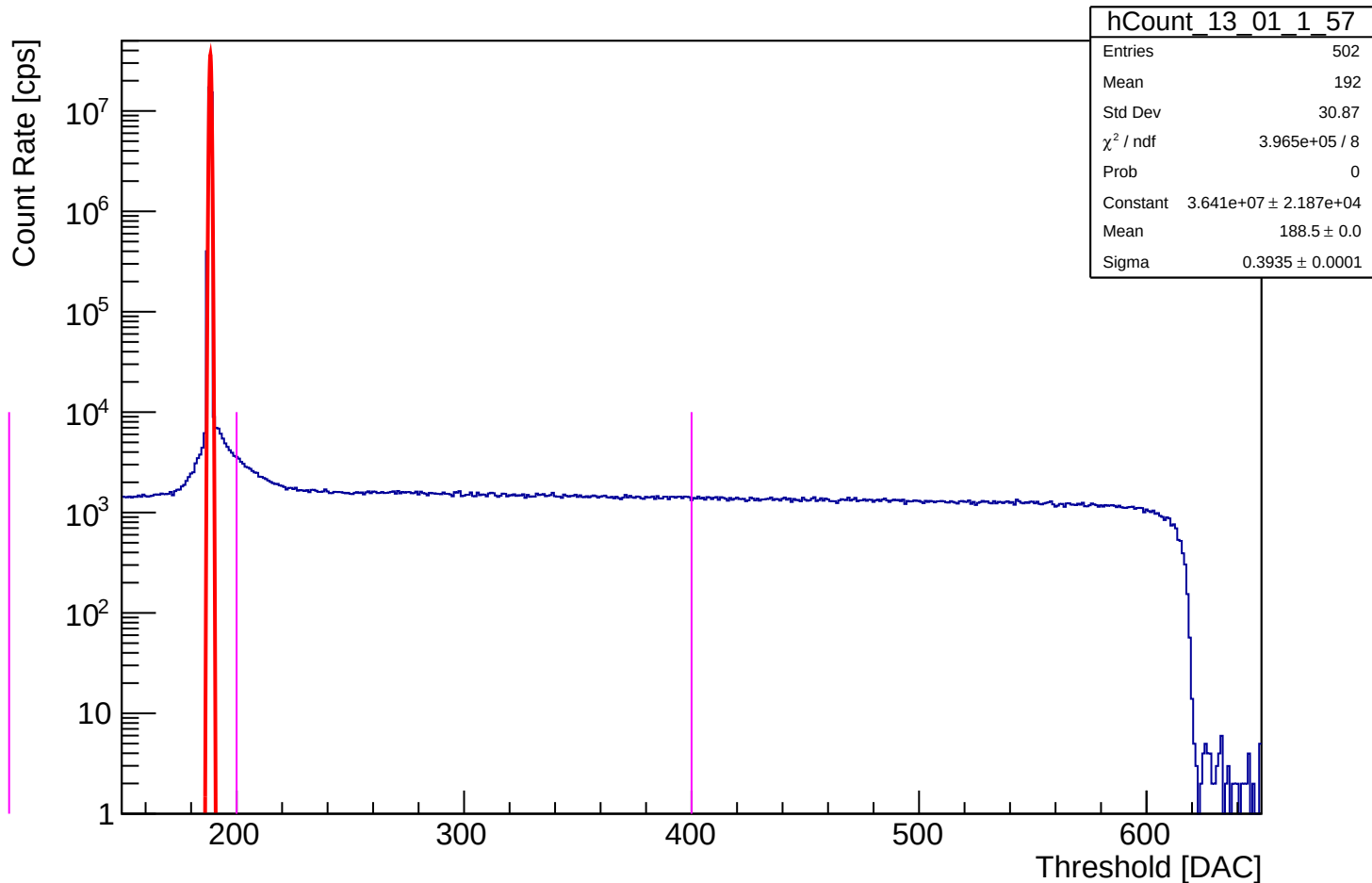
Row 1 Tile 1 Pmt 5 Pixel 53 Slot 13 Fiber 1 Asic 1 Channel 56



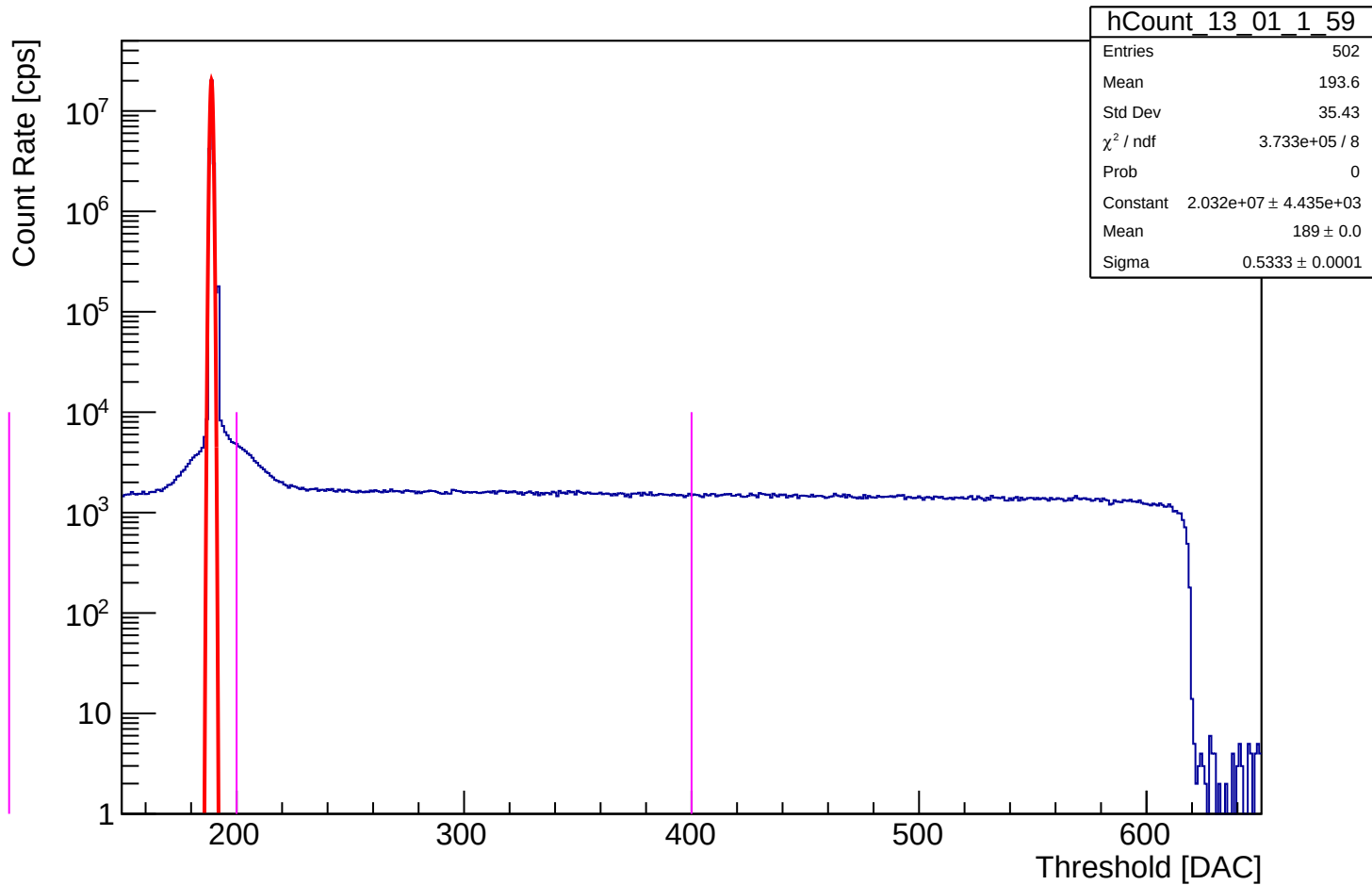
Row 1 Tile 1 Pmt 5 Pixel 54 Slot 13 Fiber 1 Asic 1 Channel 58



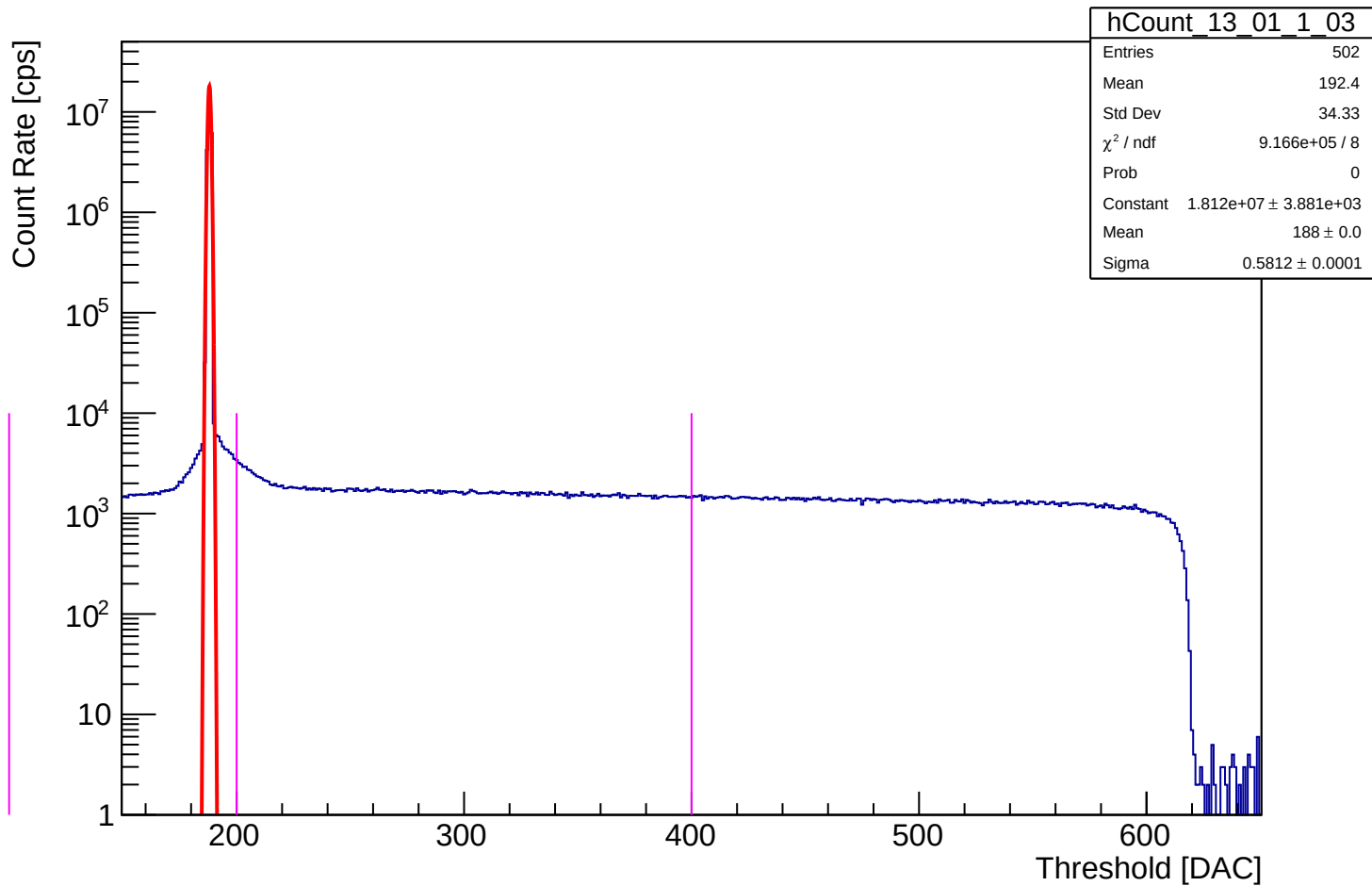
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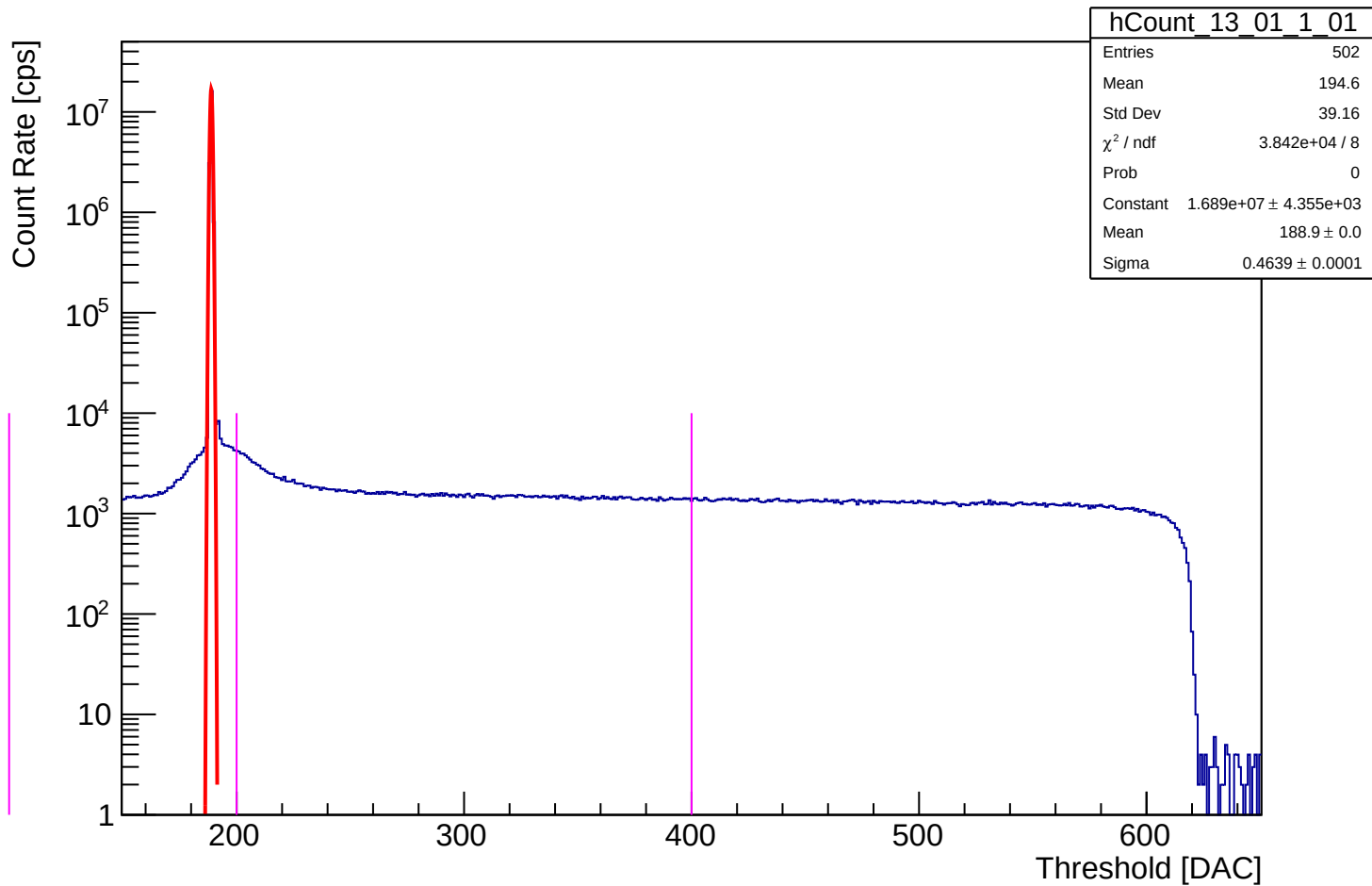
Row 1 Tile 1 Pmt 5 Pixel 56 Slot 13 Fiber 1 Asic 1 Channel 59



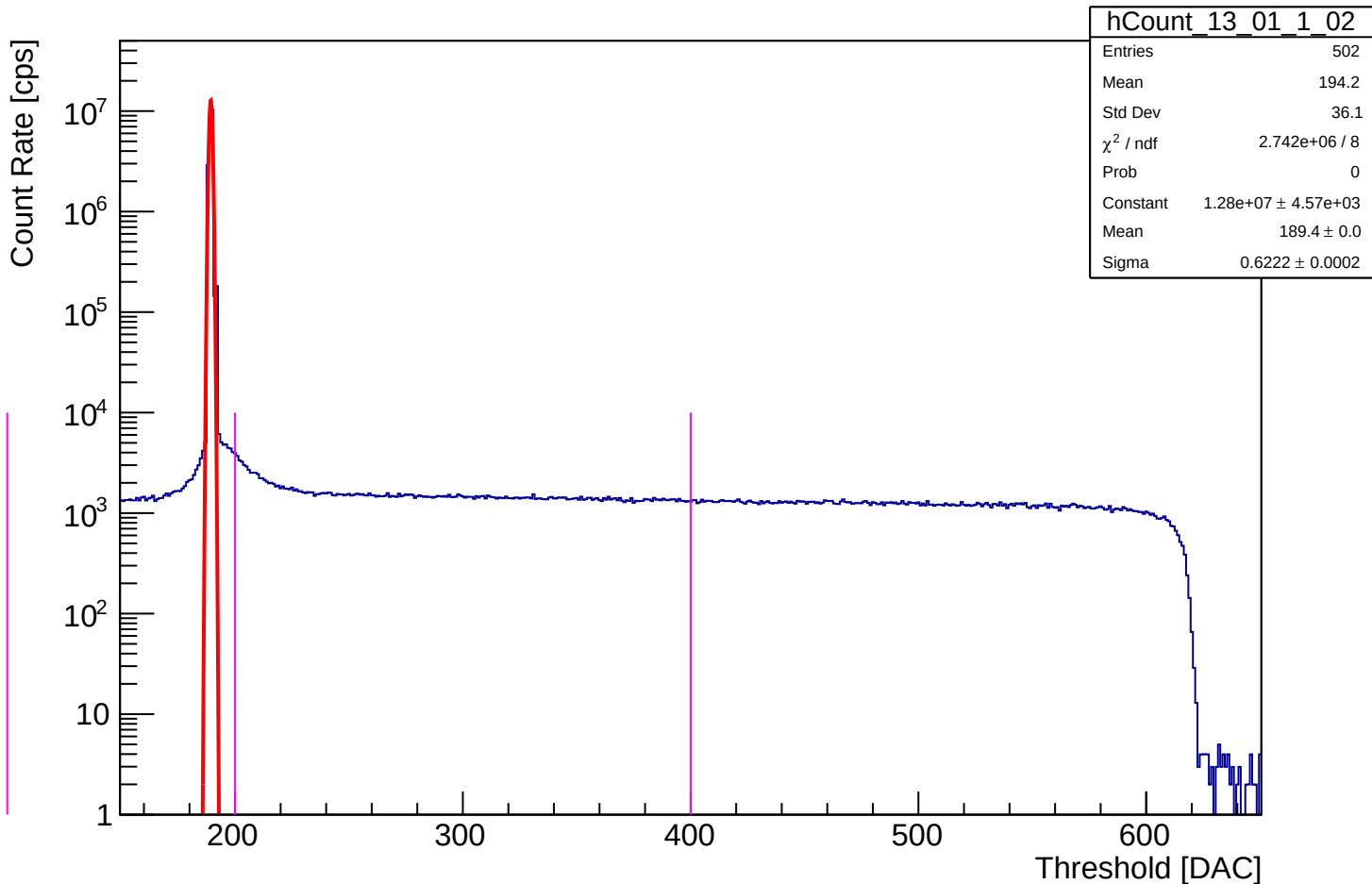
Row 1 Tile 1 Pmt 5 Pixel 57 Slot 13 Fiber 1 Asic 1 Channel 3



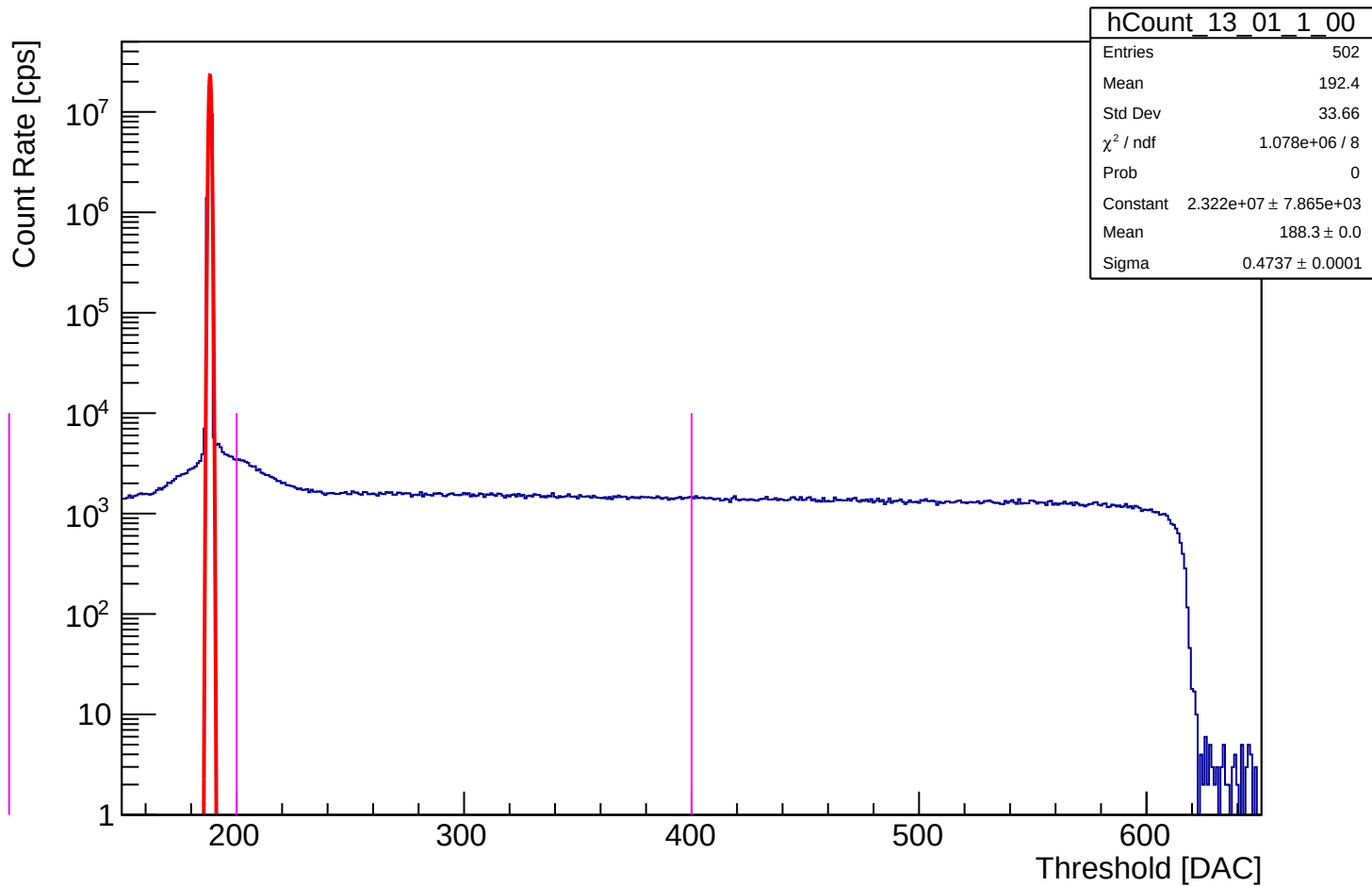
Row 1 Tile 1 Pmt 5 Pixel 58 Slot 13 Fiber 1 Asic 1 Channel 1



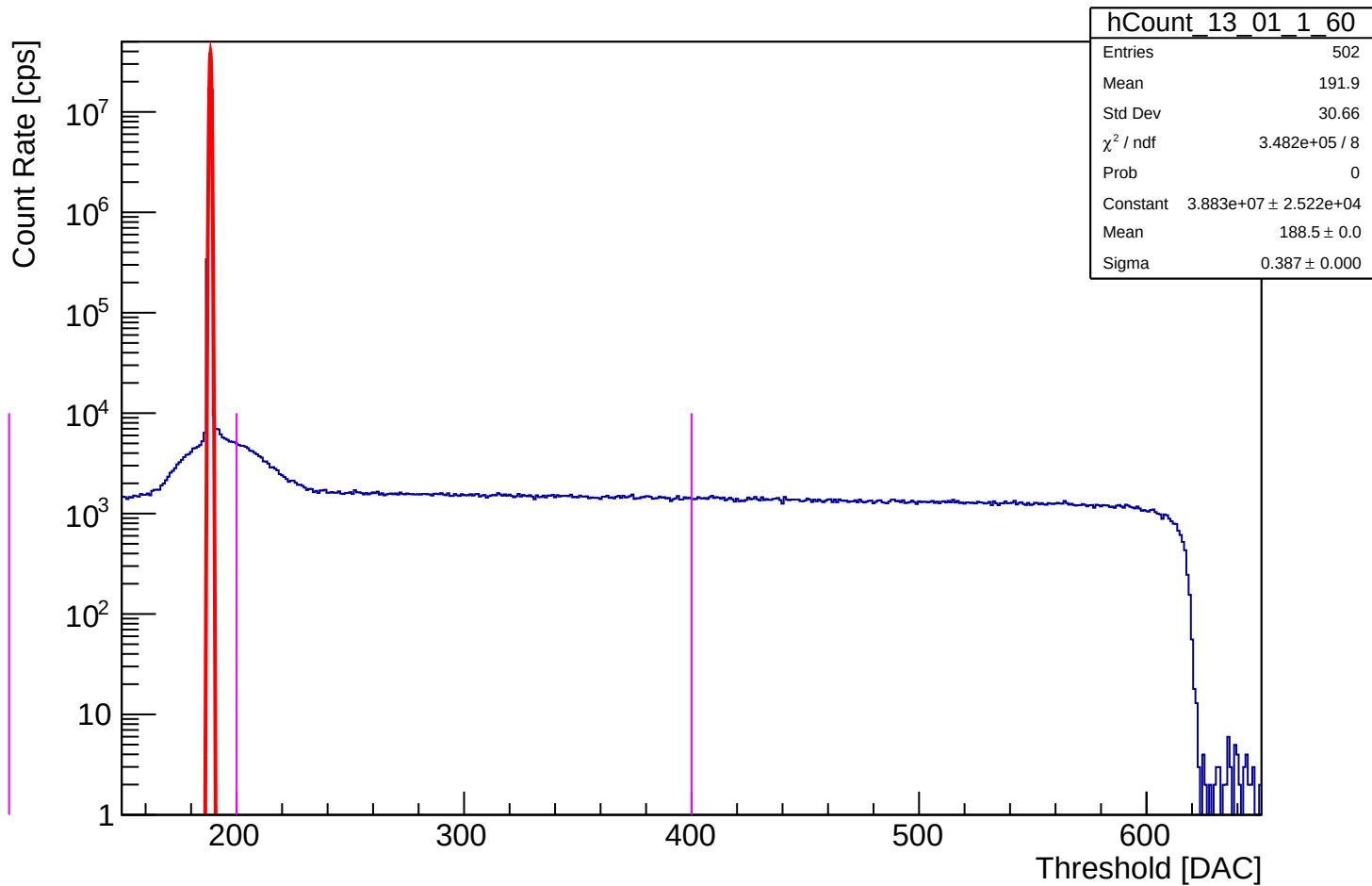
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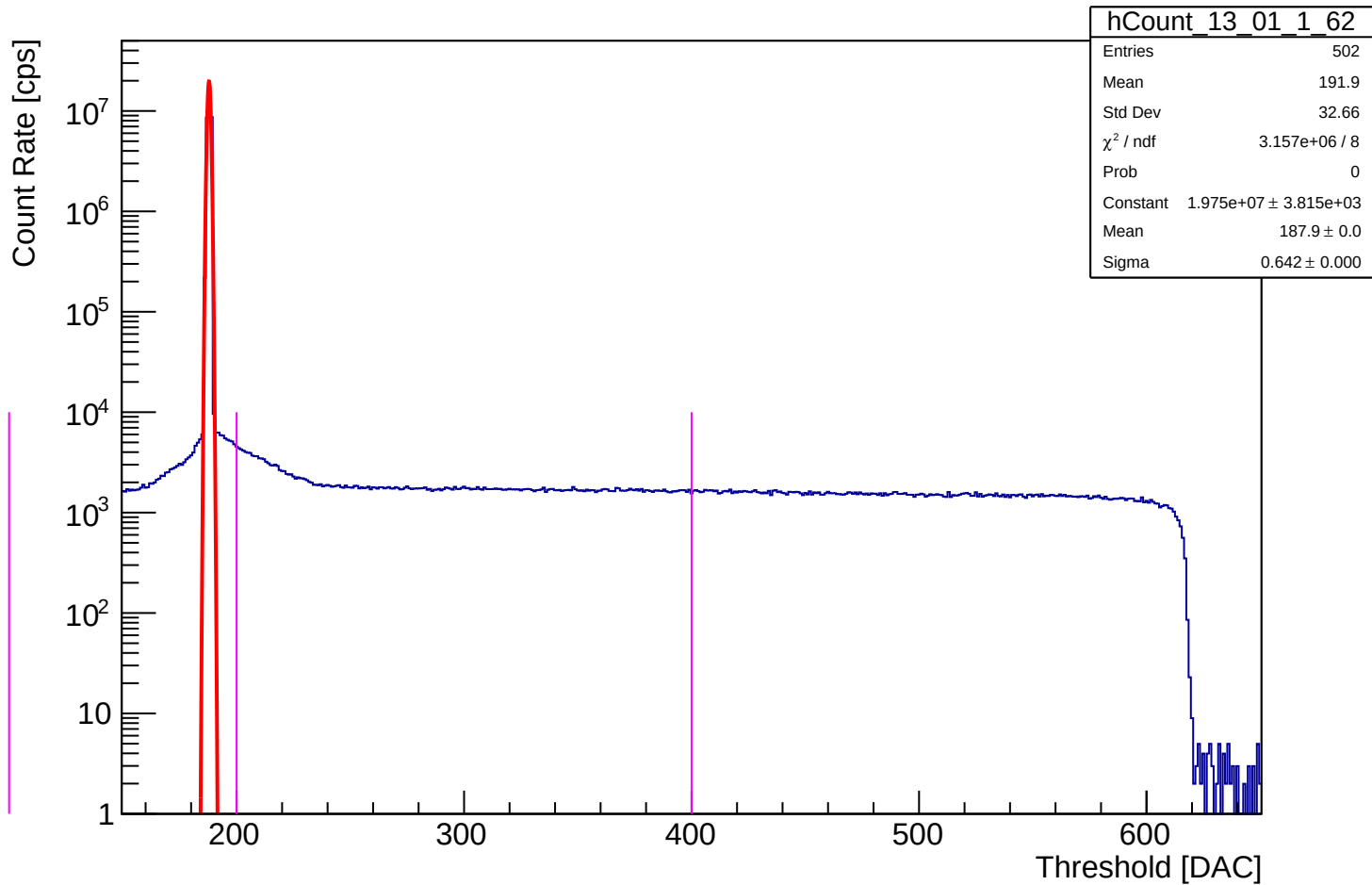
Row 1 Tile 1 Pmt 5 Pixel 60 Slot 13 Fiber 1 Asic 1 Channel 0



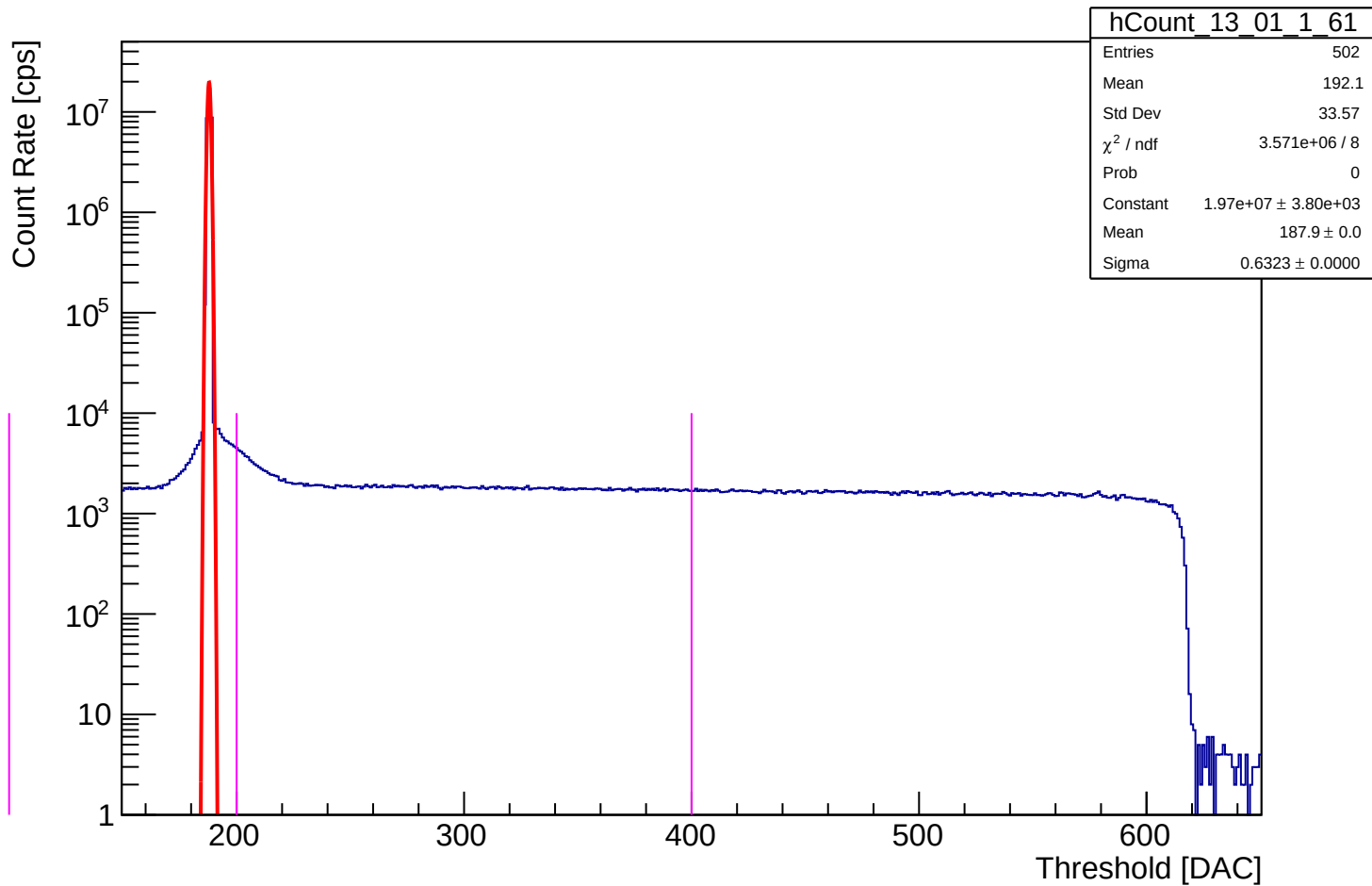
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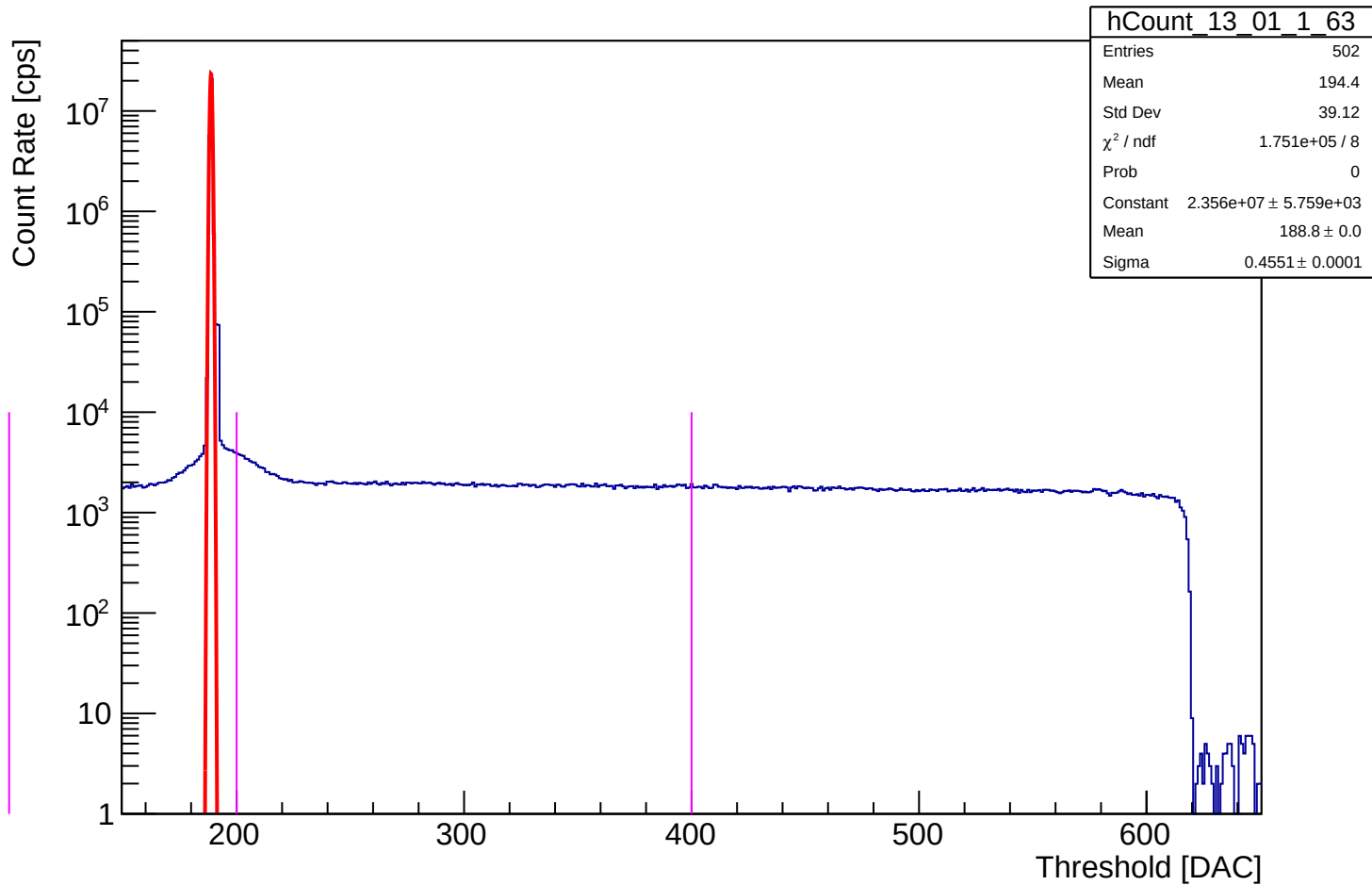
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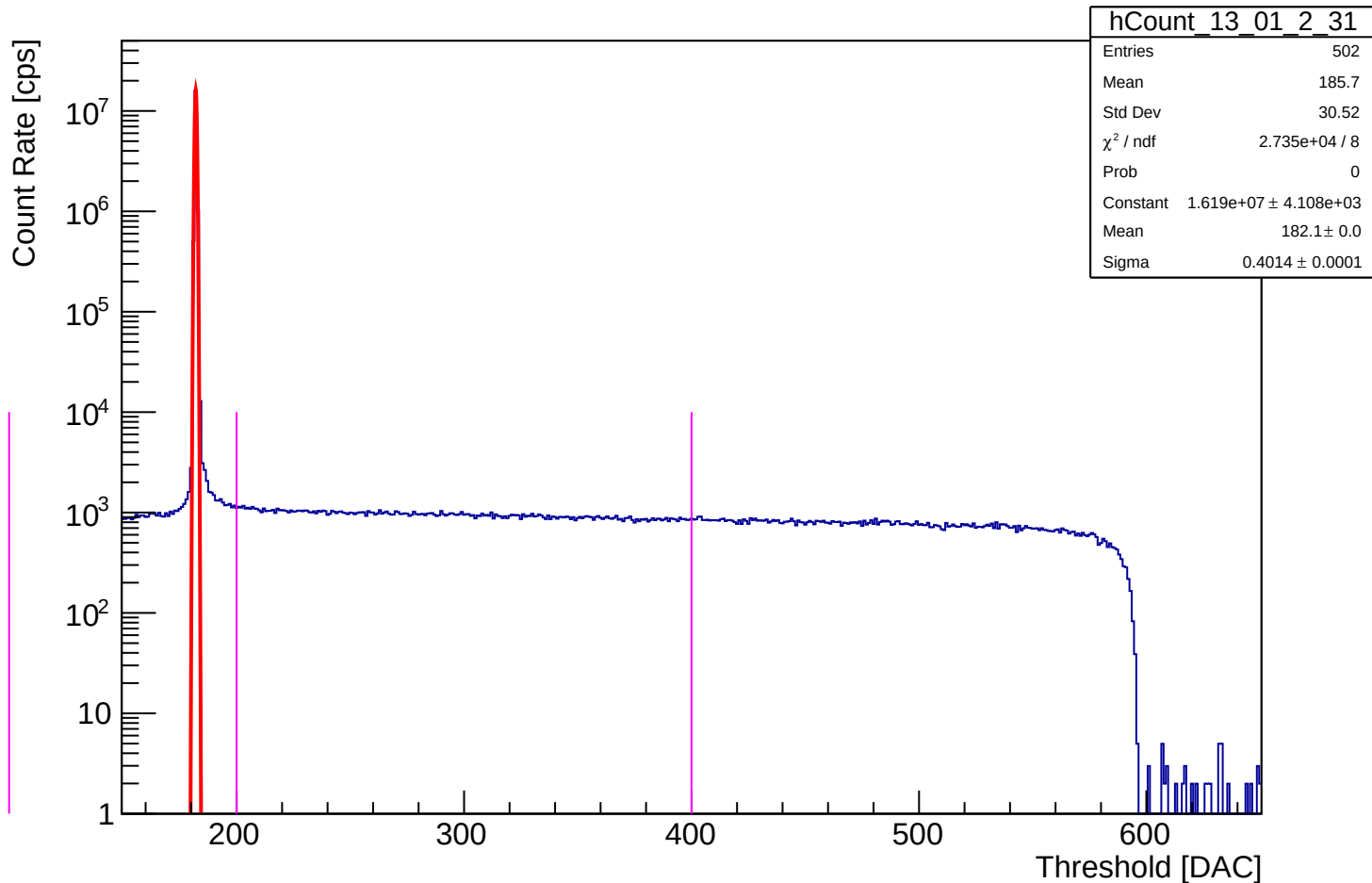
Row 1 Tile 1 Pmt 5 Pixel 63 Slot 13 Fiber 1 Asic 1 Channel 61



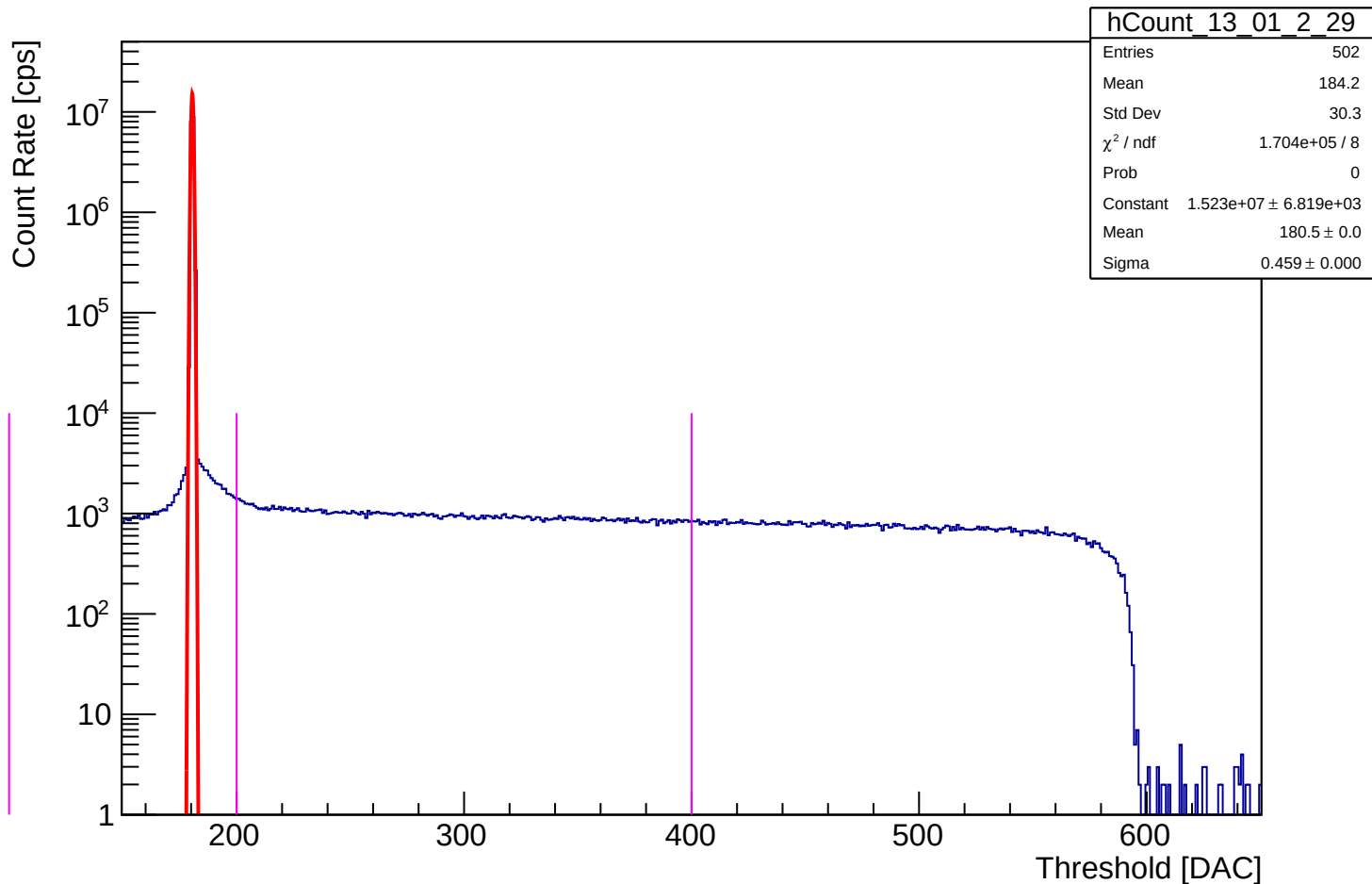
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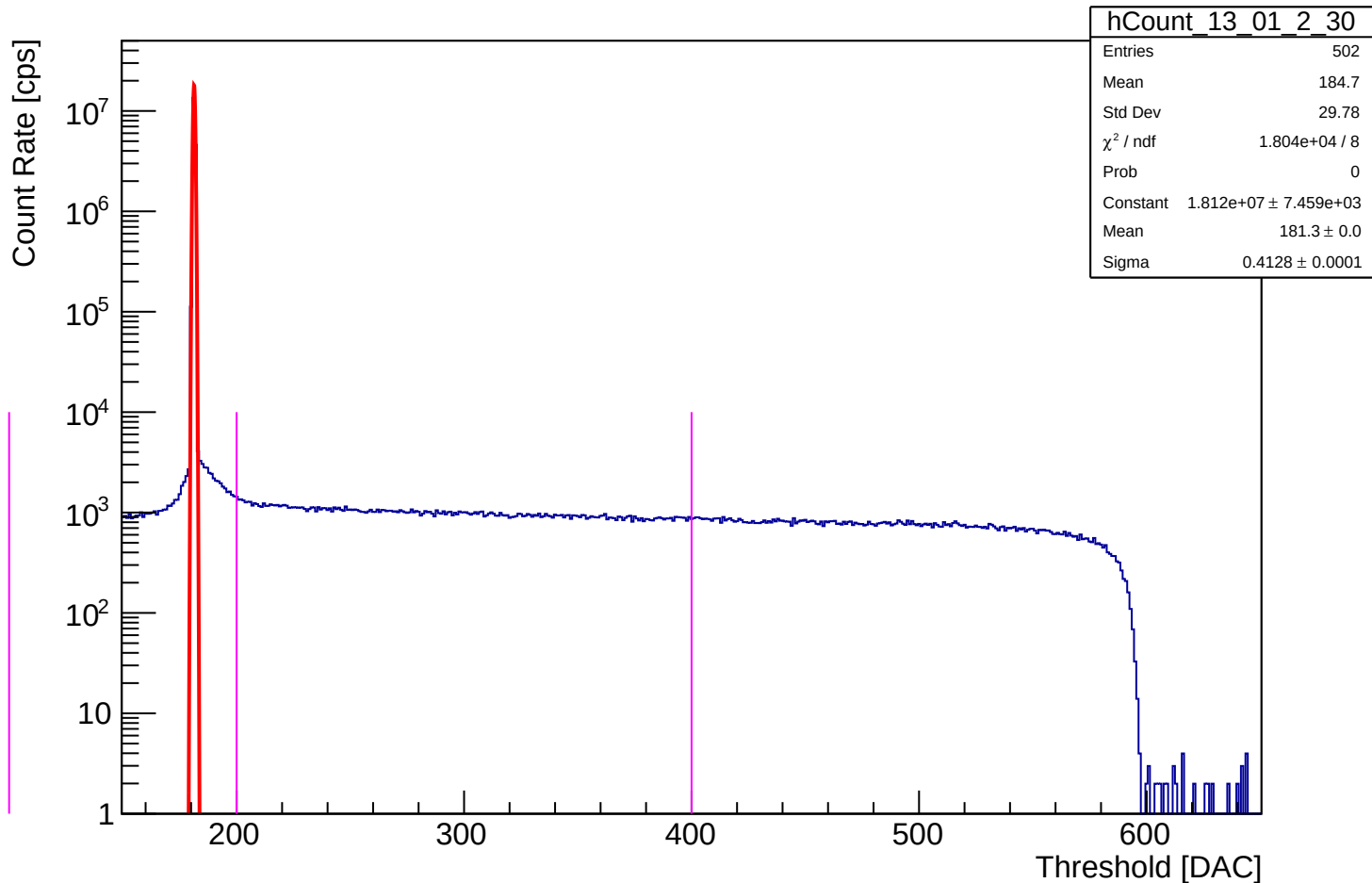


Row 1 Tile 1 Pmt 6 Pixel 1 Slot 13 Fiber 1 Asic 2 Channel 31

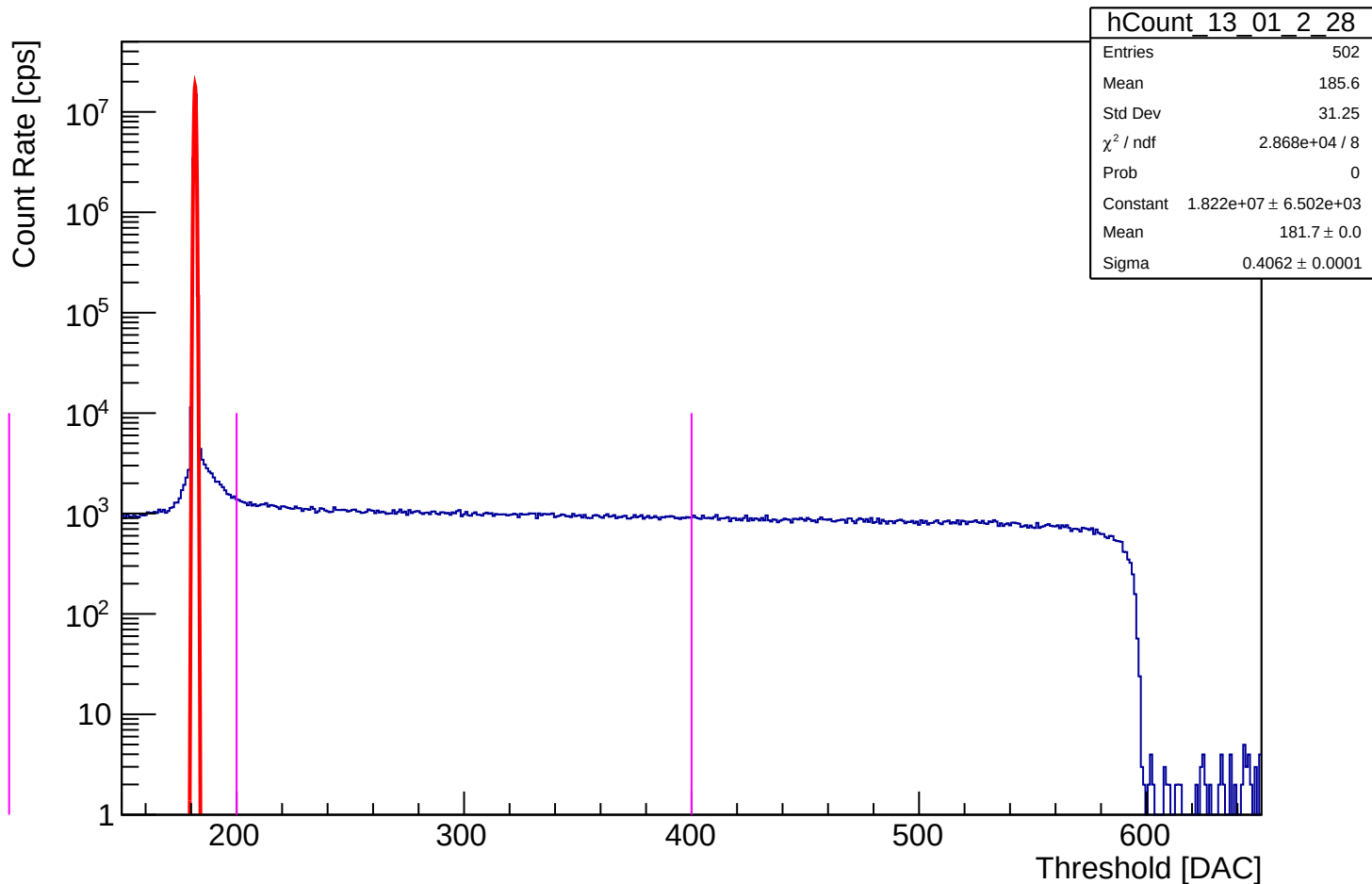


Row 1 Tile 1 Pmt 6 Pixel 2 Slot 13 Fiber 1 Asic 2 Channel 29

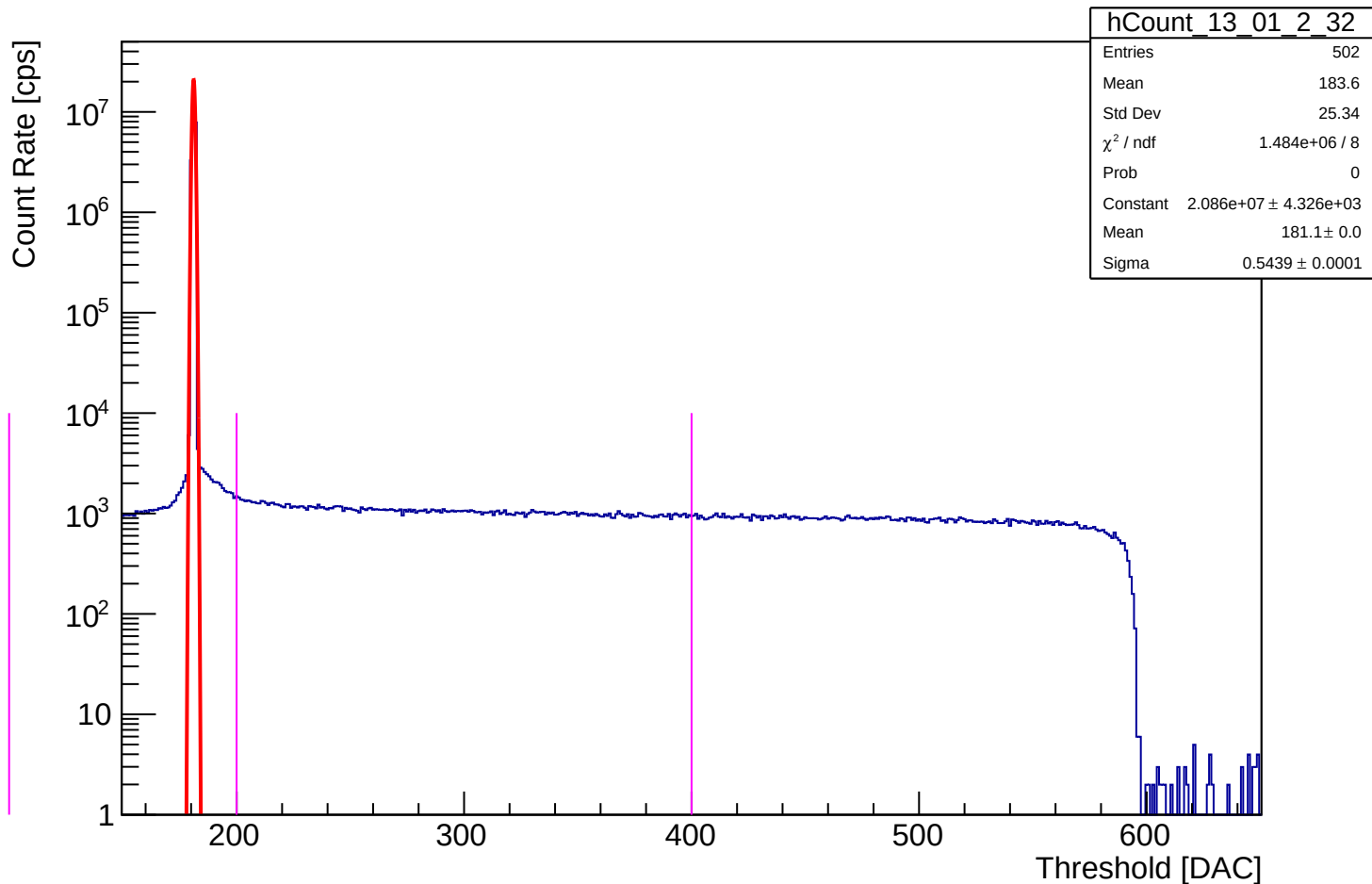




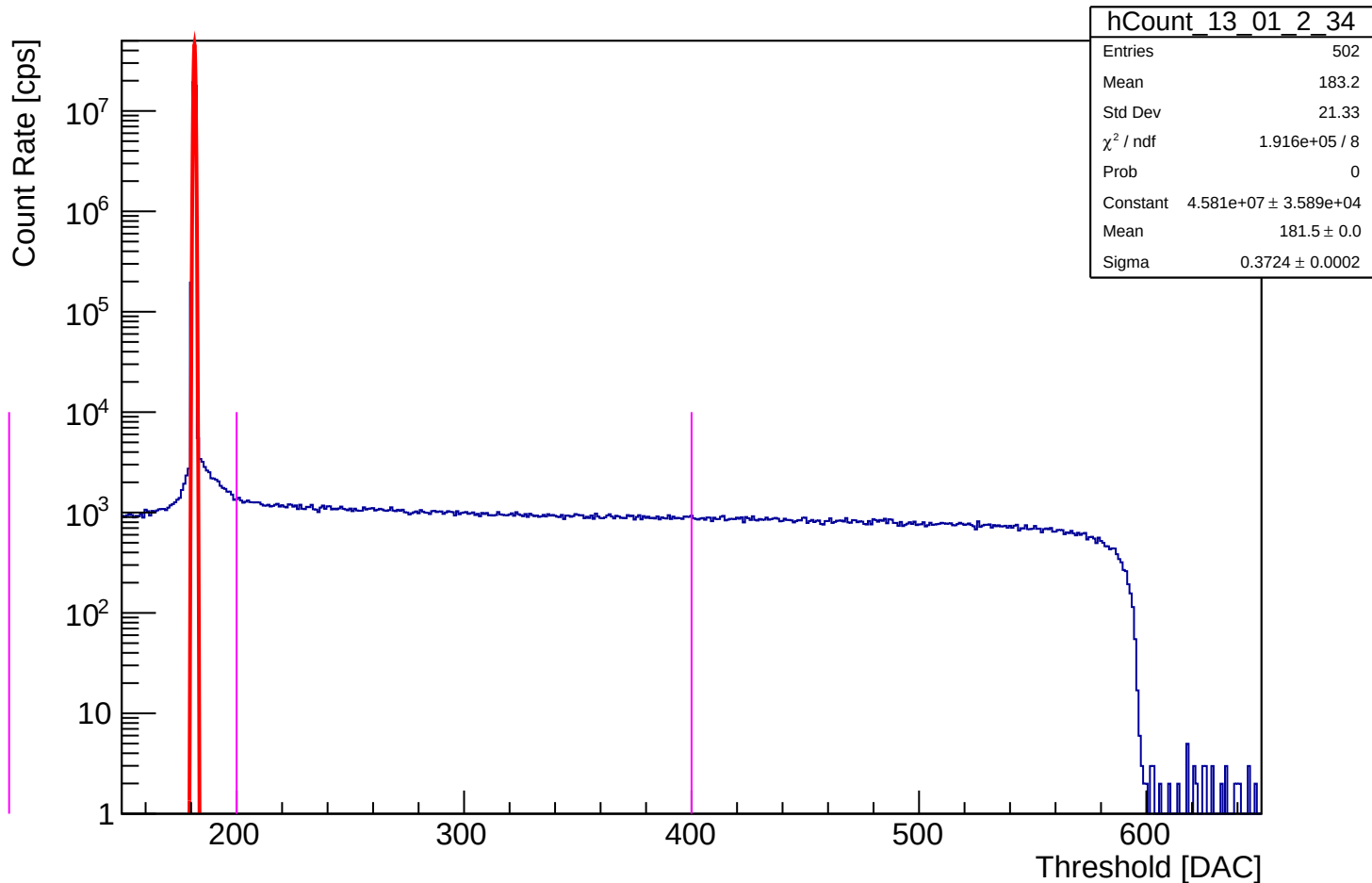
Row 1 Tile 1 Pmt 6 Pixel 4 Slot 13 Fiber 1 Asic 2 Channel 28



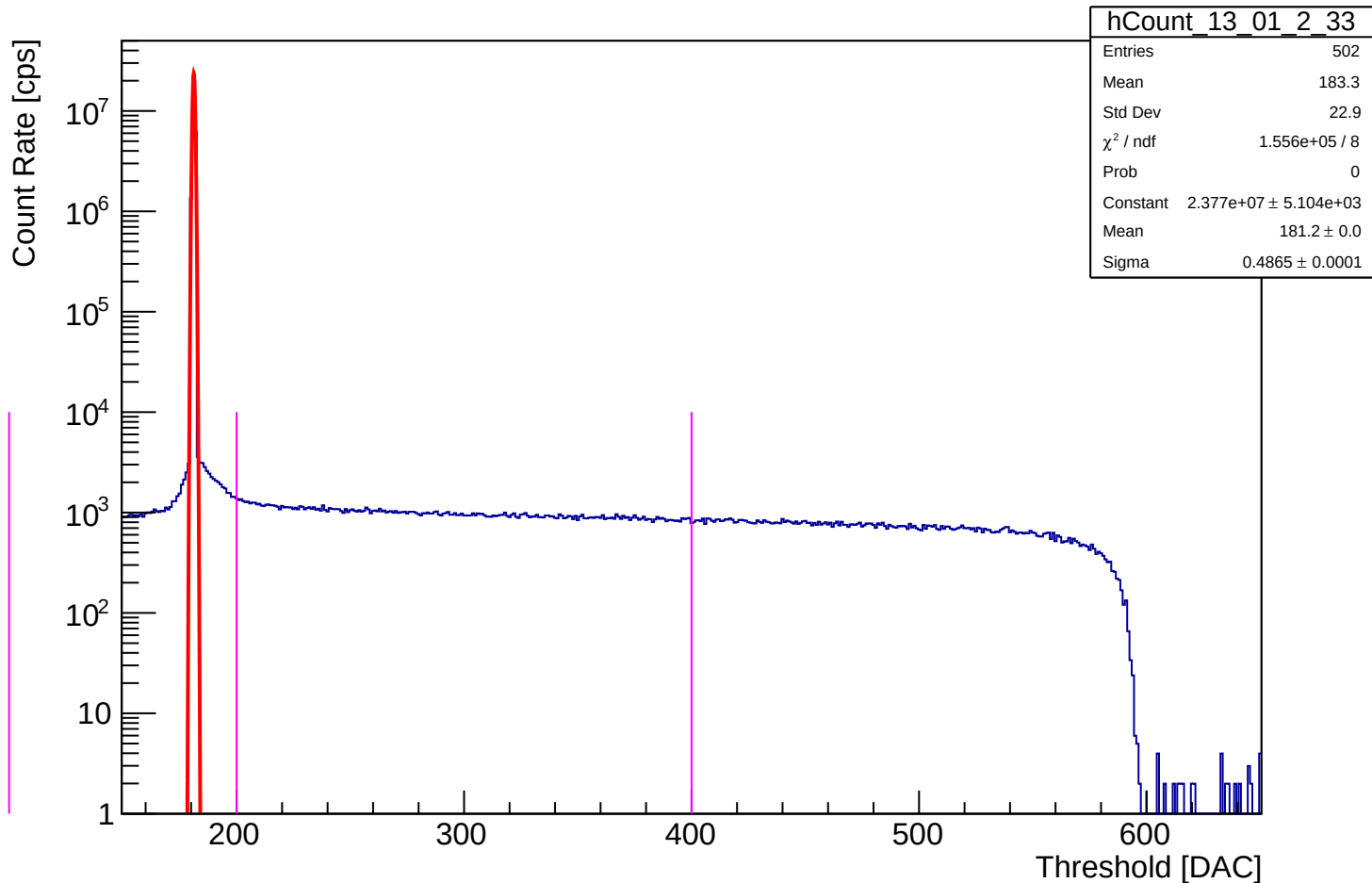
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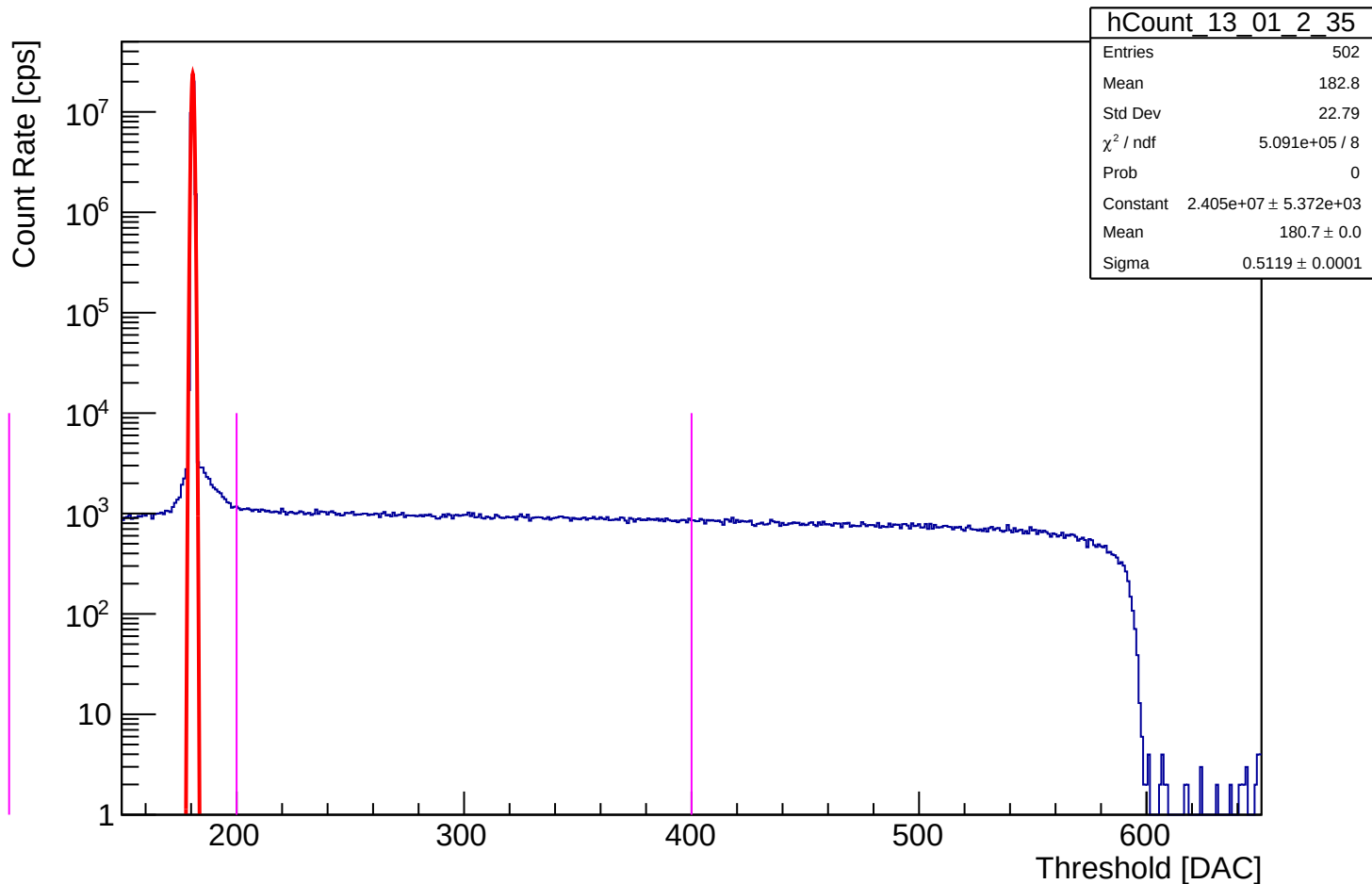
Row 1 Tile 1 Pmt 6 Pixel 6 Slot 13 Fiber 1 Asic 2 Channel 34

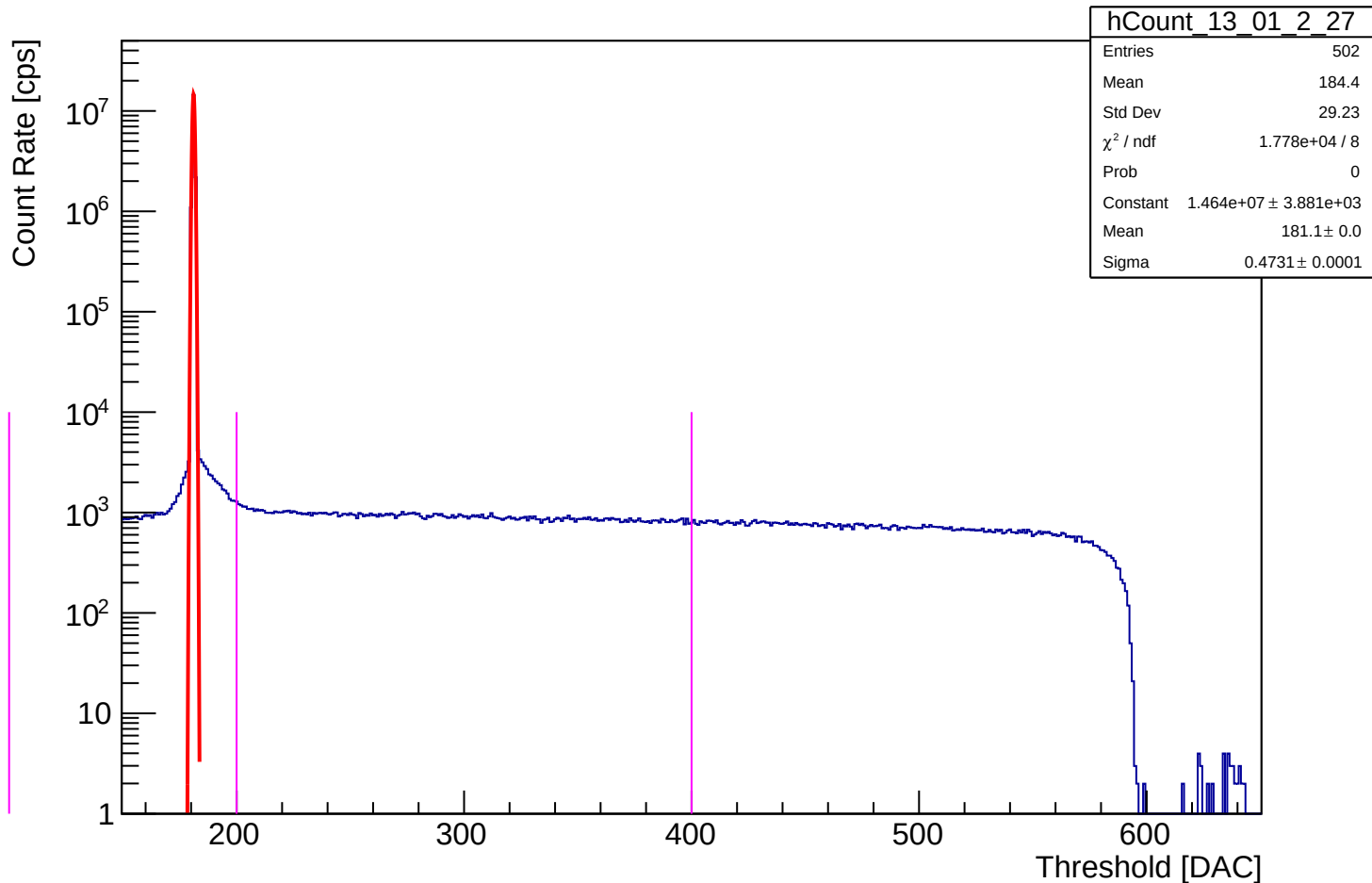


Row 1 Tile 1 Pmt 6 Pixel 7 Slot 13 Fiber 1 Asic 2 Channel 33

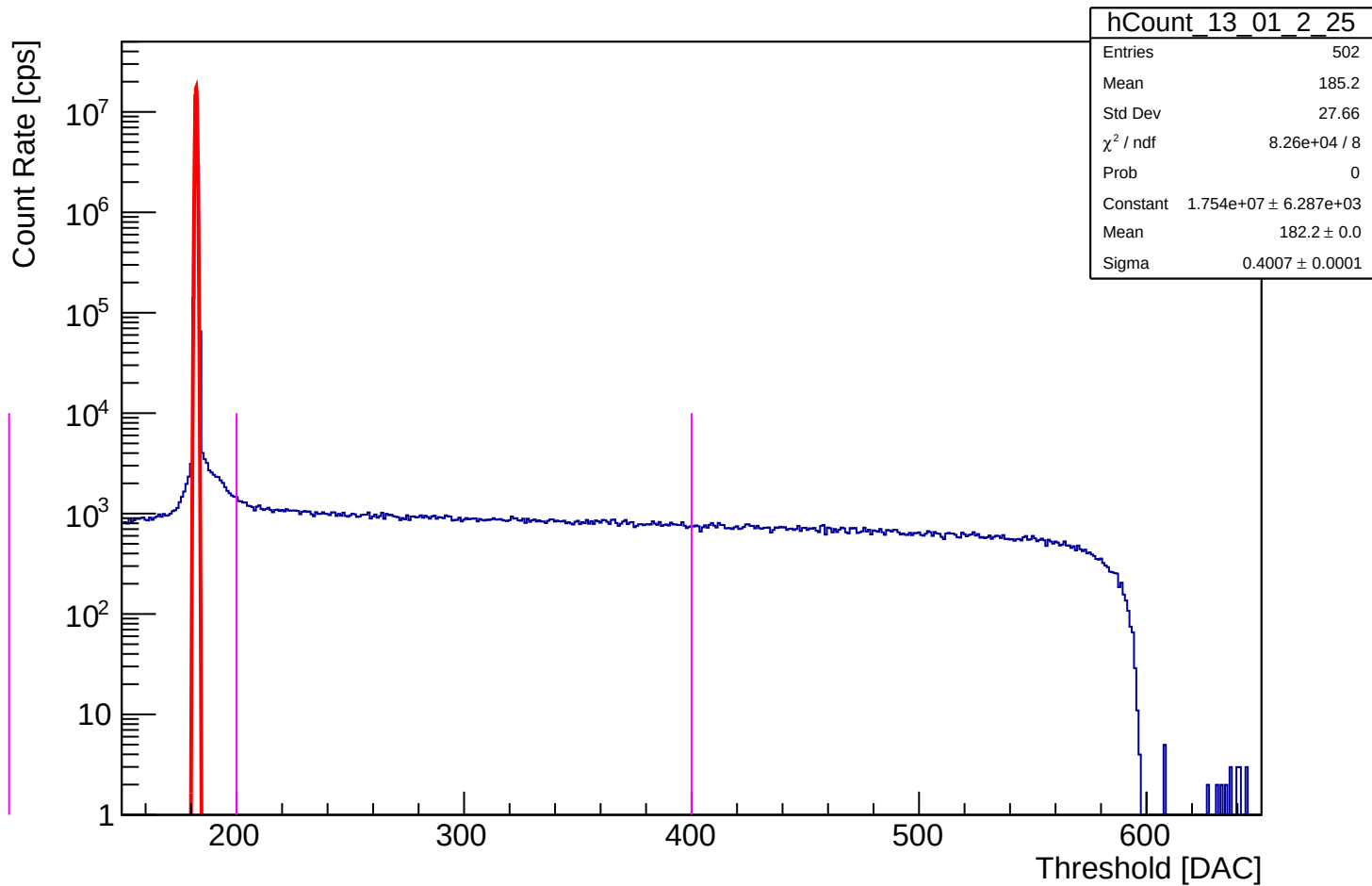


Row 1 Tile 1 Pmt 6 Pixel 8 Slot 13 Fiber 1 Asic 2 Channel 35

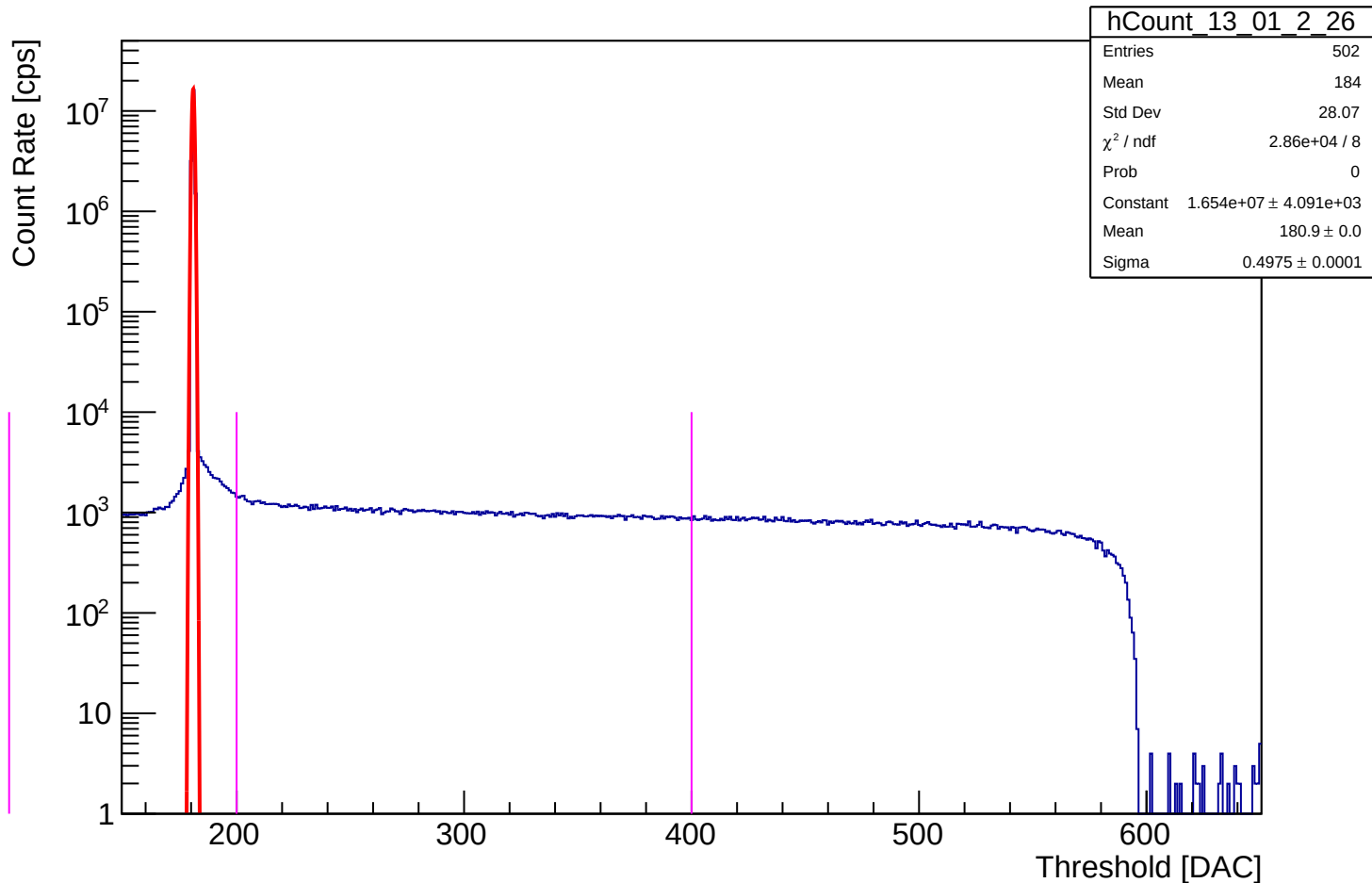




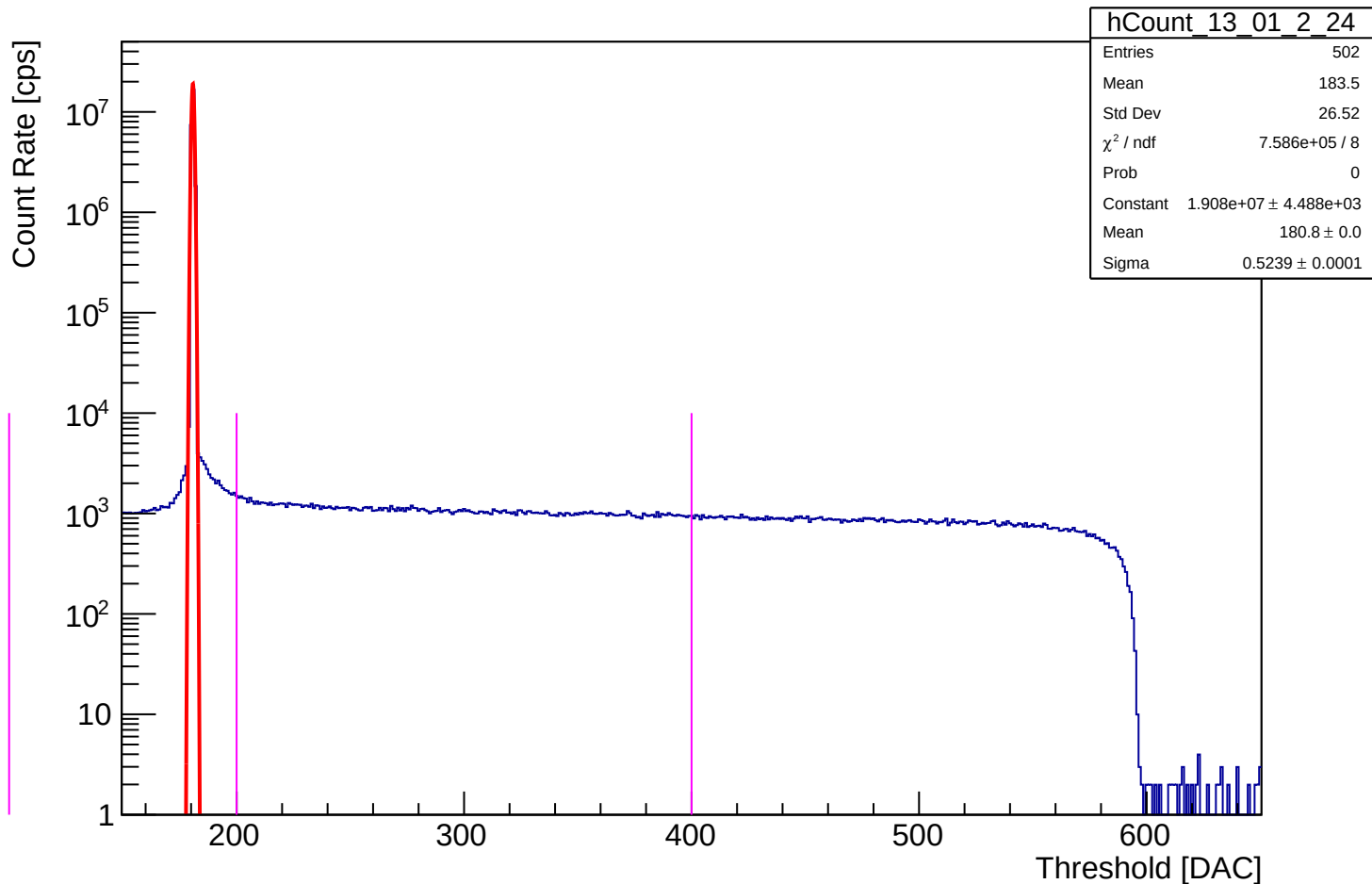
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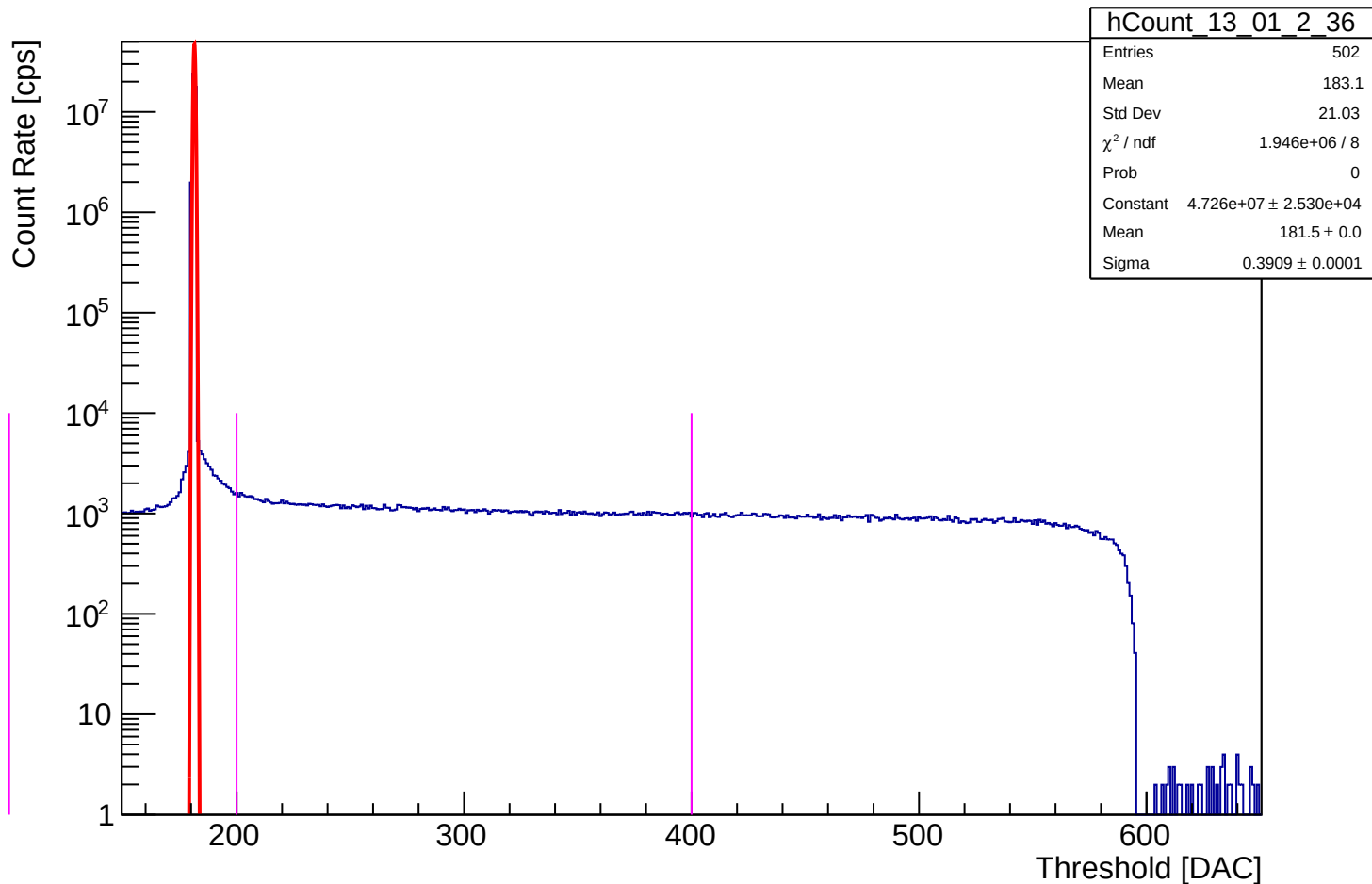
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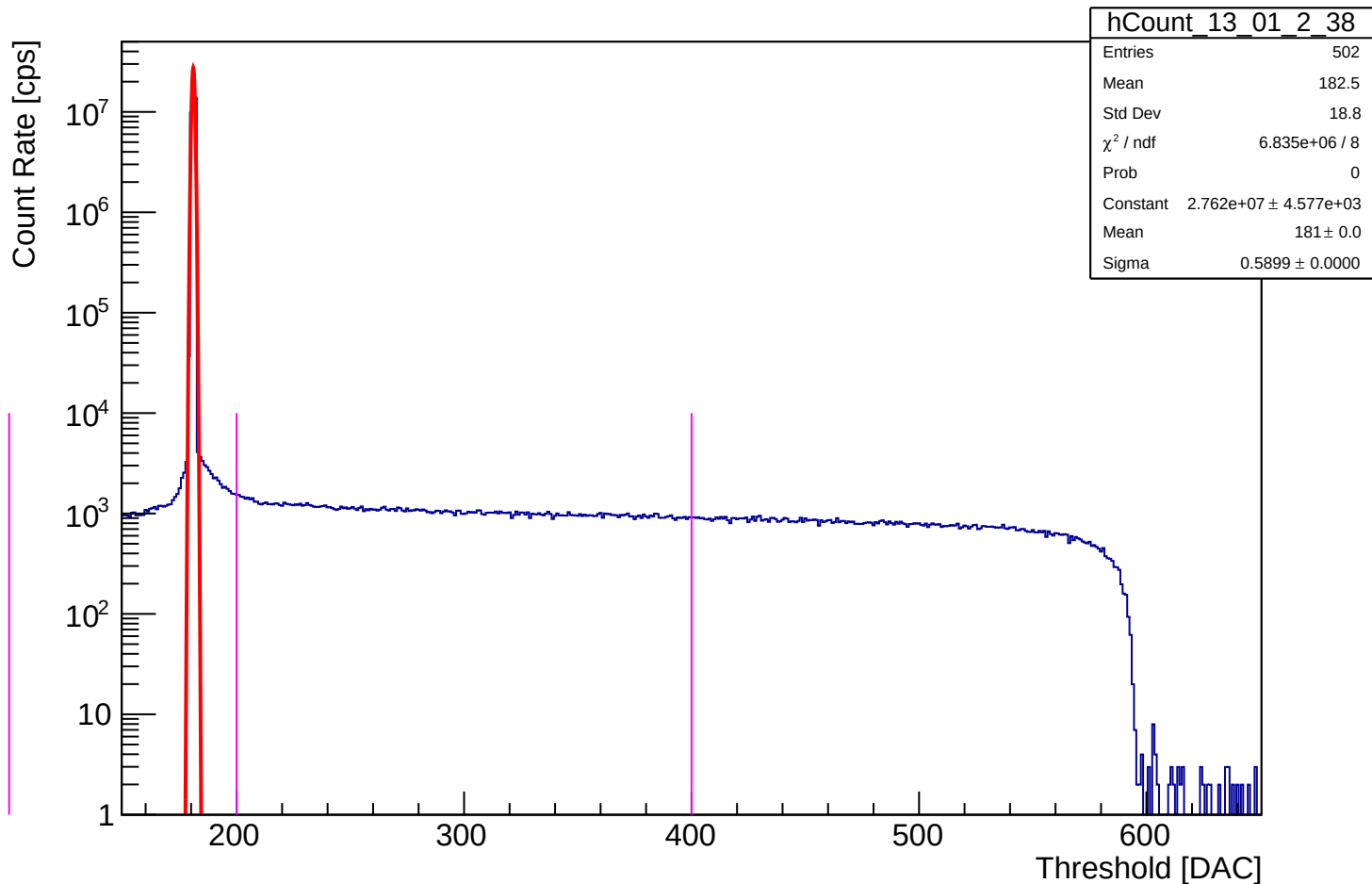
Row 1 Tile 1 Pmt 6 Pixel 12 Slot 13 Fiber 1 Asic 2 Channel 24



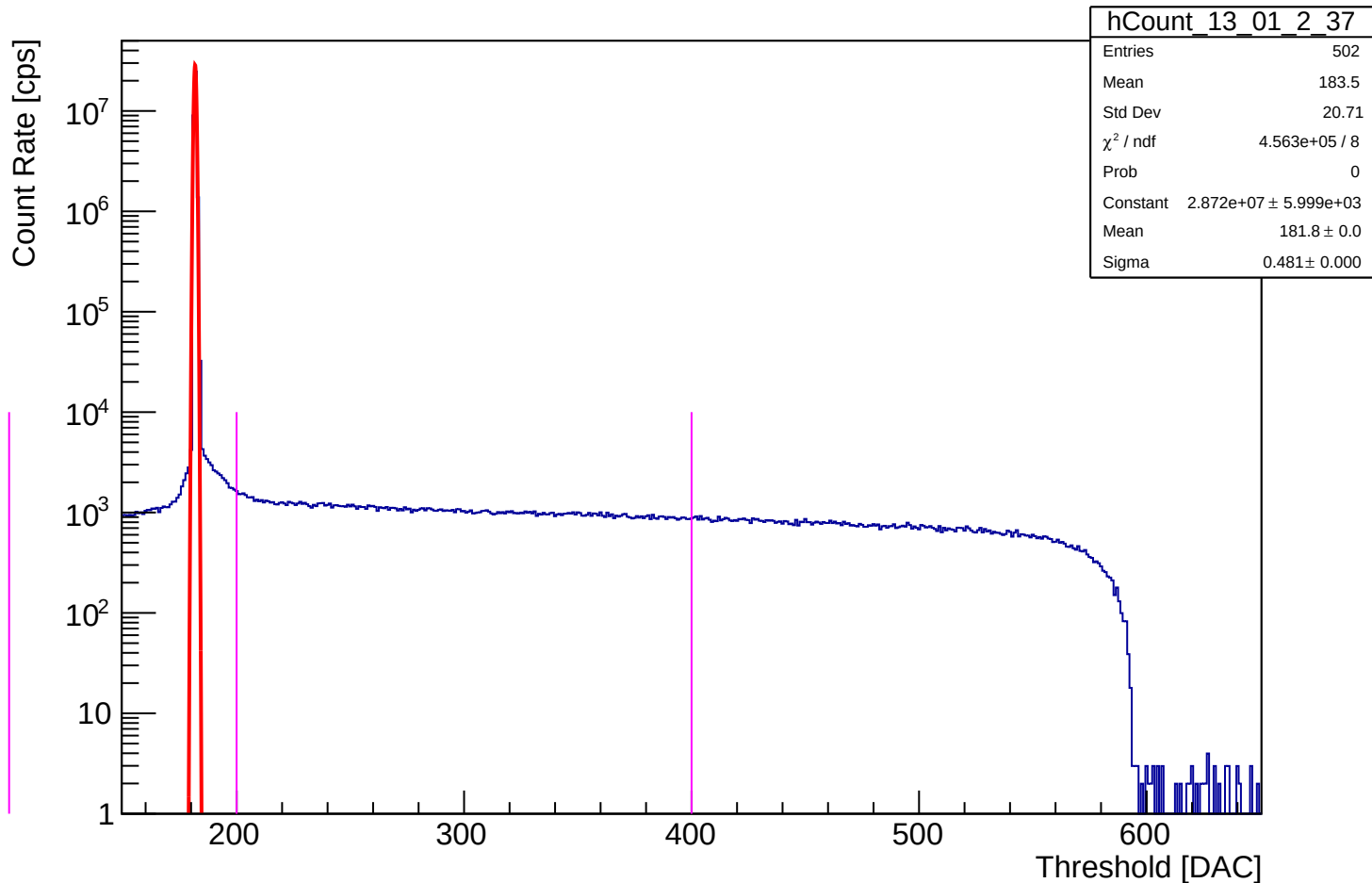
Row 1 Tile 1 Pmt 6 Pixel 13 Slot 13 Fiber 1 Asic 2 Channel 36

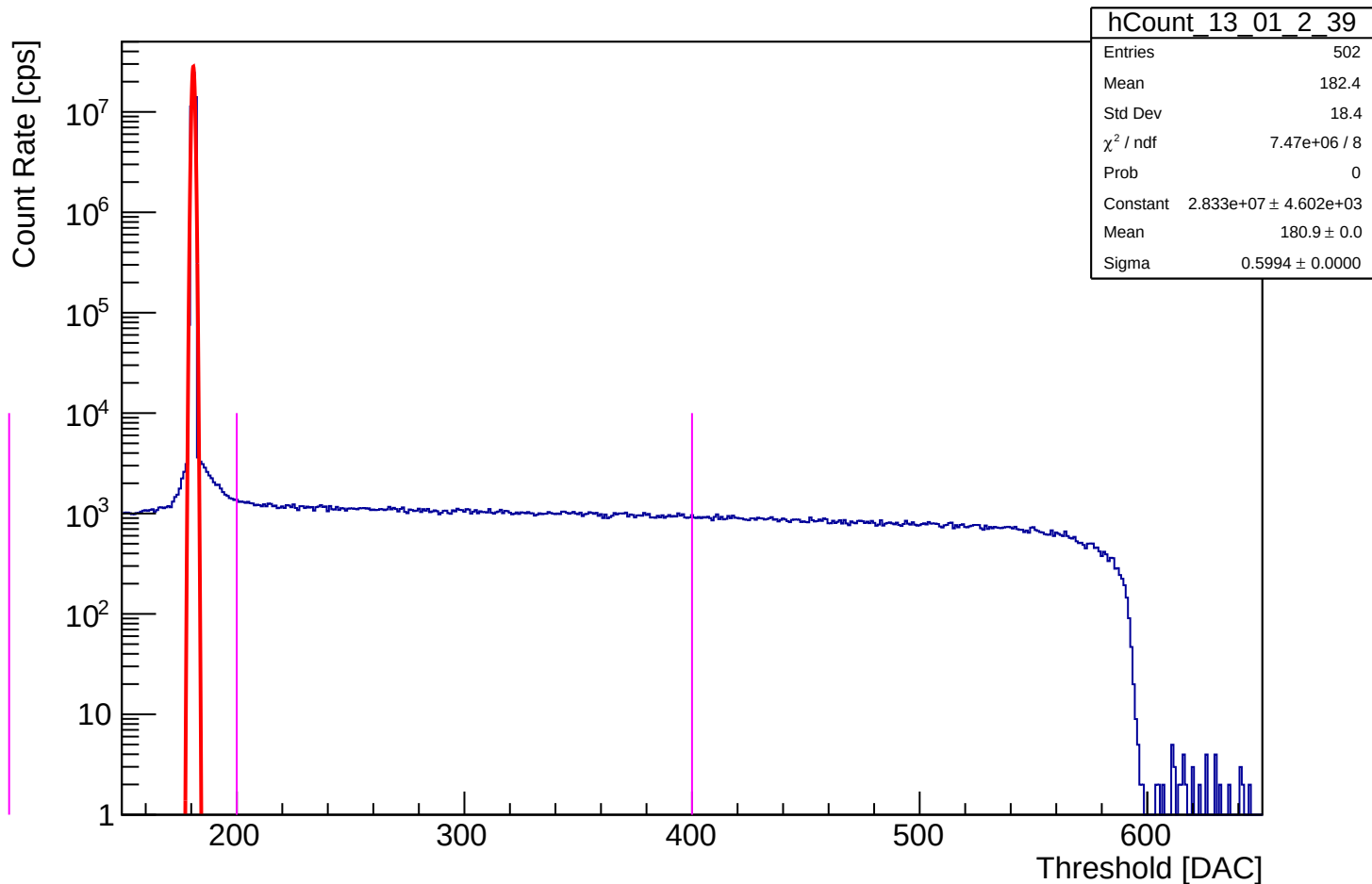


Row 1 Tile 1 Pmt 6 Pixel 14 Slot 13 Fiber 1 Asic 2 Channel 38

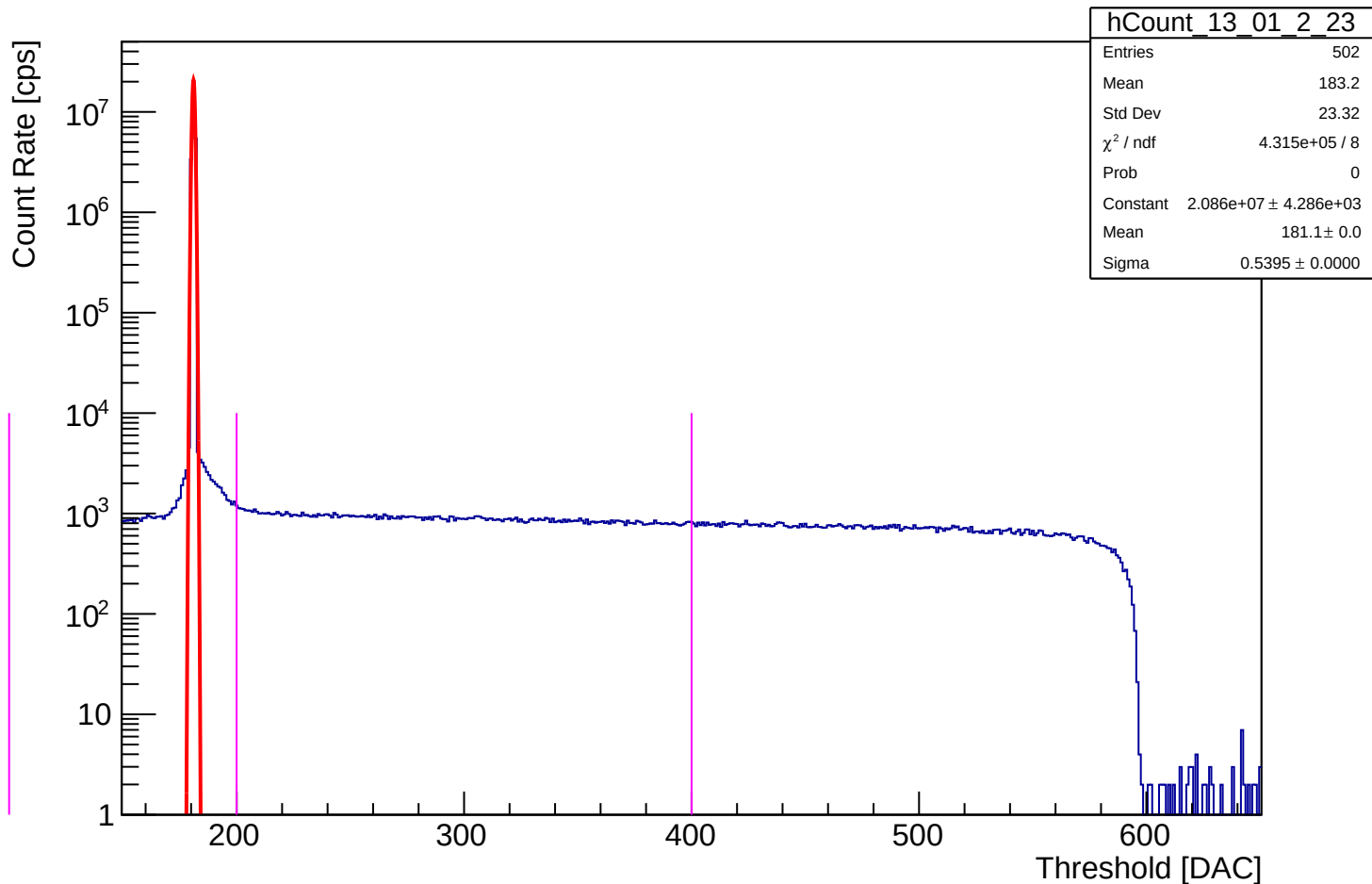


Row 1 Tile 1 Pmt 6 Pixel 15 Slot 13 Fiber 1 Asic 2 Channel 37

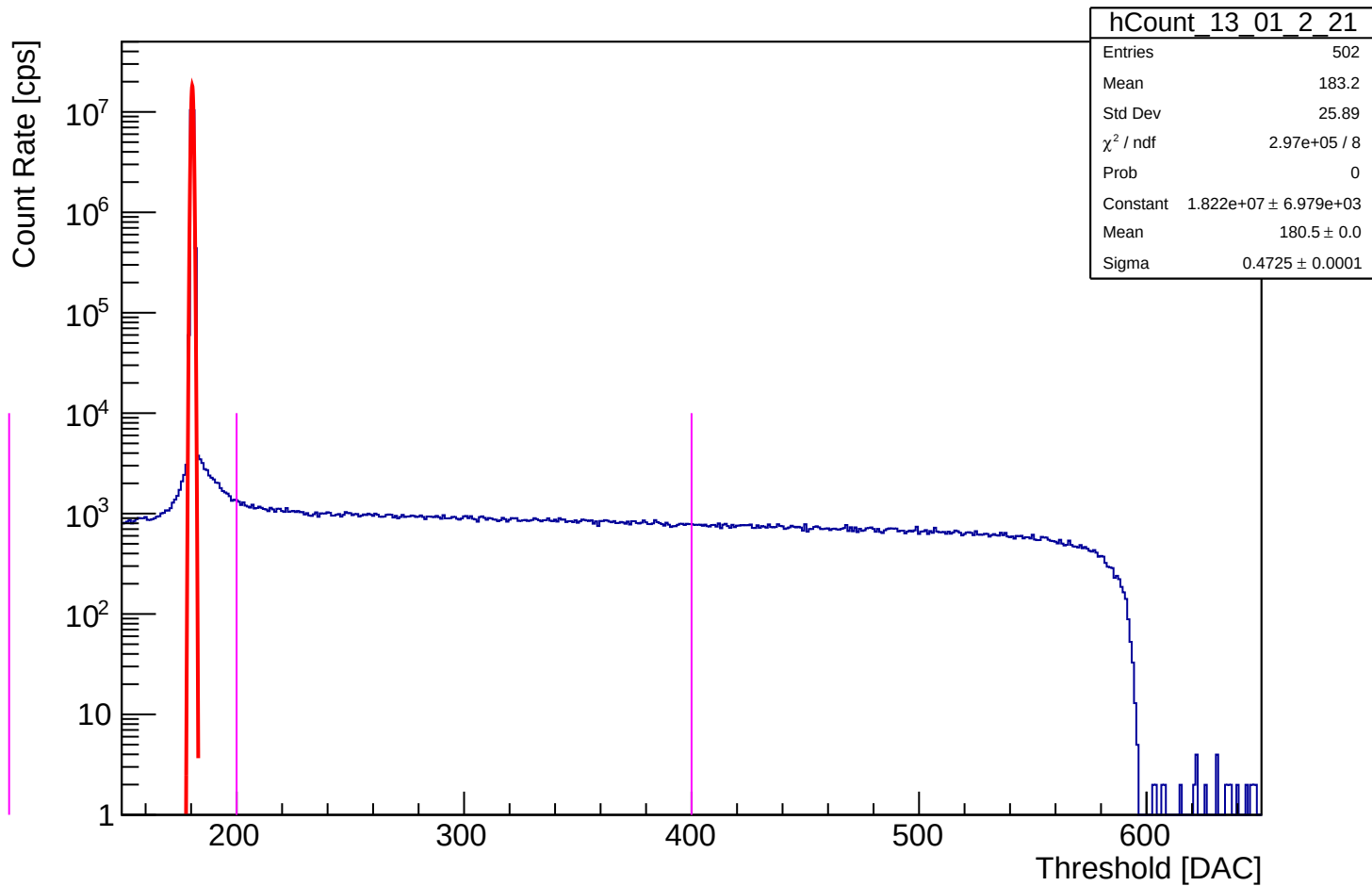




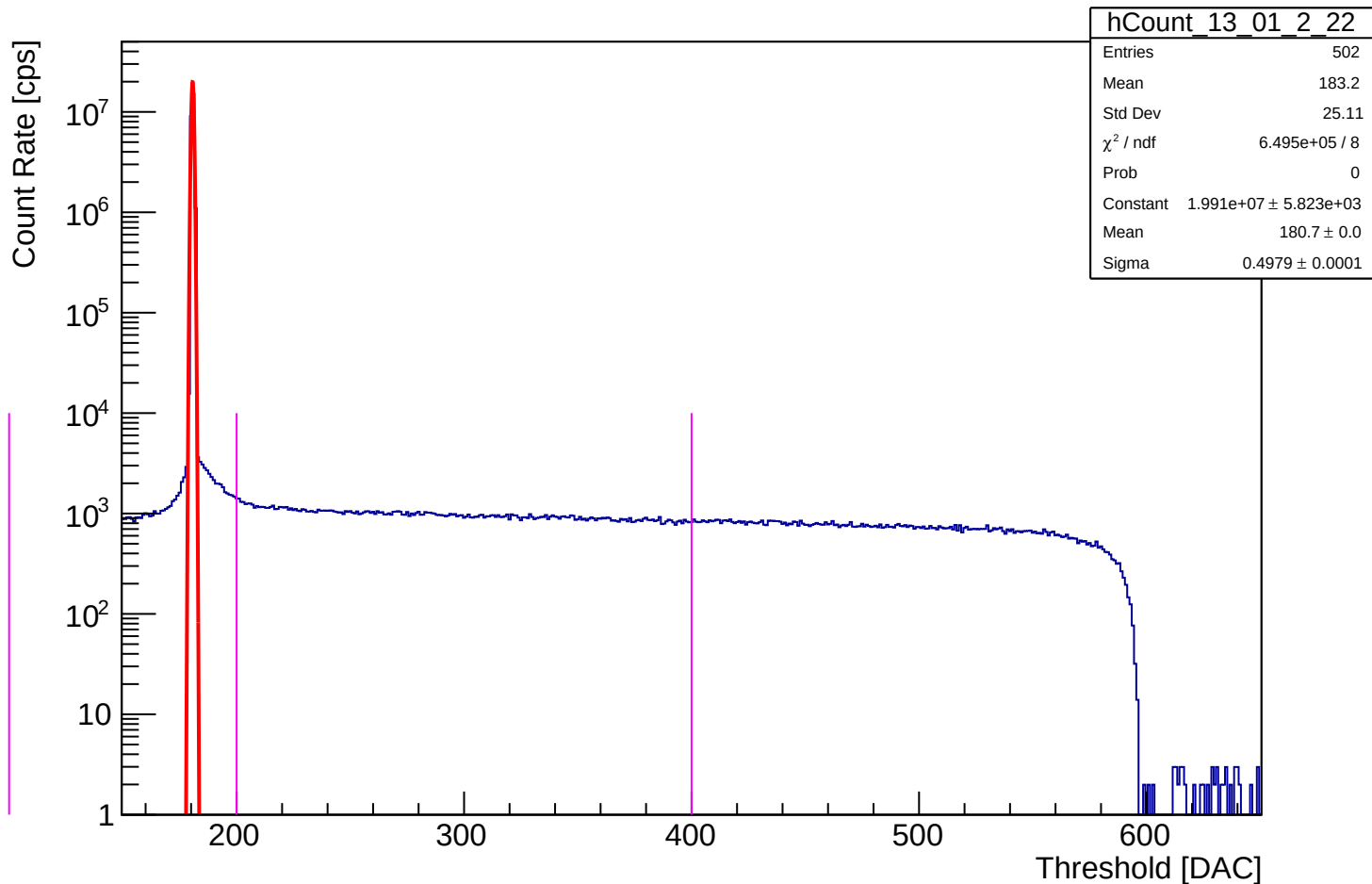
Row 1 Tile 1 Pmt 6 Pixel 17 Slot 13 Fiber 1 Asic 2 Channel 23



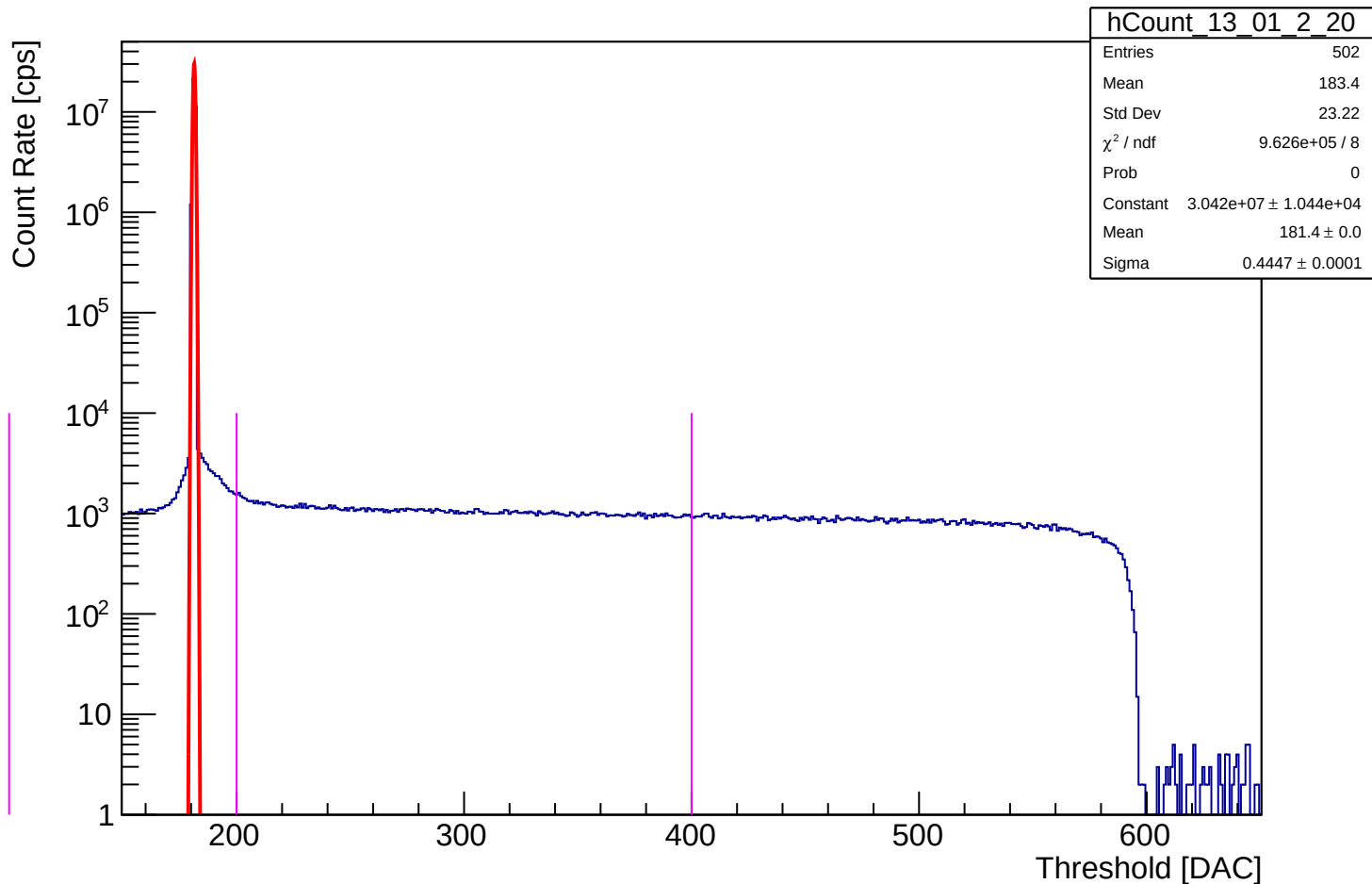
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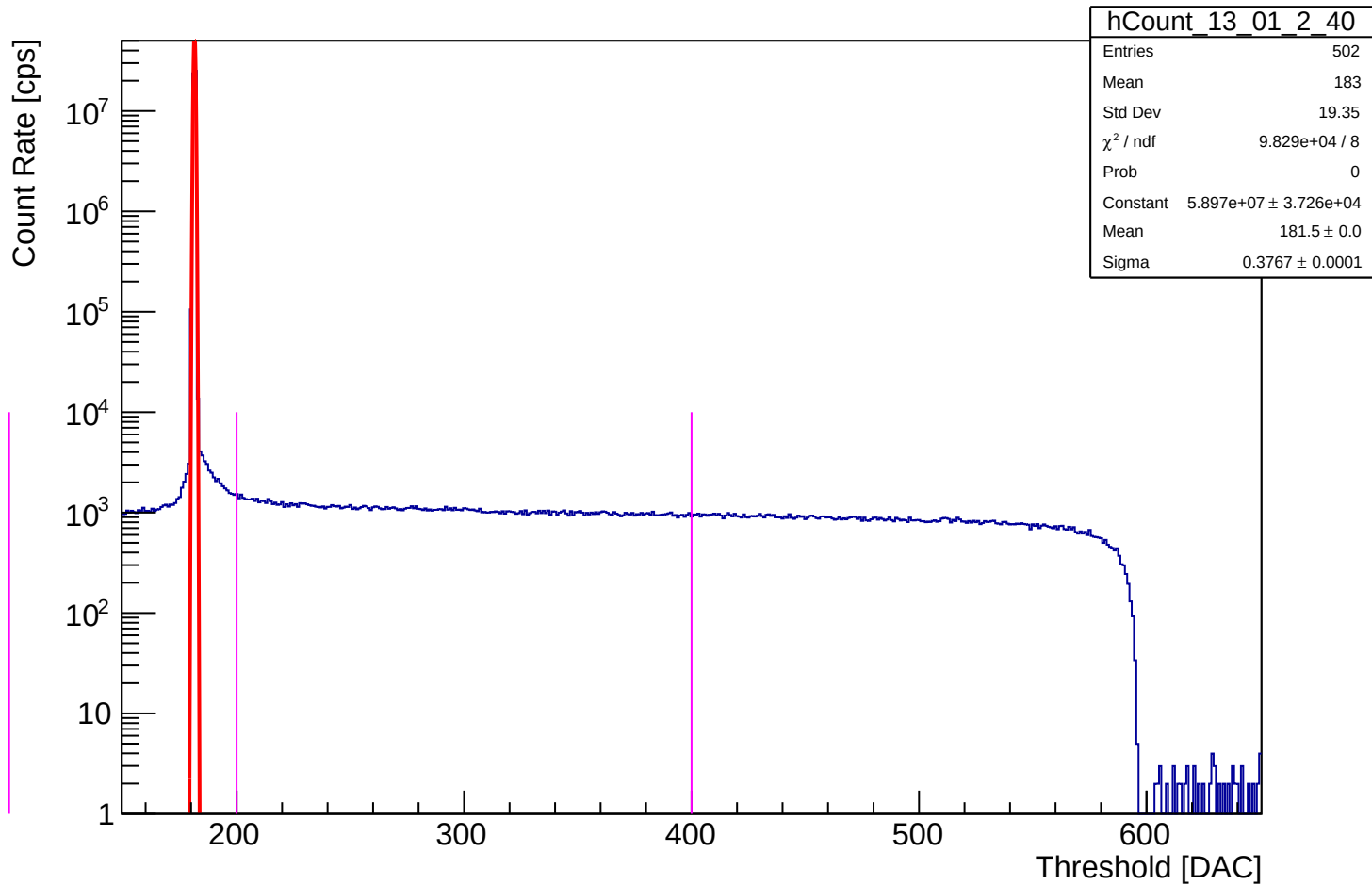
Row 1 Tile 1 Pmt 6 Pixel 19 Slot 13 Fiber 1 Asic 2 Channel 22



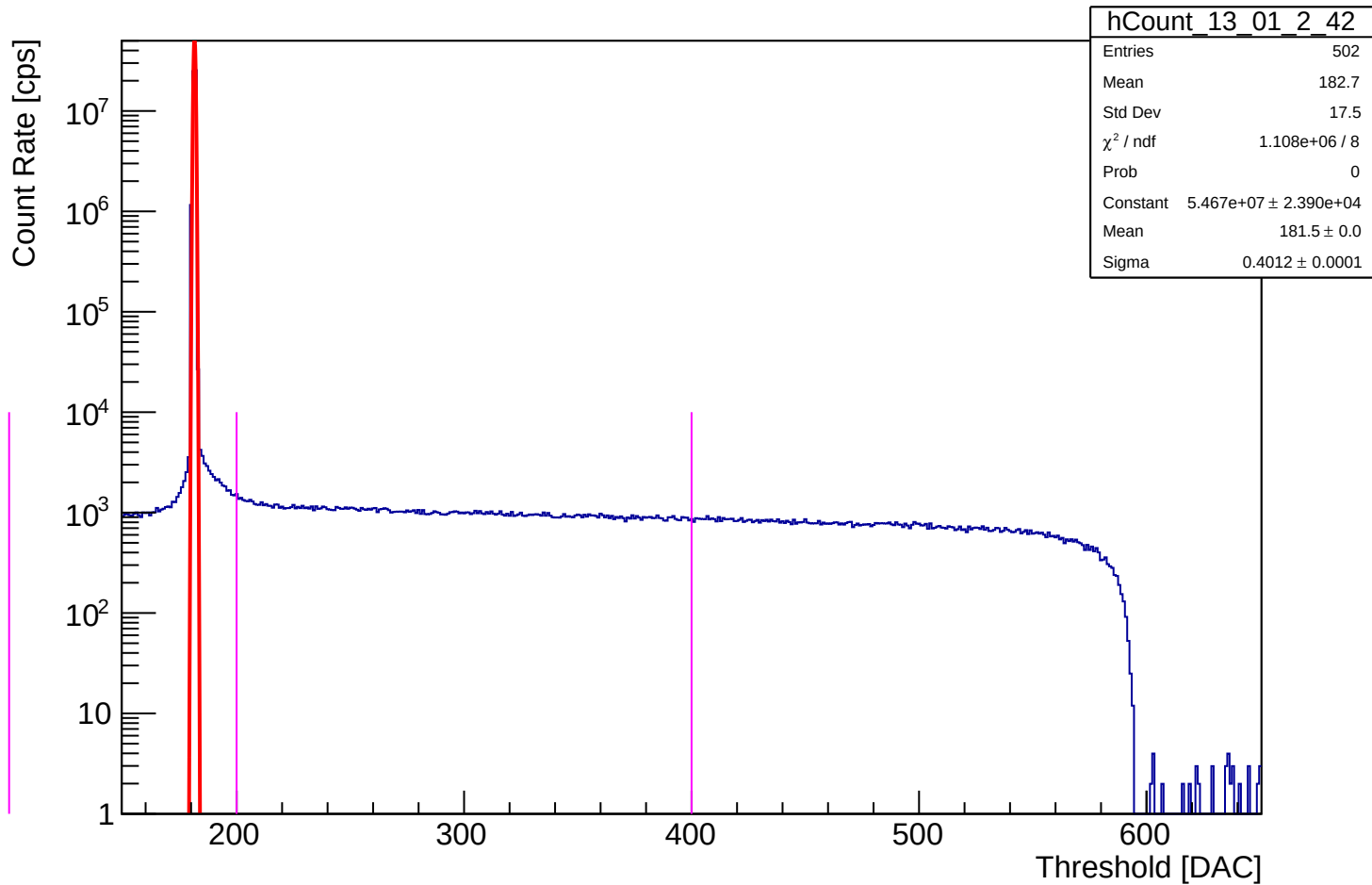
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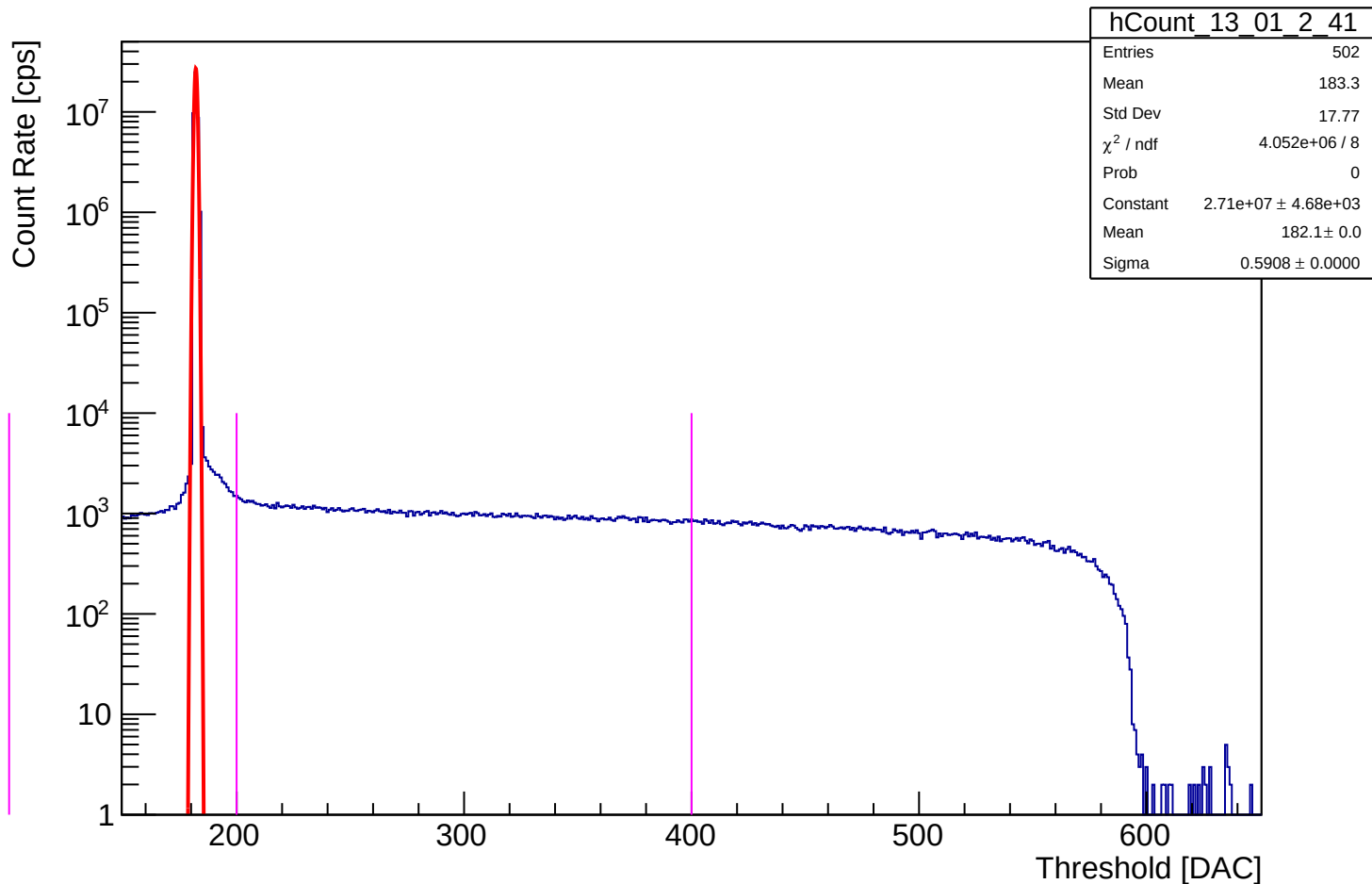
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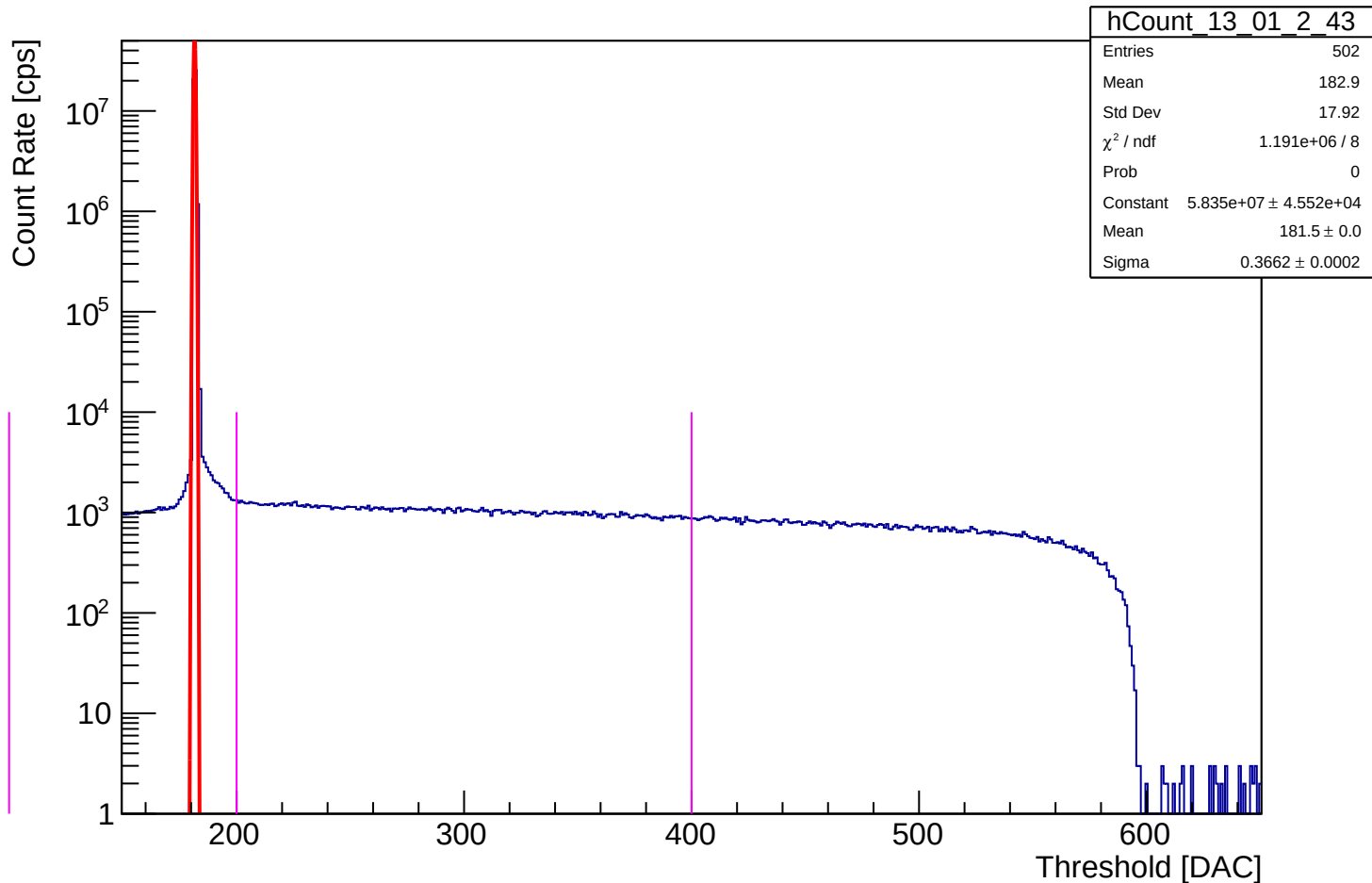
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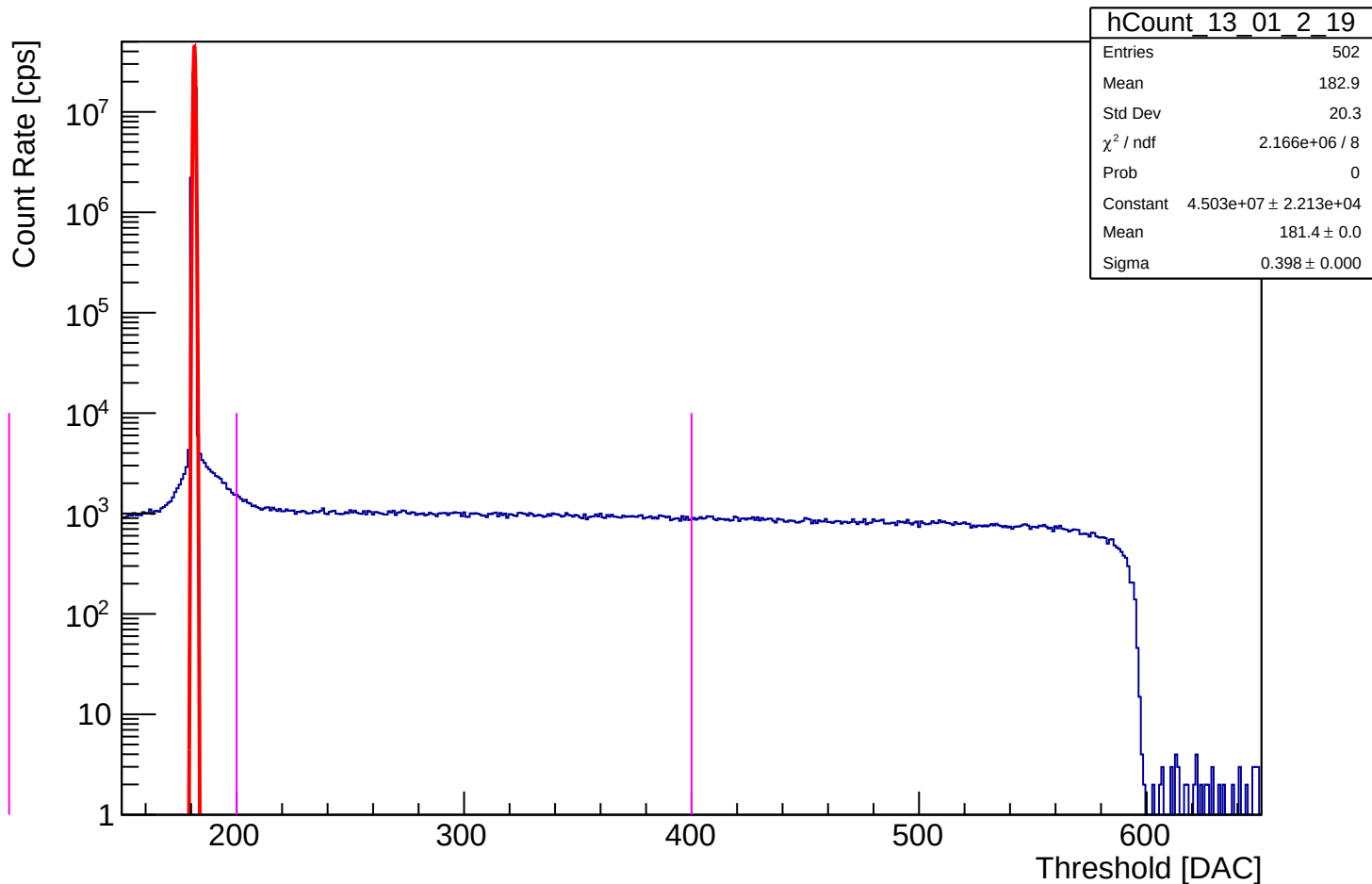
Row 1 Tile 1 Pmt 6 Pixel 23 Slot 13 Fiber 1 Asic 2 Channel 41

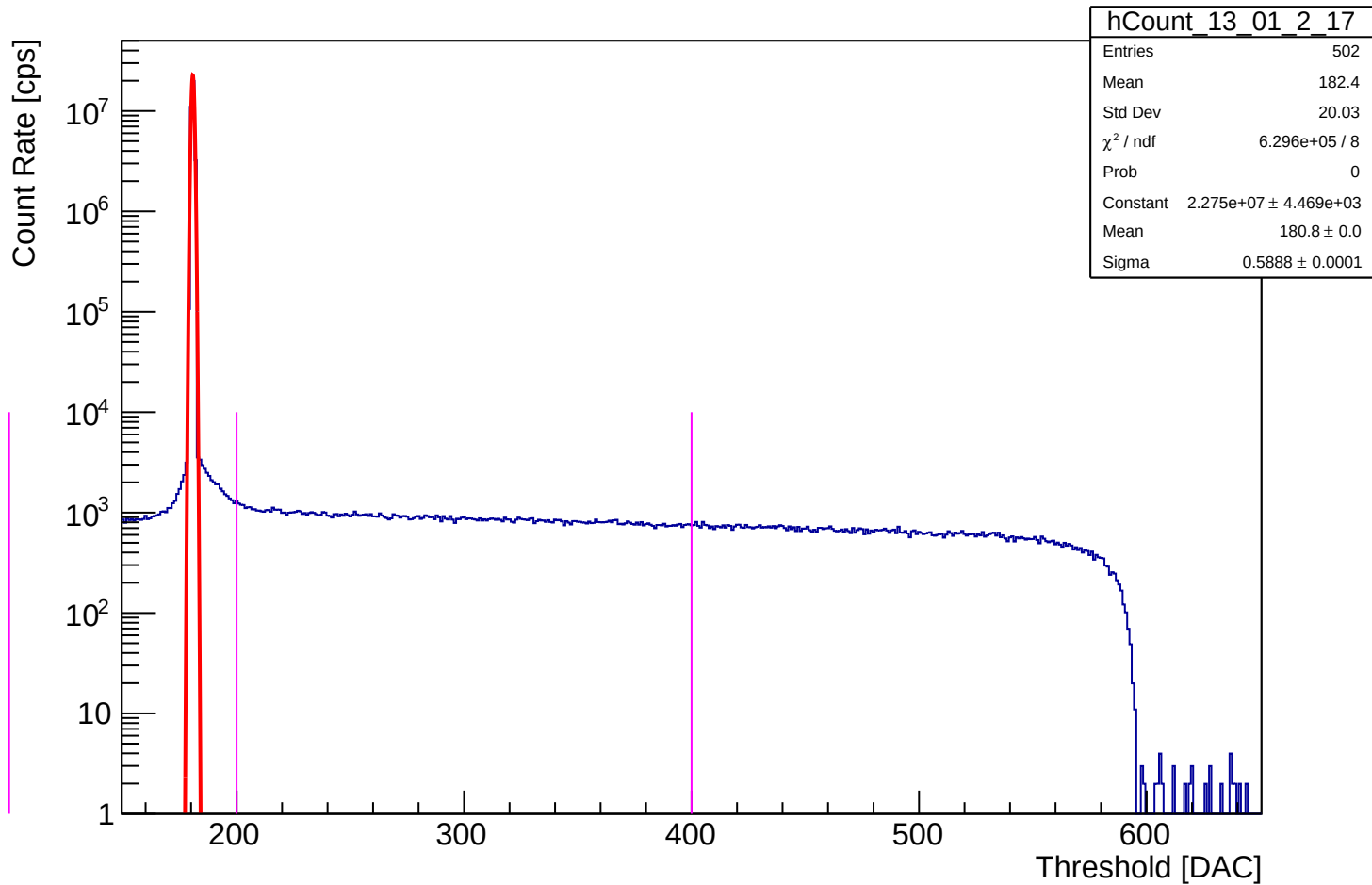


Row 1 Tile 1 Pmt 6 Pixel 24 Slot 13 Fiber 1 Asic 2 Channel 43

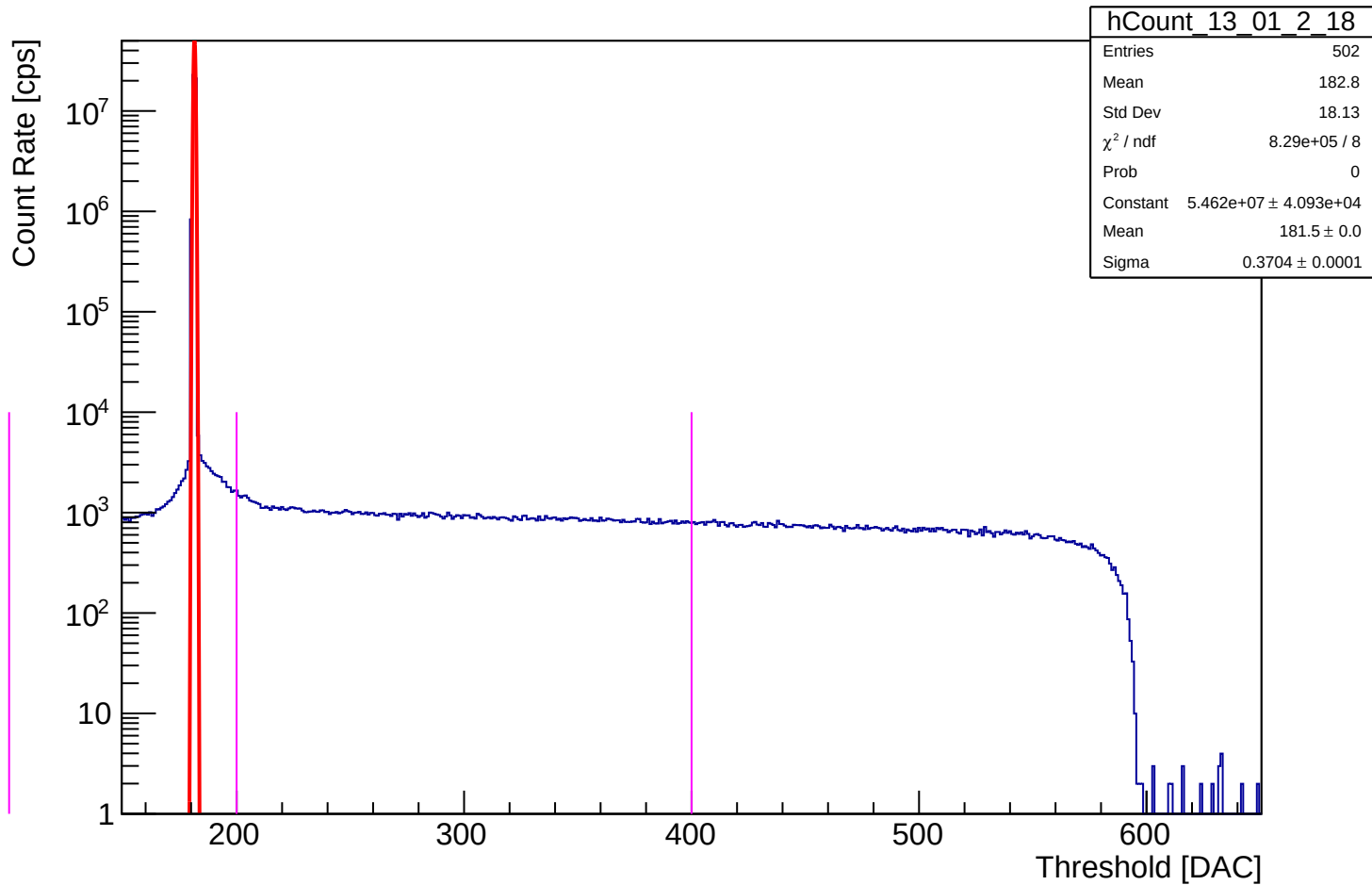


Row 1 Tile 1 Pmt 6 Pixel 25 Slot 13 Fiber 1 Asic 2 Channel 19

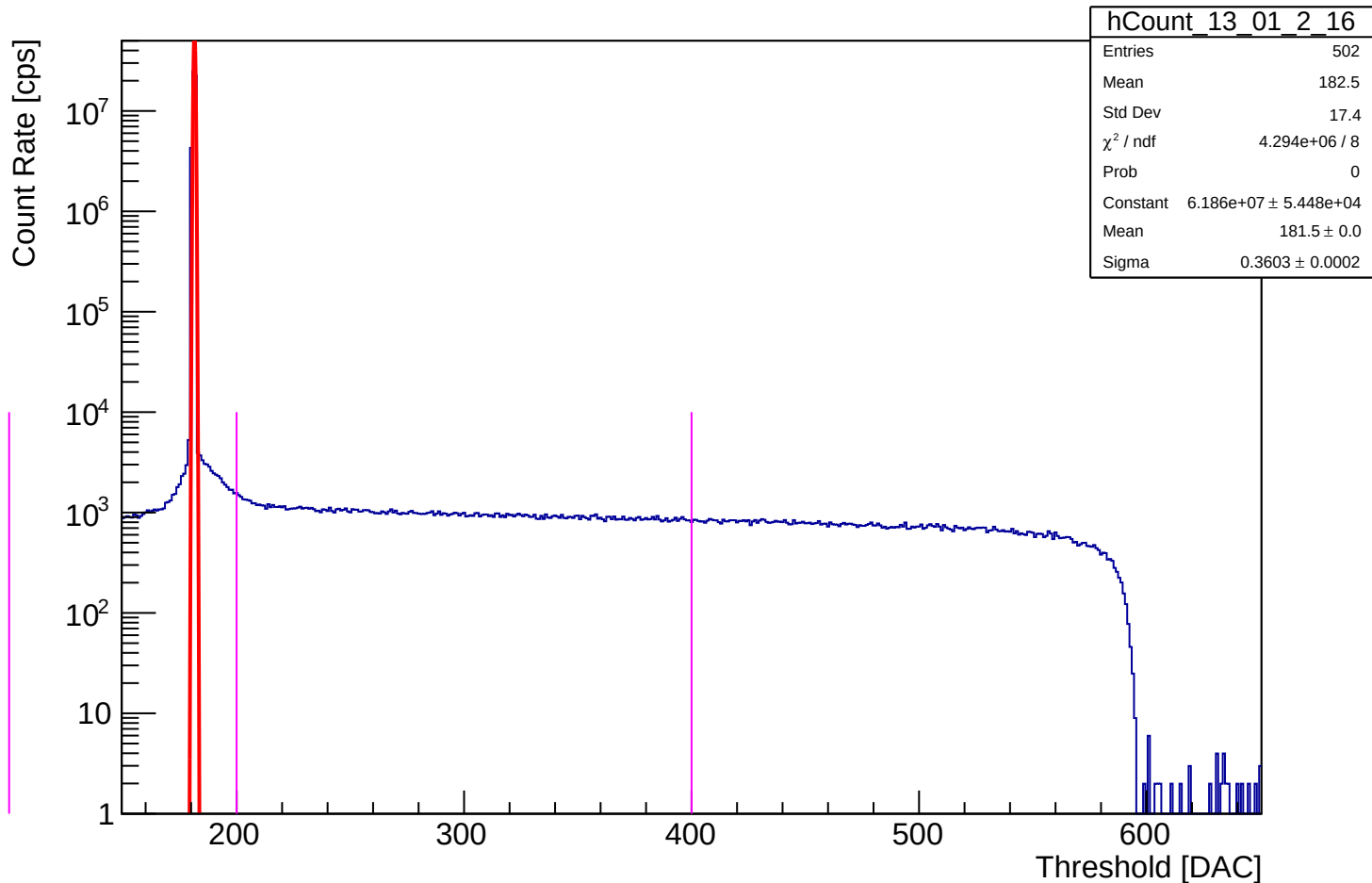




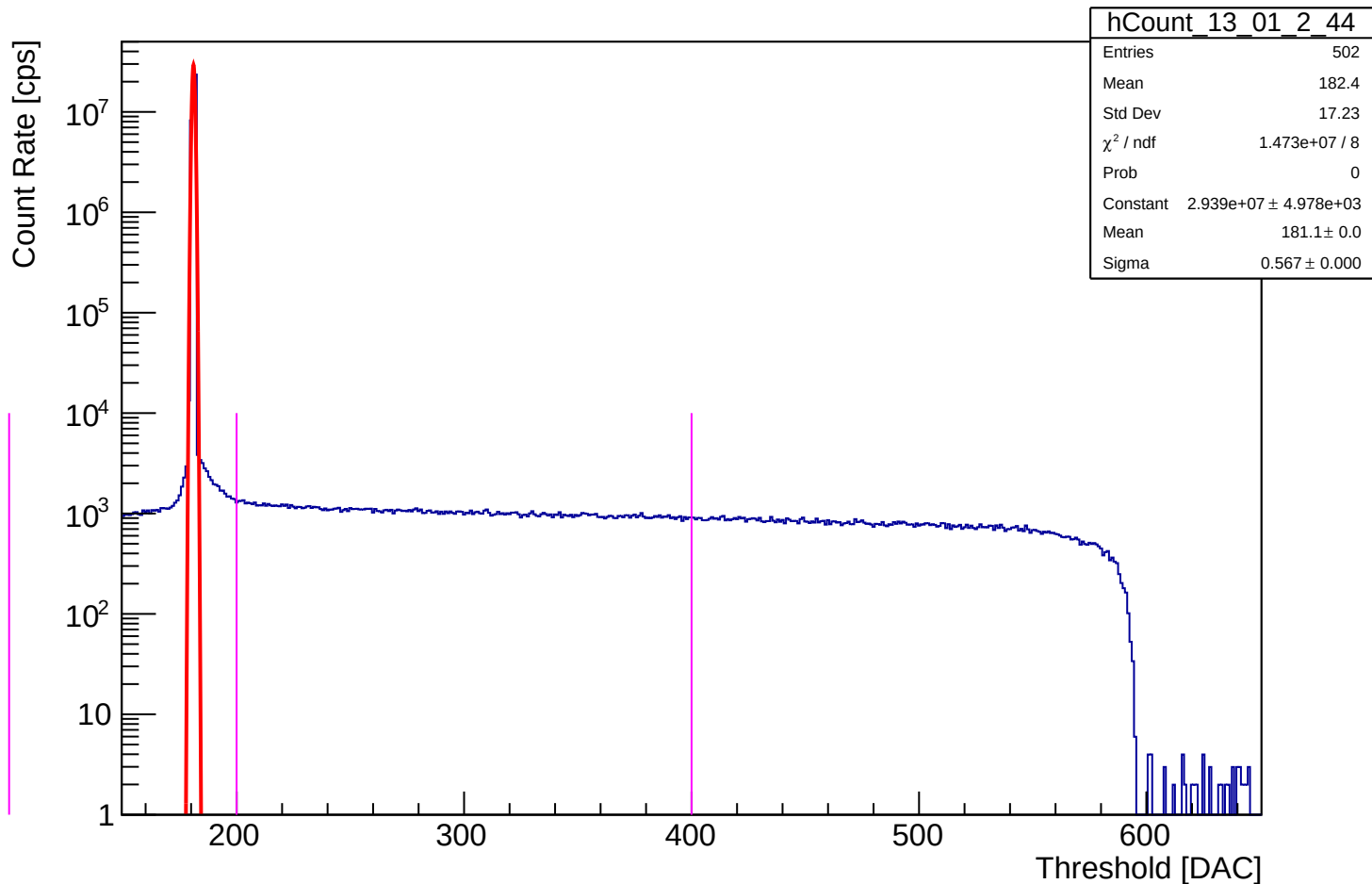
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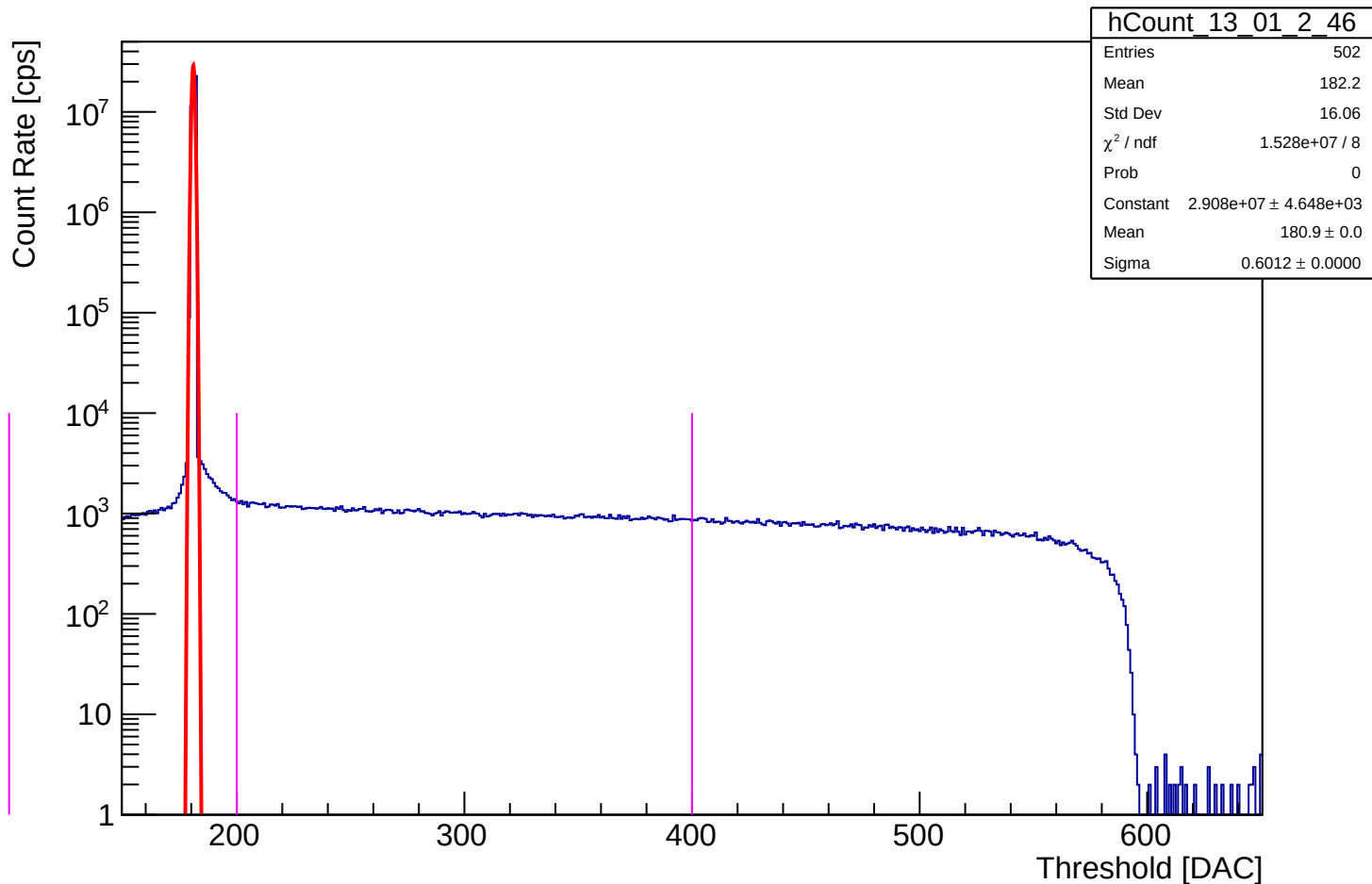


Row 1 Tile 1 Pmt 6 Pixel 28 Slot 13 Fiber 1 Asic 2 Channel 16

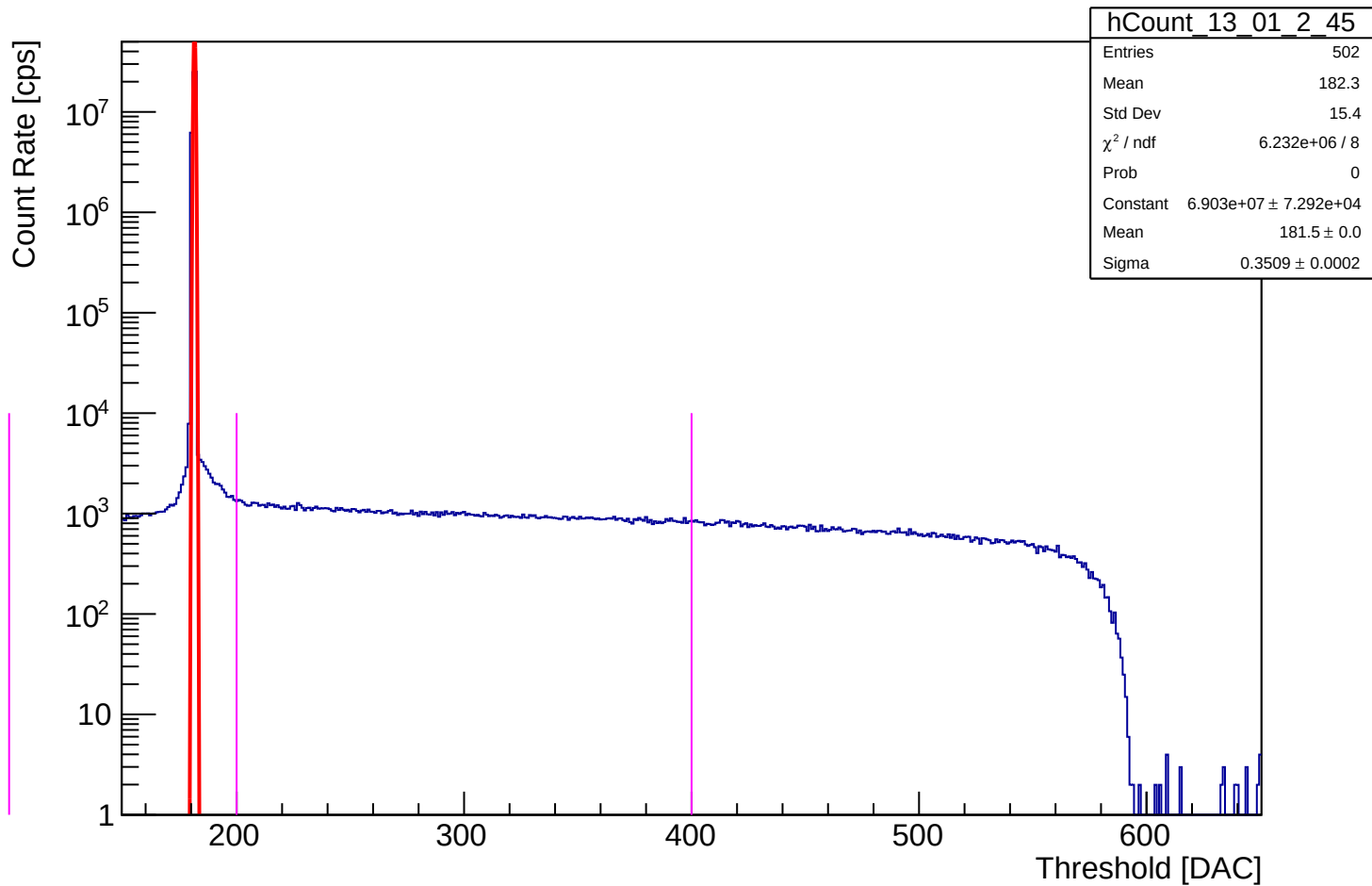


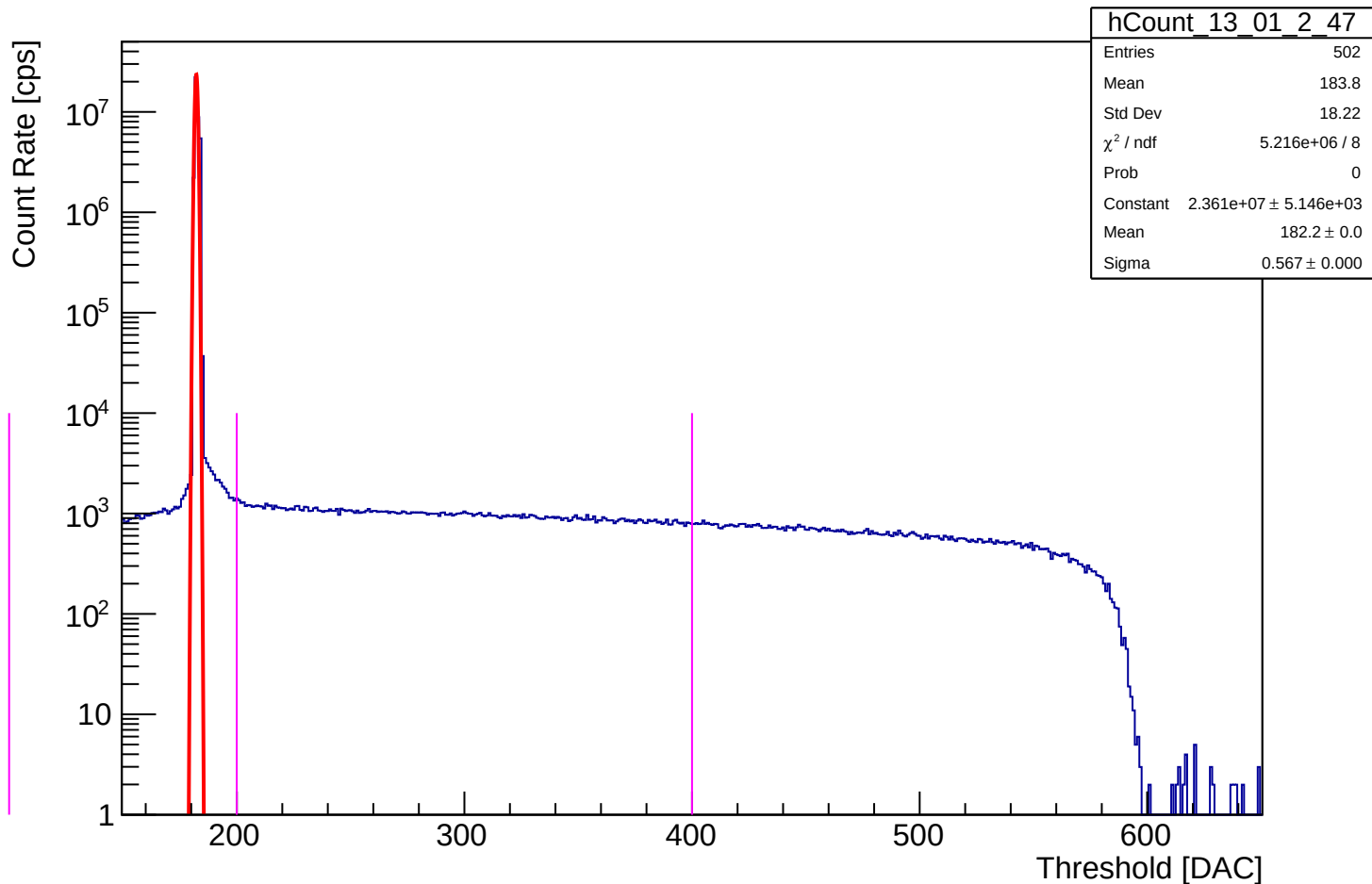
Row 1 Tile 1 Pmt 6 Pixel 29 Slot 13 Fiber 1 Asic 2 Channel 44



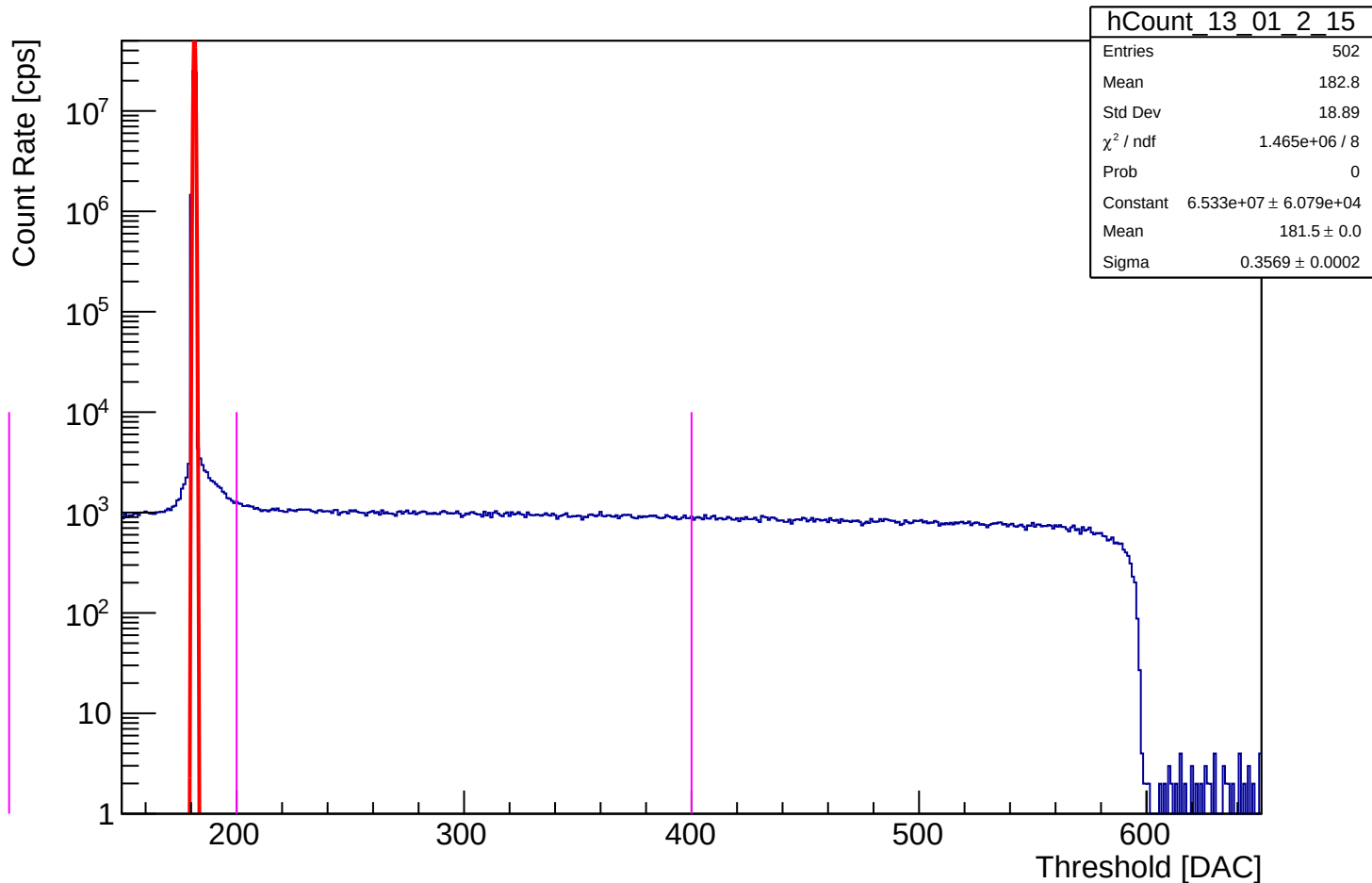


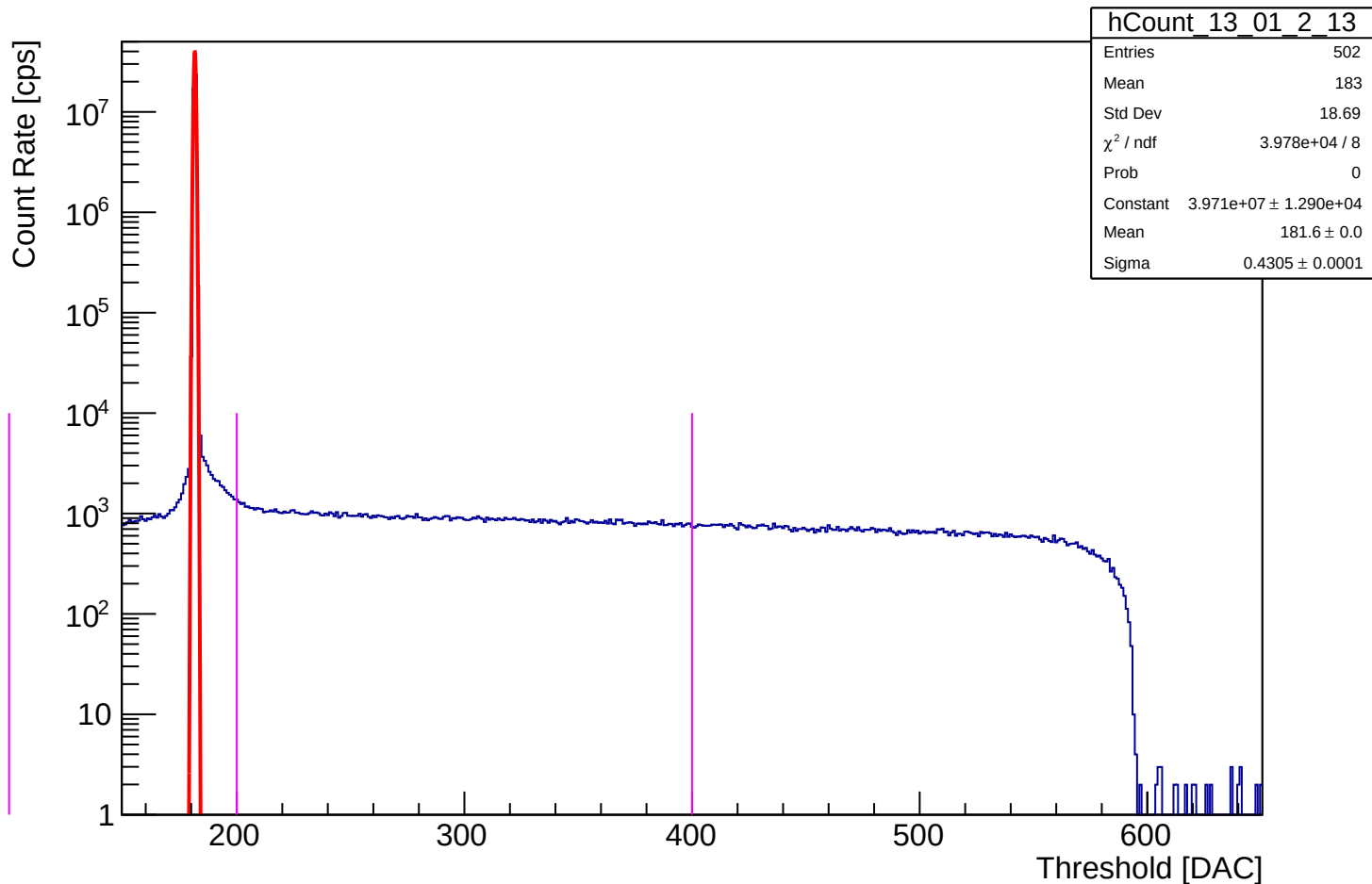
Row 1 Tile 1 Pmt 6 Pixel 31 Slot 13 Fiber 1 Asic 2 Channel 45



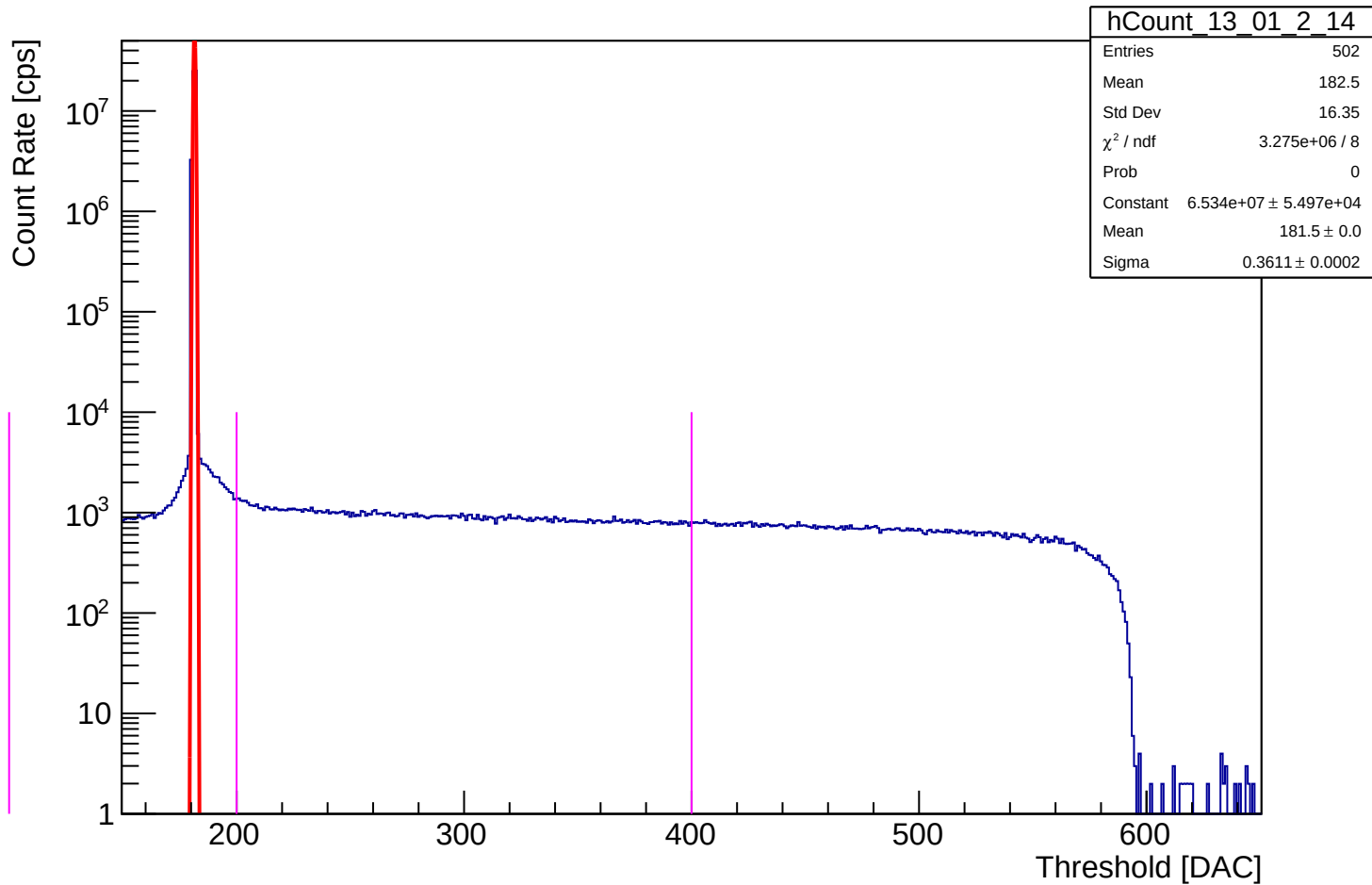


Row 1 Tile 1 Pmt 6 Pixel 33 Slot 13 Fiber 1 Asic 2 Channel 15

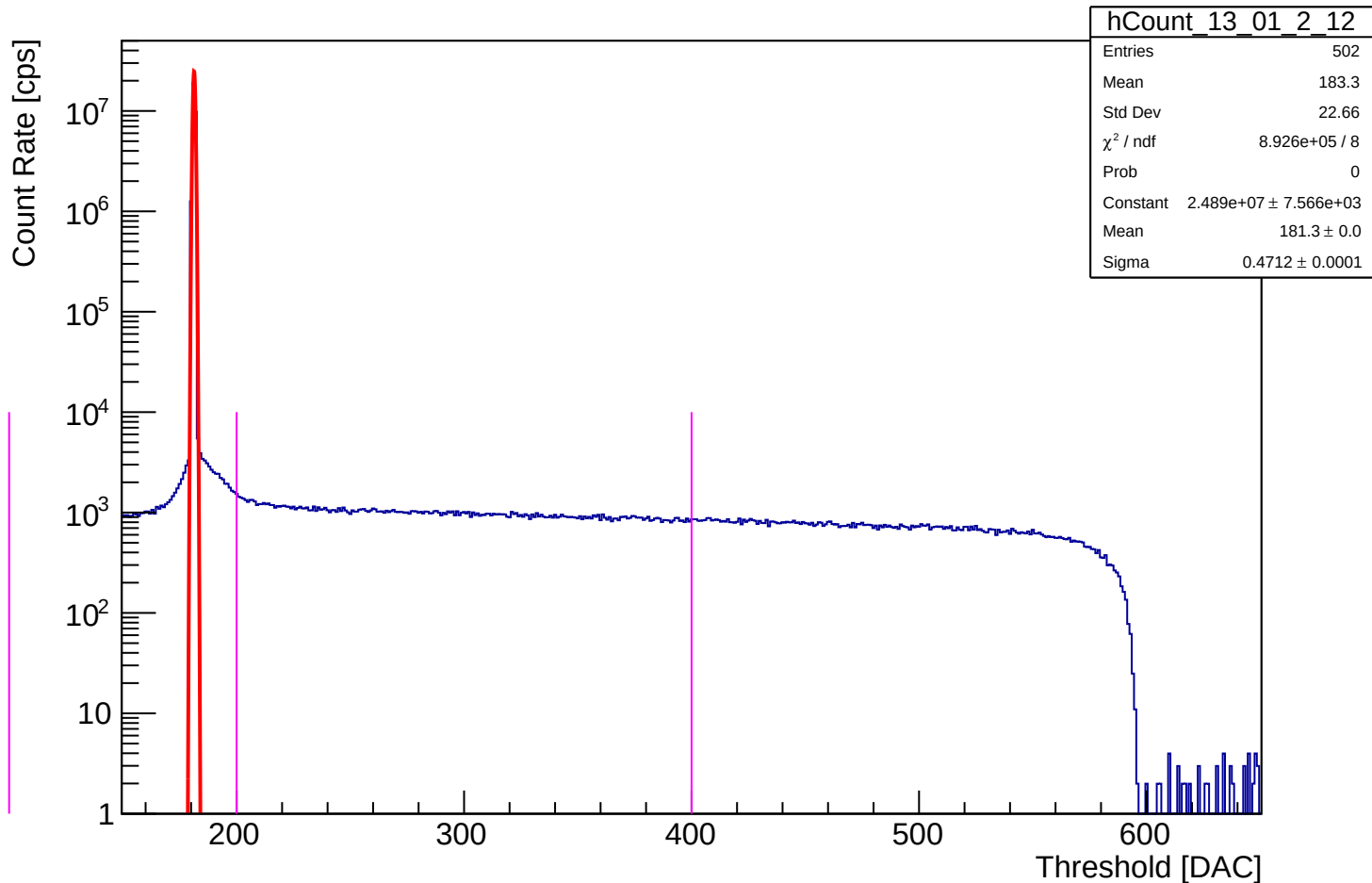




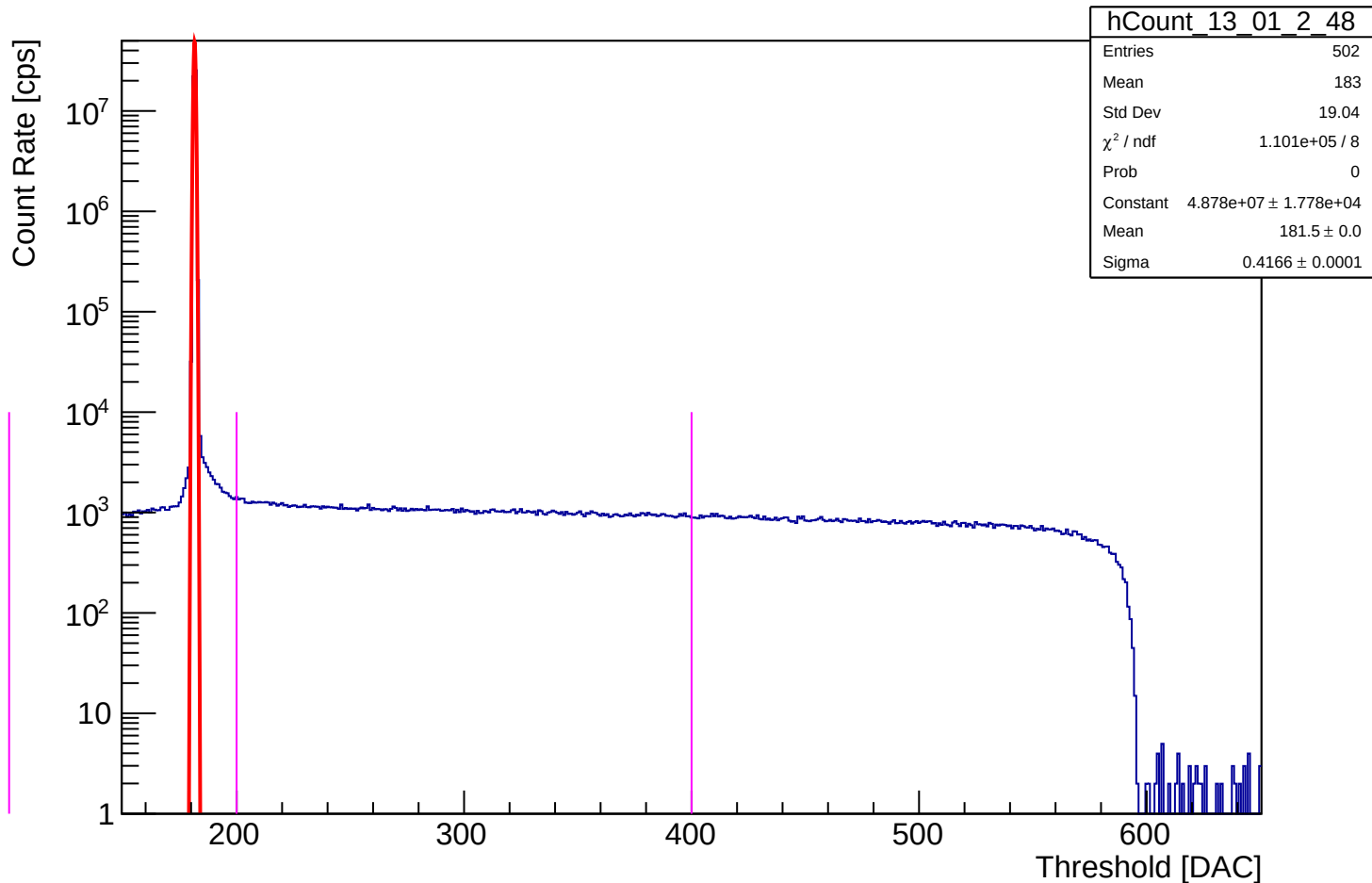
Row 1 Tile 1 Pmt 6 Pixel 35 Slot 13 Fiber 1 Asic 2 Channel 14



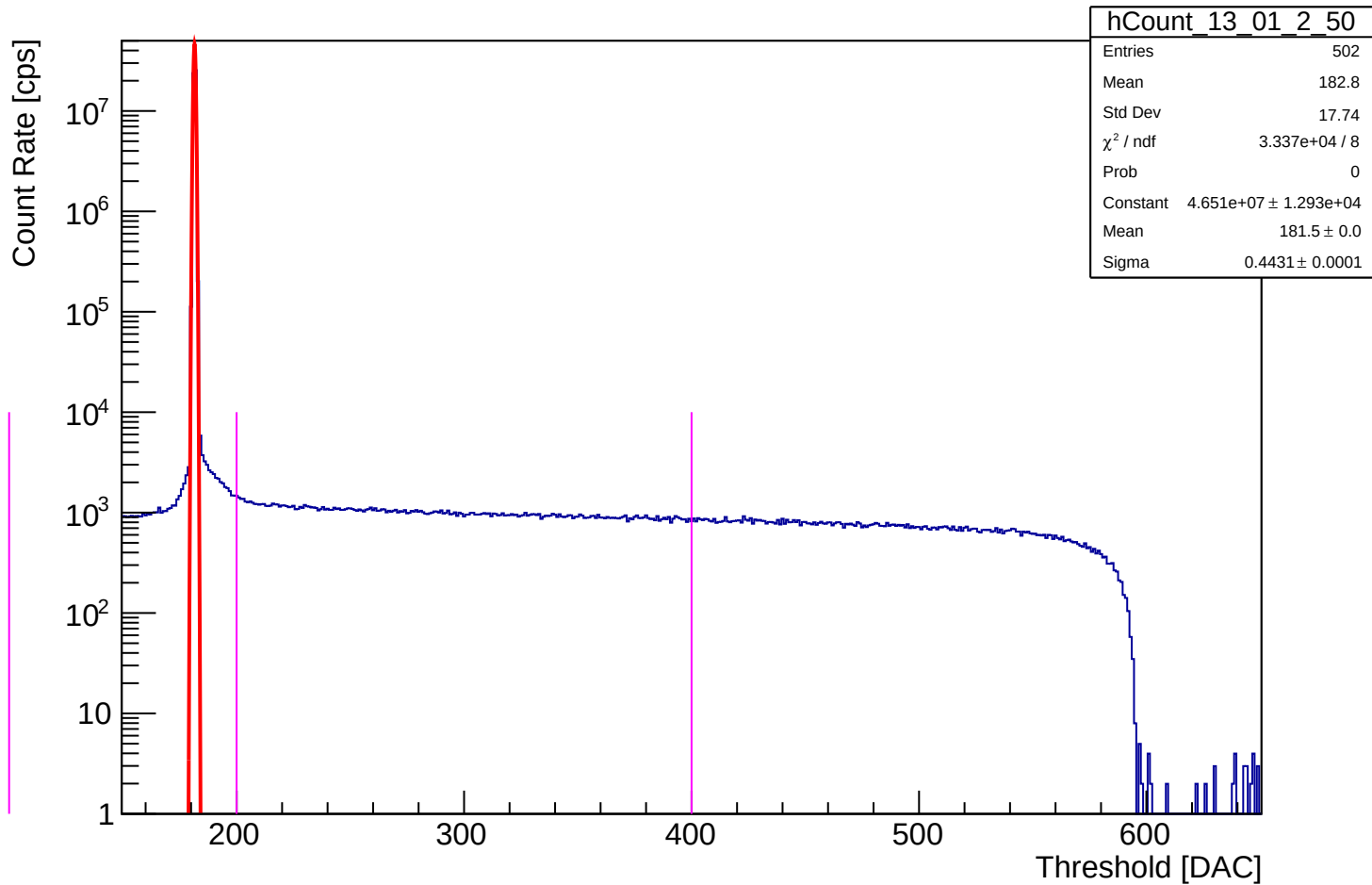
Row 1 Tile 1 Pmt 6 Pixel 36 Slot 13 Fiber 1 Asic 2 Channel 12



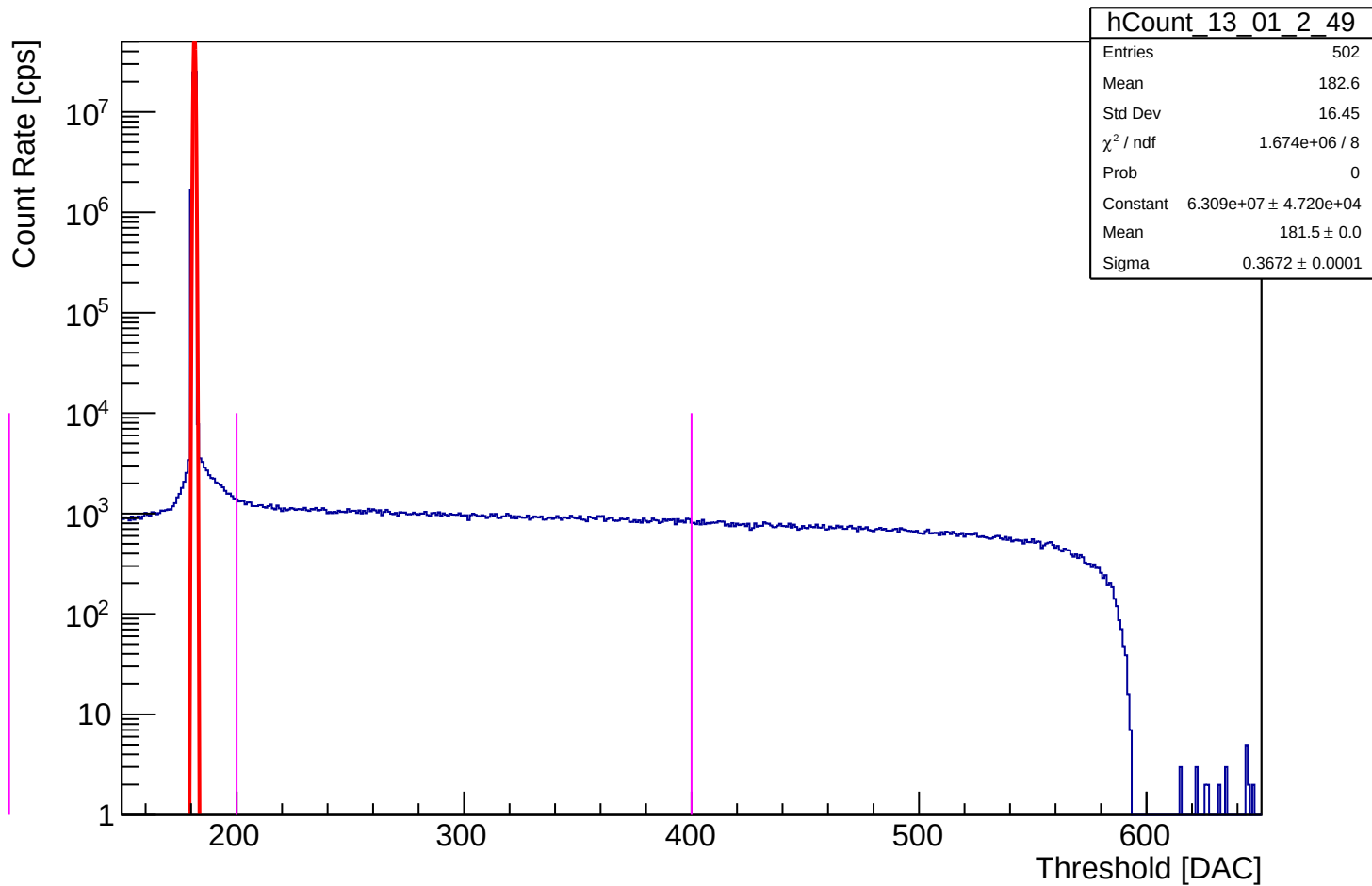
Row 1 Tile 1 Pmt 6 Pixel 37 Slot 13 Fiber 1 Asic 2 Channel 48

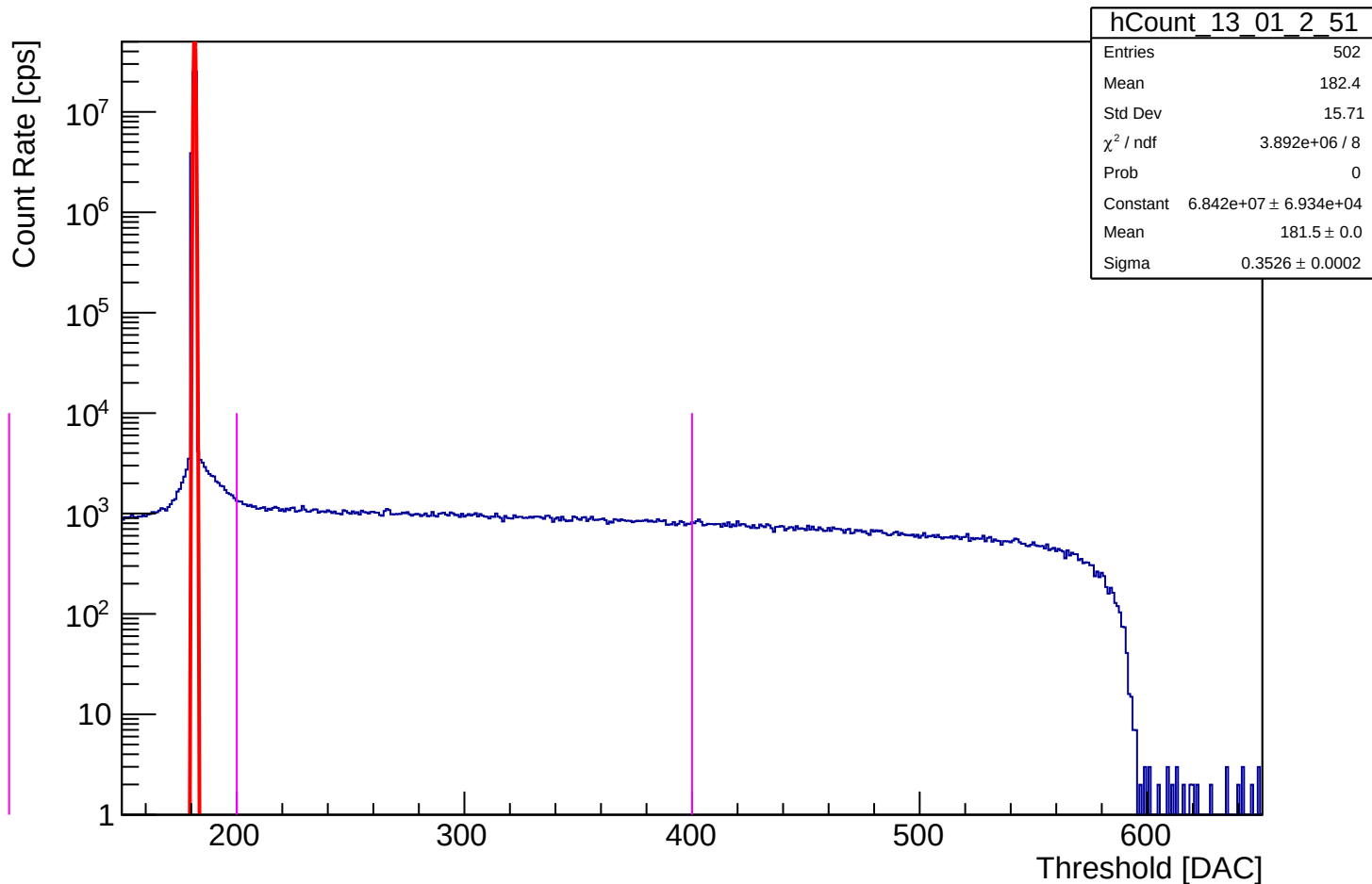


Row 1 Tile 1 Pmt 6 Pixel 38 Slot 13 Fiber 1 Asic 2 Channel 50

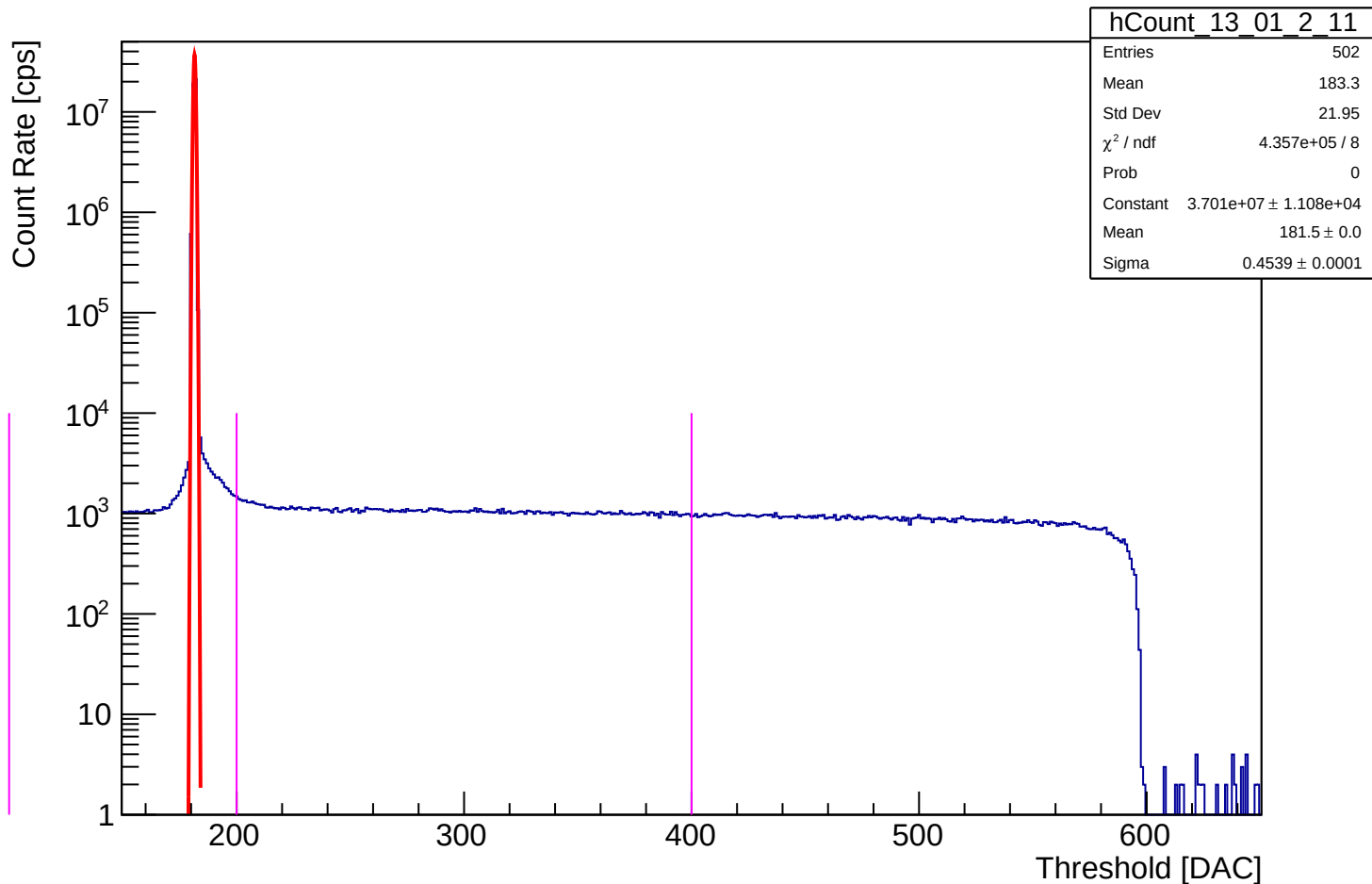


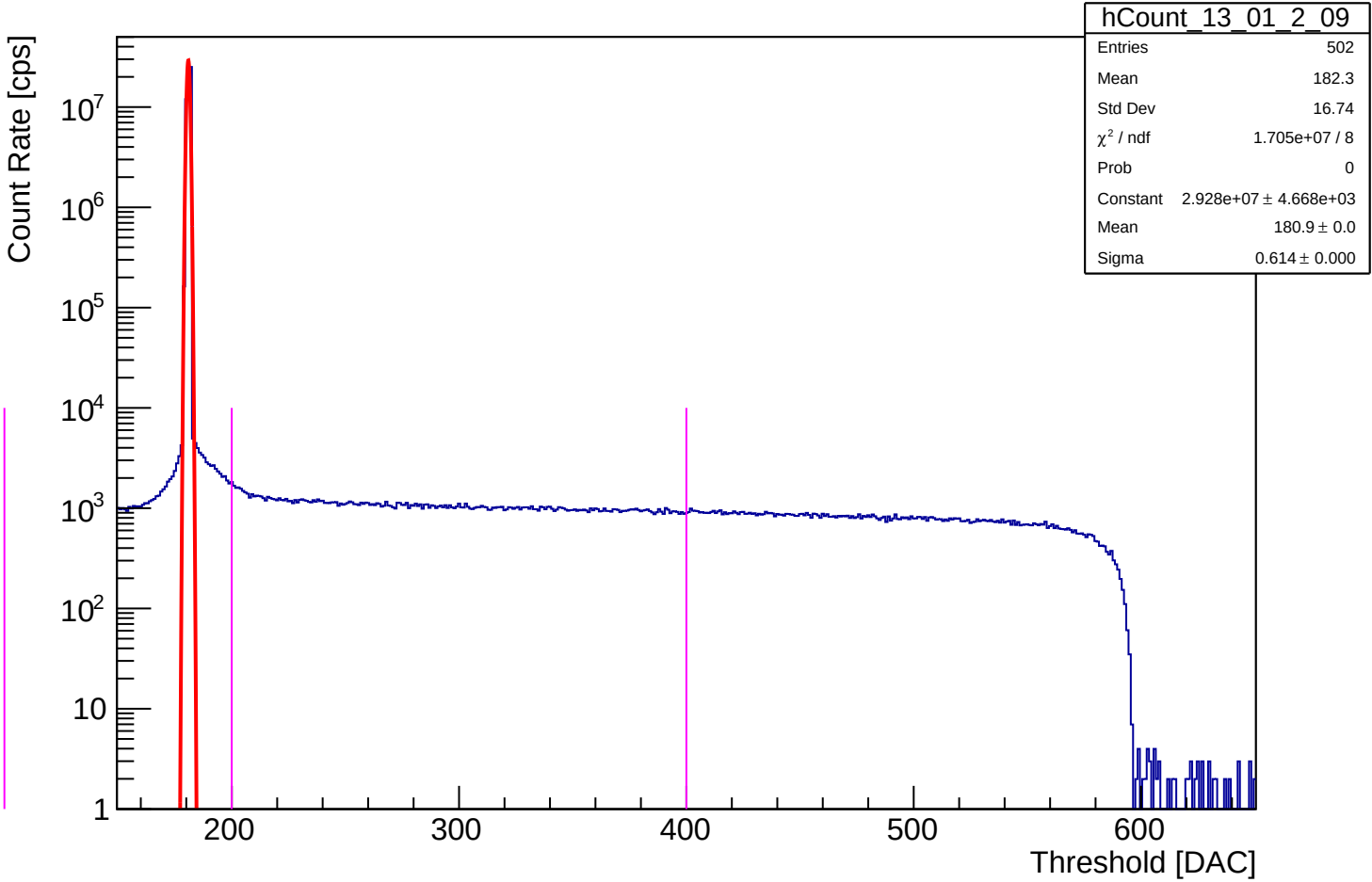
Row 1 Tile 1 Pmt 6 Pixel 39 Slot 13 Fiber 1 Asic 2 Channel 49

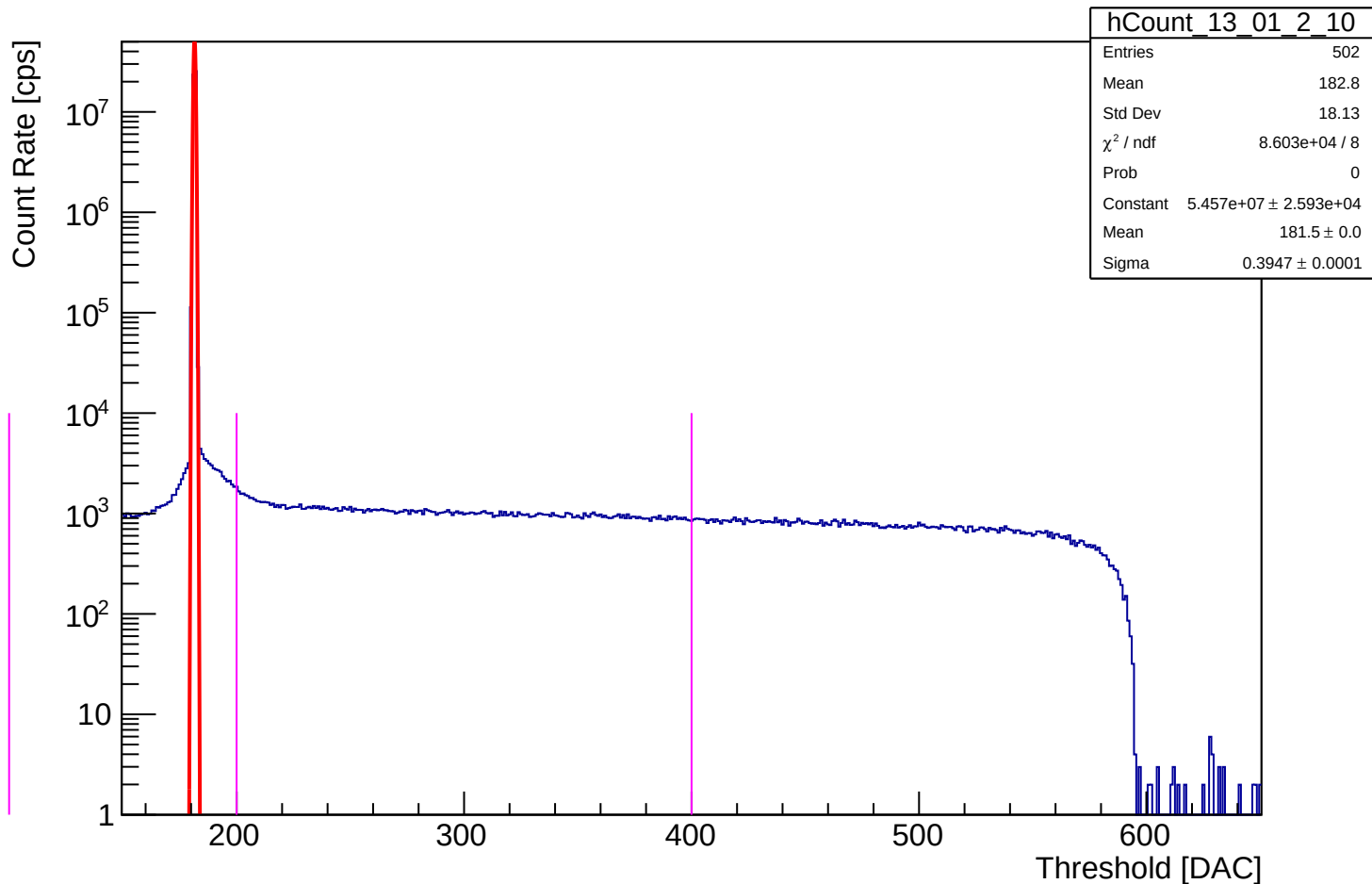


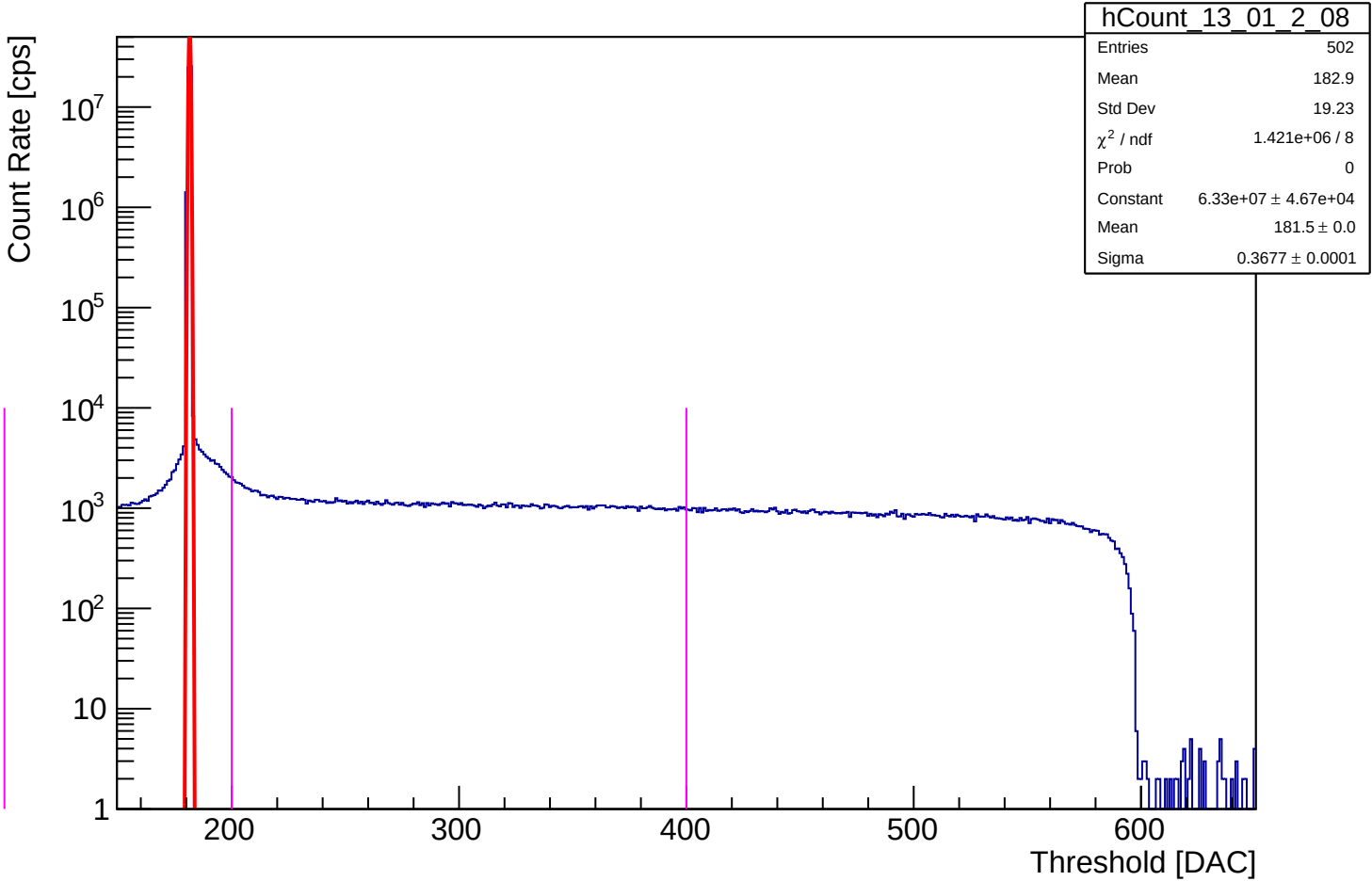


Row 1 Tile 1 Pmt 6 Pixel 41 Slot 13 Fiber 1 Asic 2 Channel 11

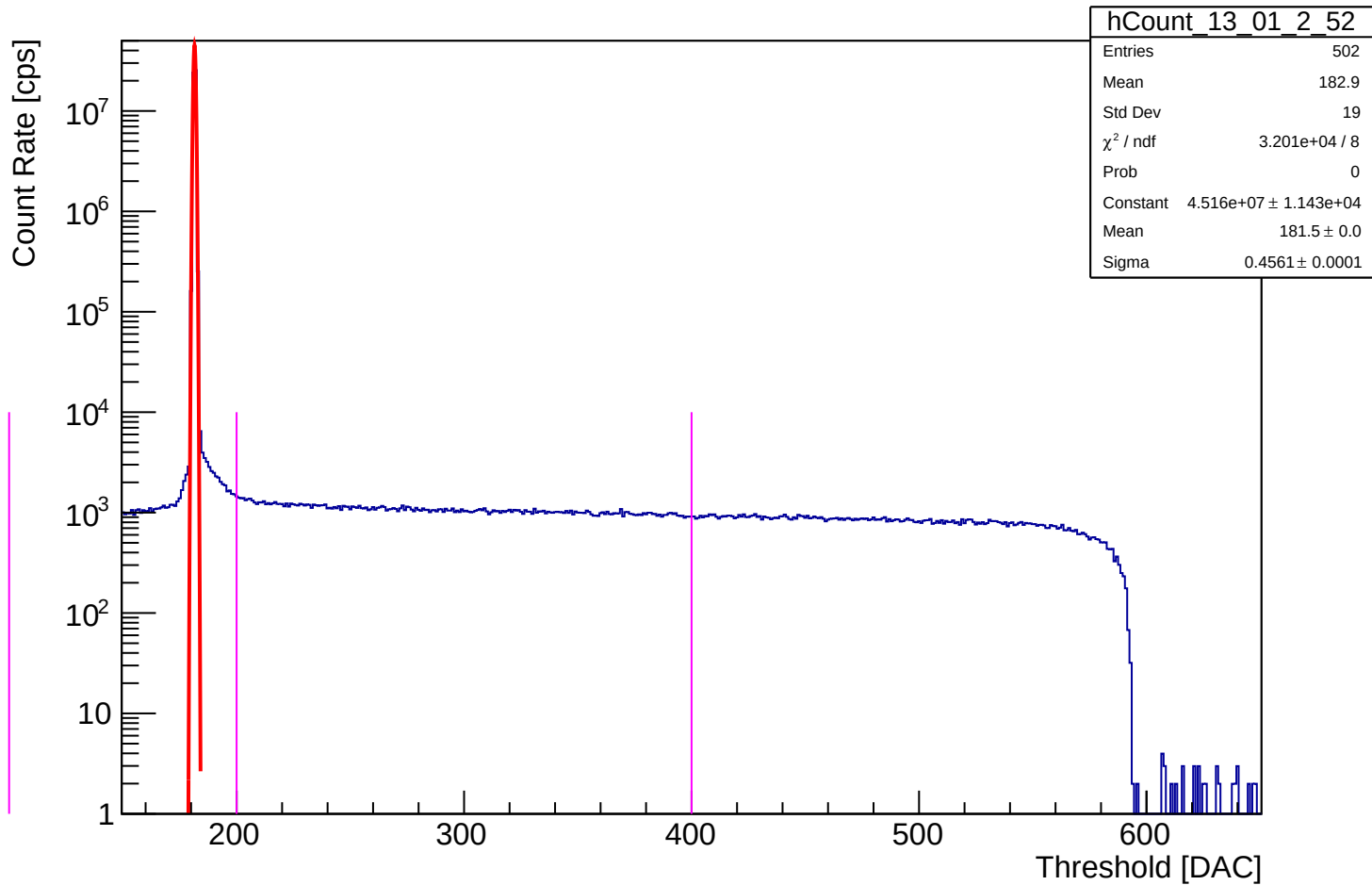




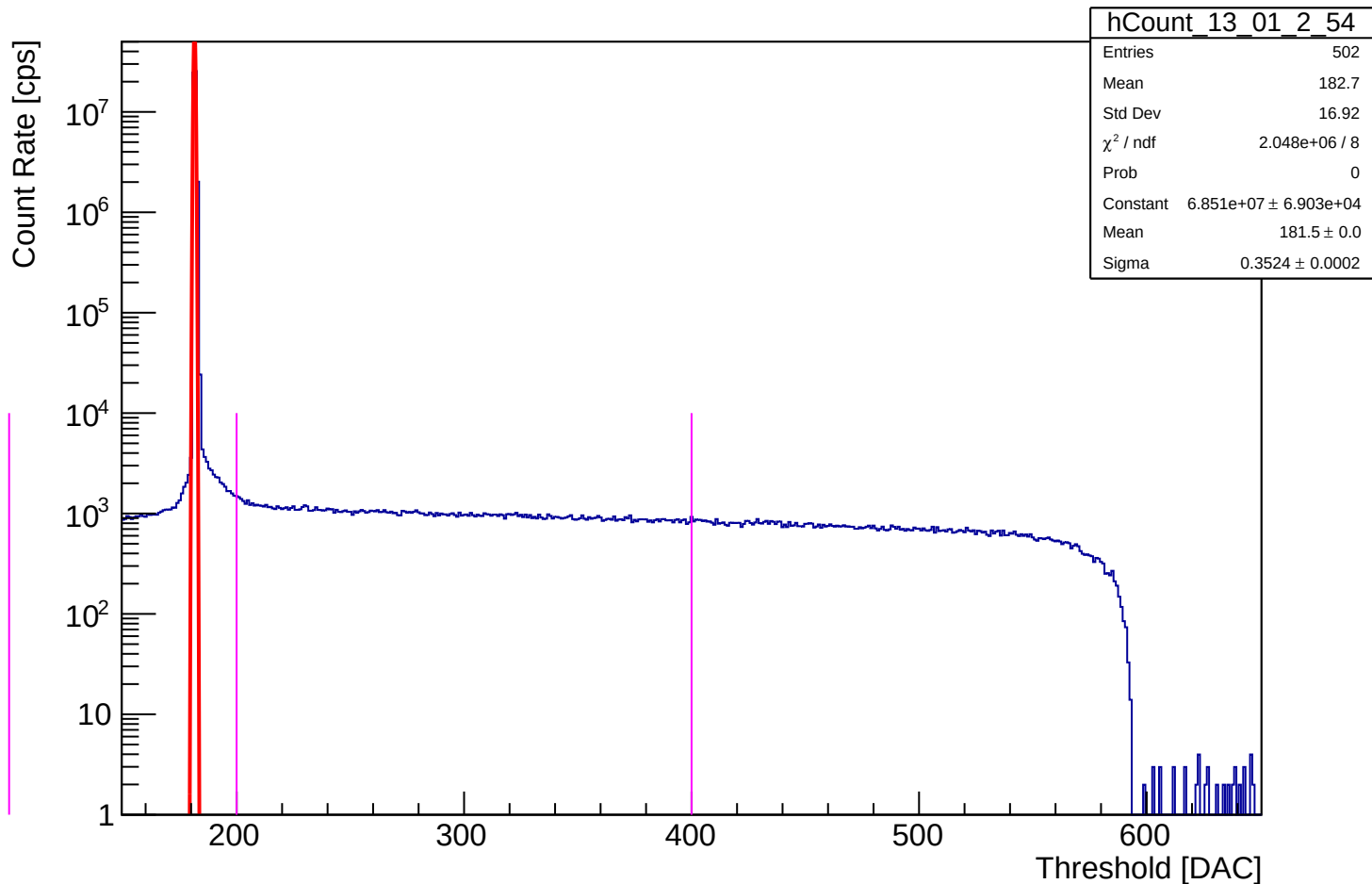




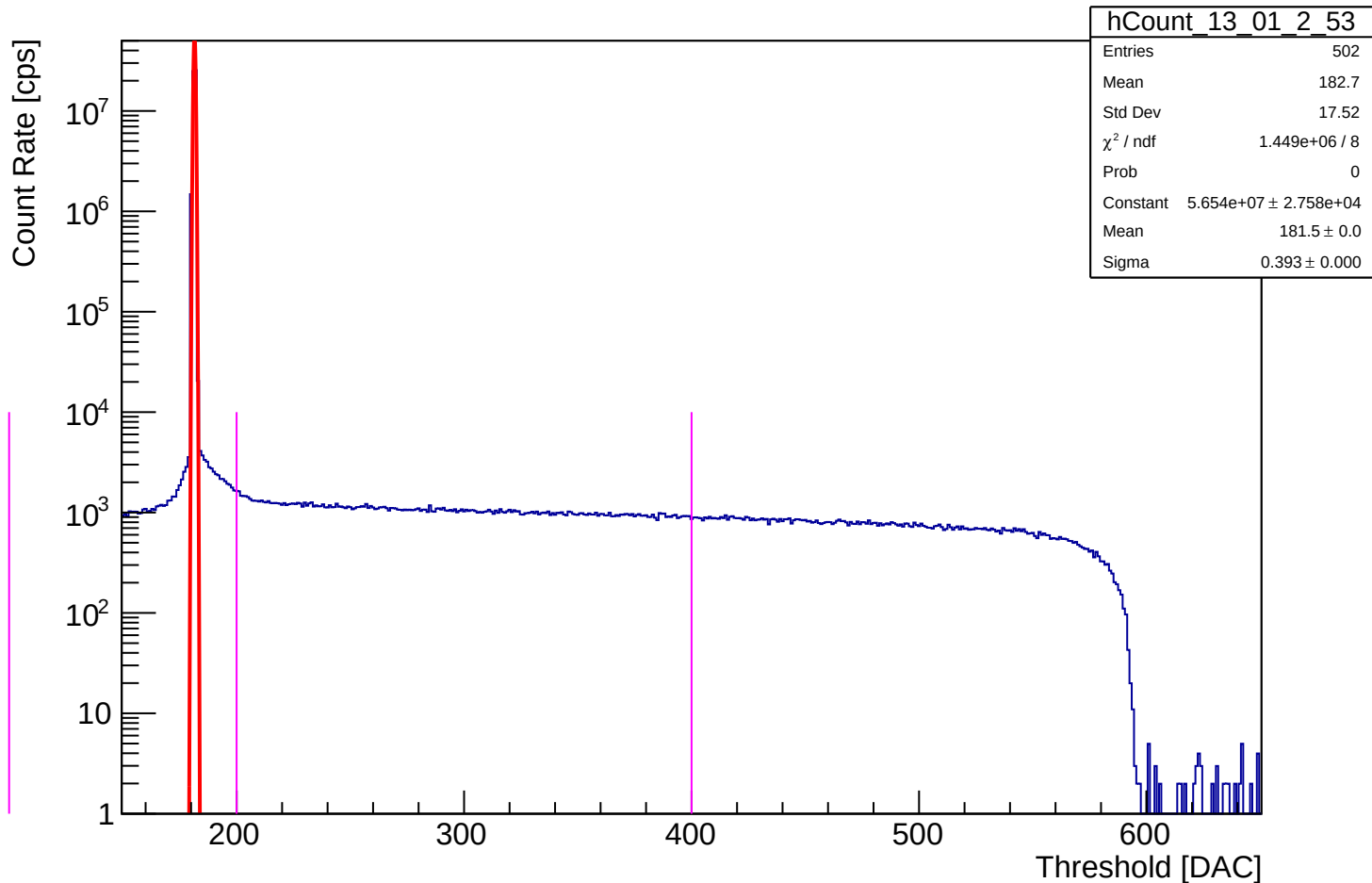
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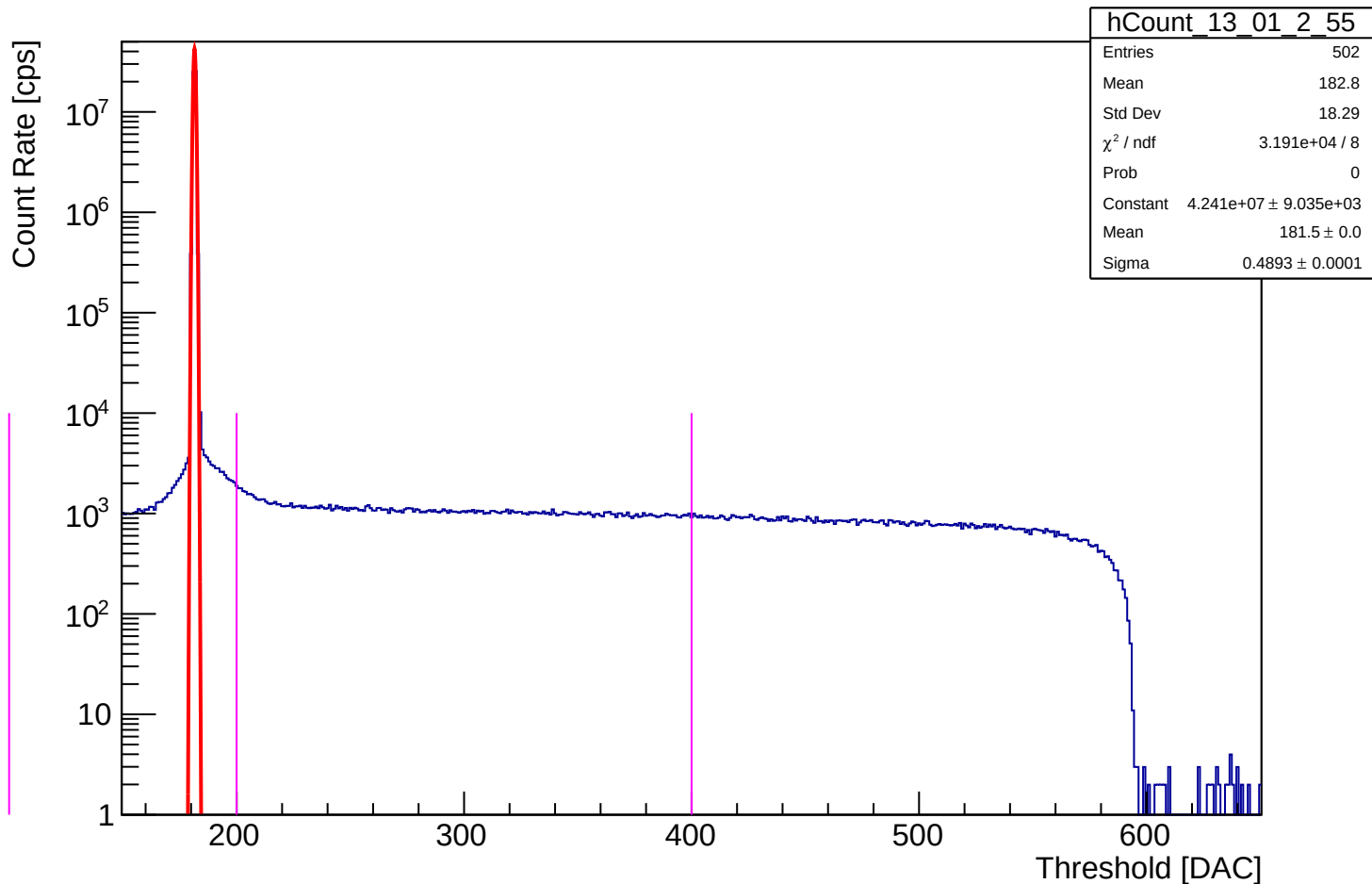


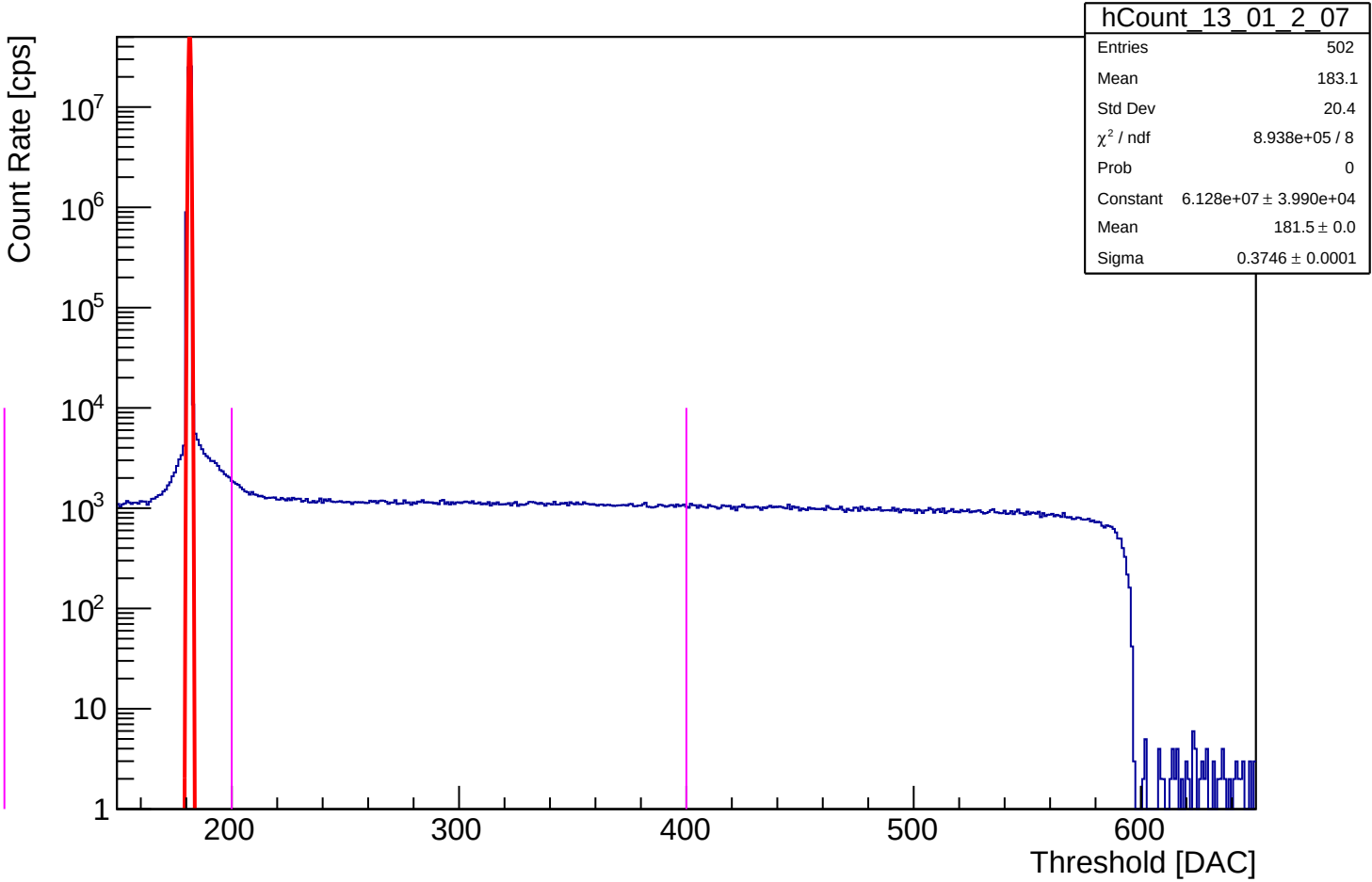
Row 1 Tile 1 Pmt 6 Pixel 46 Slot 13 Fiber 1 Asic 2 Channel 54



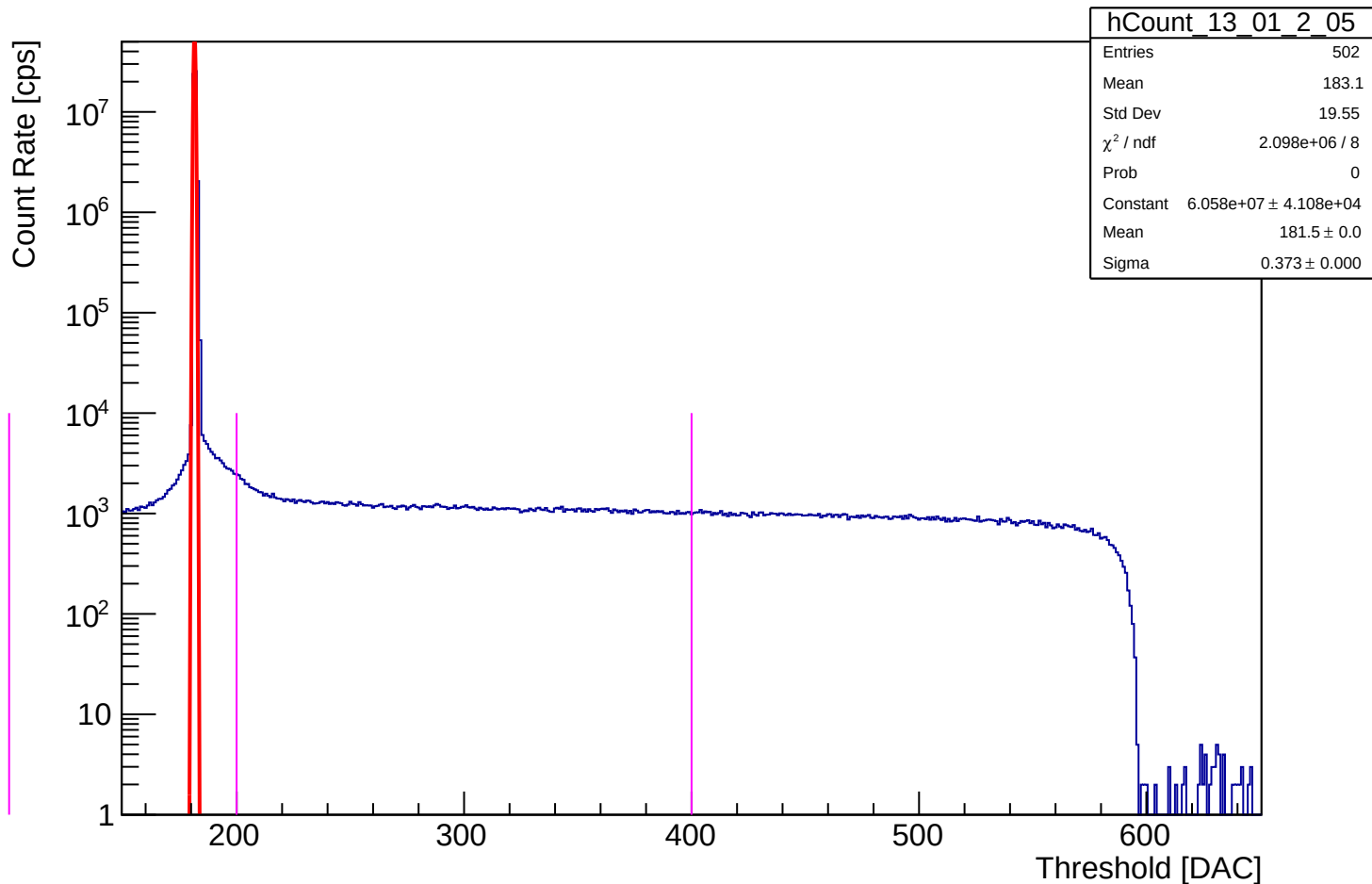
Row 1 Tile 1 Pmt 6 Pixel 47 Slot 13 Fiber 1 Asic 2 Channel 53

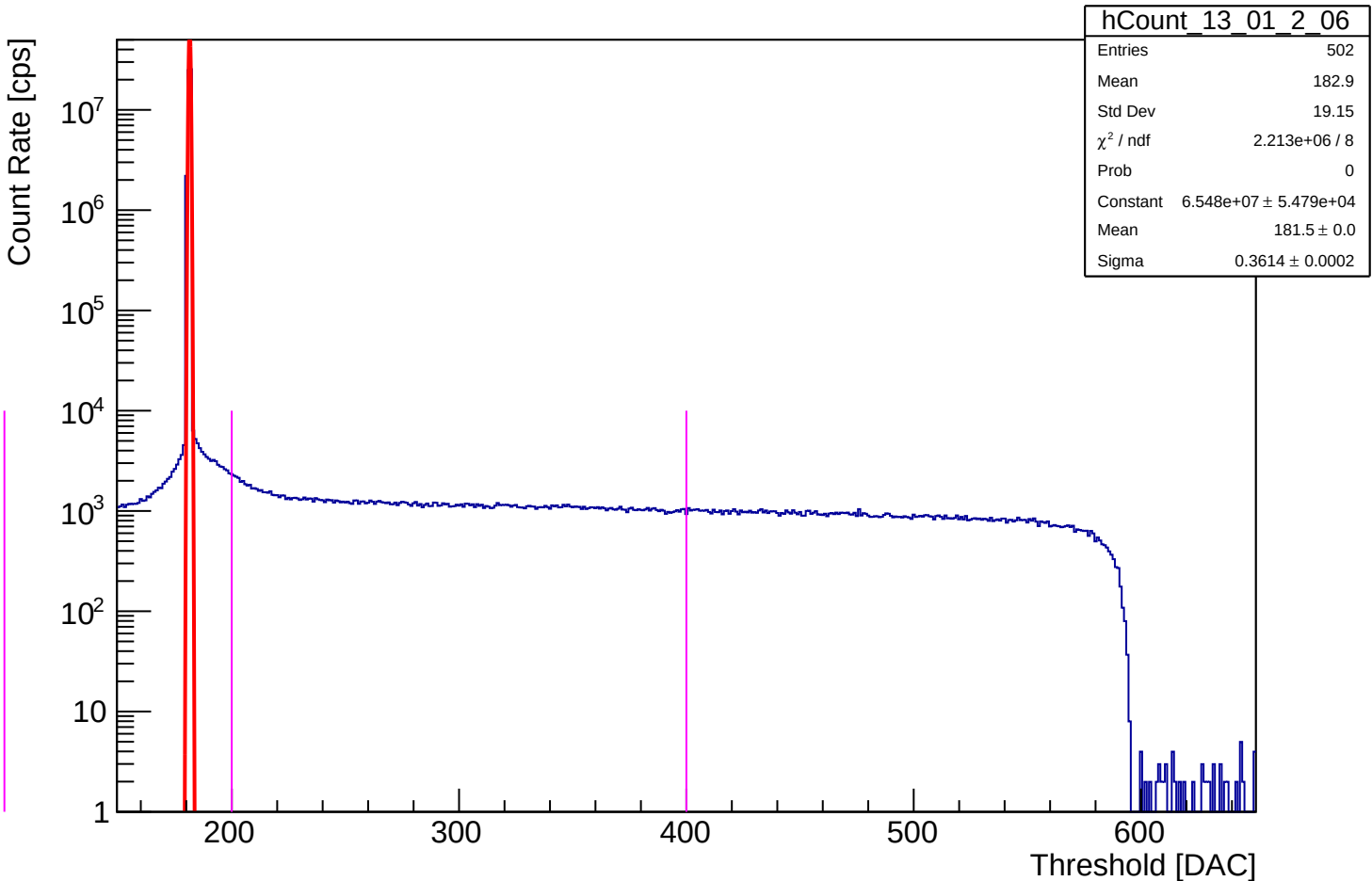


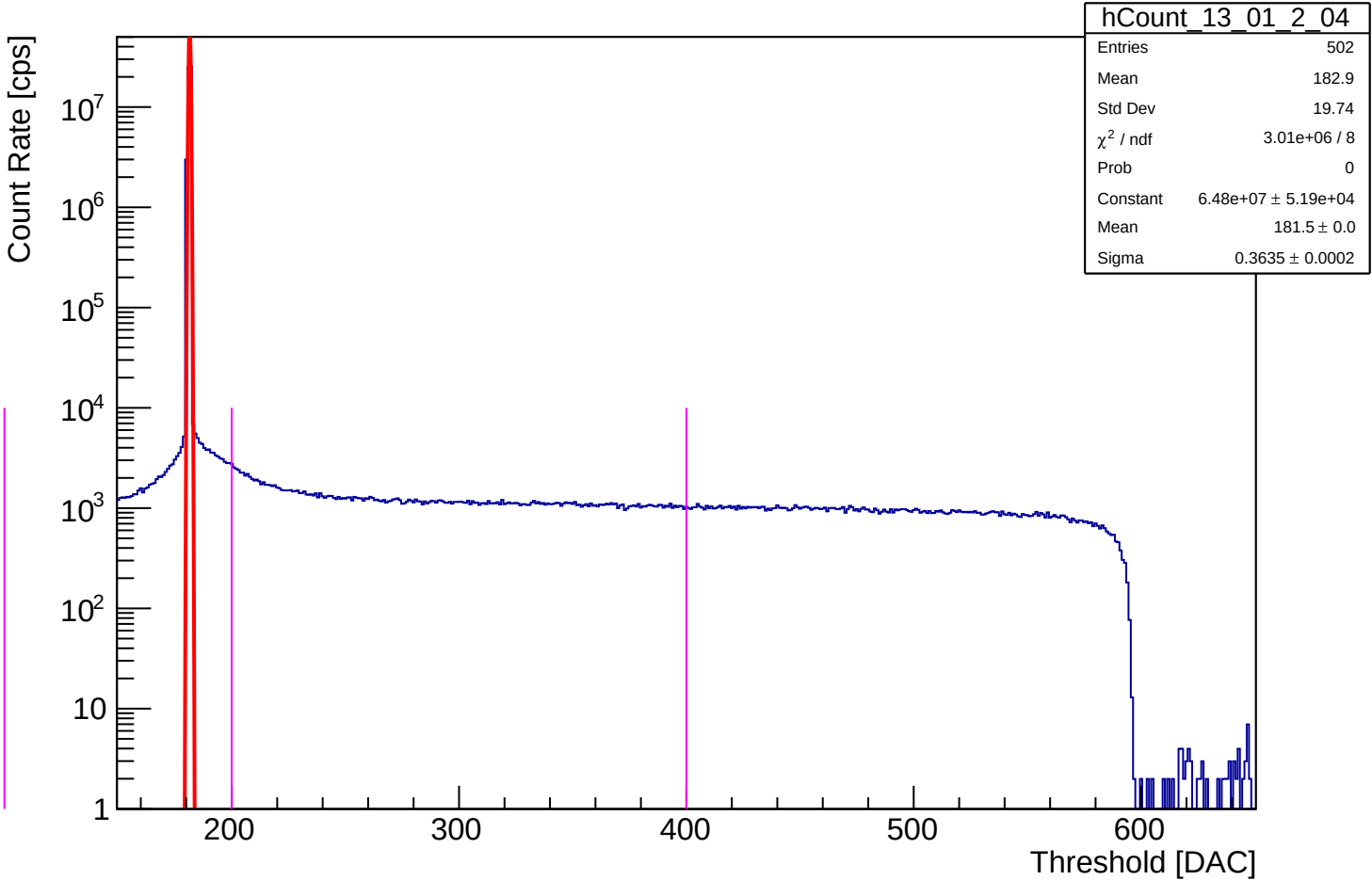




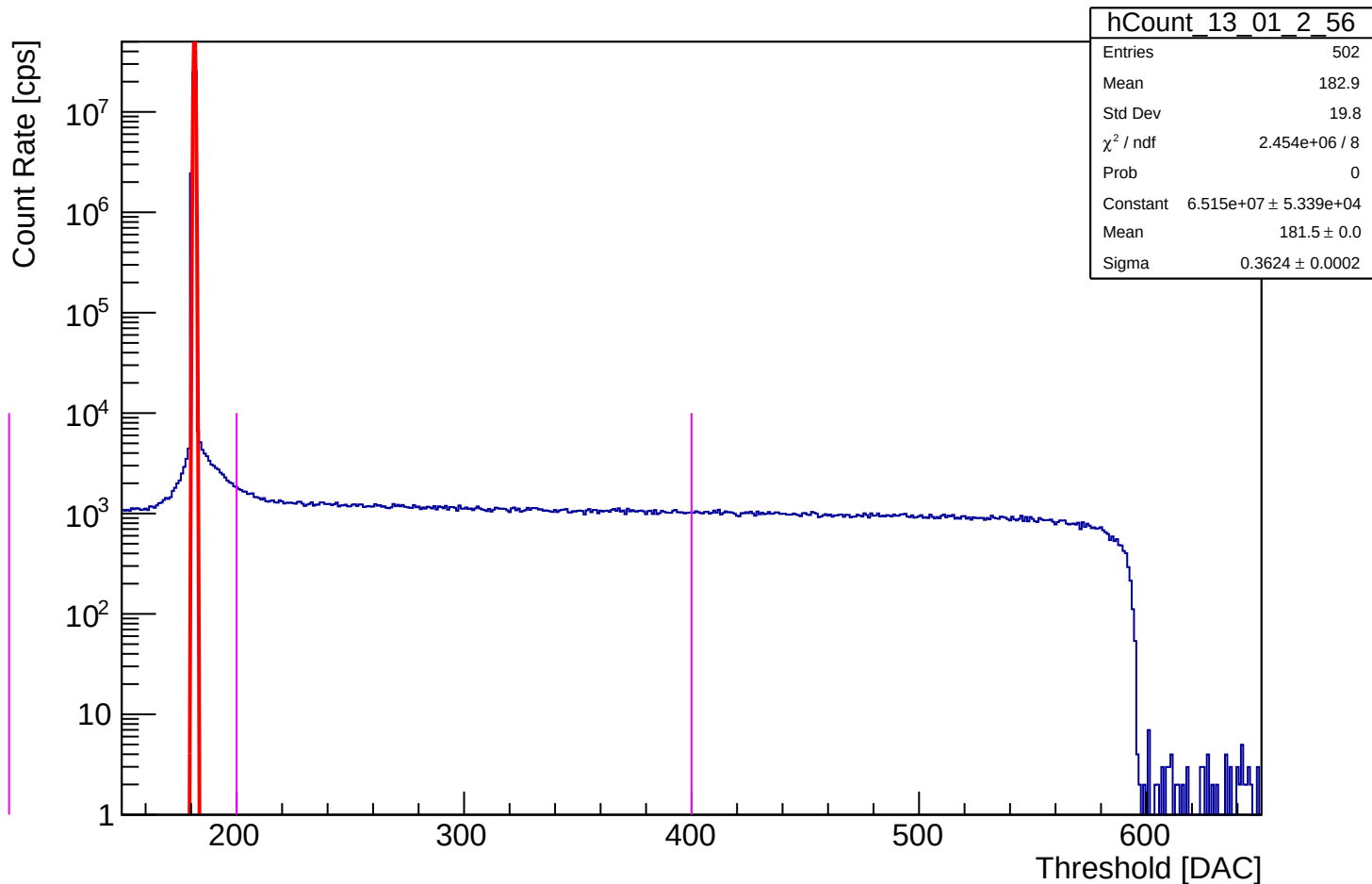
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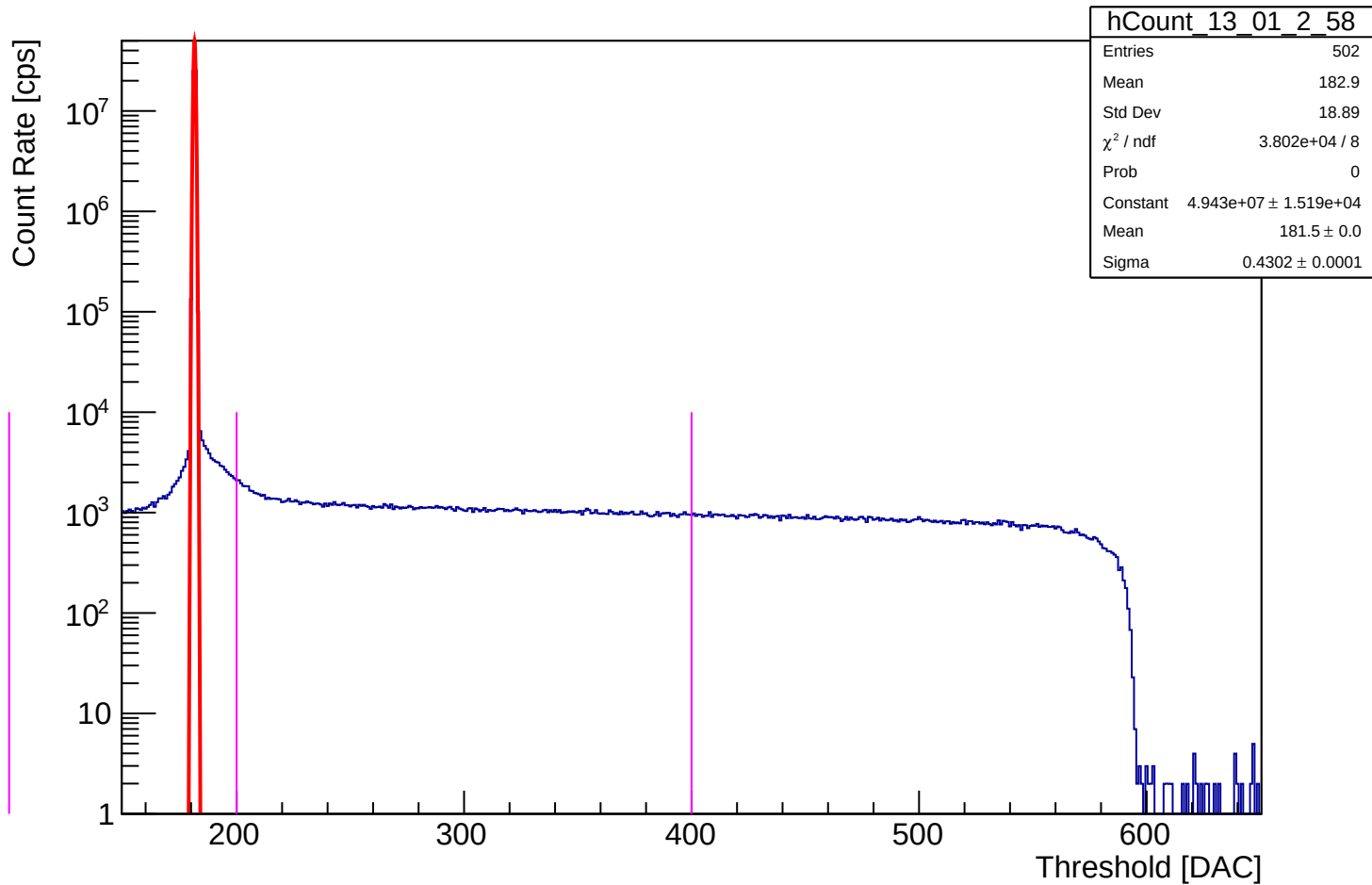




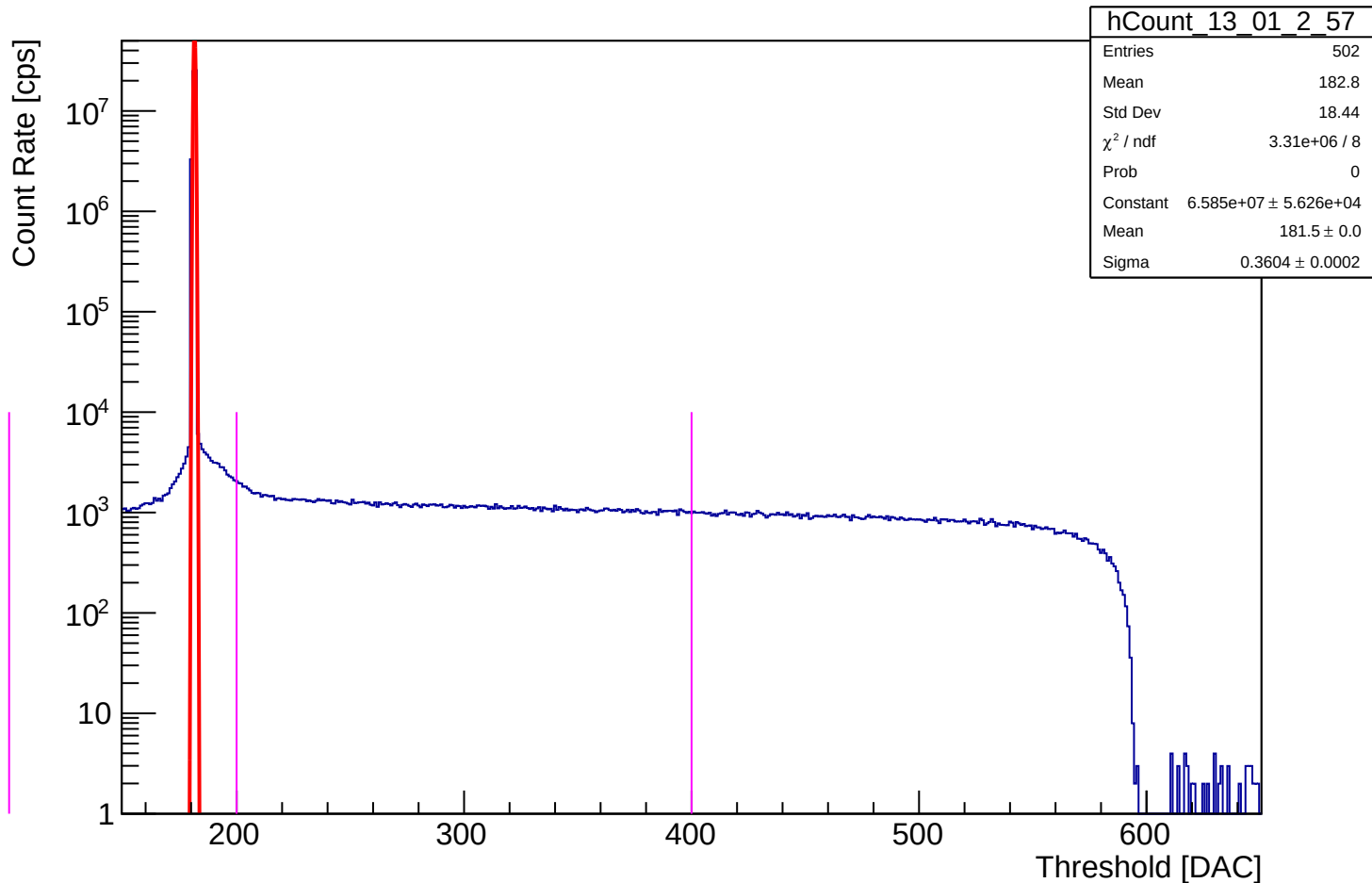
Row 1 Tile 1 Pmt 6 Pixel 53 Slot 13 Fiber 1 Asic 2 Channel 56



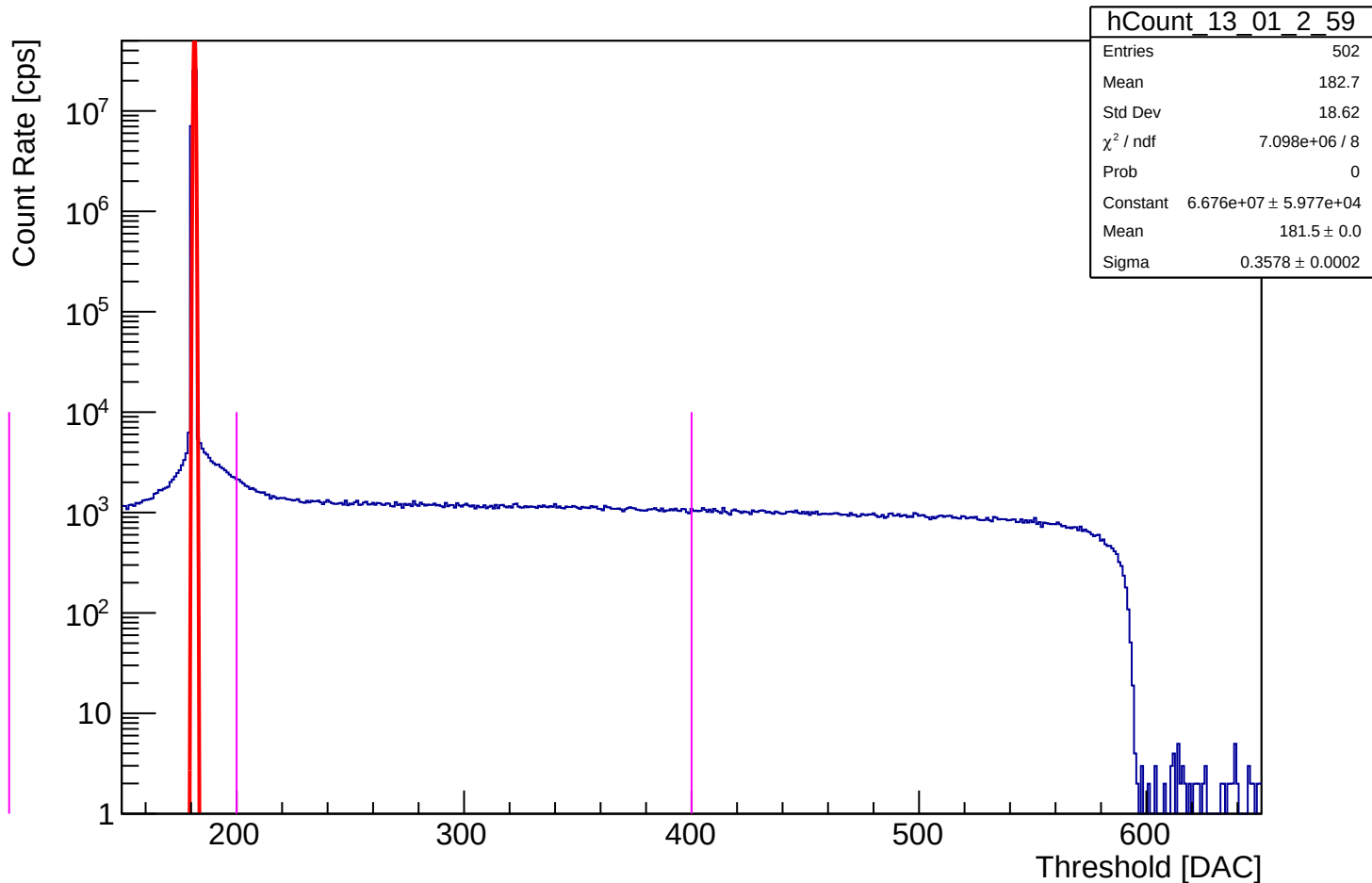
Row 1 Tile 1 Pmt 6 Pixel 54 Slot 13 Fiber 1 Asic 2 Channel 58

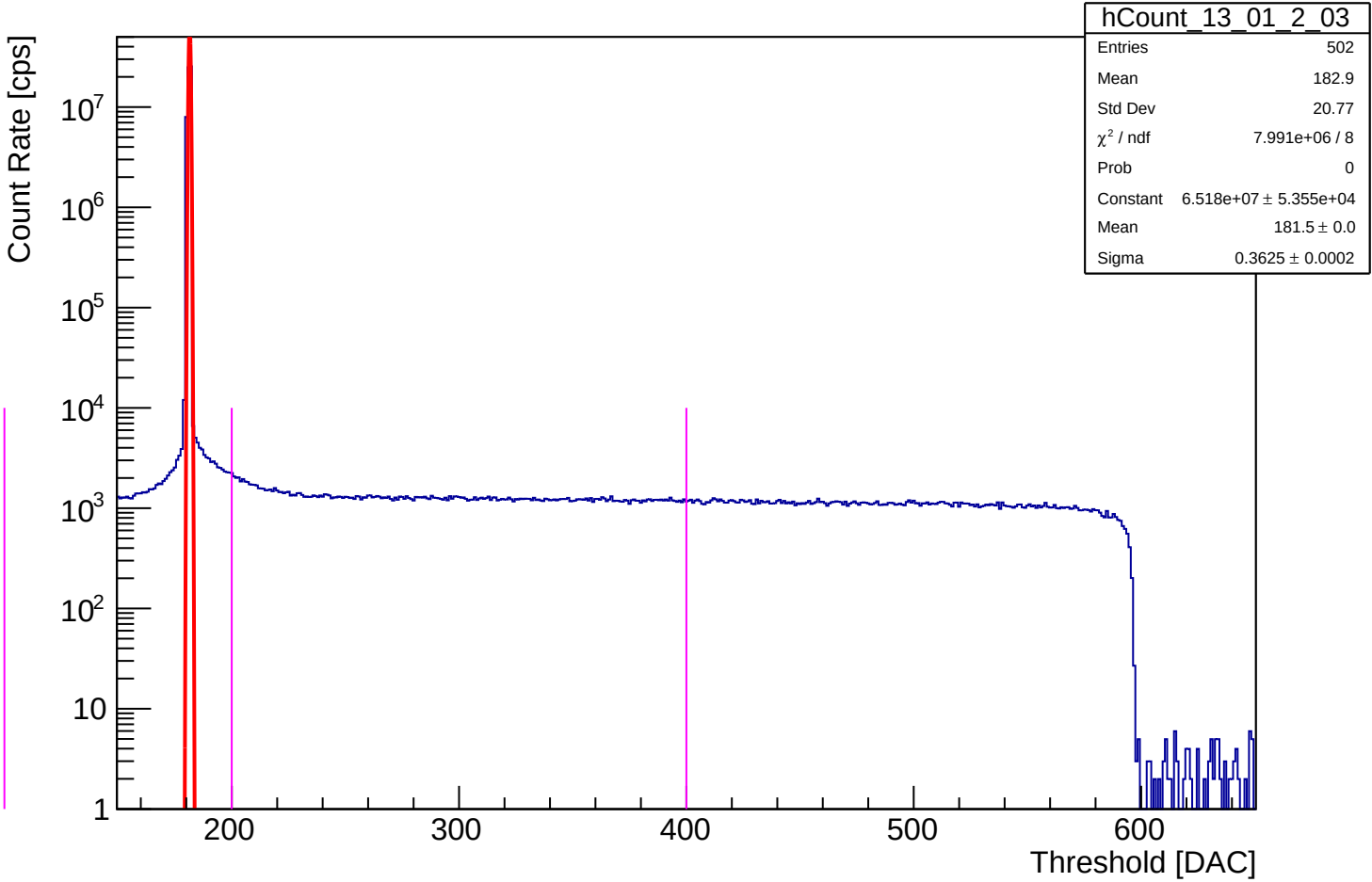


Row 1 Tile 1 Pmt 6 Pixel 55 Slot 13 Fiber 1 Asic 2 Channel 57

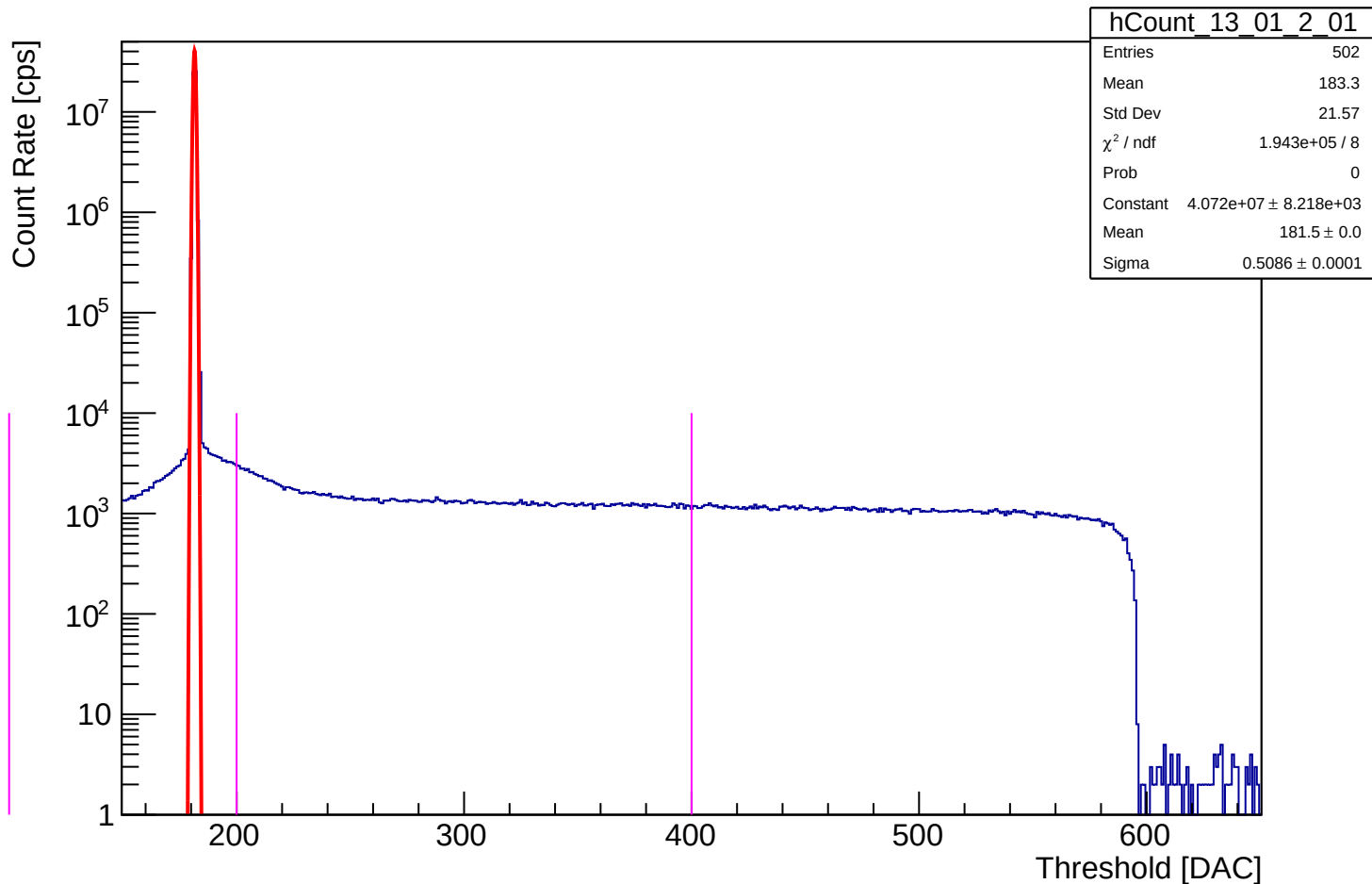


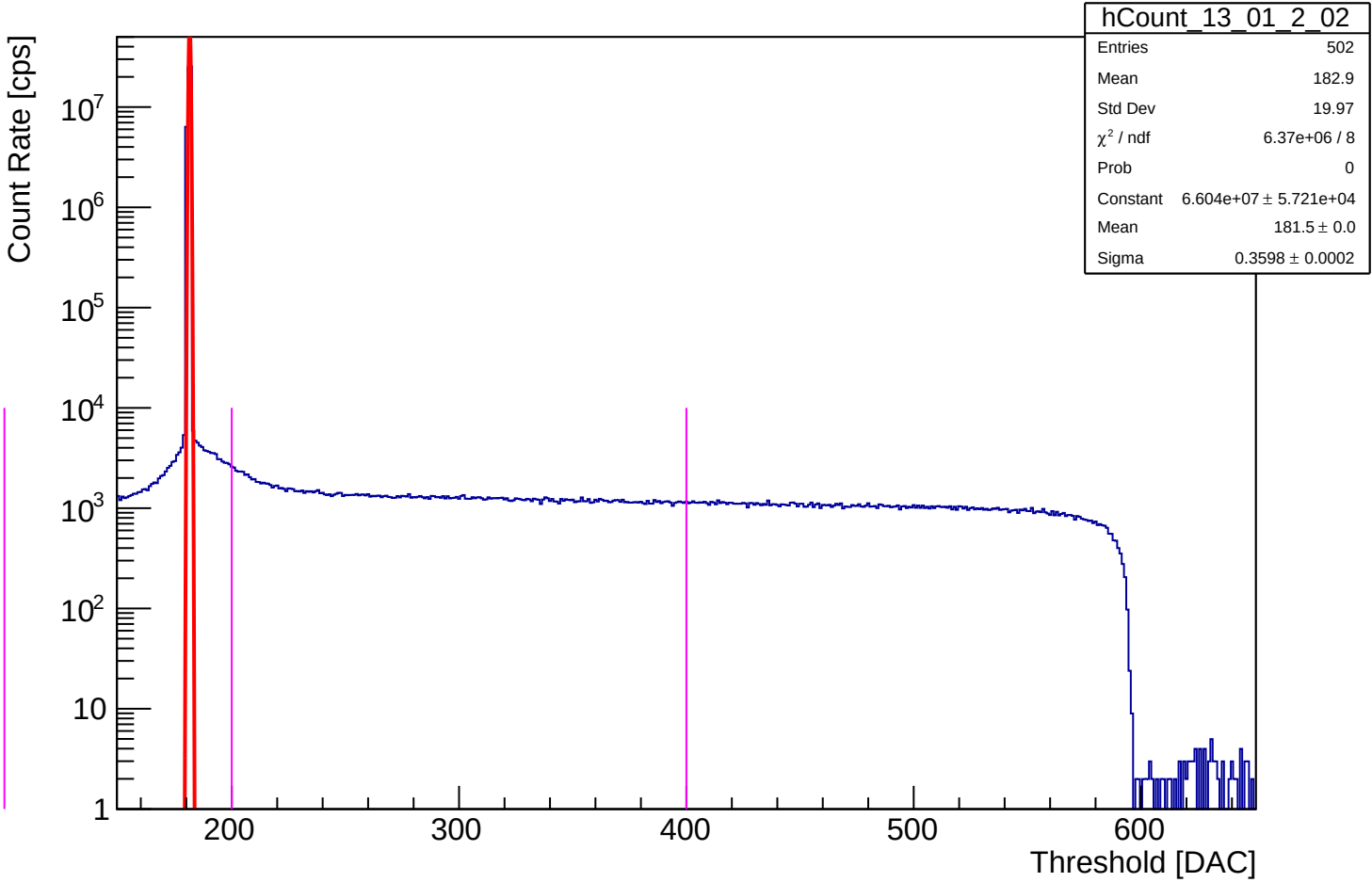
Row 1 Tile 1 Pmt 6 Pixel 56 Slot 13 Fiber 1 Asic 2 Channel 59

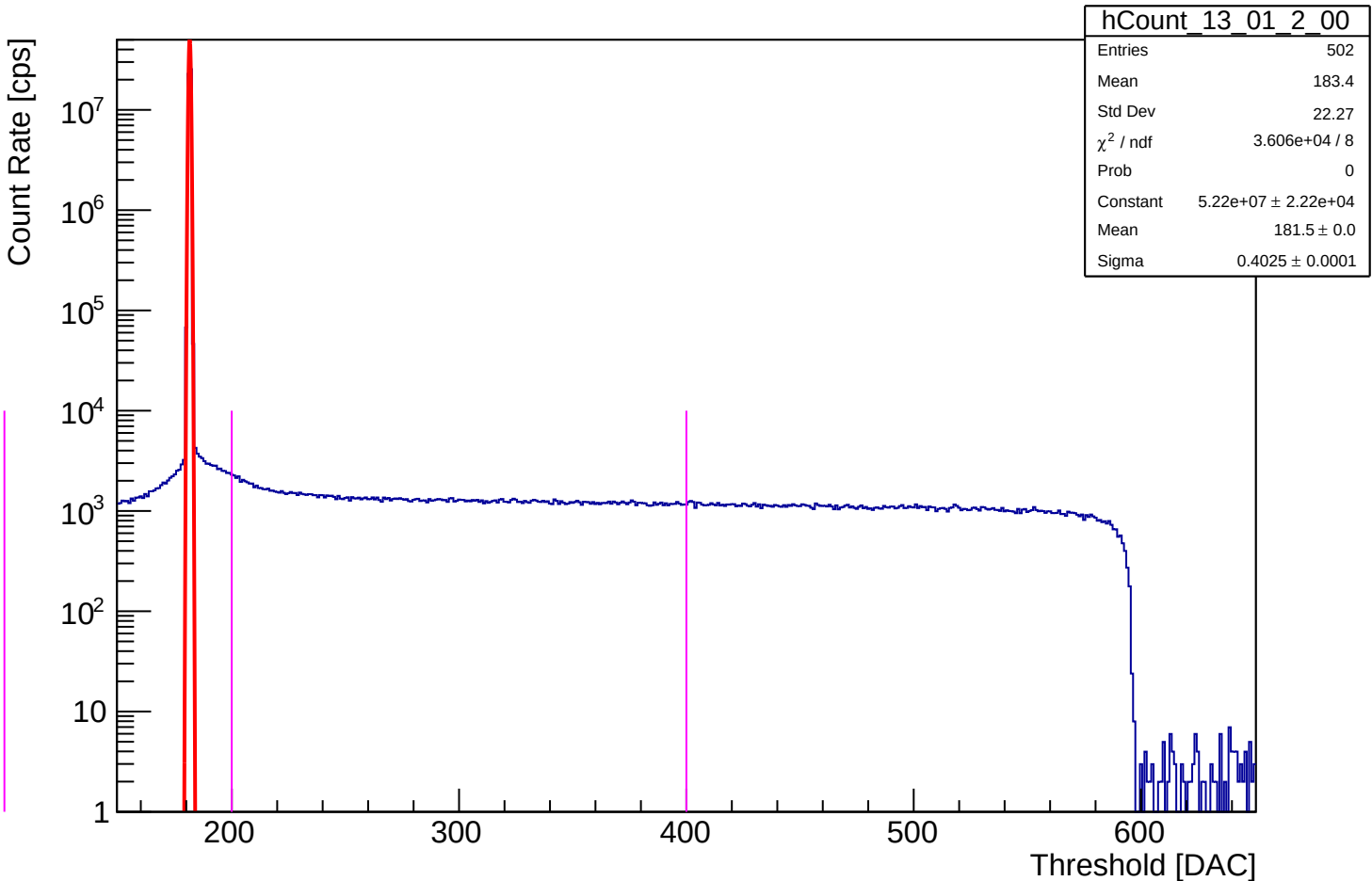




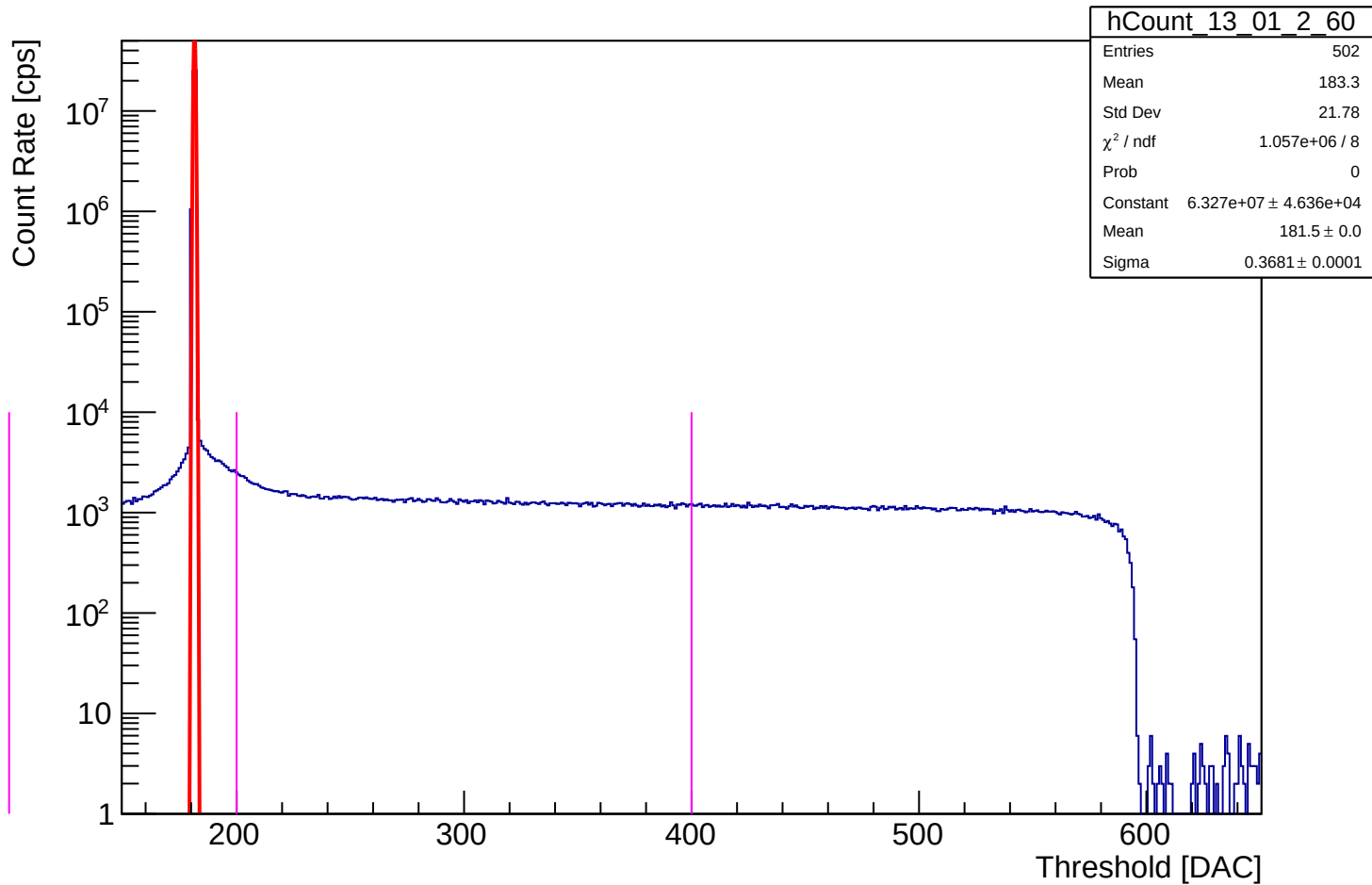
Row 1 Tile 1 Pmt 6 Pixel 58 Slot 13 Fiber 1 Asic 2 Channel 1



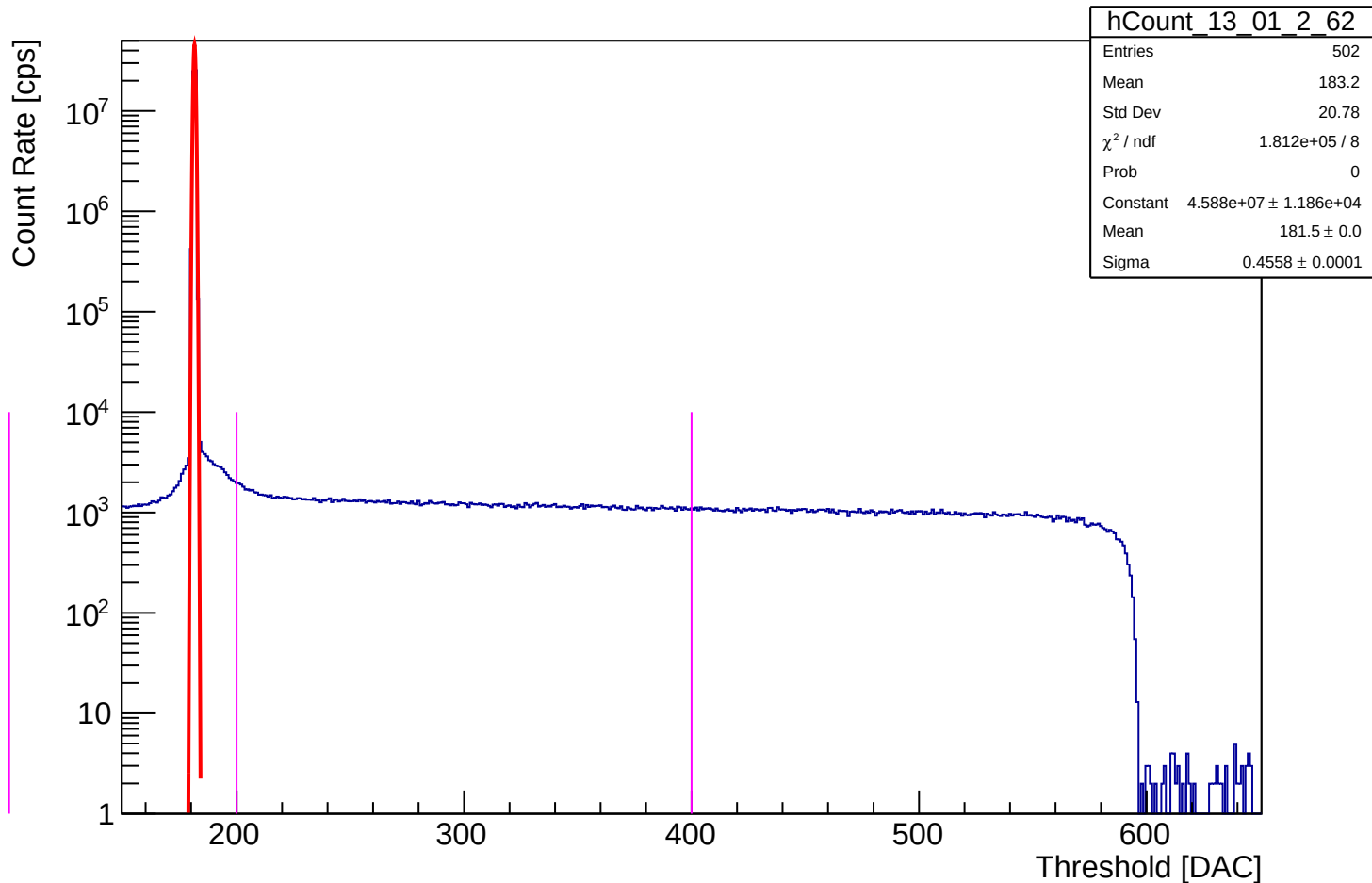




Row 1 Tile 1 Pmt 6 Pixel 61 Slot 13 Fiber 1 Asic 2 Channel 60



Row 1 Tile 1 Pmt 6 Pixel 62 Slot 13 Fiber 1 Asic 2 Channel 62



Row 1 Tile 1 Pmt 6 Pixel 63 Slot 13 Fiber 1 Asic 2 Channel 61

